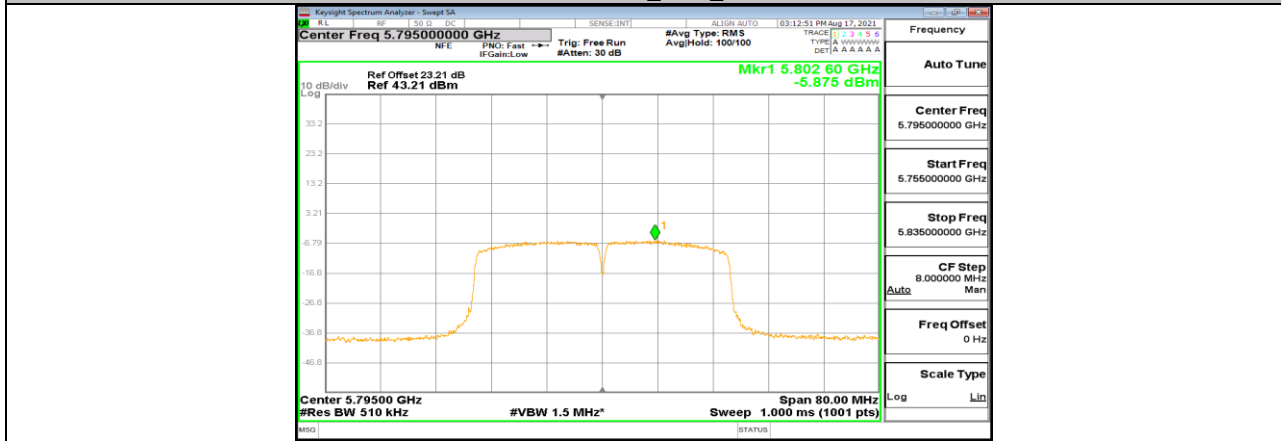
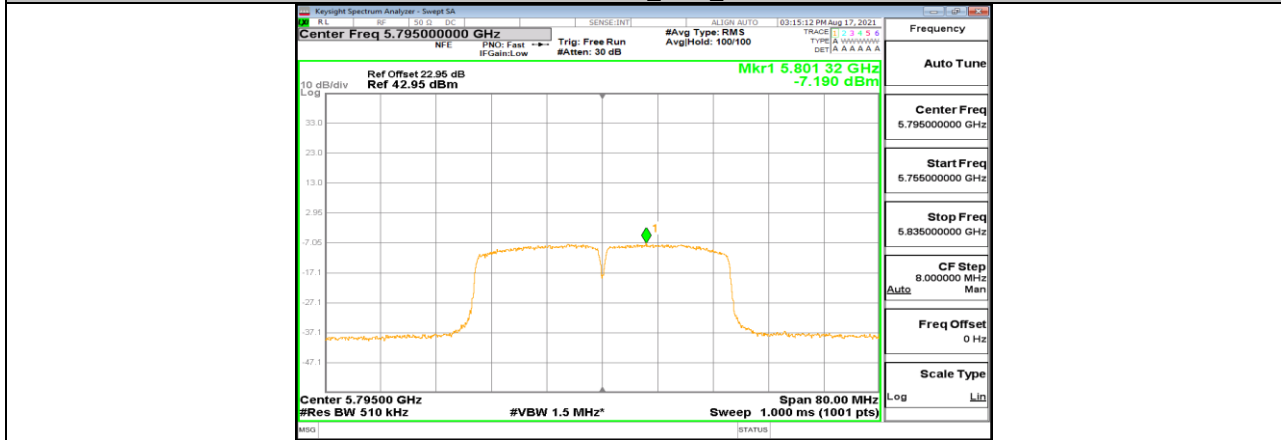


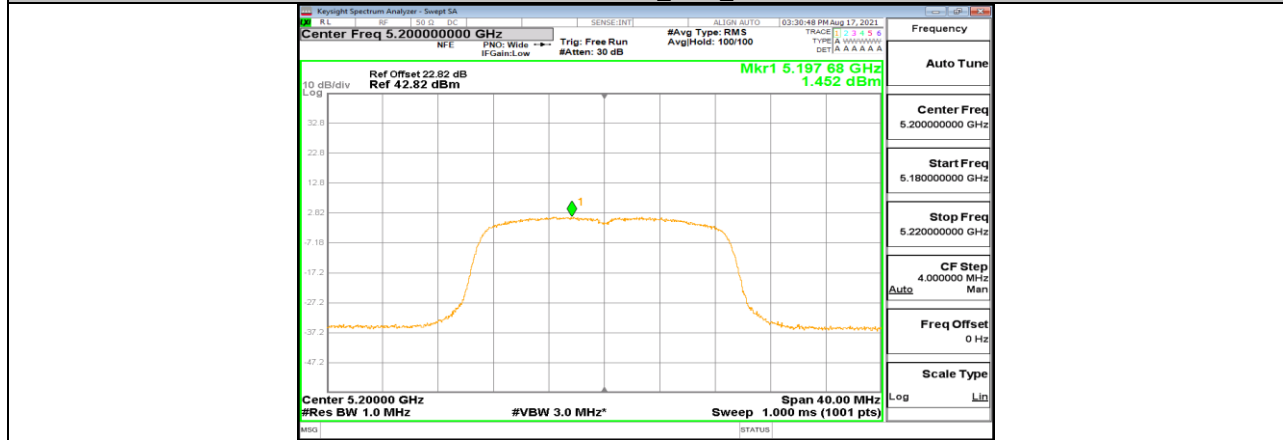
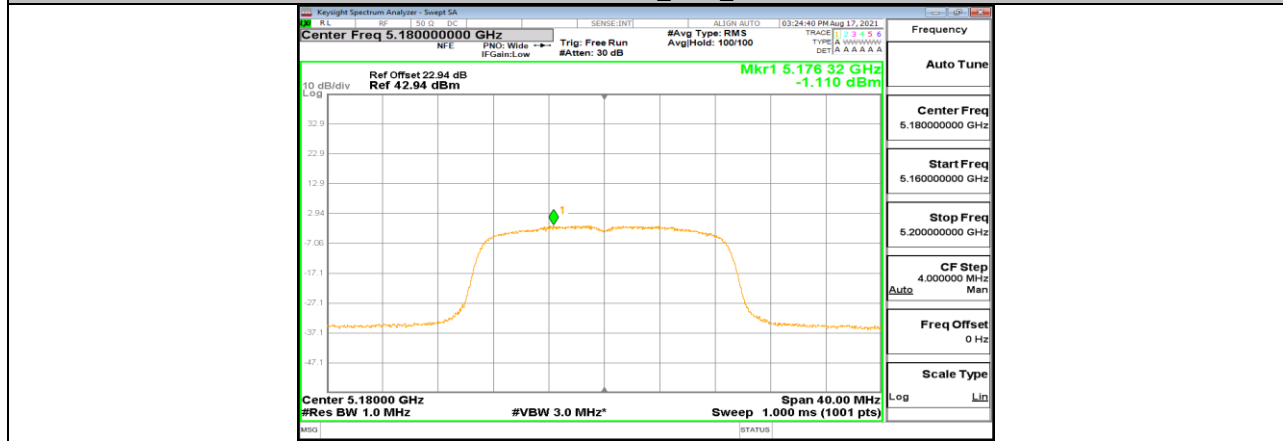
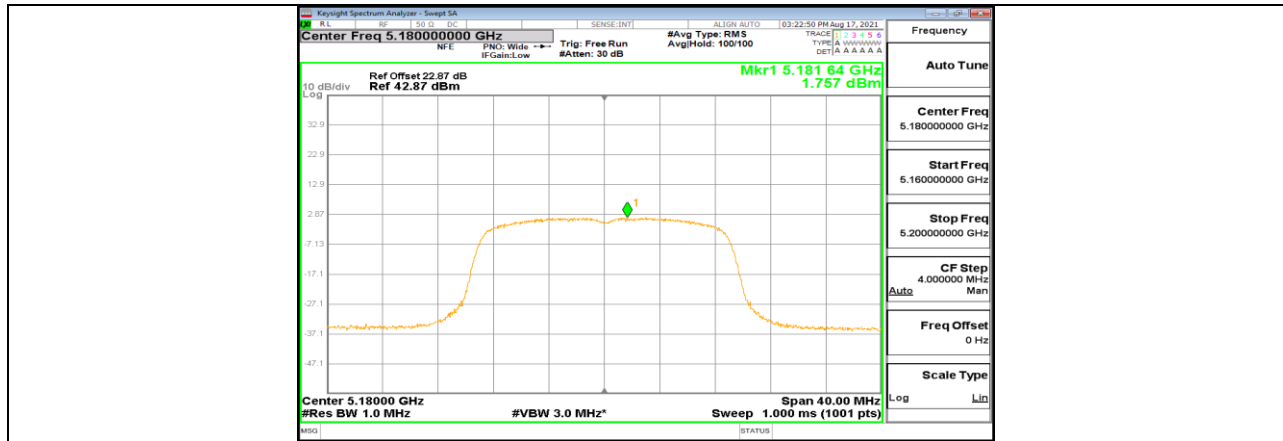
11N40MIMO_Ant2_5755

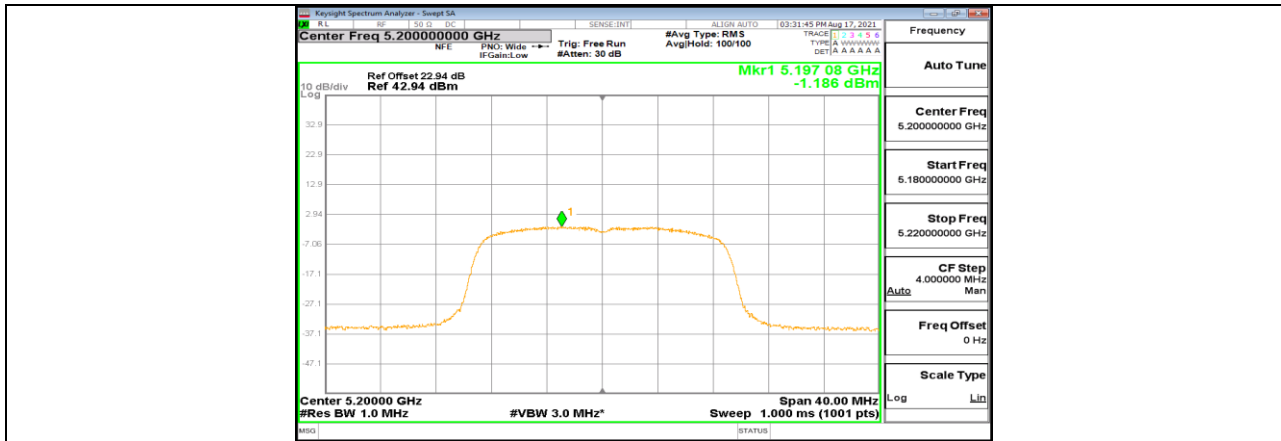


11N40MIMO_Ant1_5795

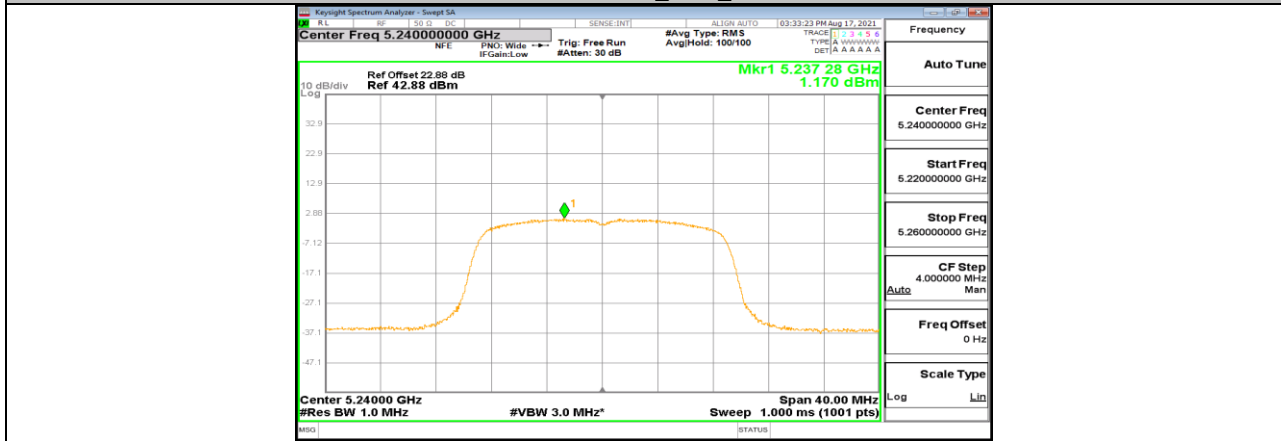


11N40MIMO_Ant2_5795

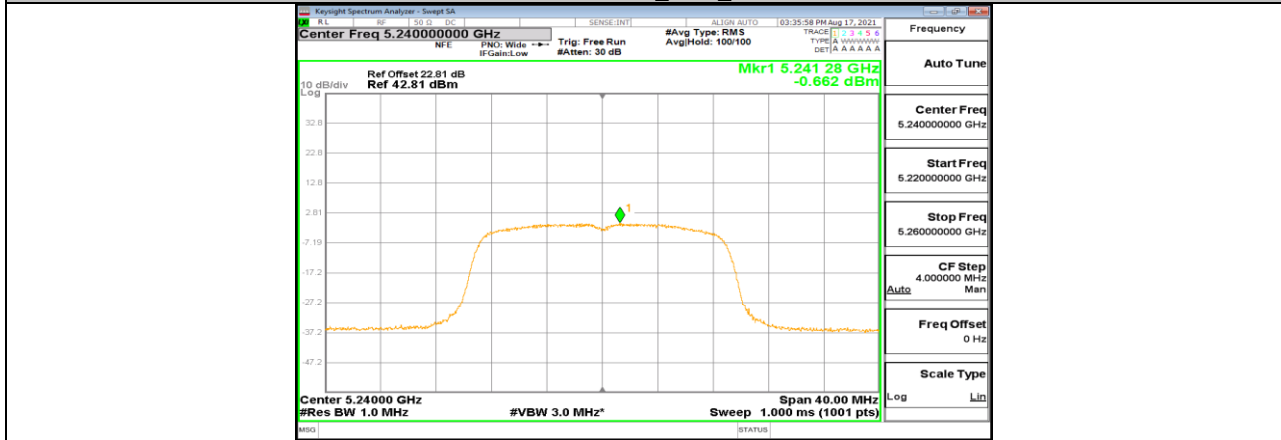




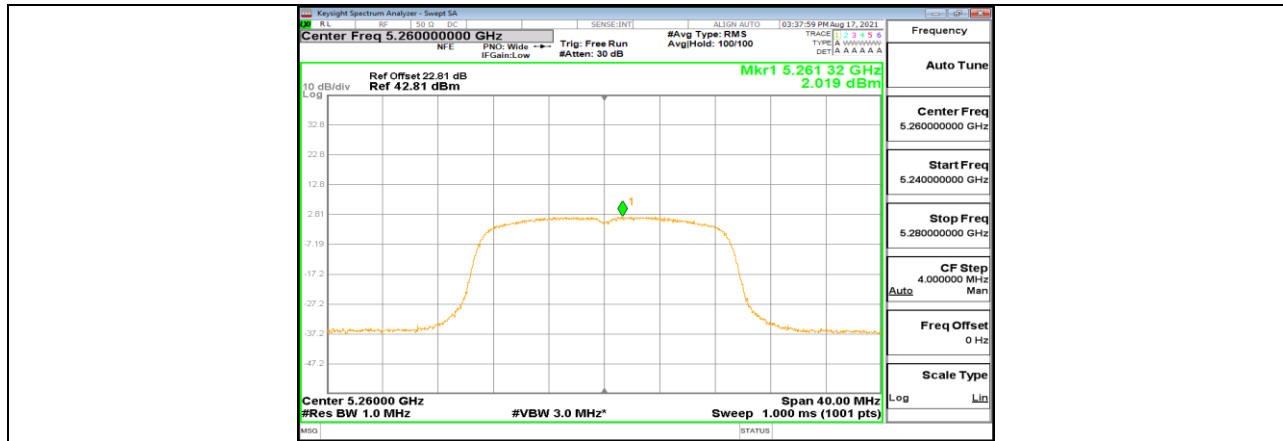
11AC20MIMO_Ant2_5200



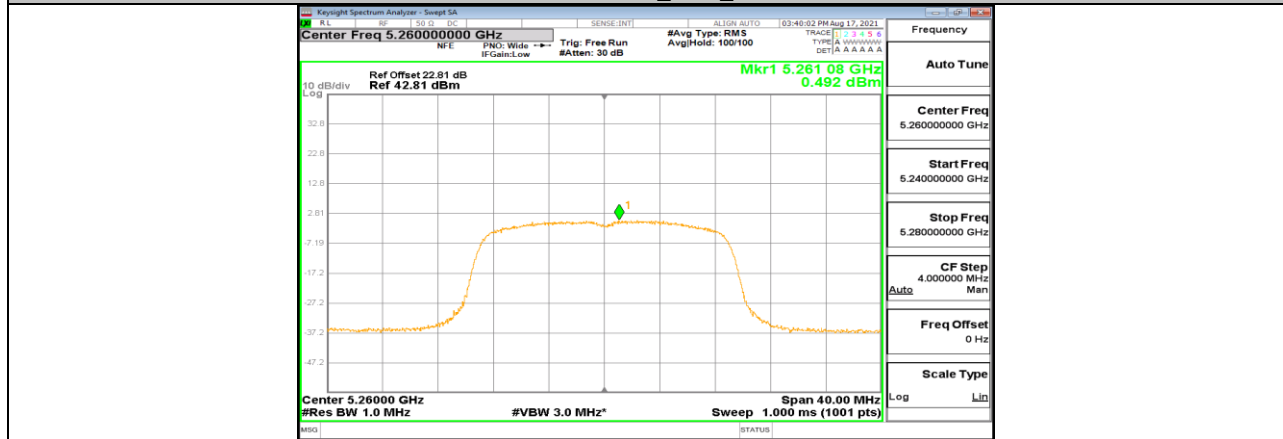
11AC20MIMO_Ant1_5240



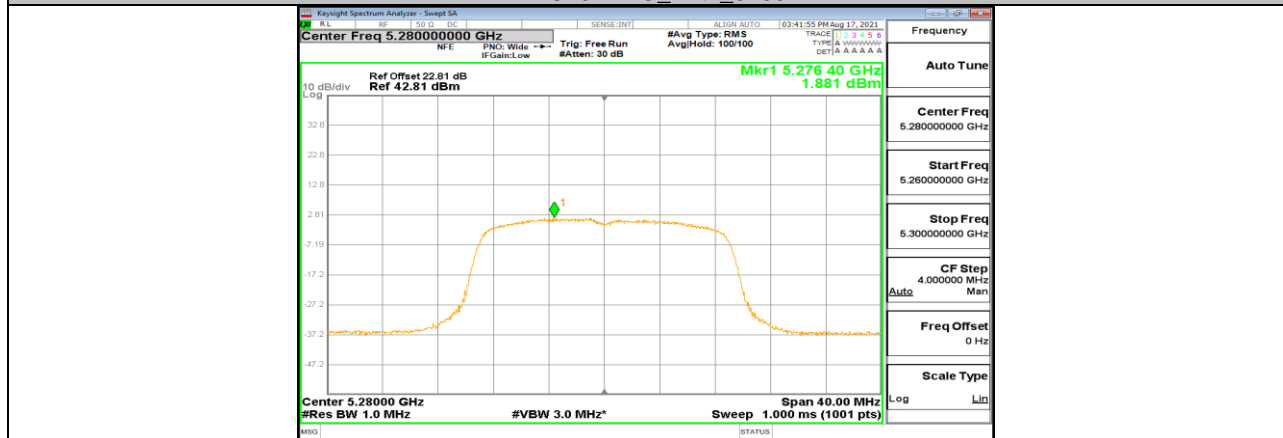
11AC20MIMO_Ant2_5240



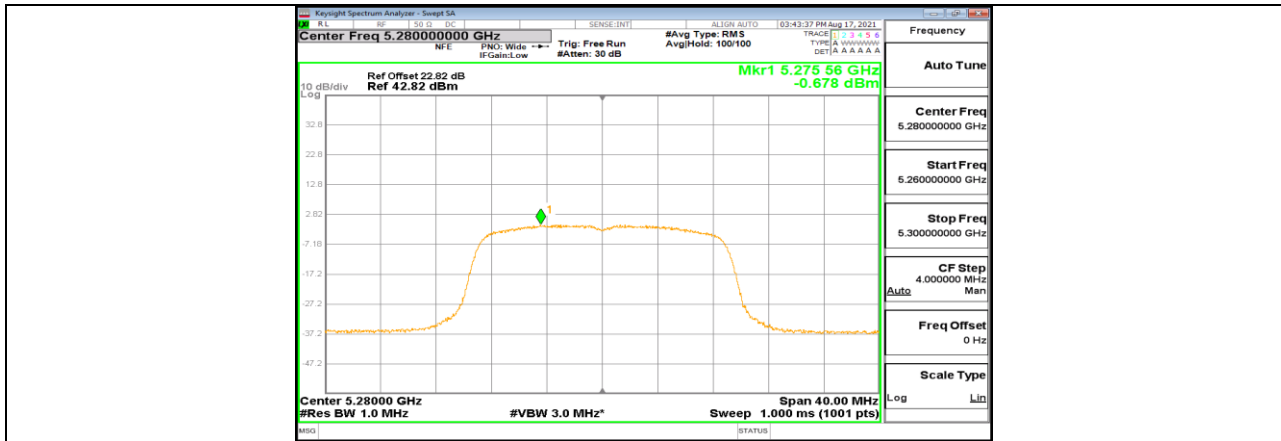
11AC20MIMO_Ant1_5260



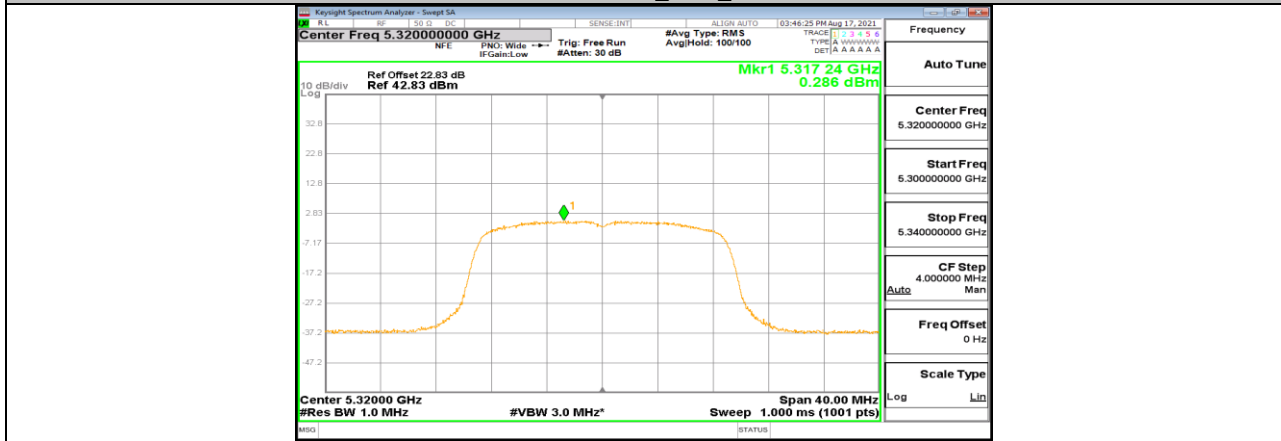
11AC20MIMO_Ant2_5260



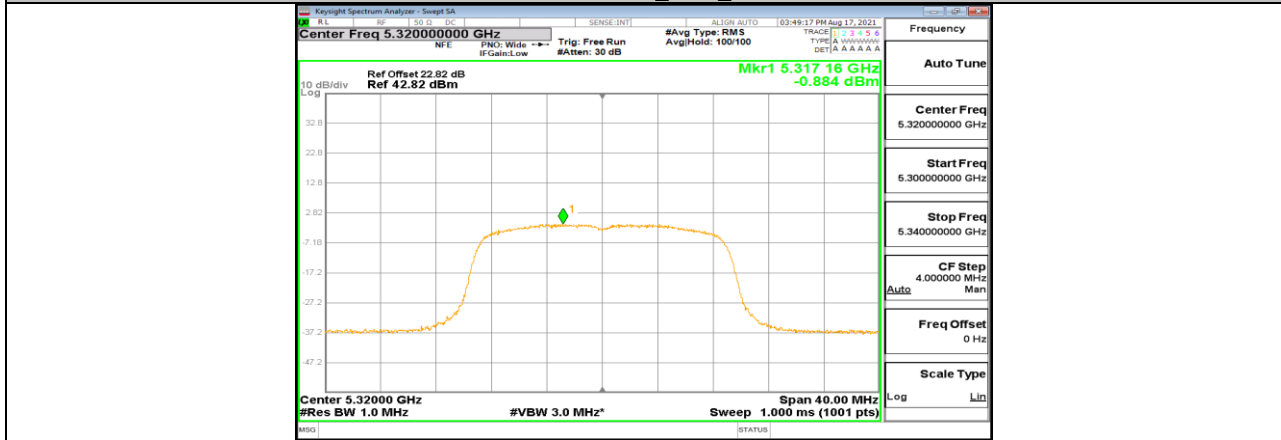
11AC20MIMO_Ant1_5280



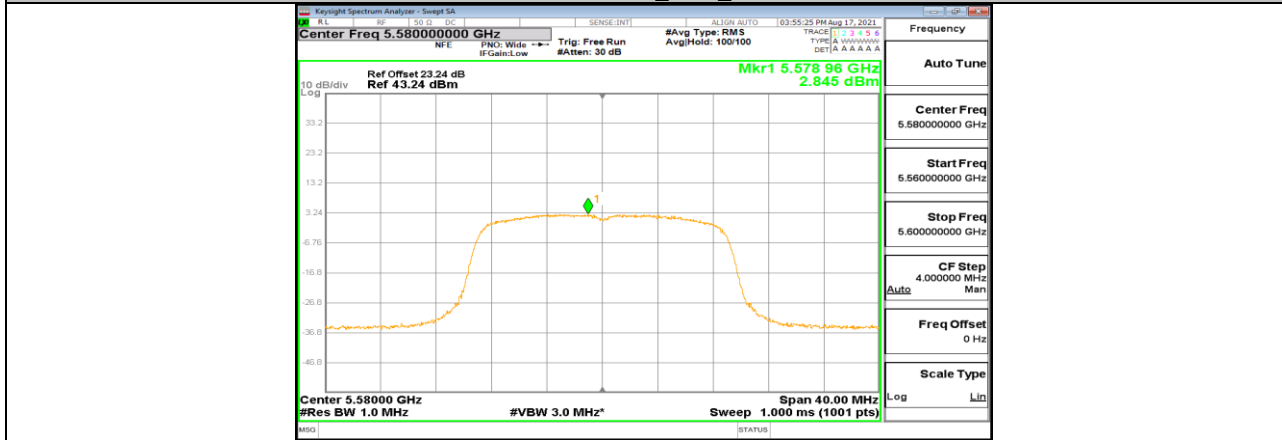
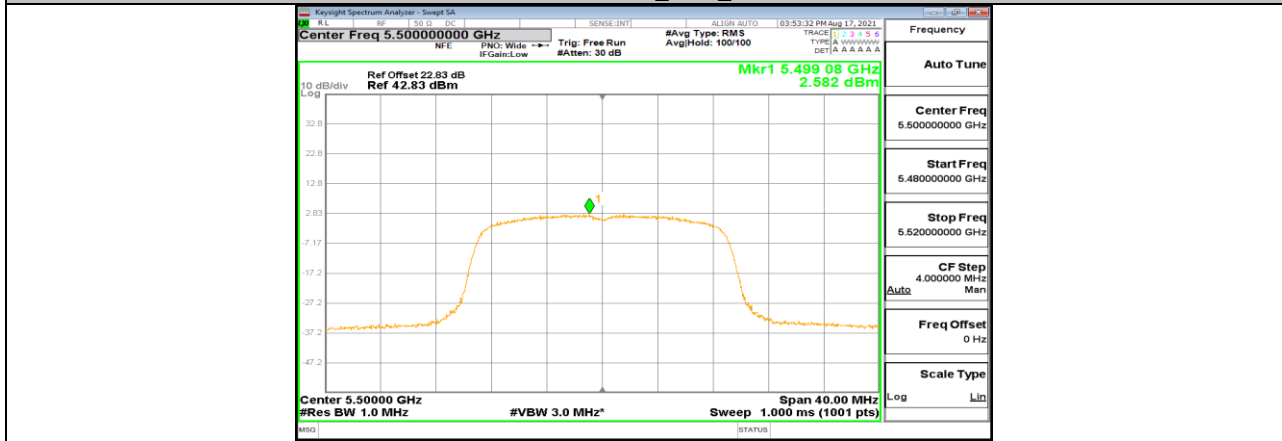
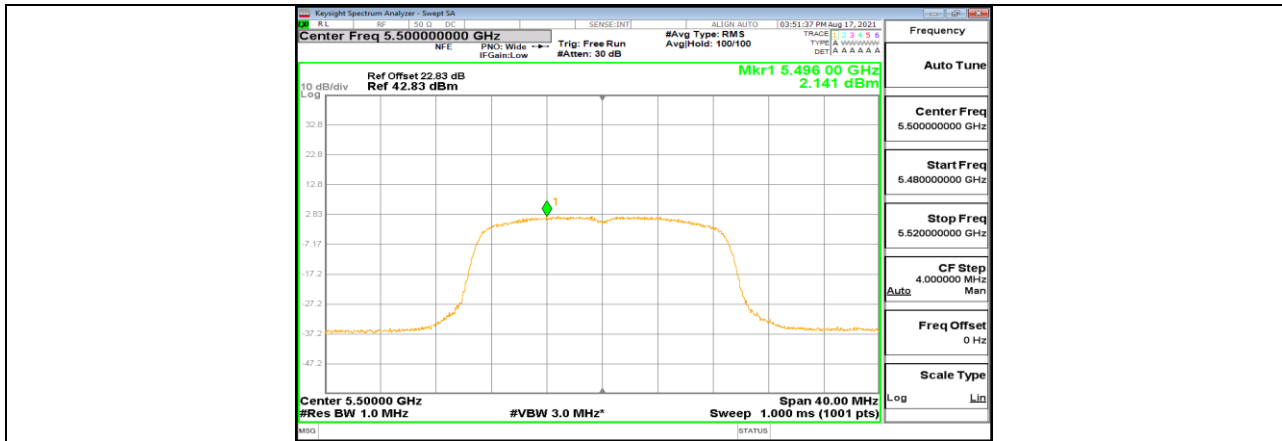
11AC20MIMO_Ant2_5280

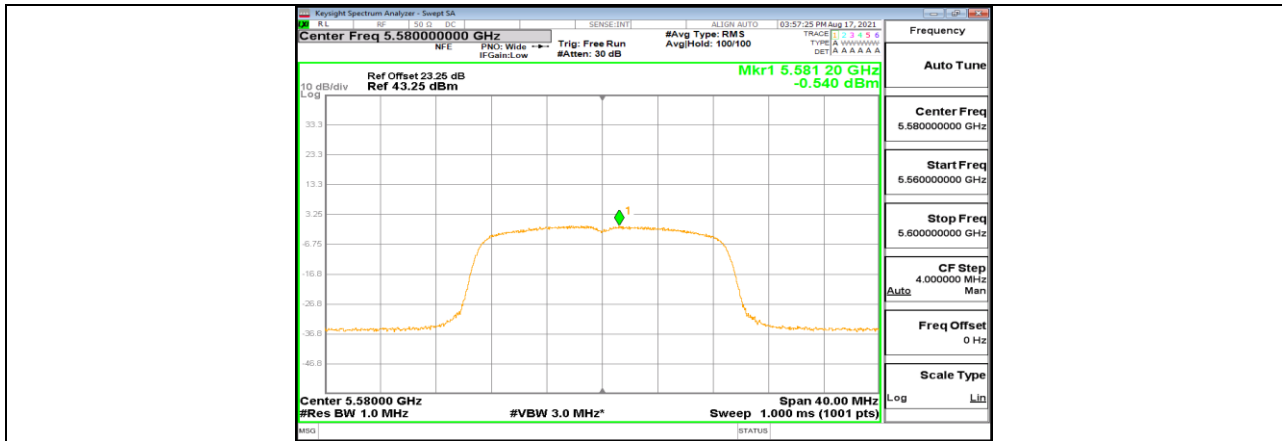


11AC20MIMO_Ant1_5320

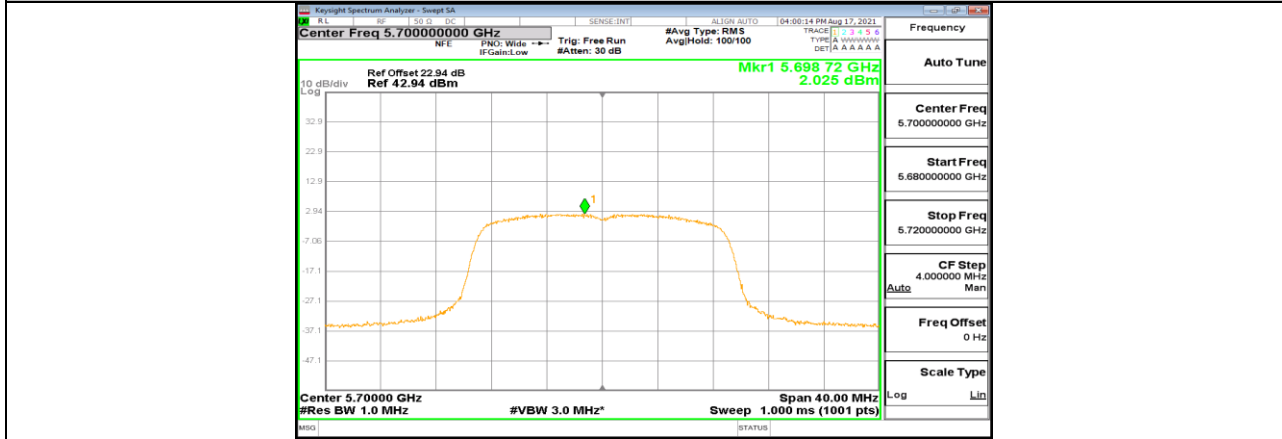


11AC20MIMO_Ant2_5320

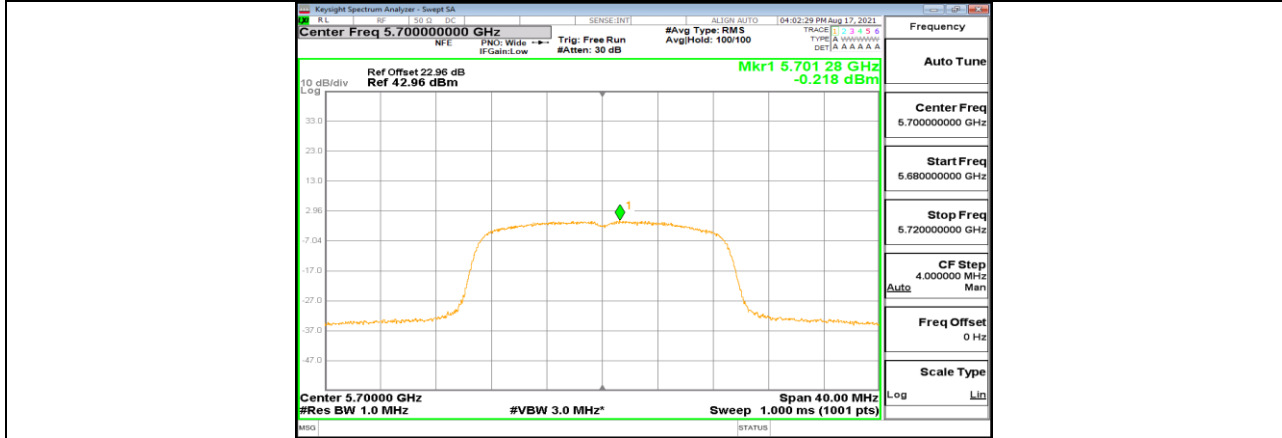




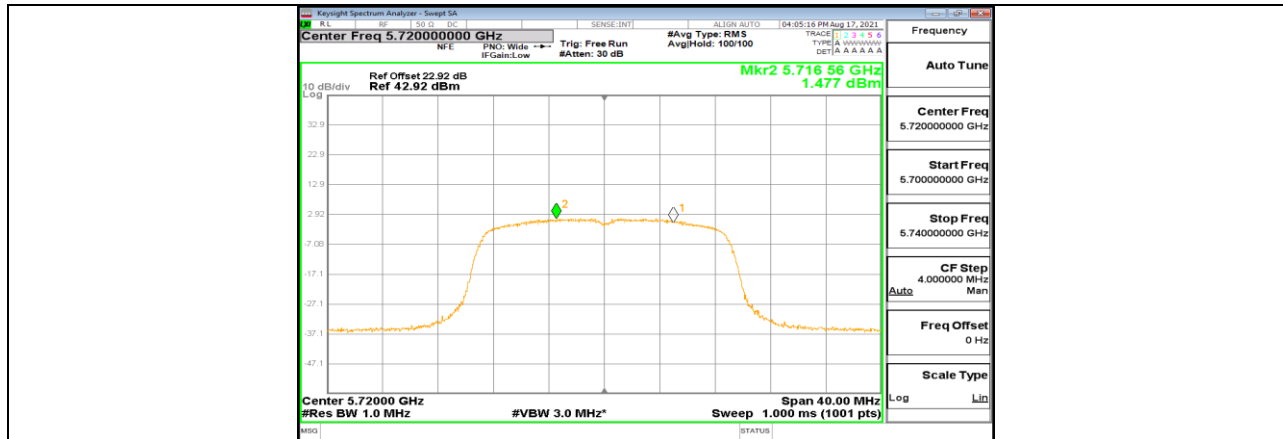
11AC20MIMO_Ant2_5580



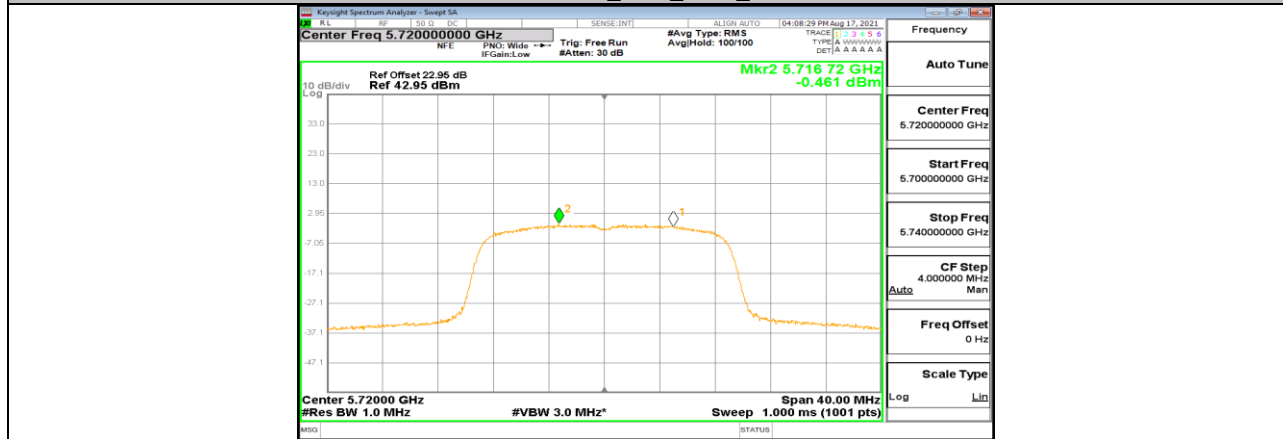
11AC20MIMO_Ant1_5700



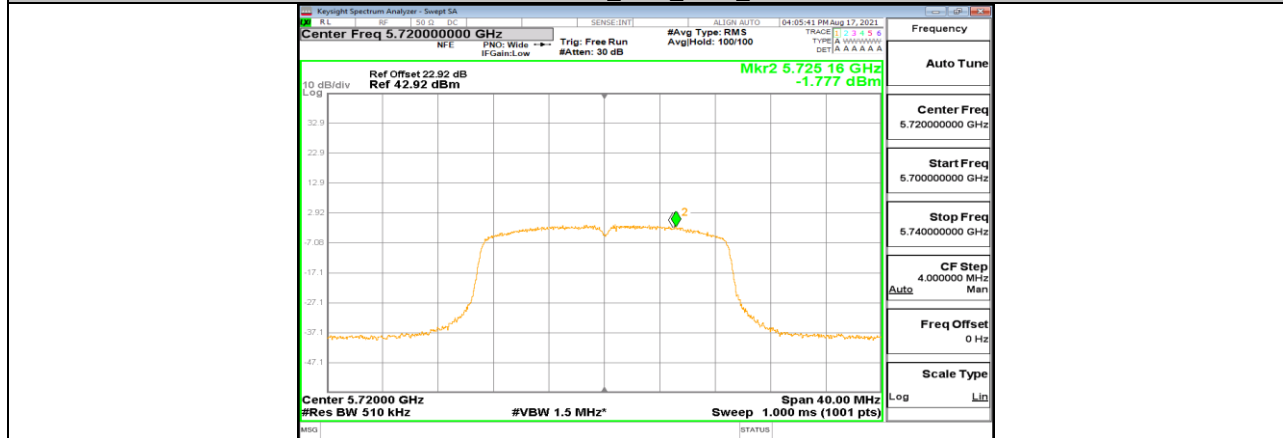
11AC20MIMO_Ant2_5700



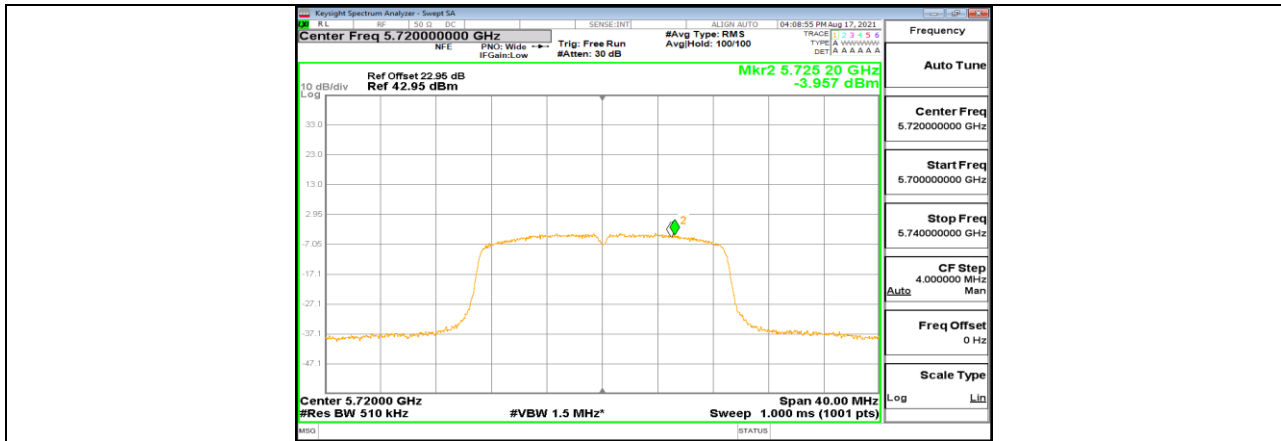
11AC20MIMO_Ant1_5720_UNII-2C



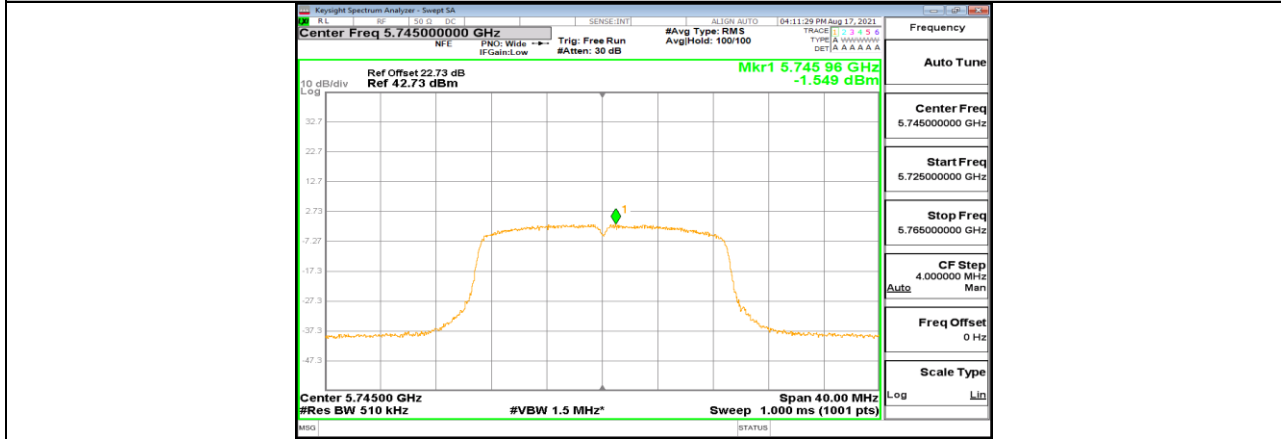
11AC20MIMO_Ant2_5720_UNII-2C



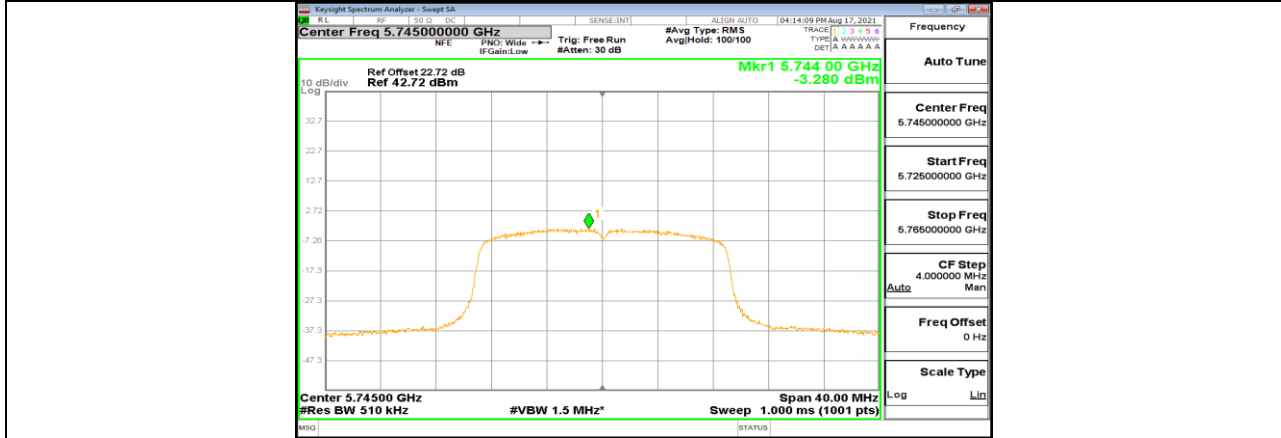
11AC20MIMO_Ant1_5720_UNII-3



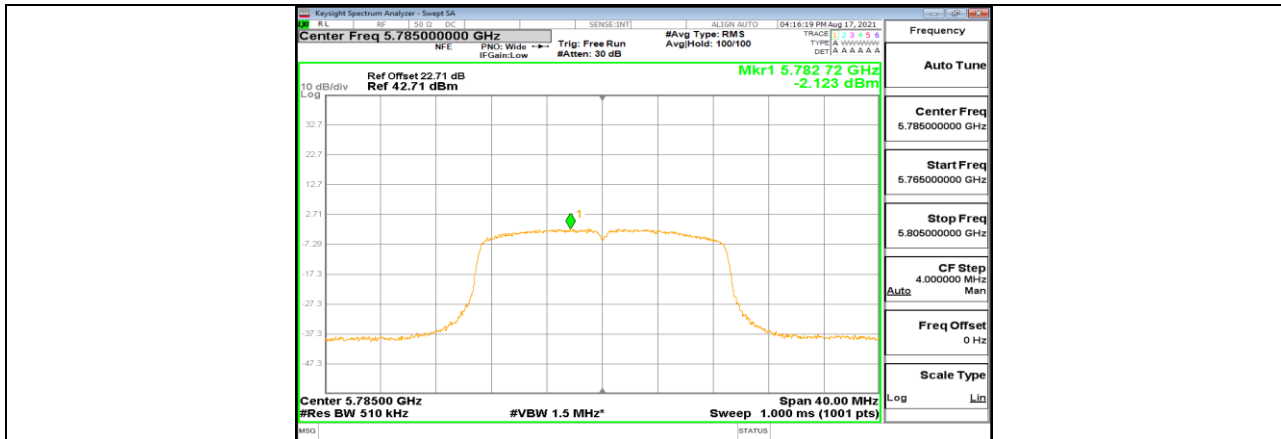
11AC20MIMO_Ant2_5720_UNII-3



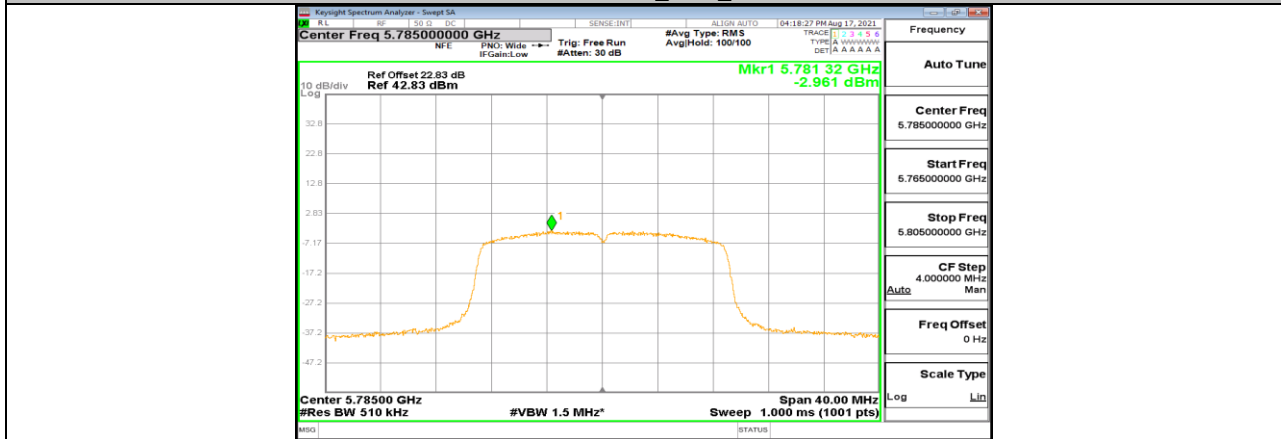
11AC20MIMO_Ant1_5745



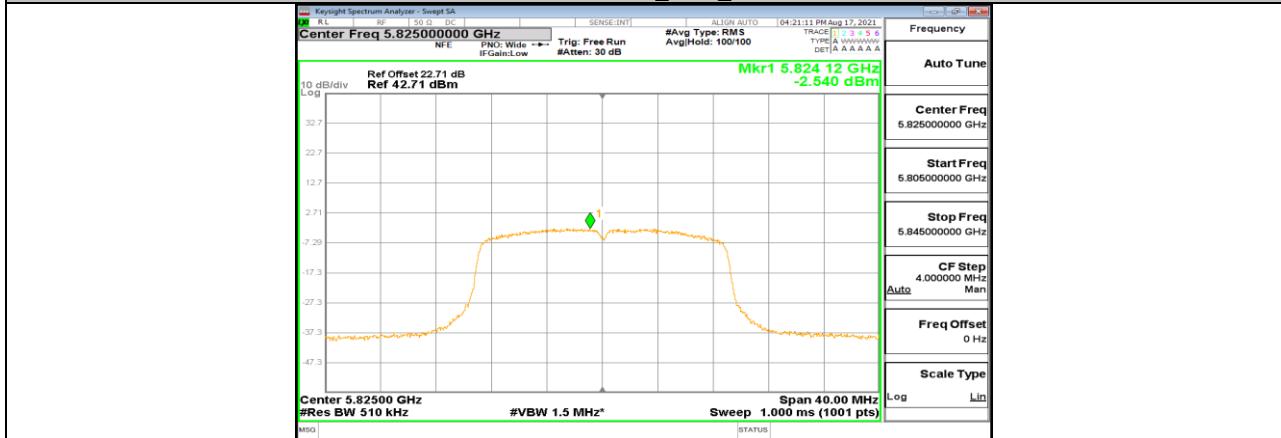
11AC20MIMO_Ant2_5745



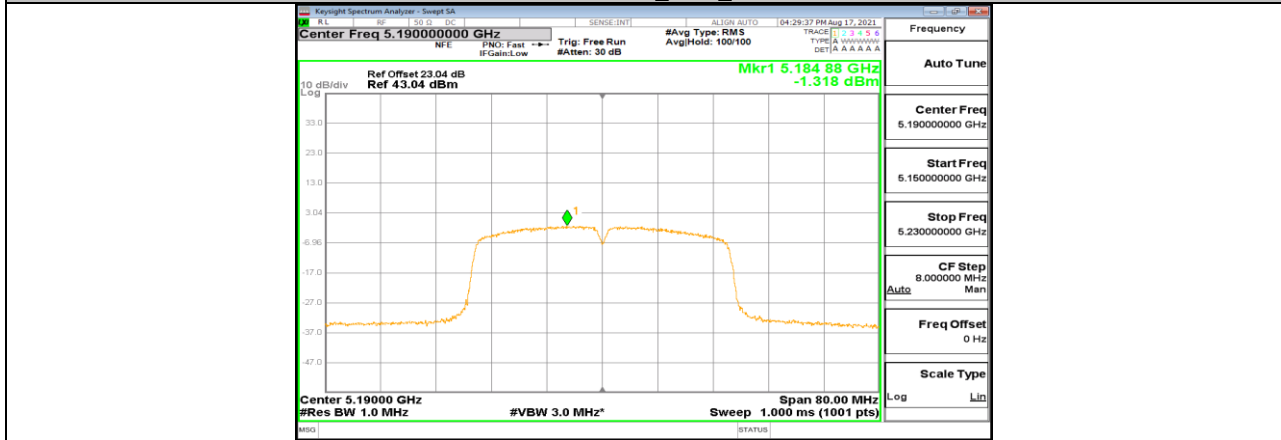
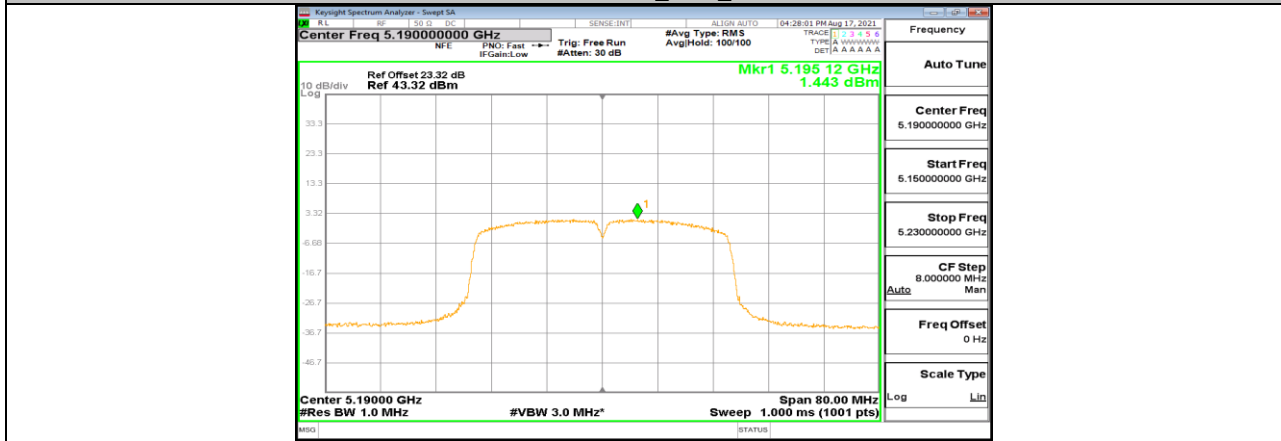
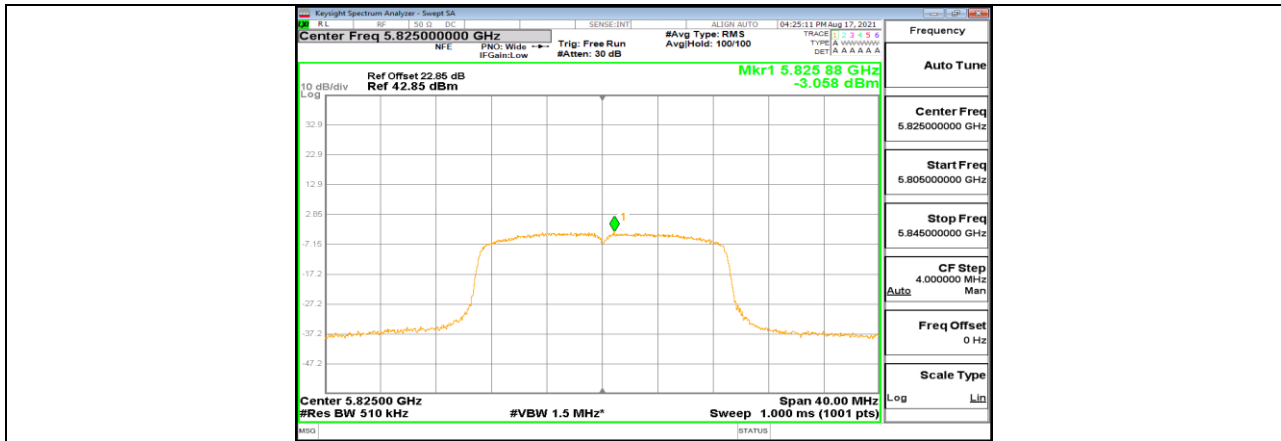
11AC20MIMO_Ant1_5785

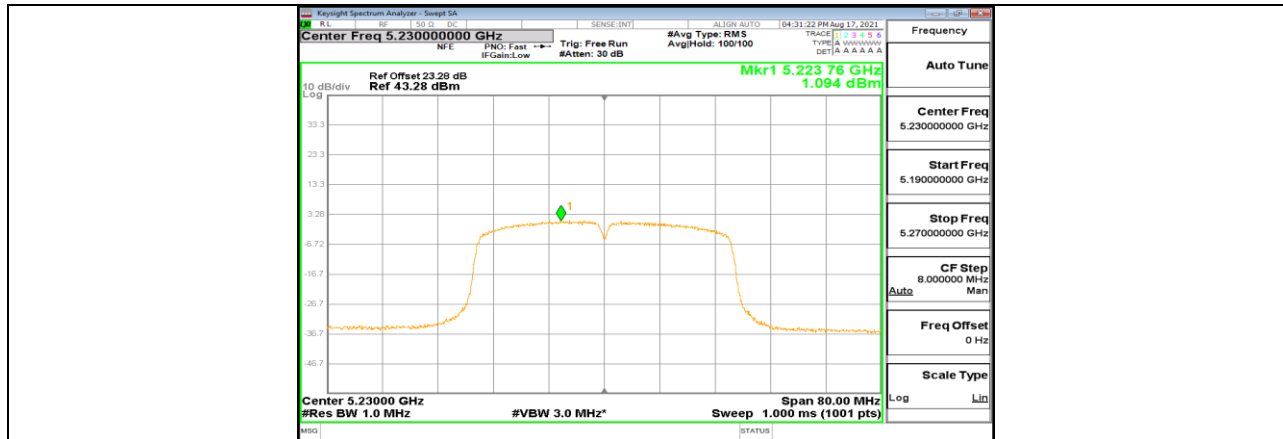


11AC20MIMO_Ant2_5785

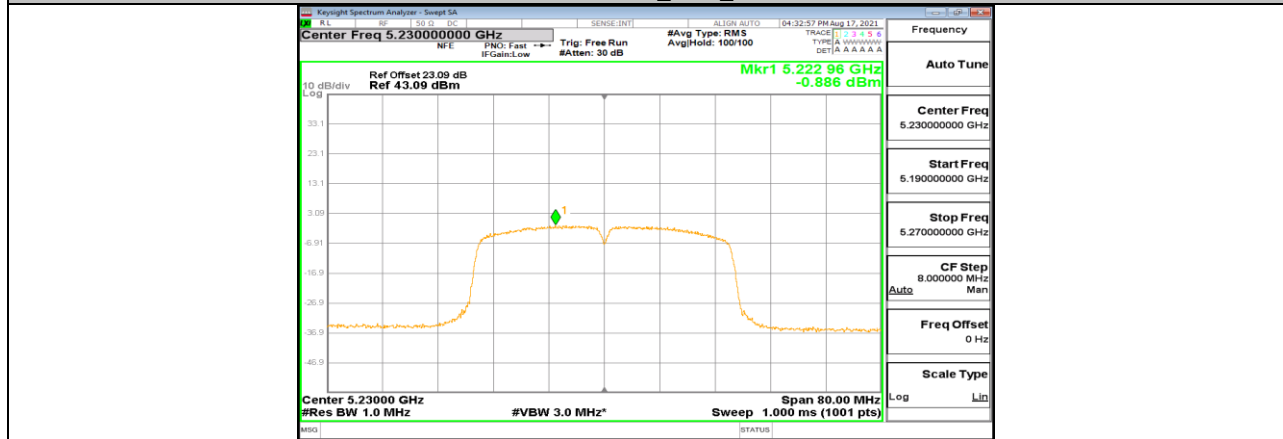


11AC20MIMO_Ant1_5825

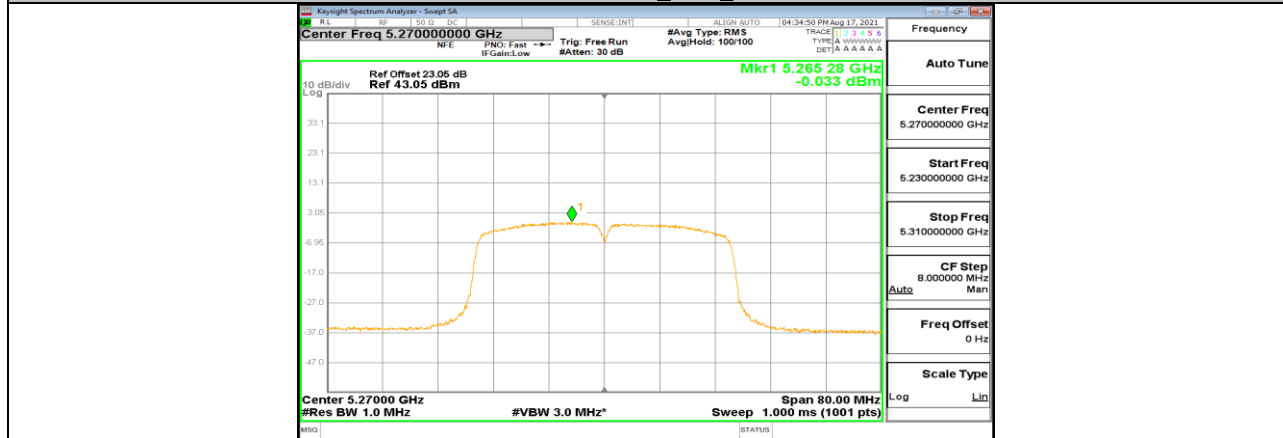




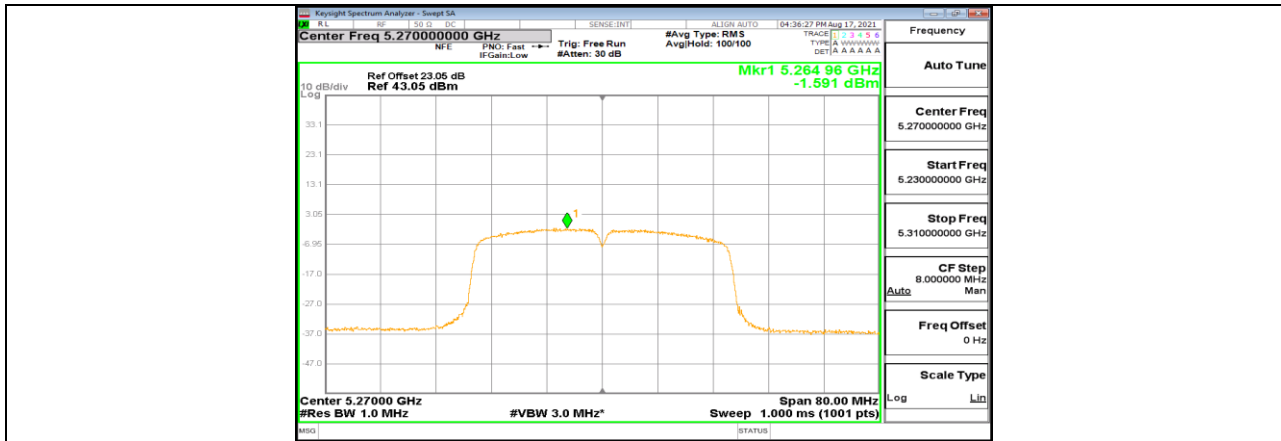
11AC40MIMO_Ant1_5230



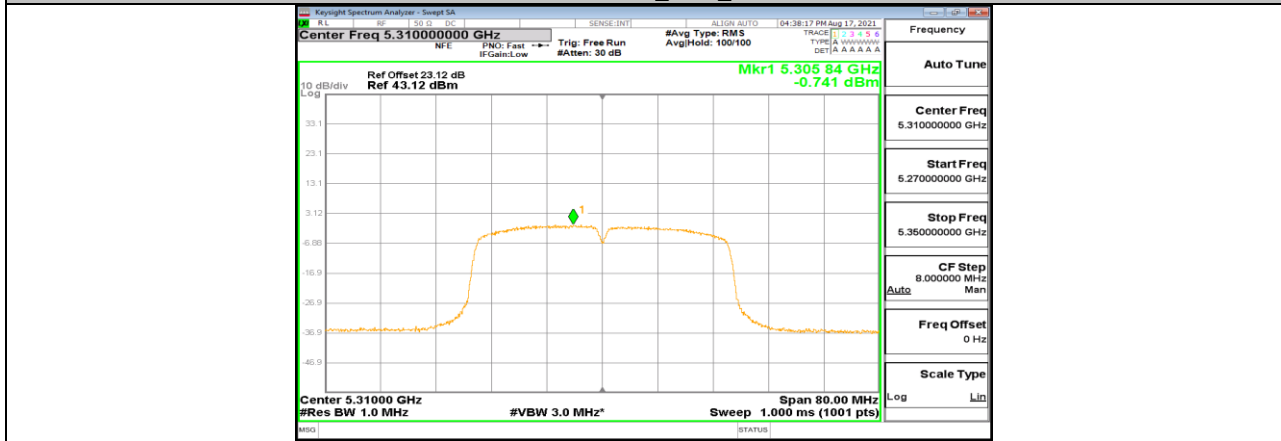
11AC40MIMO_Ant2_5230



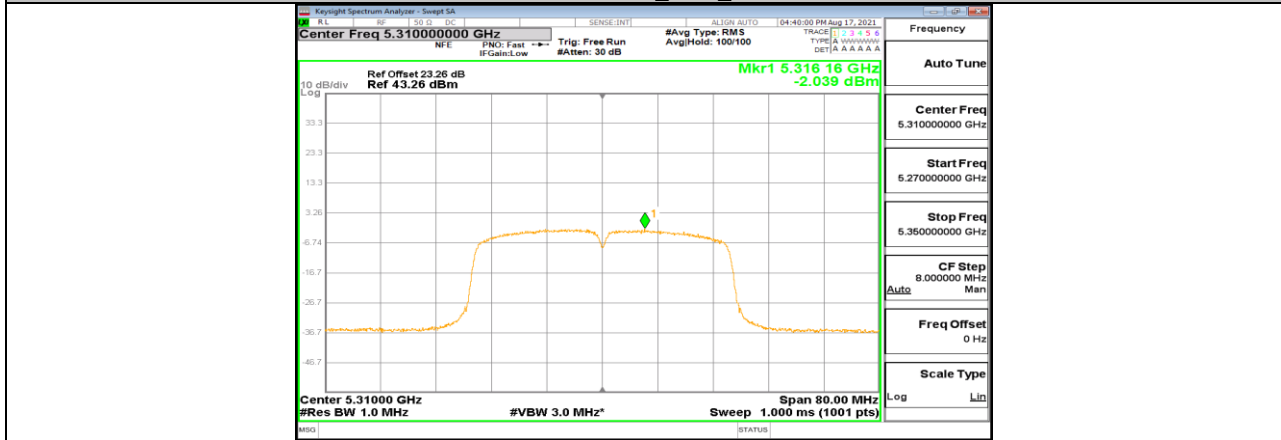
11AC40MIMO_Ant1_5270



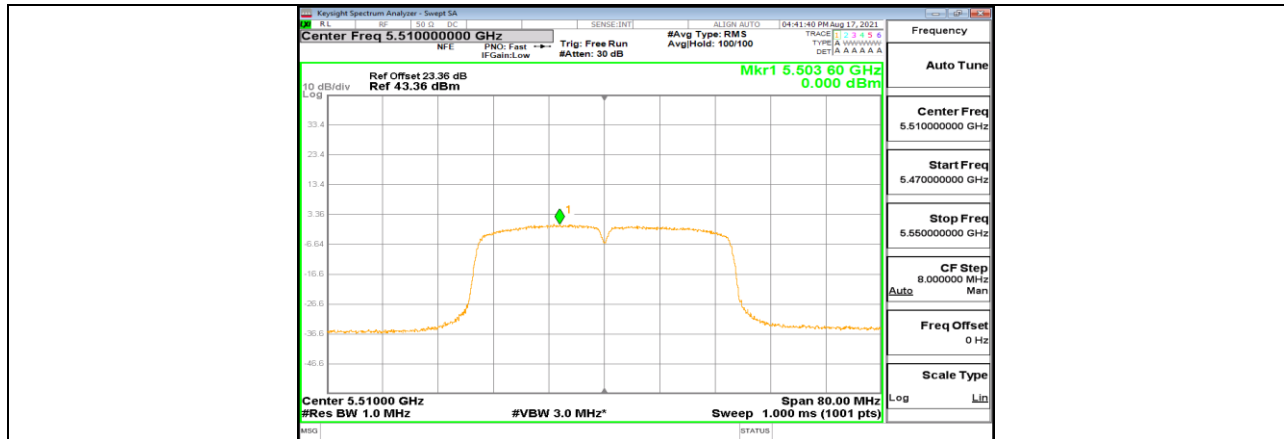
11AC40MIMO_Ant2_5270



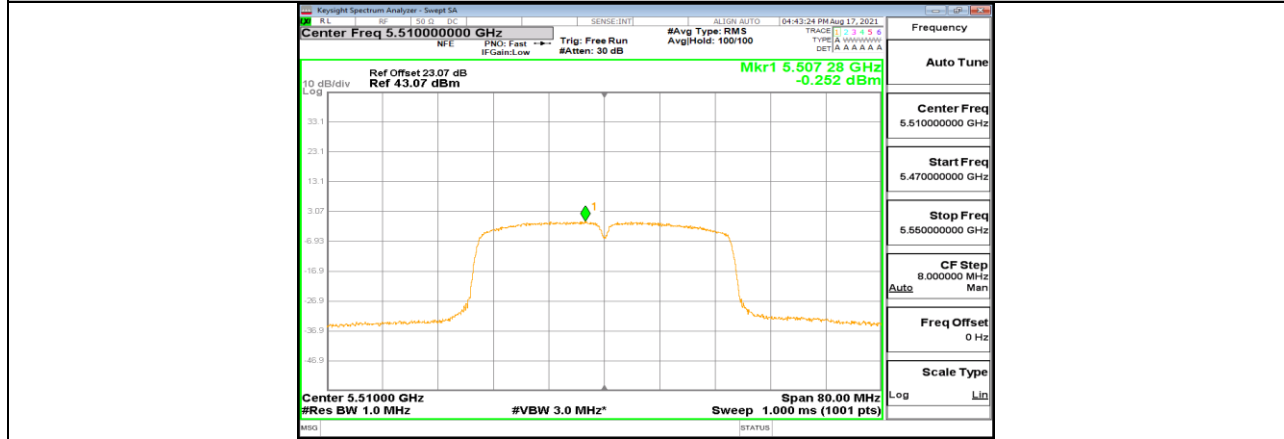
11AC40MIMO_Ant1_5310



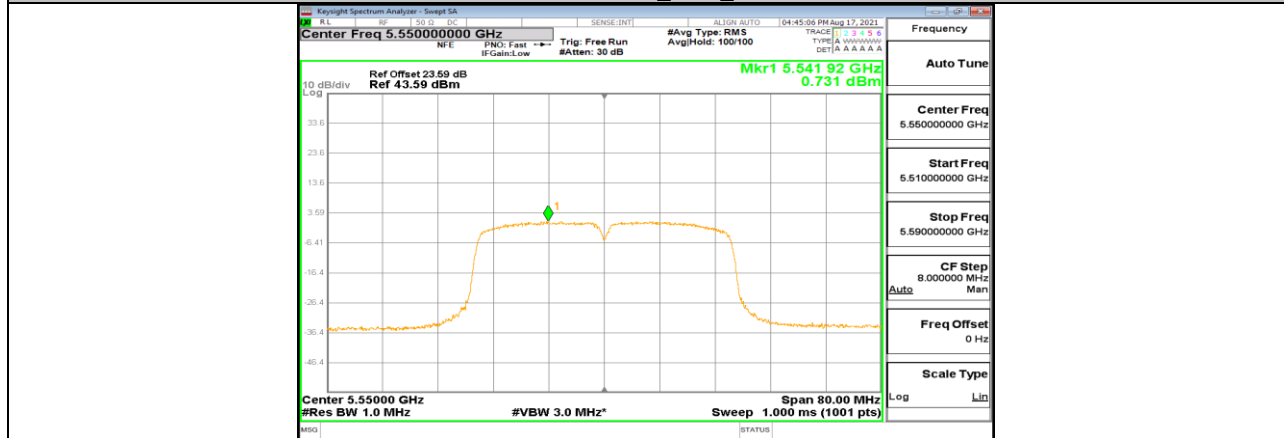
11AC40MIMO_Ant2_5310



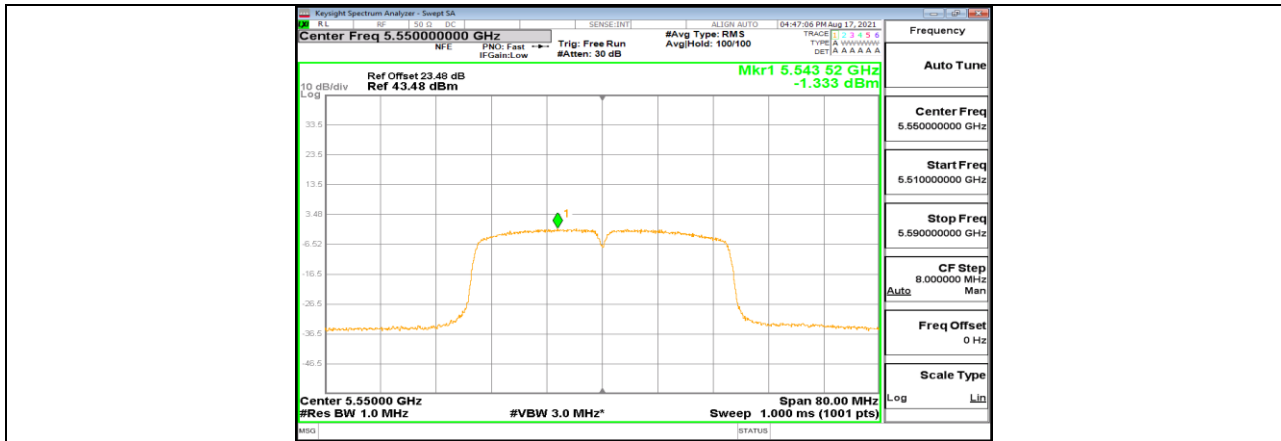
11AC40MIMO_Ant1_5510



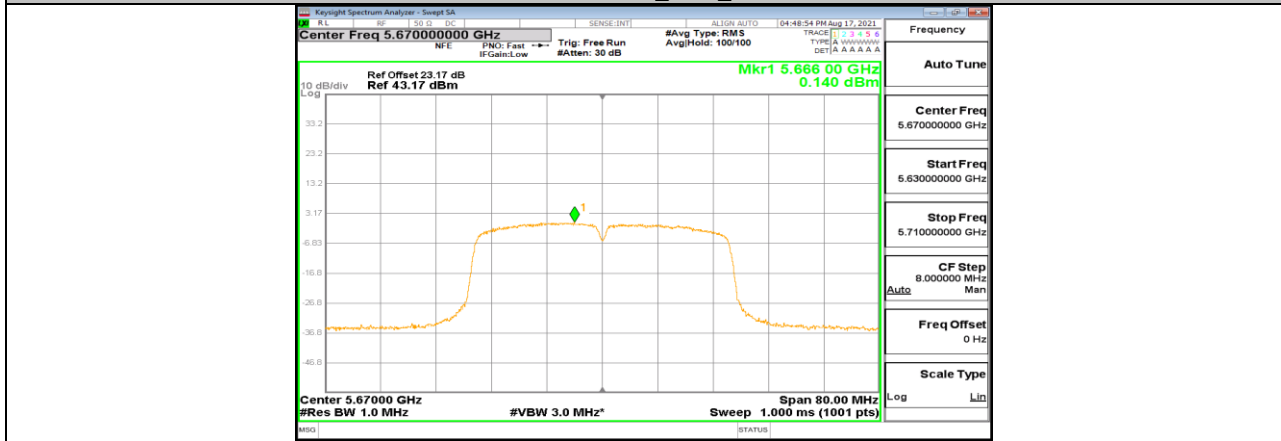
11AC40MIMO_Ant2_5510



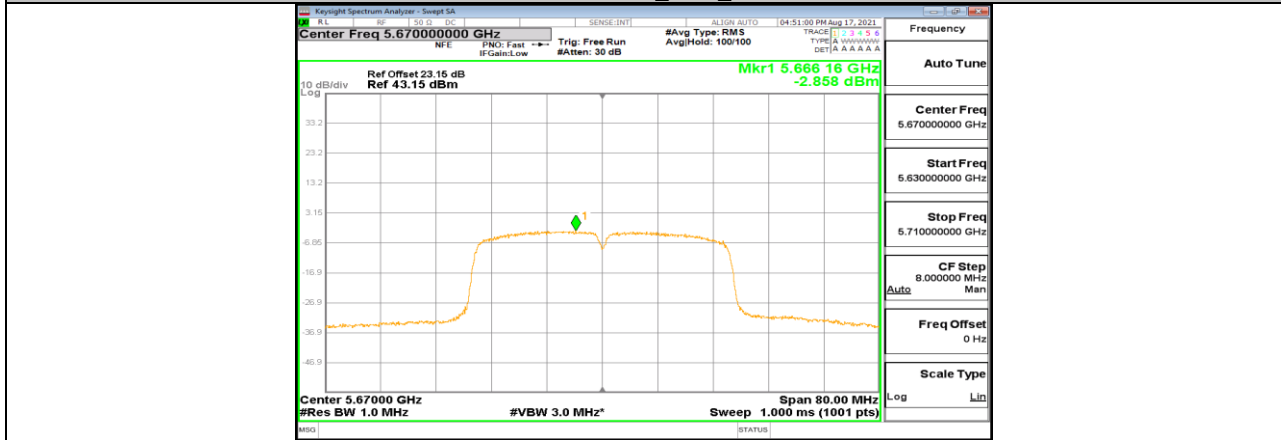
11AC40MIMO_Ant1_5550



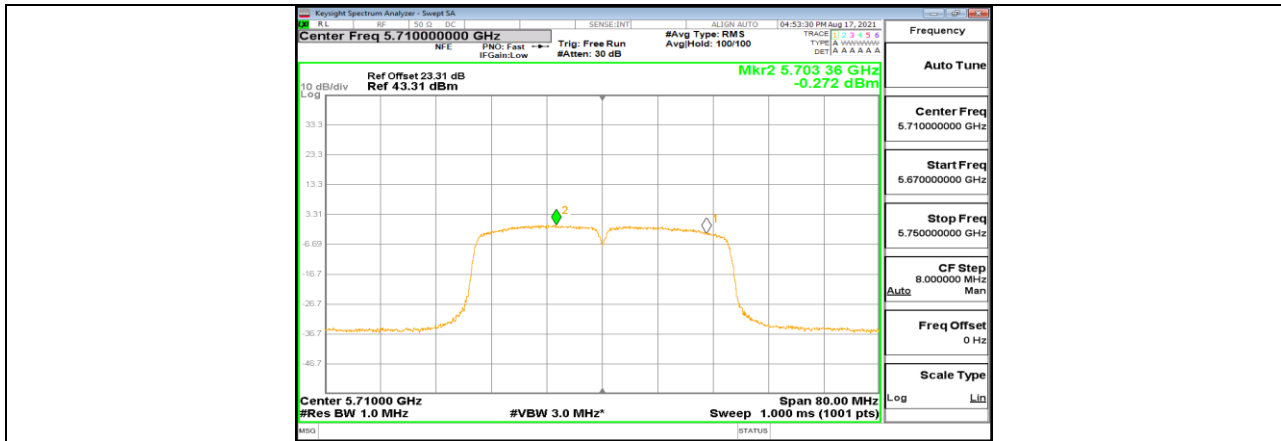
11AC40MIMO_Ant2_5550



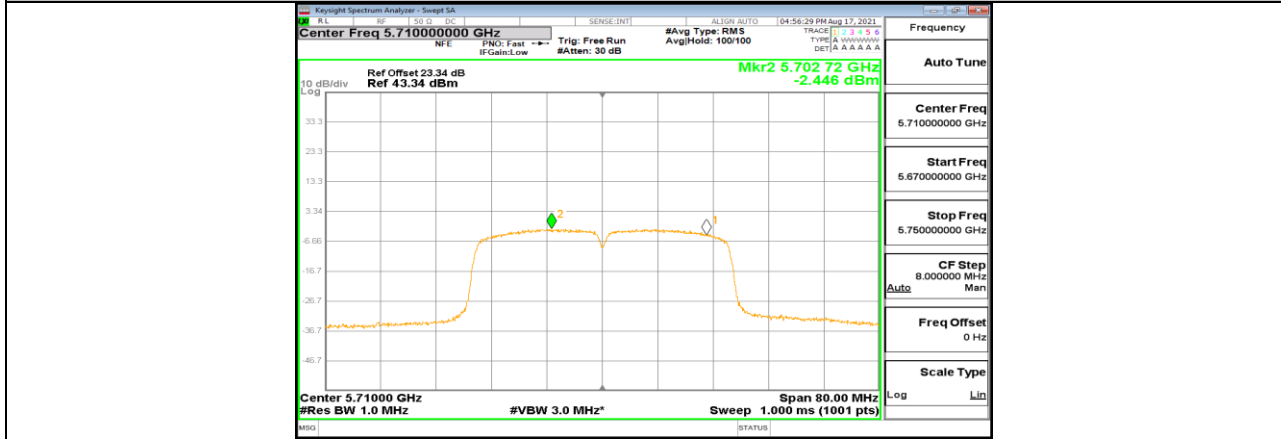
11AC40MIMO_Ant1_5670



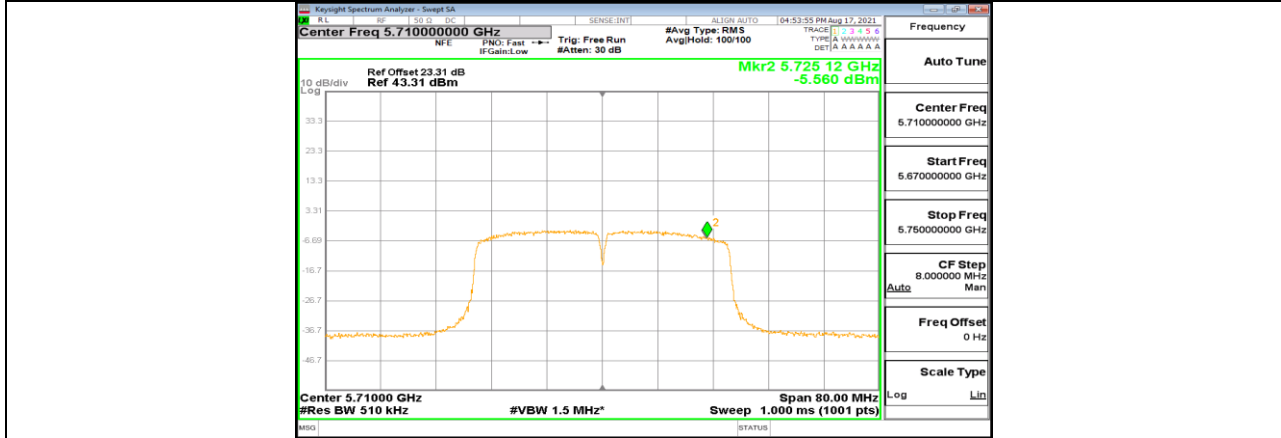
11AC40MIMO_Ant2_5670



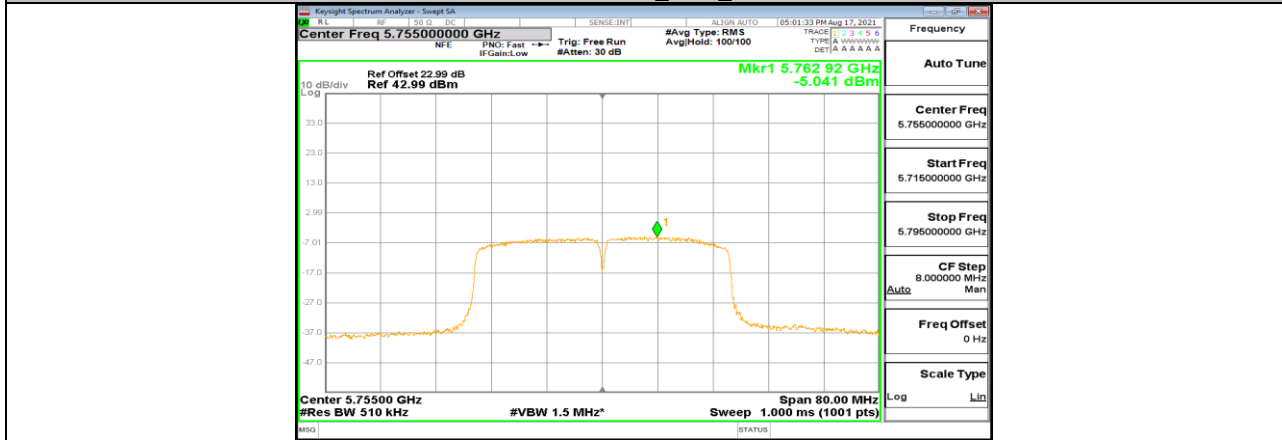
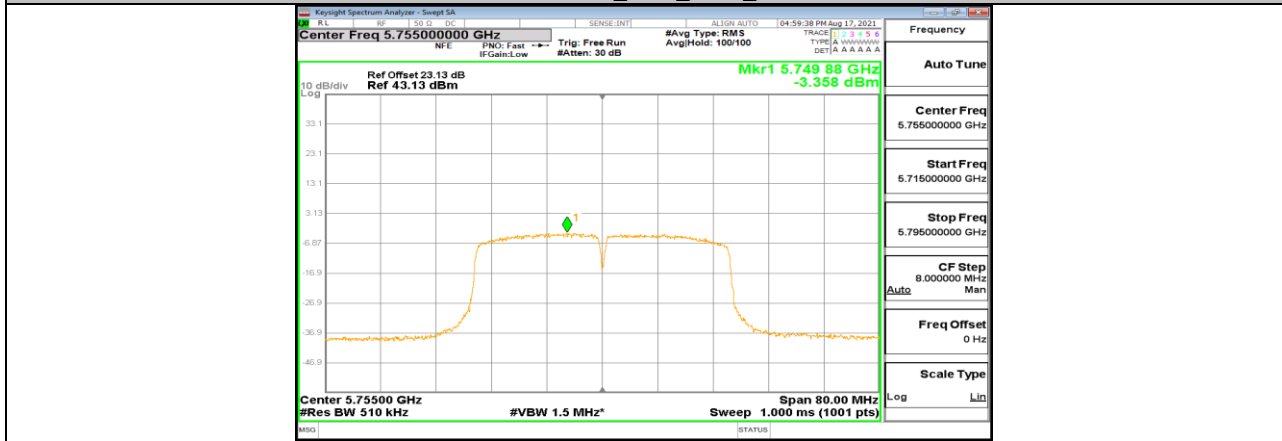
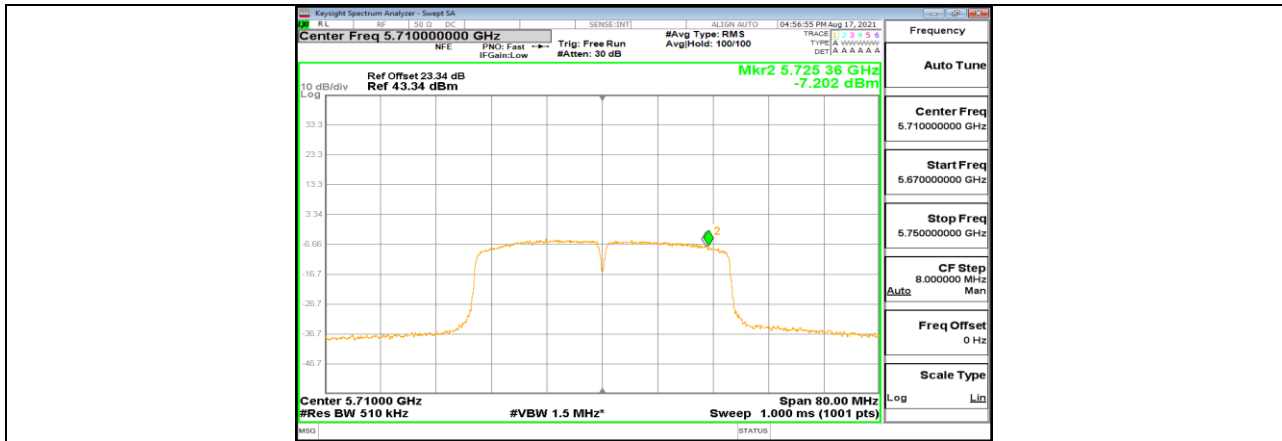
11AC40MIMO_Ant1_5710_UNII-2C

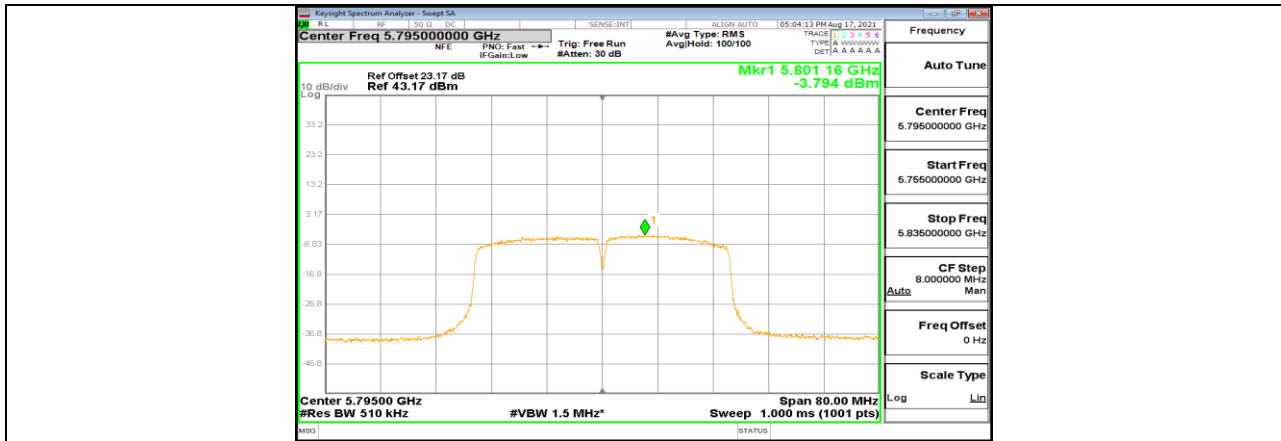


11AC40MIMO_Ant2_5710_UNII-2C

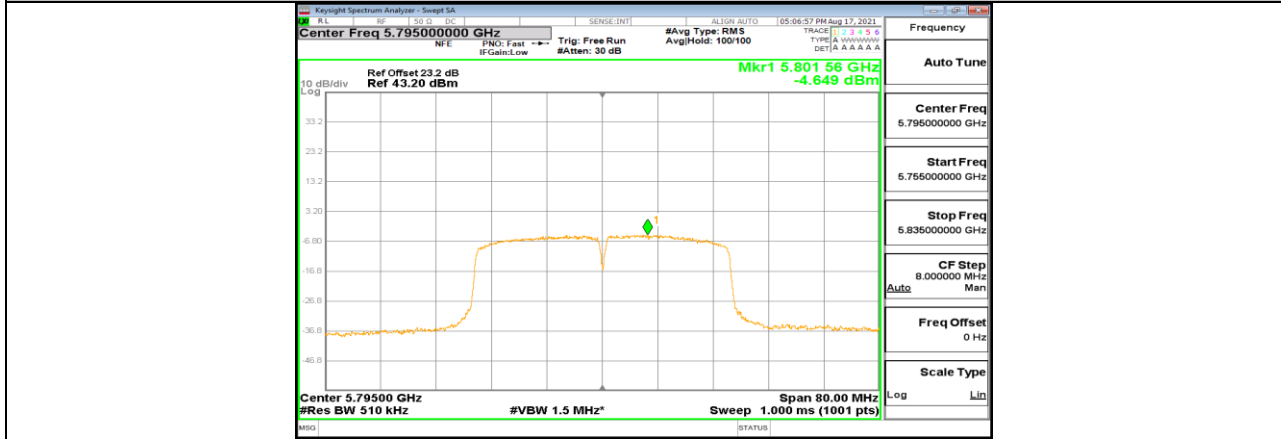


11AC40MIMO_Ant1_5710_UNII-3

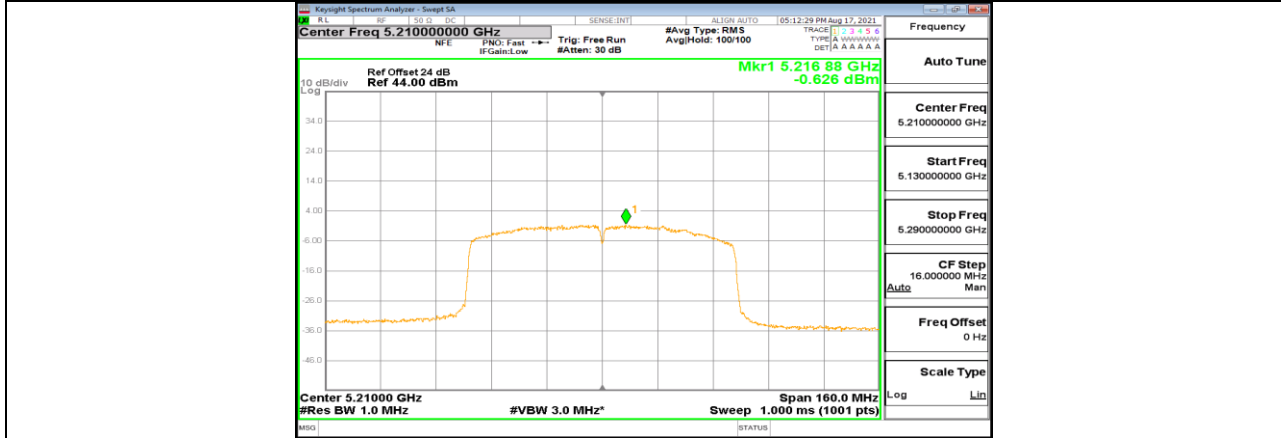




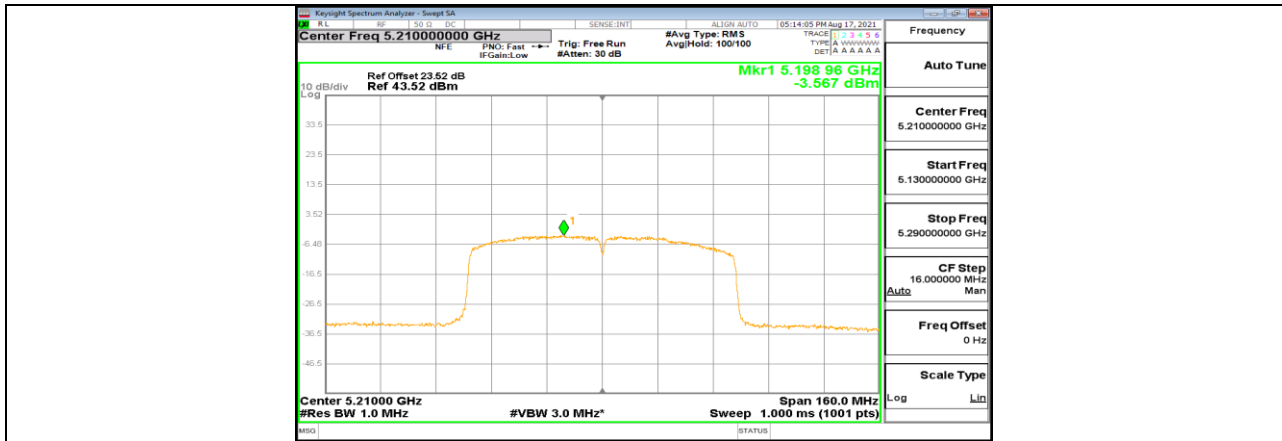
11AC40MIMO_Ant1_5795



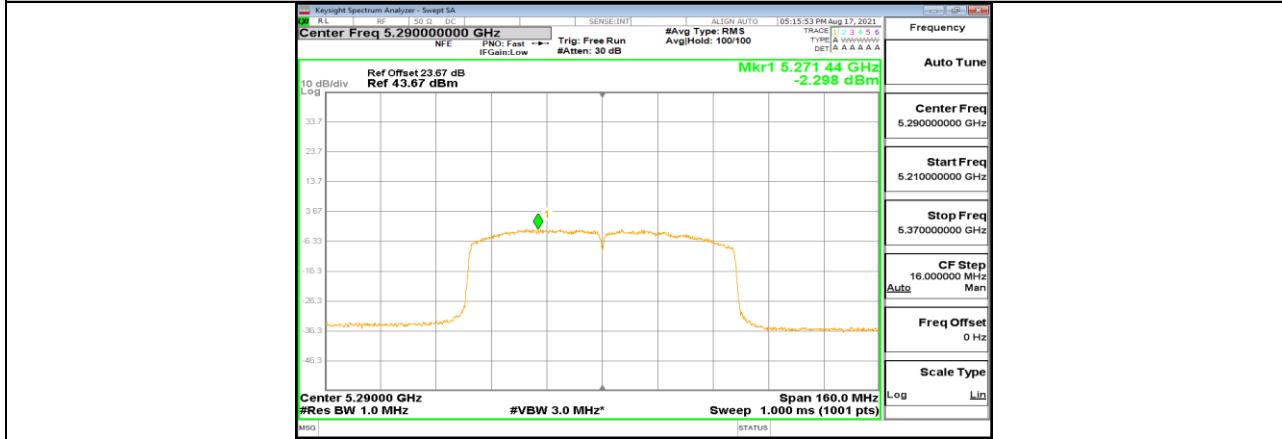
11AC40MIMO_Ant2_5795



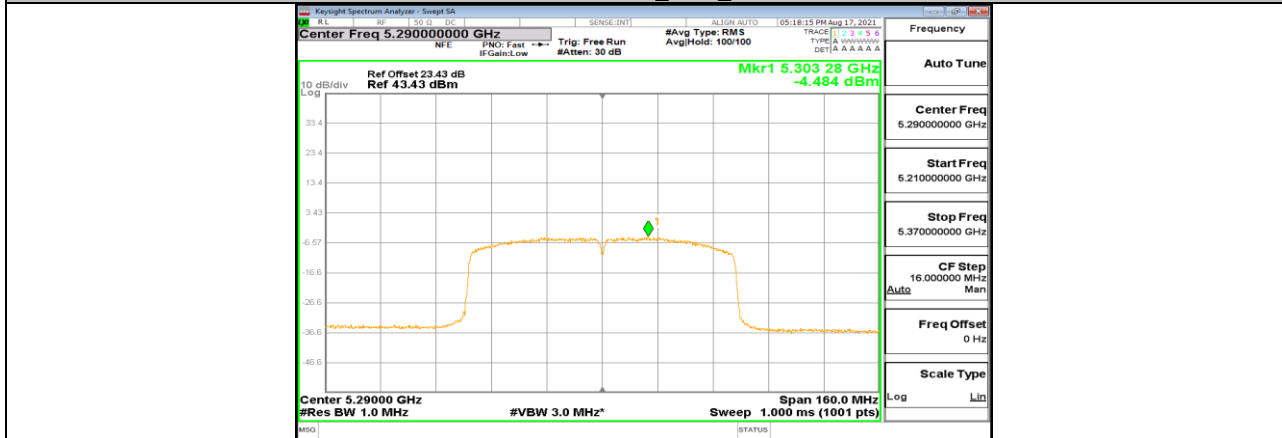
11AC80MIMO_Ant1_5210



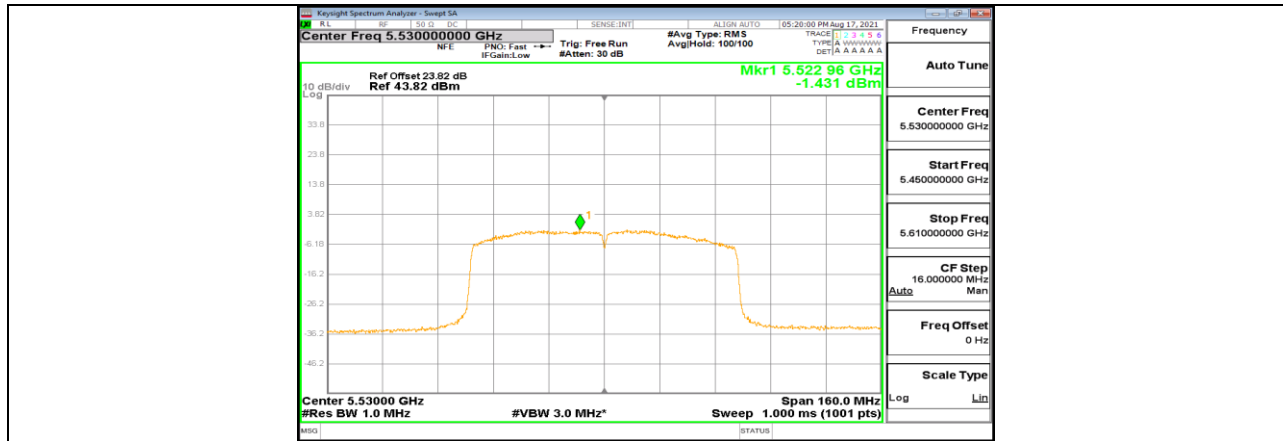
11AC80MIMO_Ant2_5210



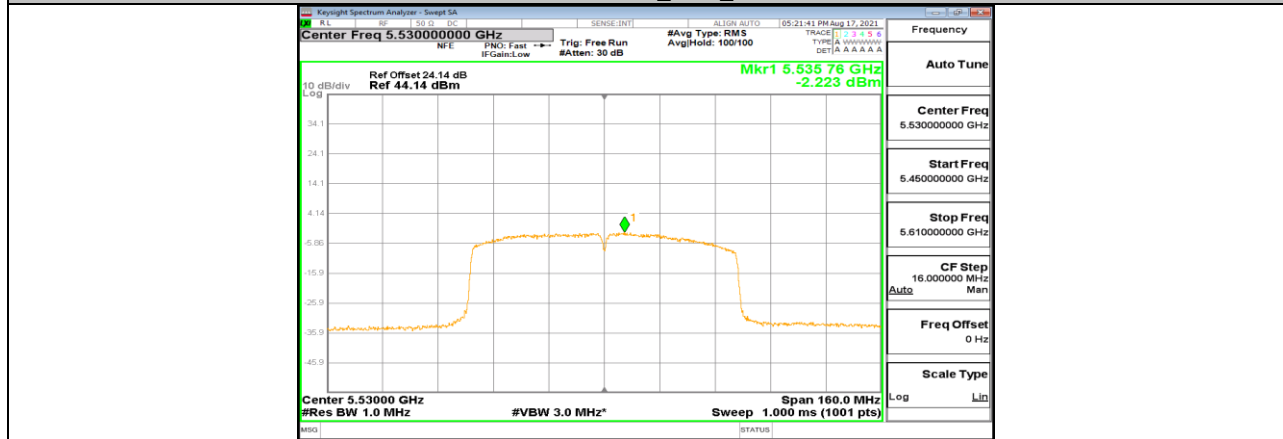
11AC80MIMO_Ant1_5290



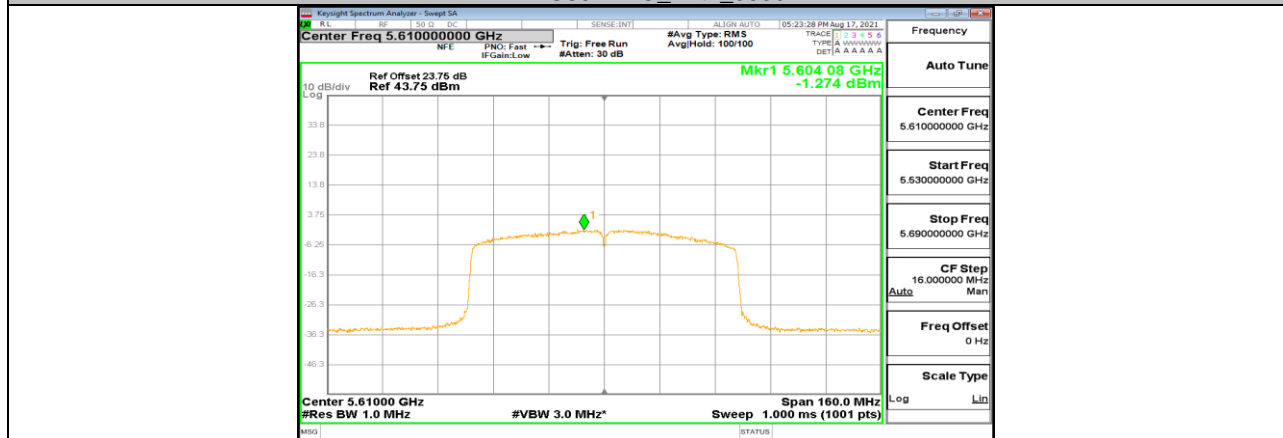
11AC80MIMO_Ant2_5290



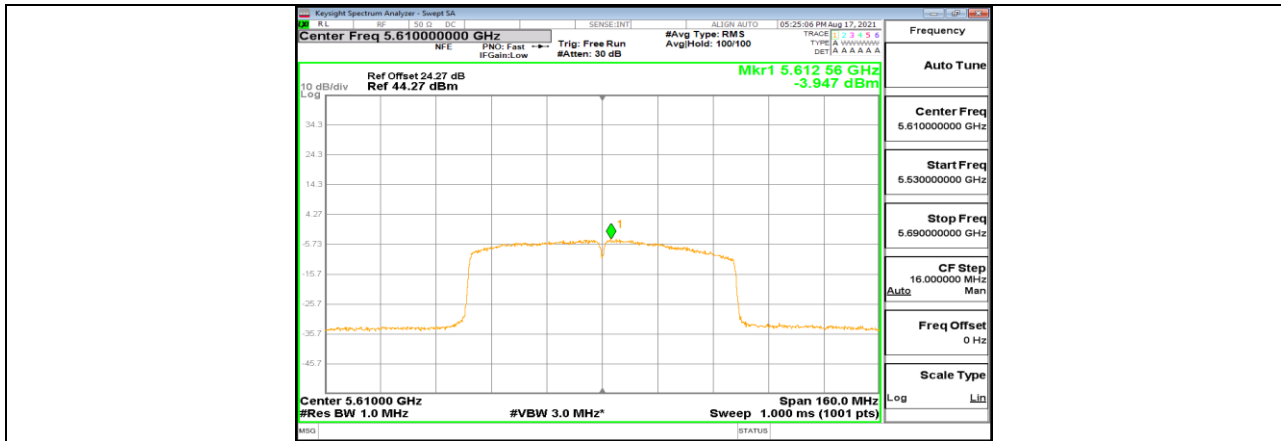
11AC80MIMO_Ant1_5530



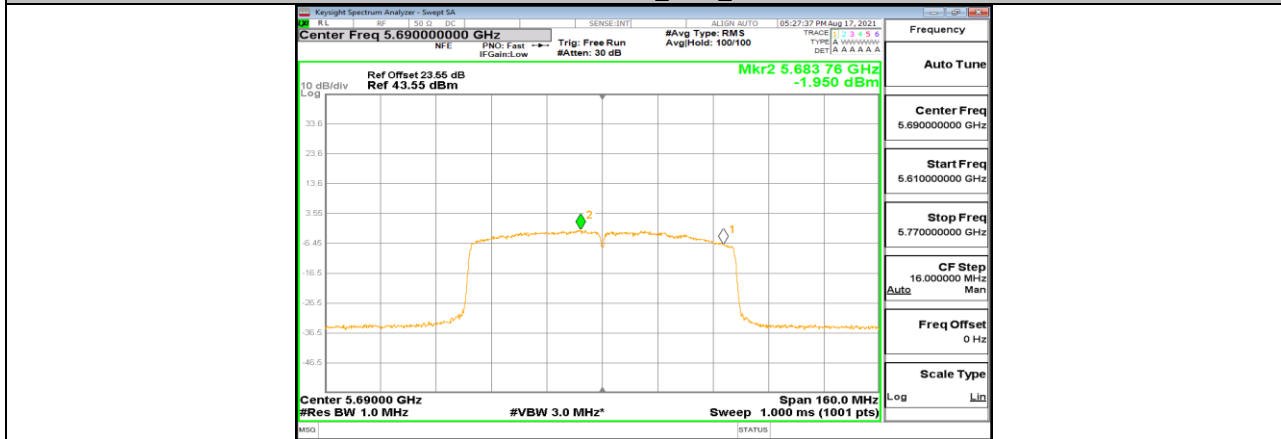
11AC80MIMO_Ant2_5530



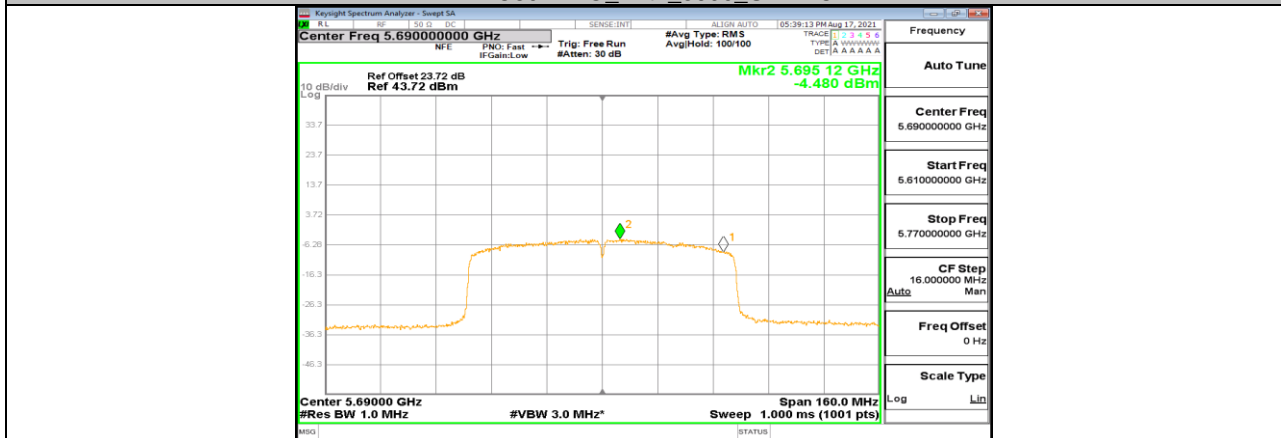
11AC80MIMO_Ant1_5610



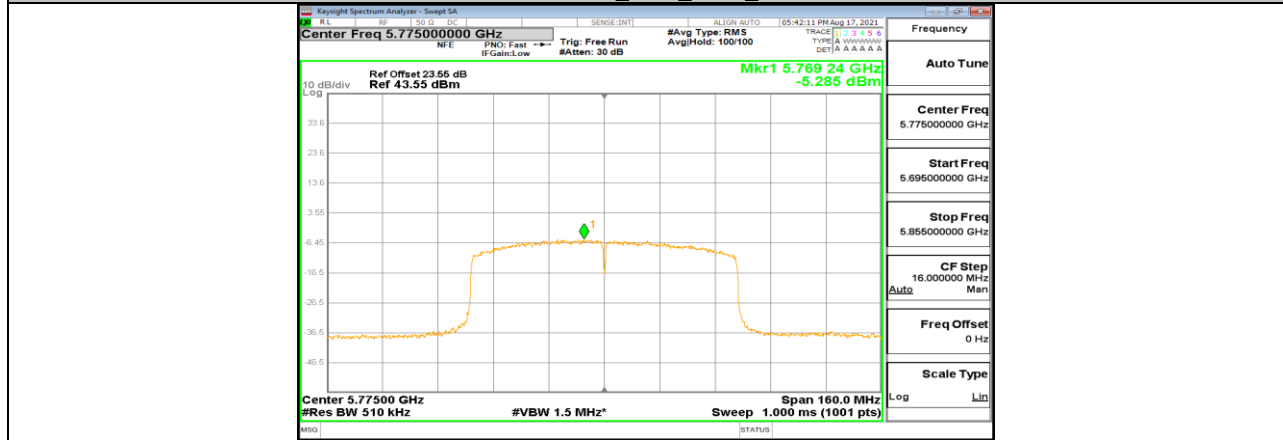
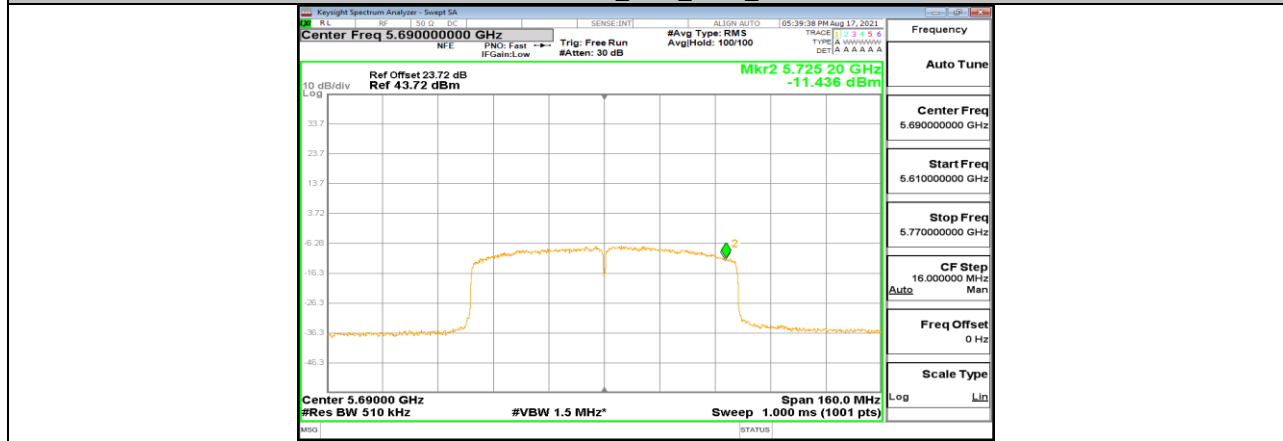
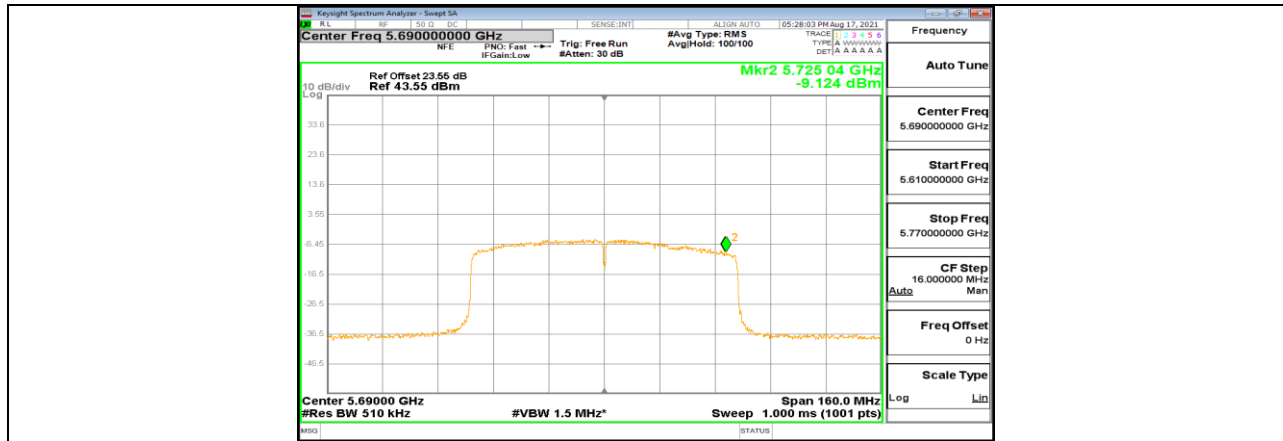
11AC80MIMO_Ant2_5610



11AC80MIMO_Ant1_5690_UNII-2C



11AC80MIMO_Ant2_5690_UNII-2C







12.6. Appendix D: Duty Cycle

12.6.1. Test Result

Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
11A MIMO	2.07	2.25	0.9200	92.00	0.36	0.48	0.5
11AC20 MIMO	1.93	2.03	0.9507	95.07	0.22	0.52	1
11AC40 MIMO	0.95	1.09	0.8716	87.16	0.60	1.05	2
11AC80 MIMO	0.46	0.60	0.7667	76.67	1.15	2.17	3

Note:

Duty Cycle Correction Factor=10log (1/x).

Where: x is Duty Cycle (Linear)

Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.