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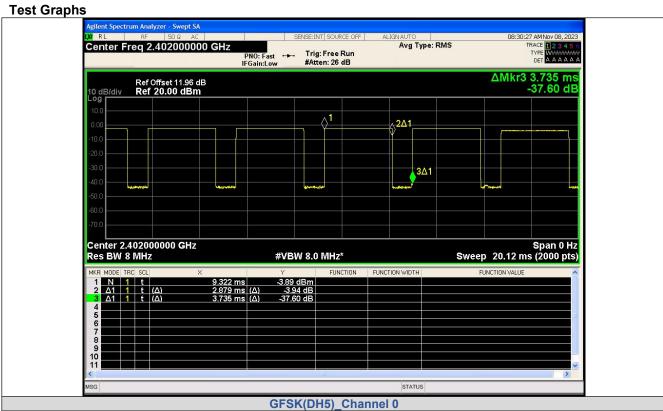


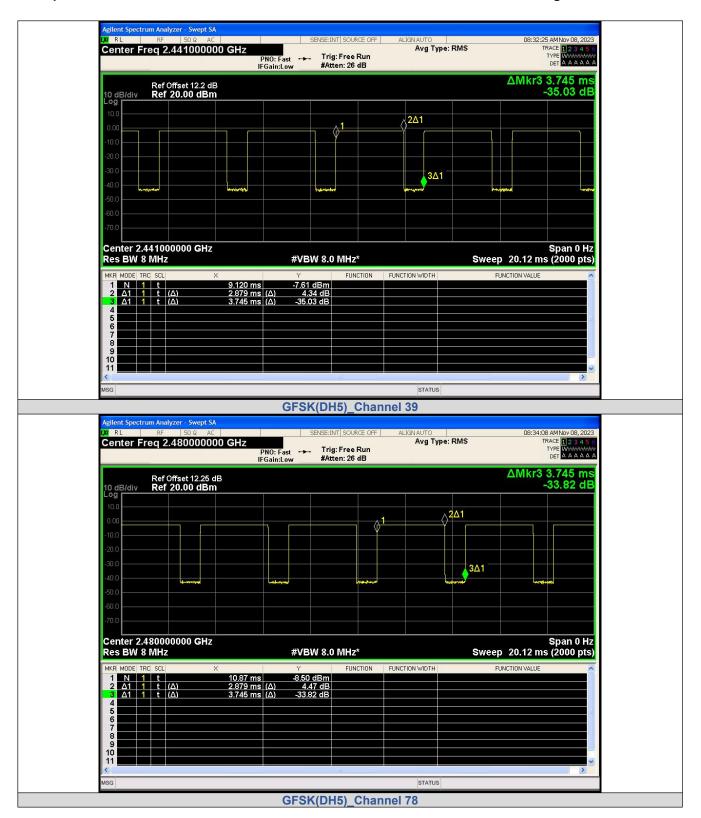
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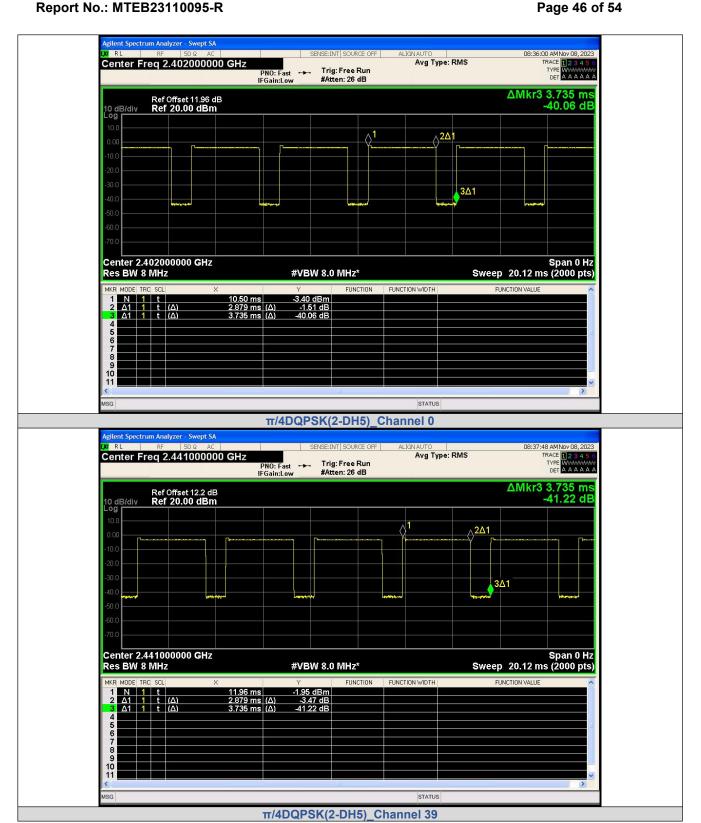
APPENDIX VI. Duty Cycle

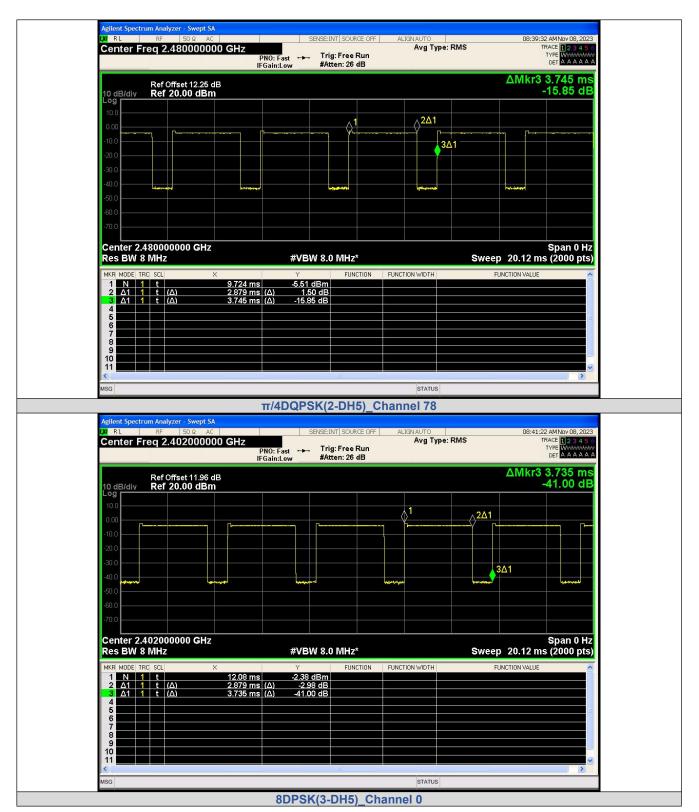
Test Result

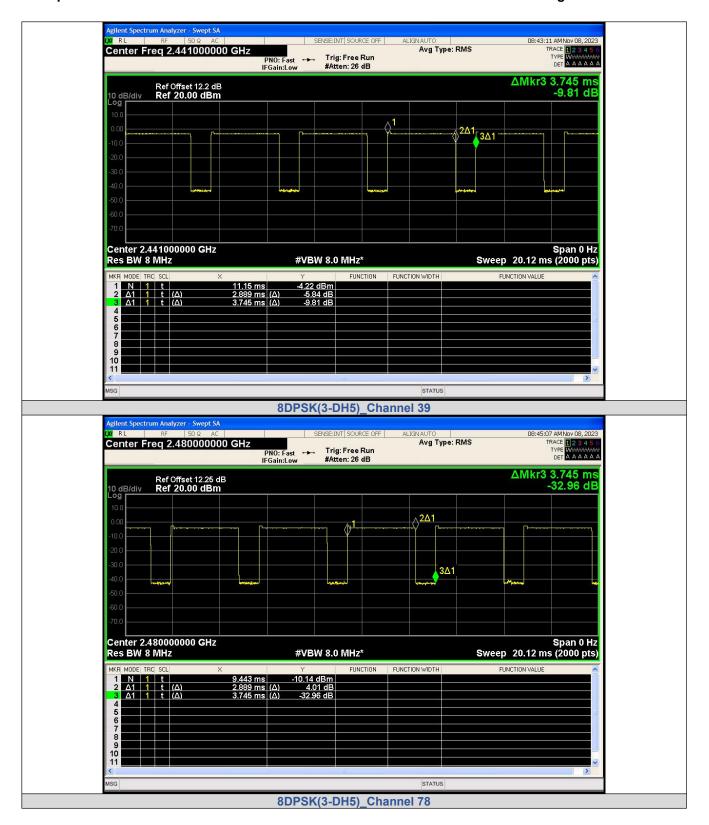
Modulation	Packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)
GFSK	DH5	0	2.879	3.735	77.09	0.7709	1.13
		39	2.879	3.745	76.88	0.7688	1.1419
		78	2.879	3.745	76.88	0.7688	1.1419
π/4DQPSK	2-DH5	0	2.879	3.735	77.09	0.7709	1.13
		39	2.879	3.735	77.09	0.7709	1.13
		78	2.879	3.745	76.88	0.7688	1.1419
8DPSK	3-DH5	0	2.879	3.735	77.09	0.7709	1.13
		39	2.889	3.745	77.15	0.7715	1.1266
		78	2.889	3.745	77.15	0.7715	1.1266











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APPENDIX VII. Dwell Time

Test Result

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH1	CH39 (2441MHz)	0.3732	319	119.05	< 400	PASS
	DH3		1.608	148	237.98		PASS
	DH5		2.880	125	360.0		PASS
π/4DQPSK	2-DH1		0.3840	318	122.11		PASS
	2-DH3		1.656	159	263.3		PASS
	2-DH5		2.880	118	339.84		PASS
8DPSK	3-DH1		0.3840	312	119.81		PASS
	3-DH3		1.632	155	252.96		PASS
	3-DH5		2.896	99	286.7		PASS

