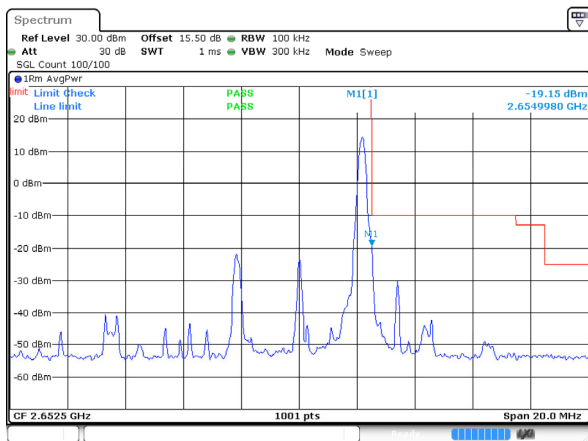
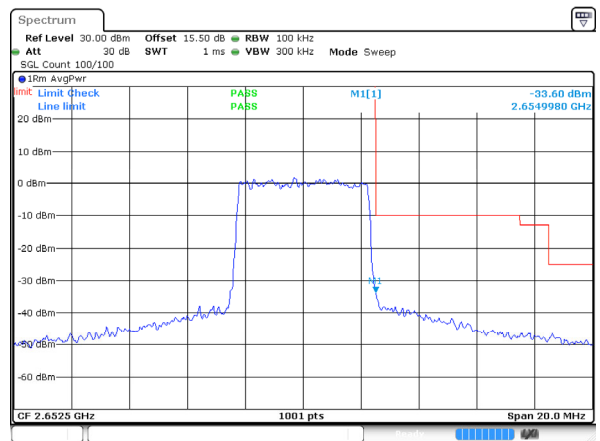


5MHz\_High\_16QAM\_1@24



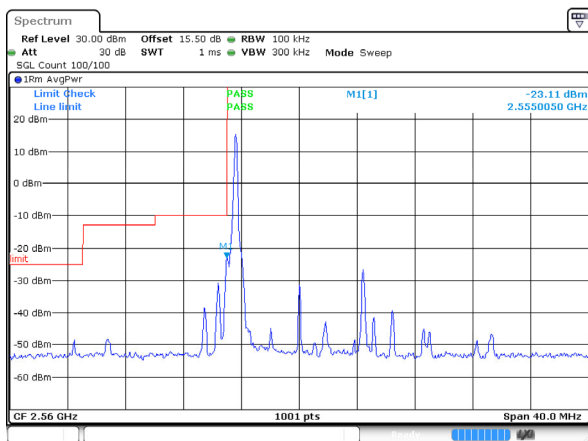
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:19:09

5MHz\_High\_16QAM\_25@0



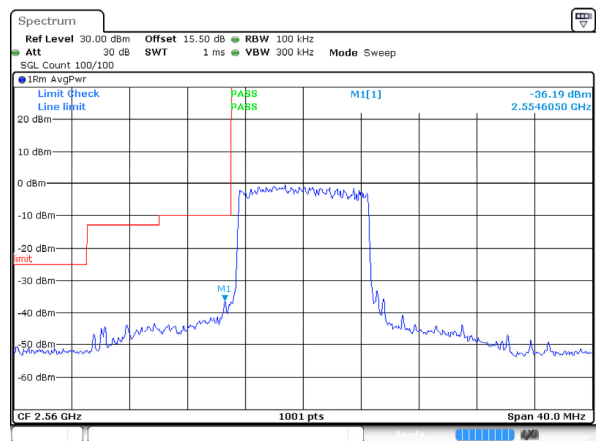
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:19:01

10MHz\_Low\_QPSK\_1@0



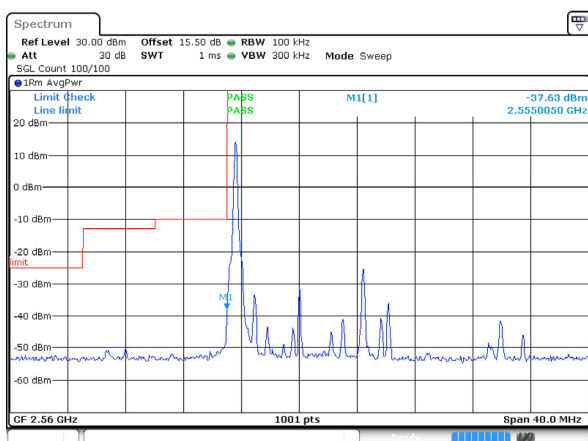
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:20:02

10MHz\_Low\_QPSK\_50@0



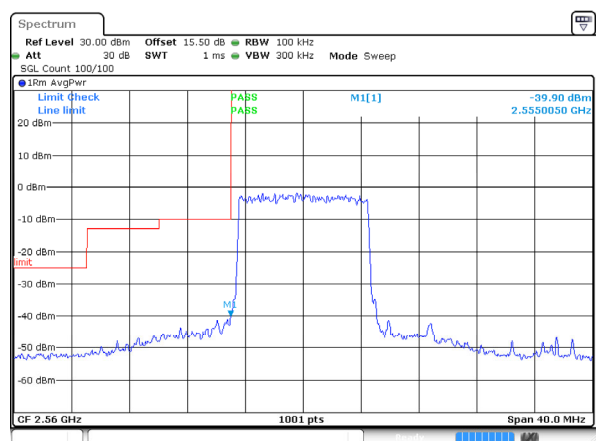
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:19:54

10MHz\_Low\_16QAM\_1@0



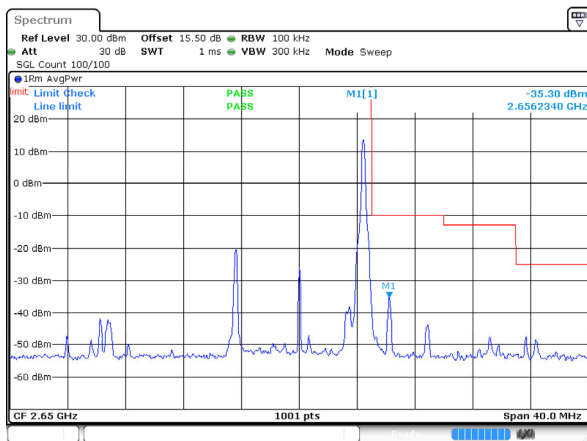
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:20:17

10MHz\_Low\_16QAM\_50@0



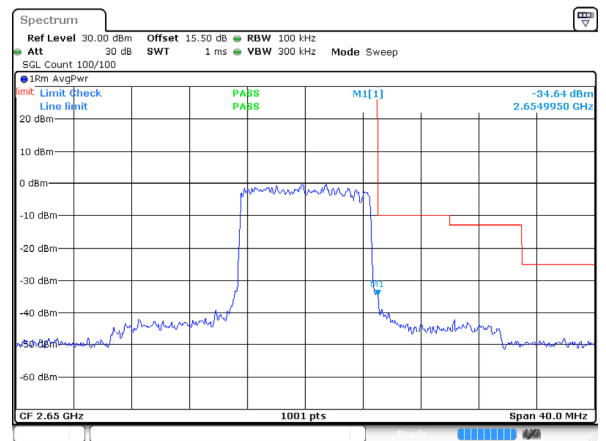
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:20:09

10MHz\_High\_QPSK\_1@49



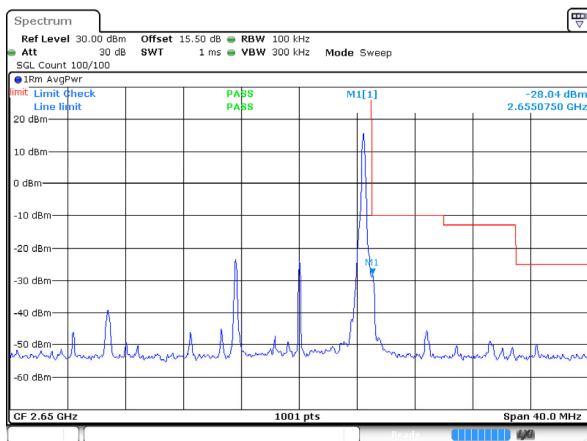
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:20:43

10MHz\_High\_QPSK\_50@0



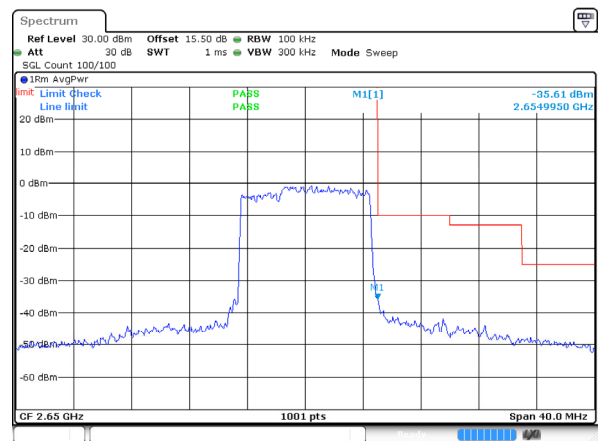
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:20:36

10MHz\_High\_16QAM\_1@49



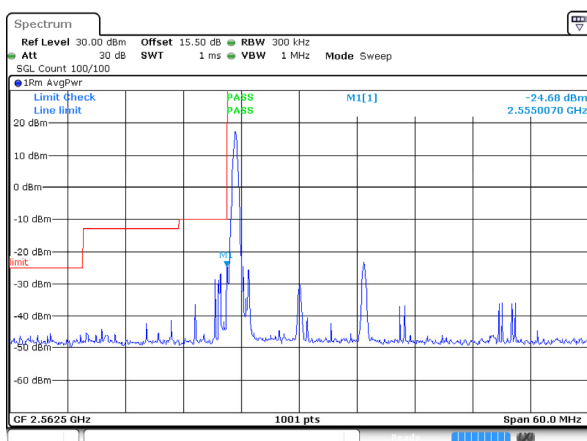
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:20:59

10MHz\_High\_16QAM\_50@0



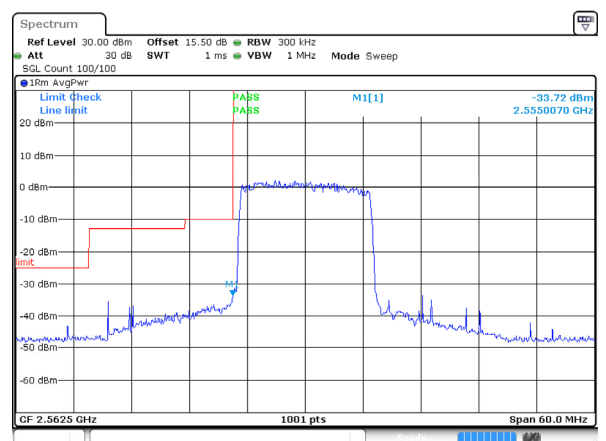
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:20:51

15MHz\_Low\_QPSK\_1@0



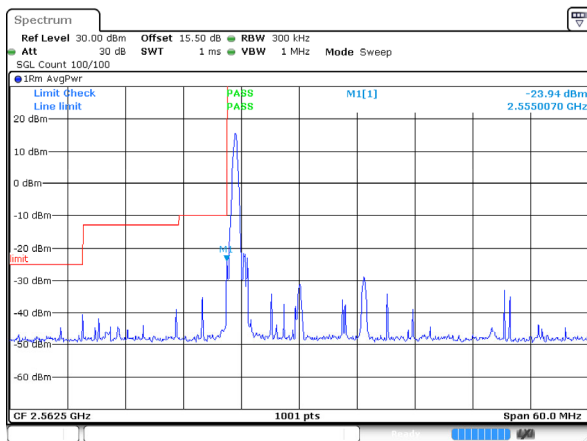
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:21:49

15MHz\_Low\_QPSK\_75@0



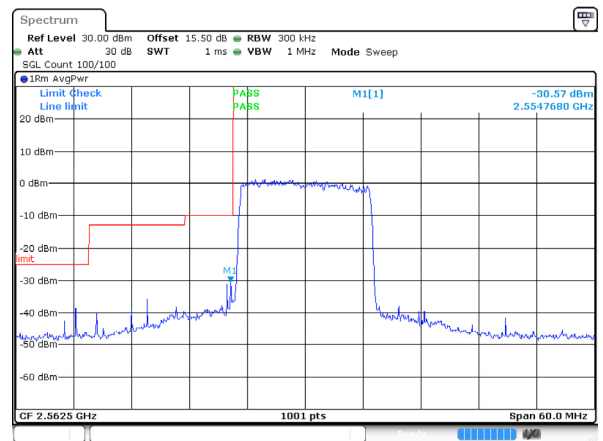
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:21:43

15MHz\_Low\_16QAM\_1@0



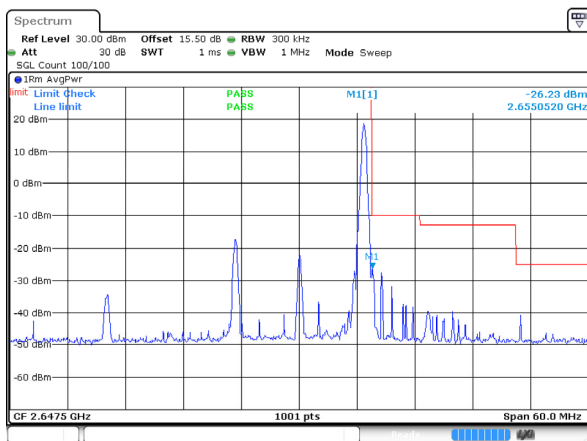
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:22:01

15MHz\_Low\_16QAM\_75@0



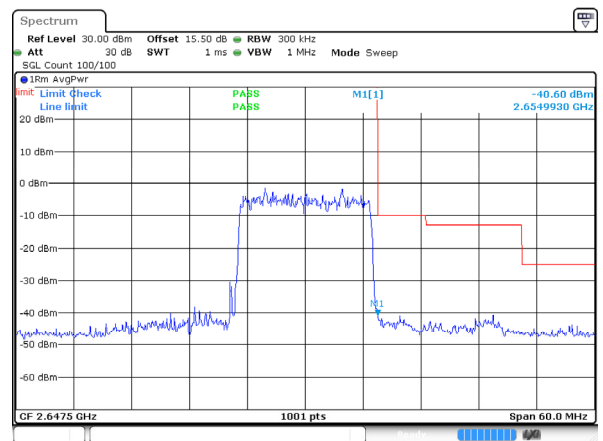
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:21:55

15MHz\_High\_QPSK\_1@74



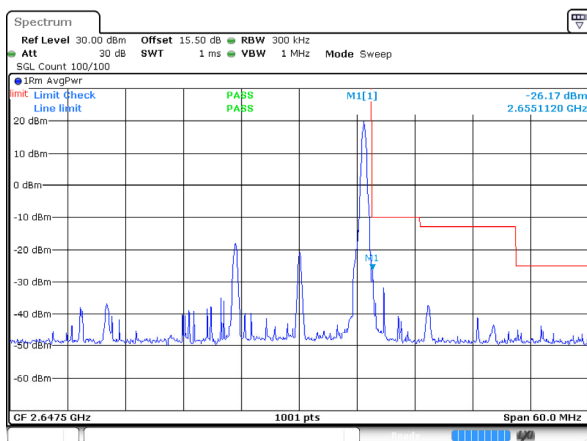
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:22:24

15MHz\_High\_QPSK\_75@0



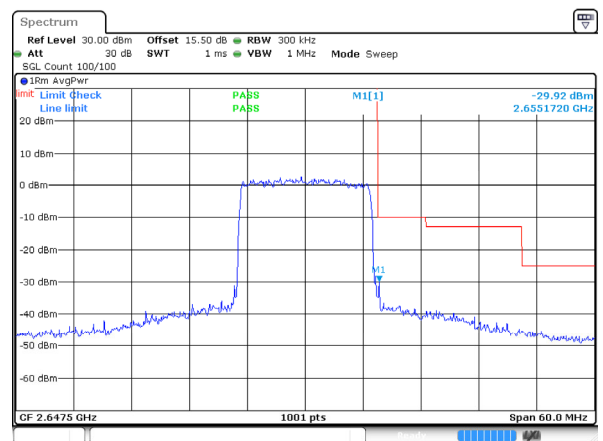
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:22:18

15MHz\_High\_16QAM\_1@74



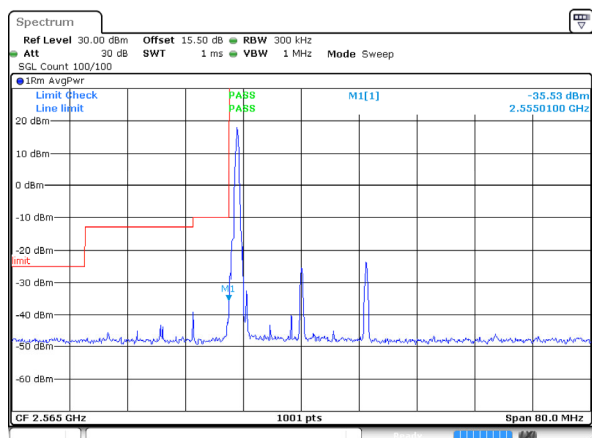
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:22:37

15MHz\_High\_16QAM\_75@0



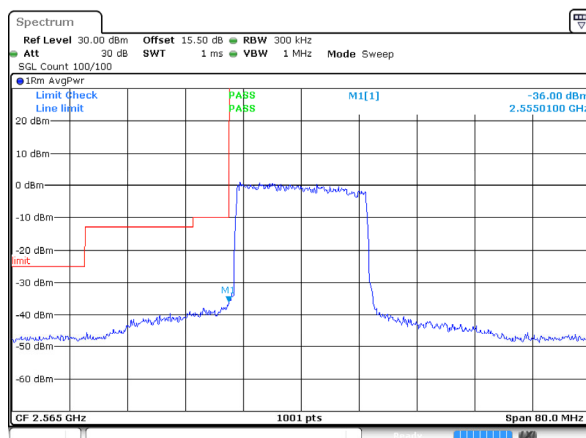
ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 16 JUL 2024 17:22:31

20MHz\_Low\_QPSK\_1@0



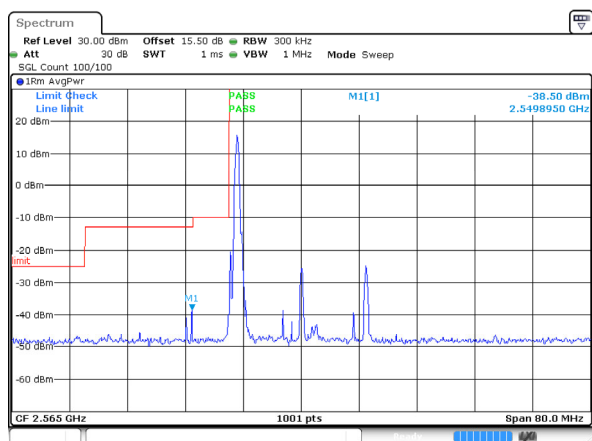
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:23:30

20MHz\_Low\_QPSK\_100@0



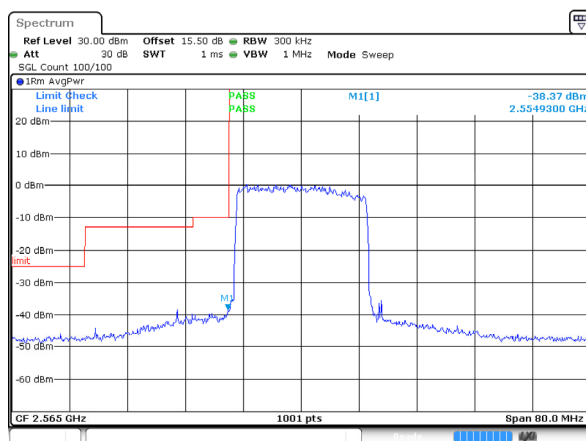
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:23:24

20MHz\_Low\_16QAM\_1@0



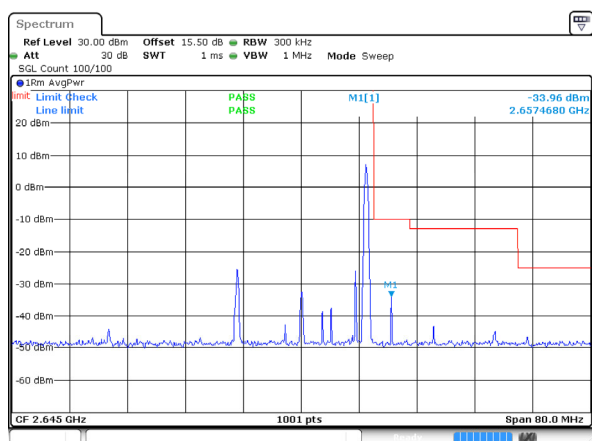
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:23:43

20MHz\_Low\_16QAM\_100@0



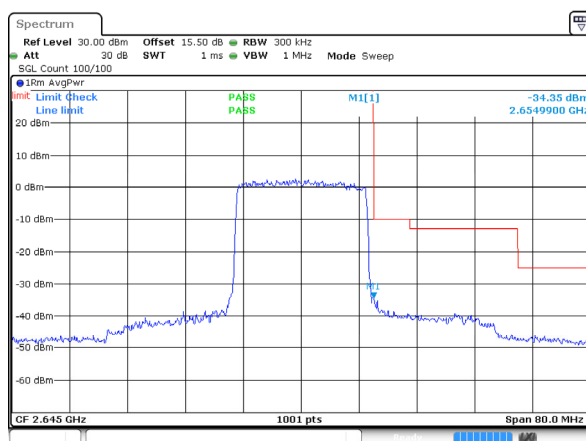
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:23:36

20MHz\_High\_QPSK\_1@99



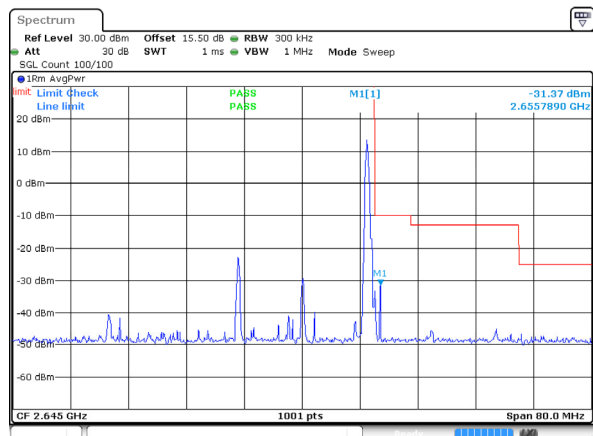
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:24:06

20MHz\_High\_QPSK\_100@0



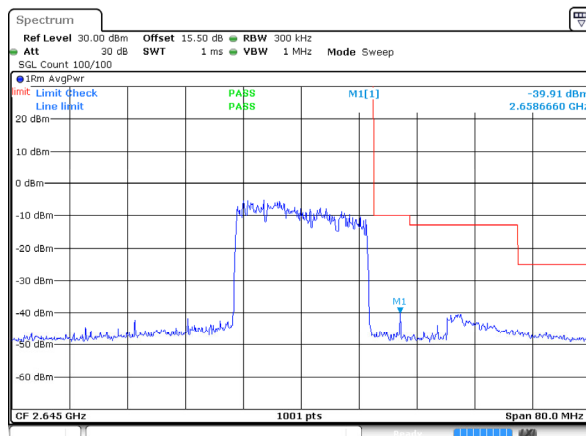
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:23:59

### 20MHz\_High\_16QAM\_1@99



ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:24:19

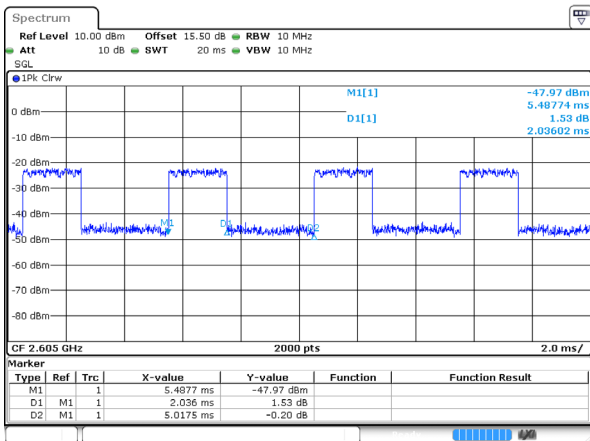
### 20MHz\_High\_16QAM\_100@0



ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 16 JUL 2024 17:24:12

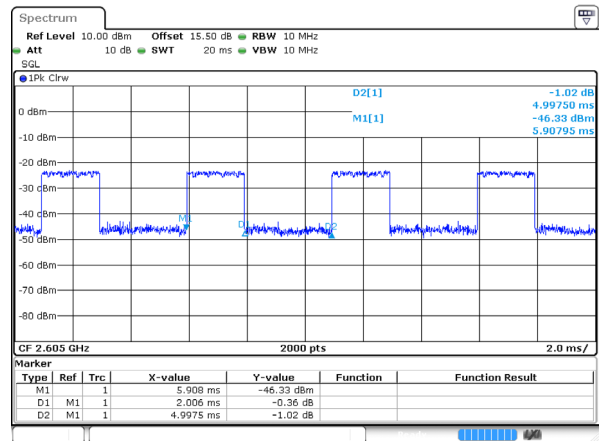
Band 41 Duty cycle

5MHz\_16QAM



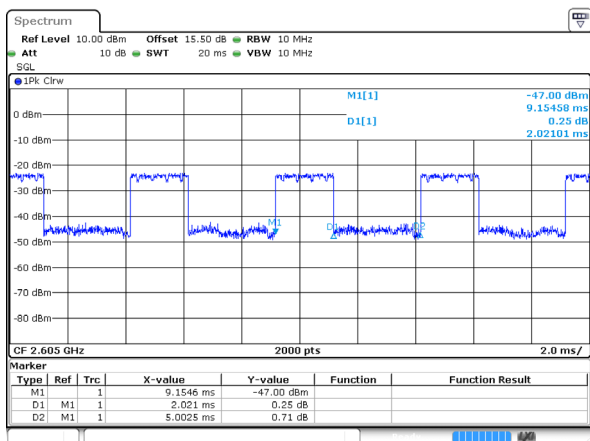
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 24.SEP.2024 10:19:37

5MHz\_QPSK



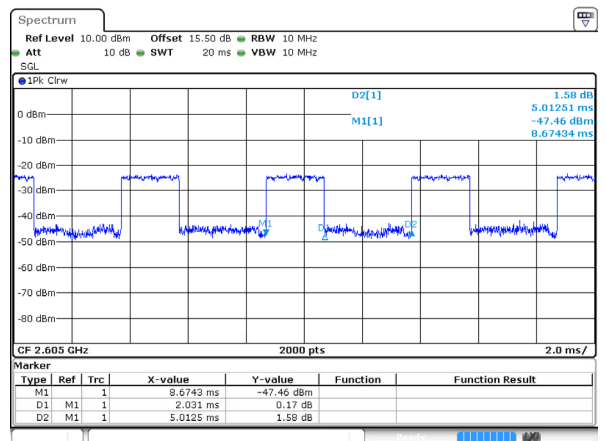
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Date: 24.SEP.2024 10:17:59

10MHz\_16QAM



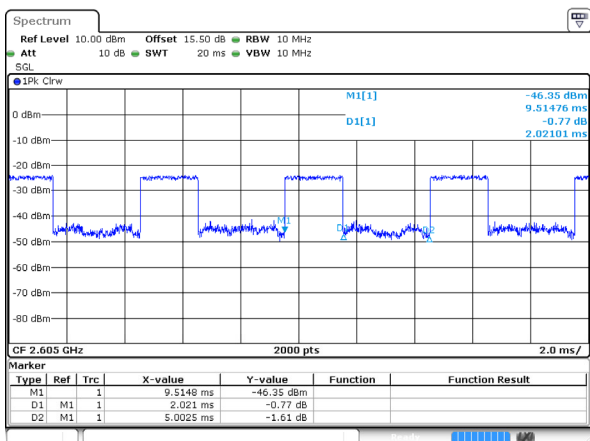
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Date: 24.SEP.2024 10:23:05

10MHz\_QPSK



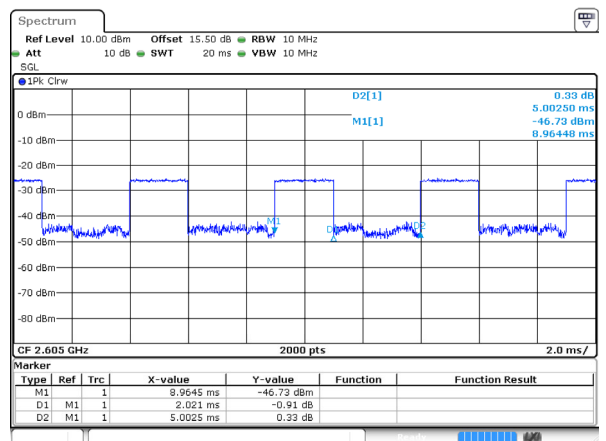
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 24.SEP.2024 10:22:02

15MHz\_16QAM



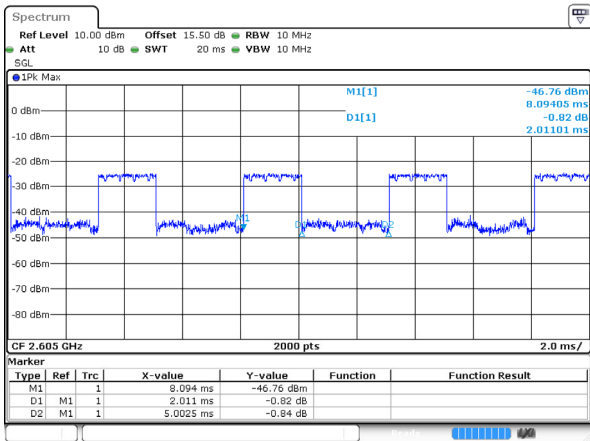
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 24.SEP.2024 10:24:59

15MHz\_QPSK



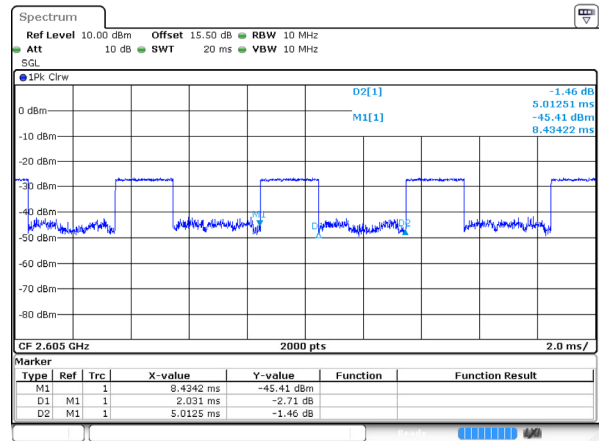
ProjectNo.:RSHA240520001 Tester:Jason Lu  
Date: 24.SEP.2024 10:23:57

### 20MHz\_16QAM



ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 24.SEP.2024 10:27:02

### 20MHz\_QPSK



ProjectNo.:RSHA240520001 Tester:Jason Lu  
 Date: 24.SEP.2024 10:28:05