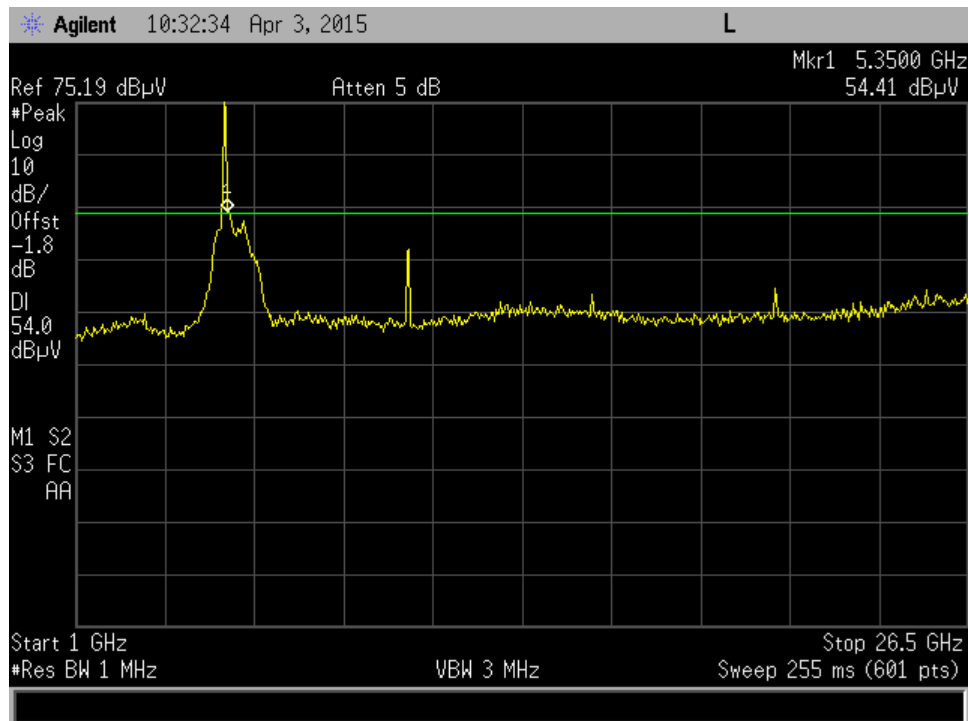


Annex B

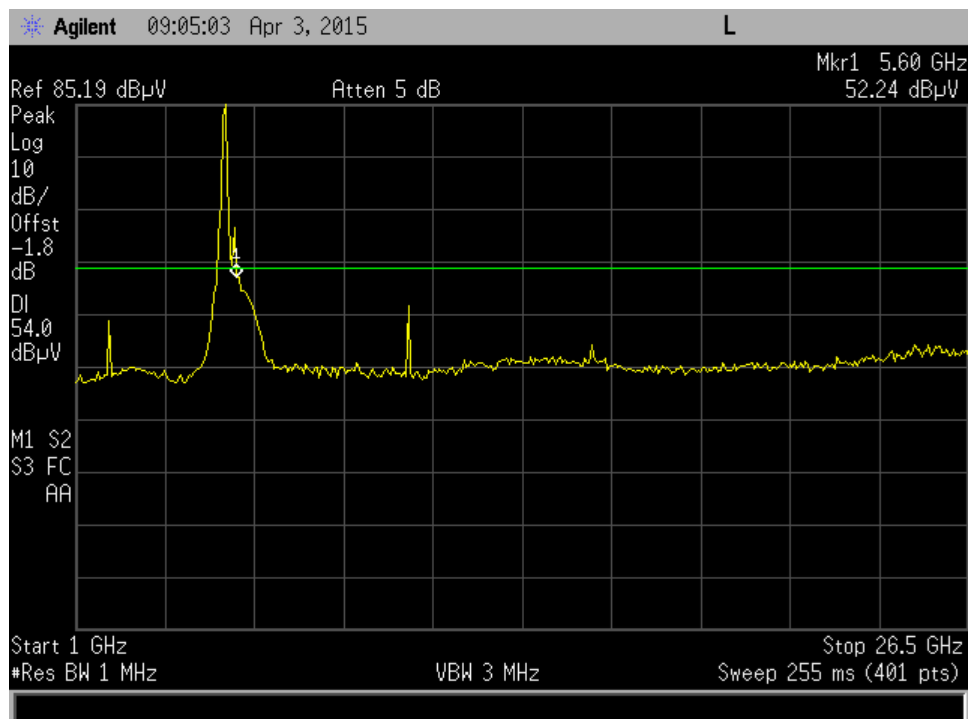
Undesirable Emissions Conducted

30-40000MHz

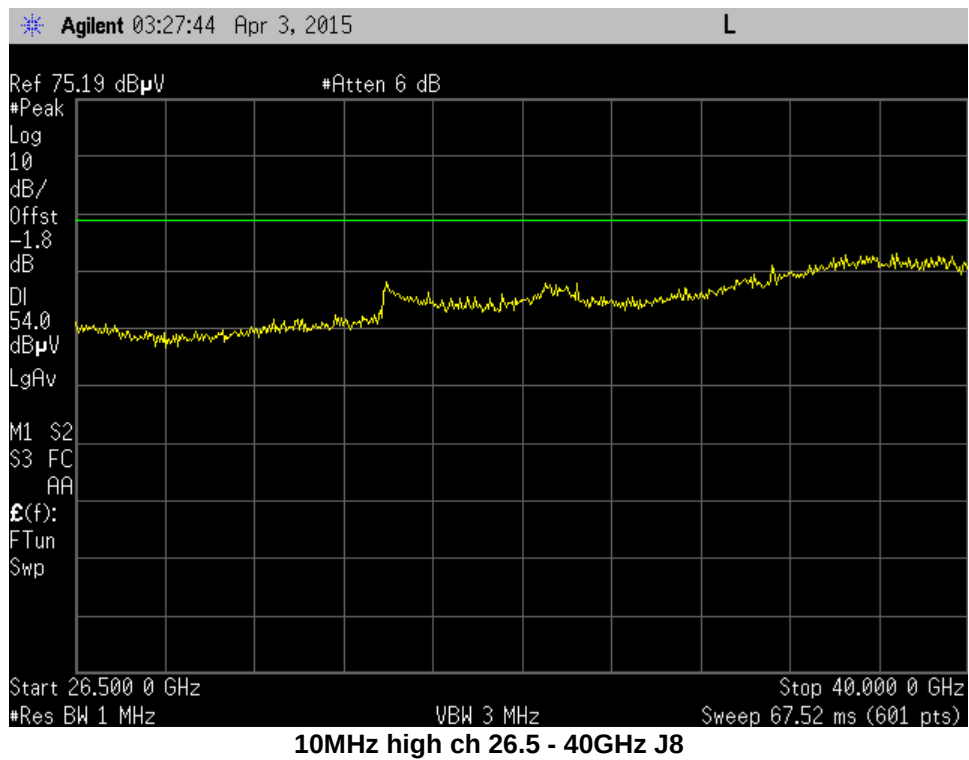
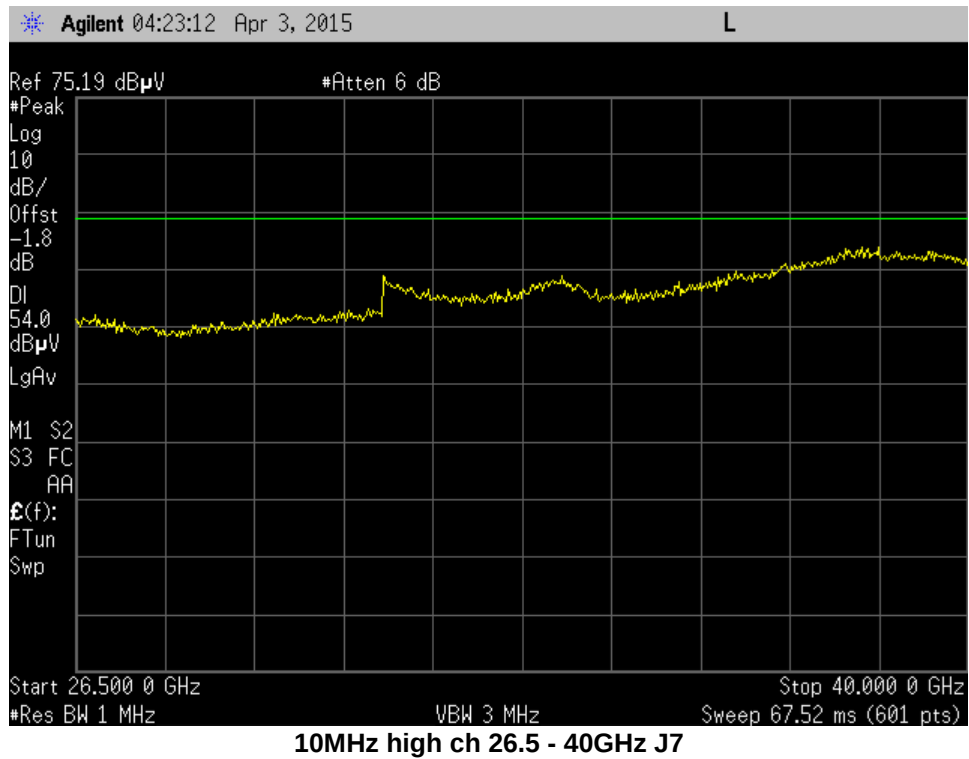
Ports J7 and J8

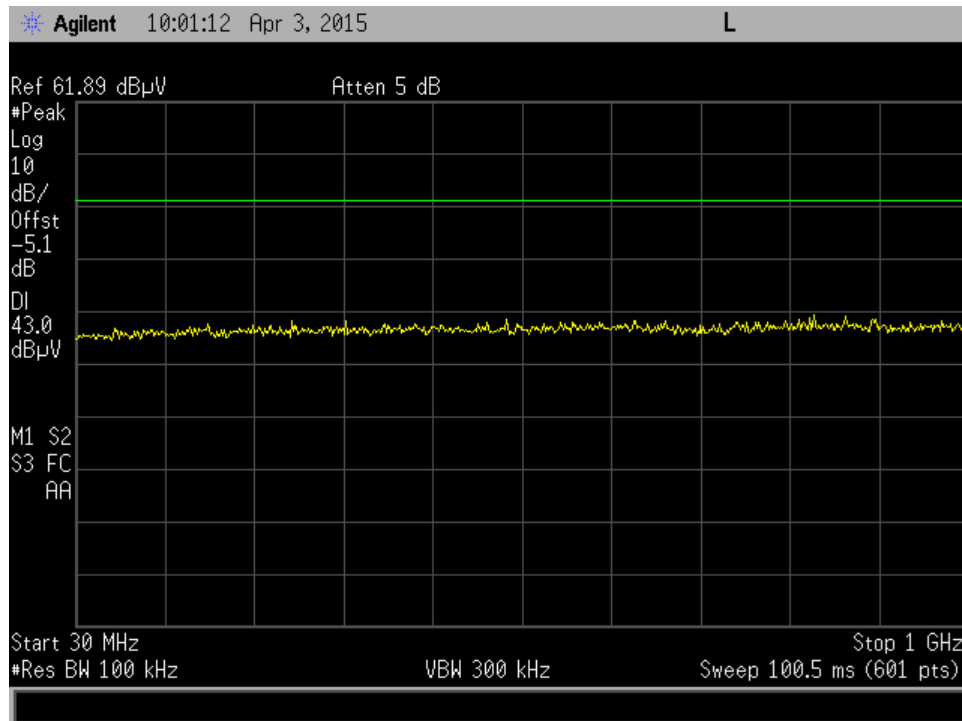


10MHz high ch 1 - 26.5GHz J8

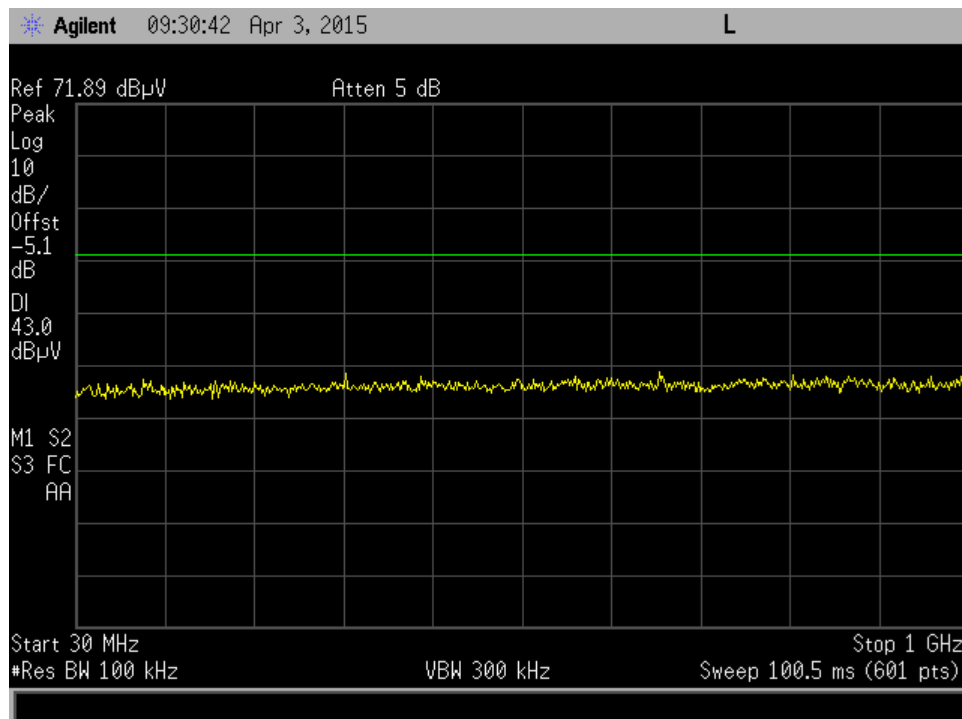


10MHz high ch 1 - 26.5GHz J7

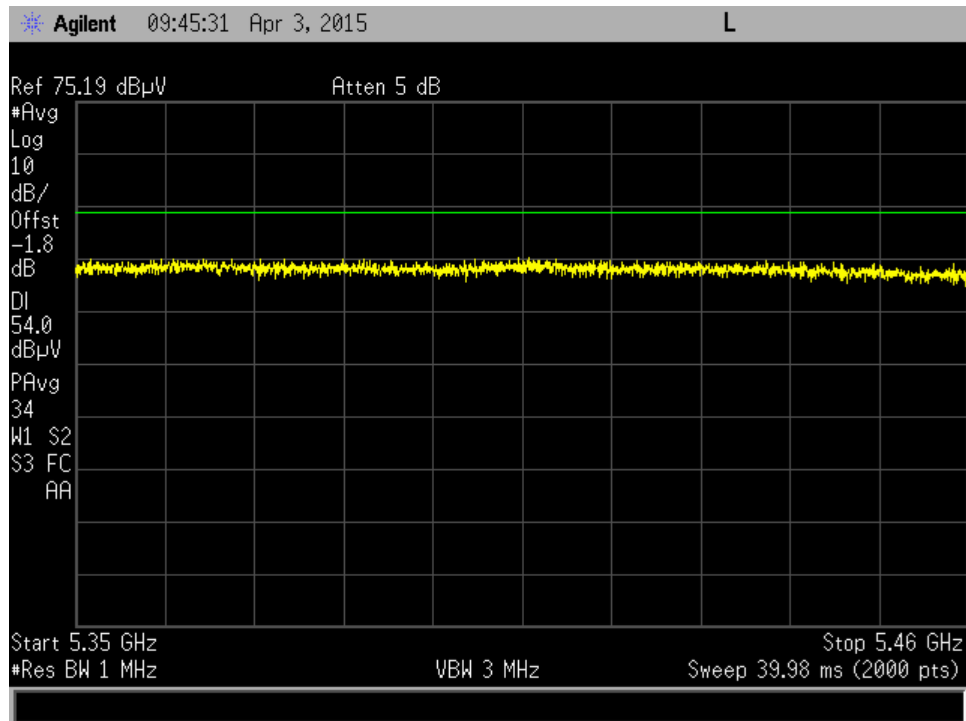




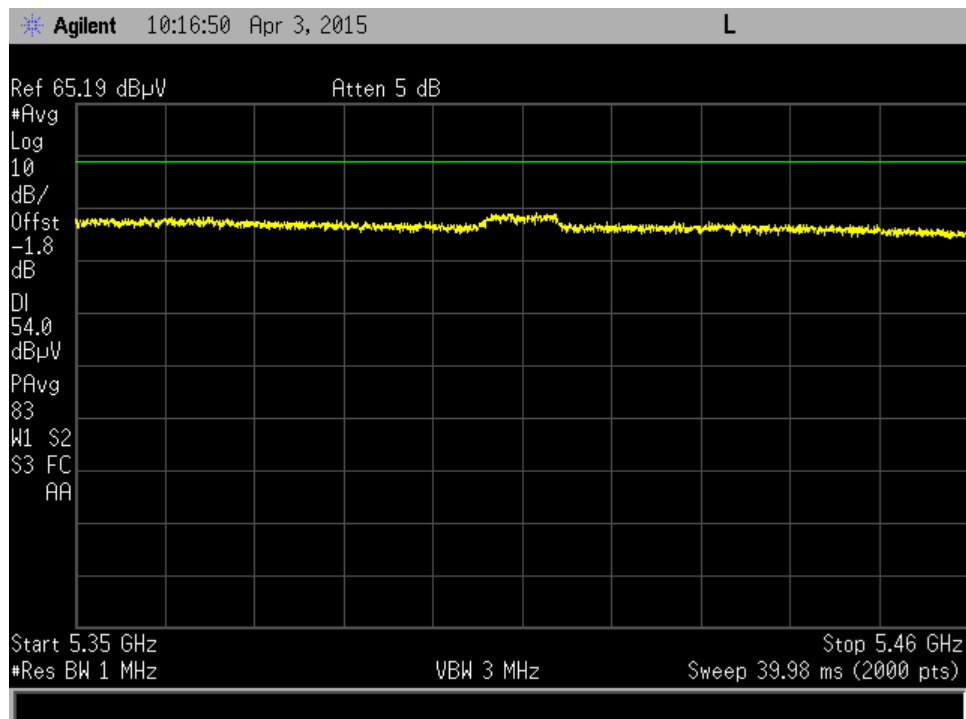
10MHz high ch 30 - 1000MHz J8



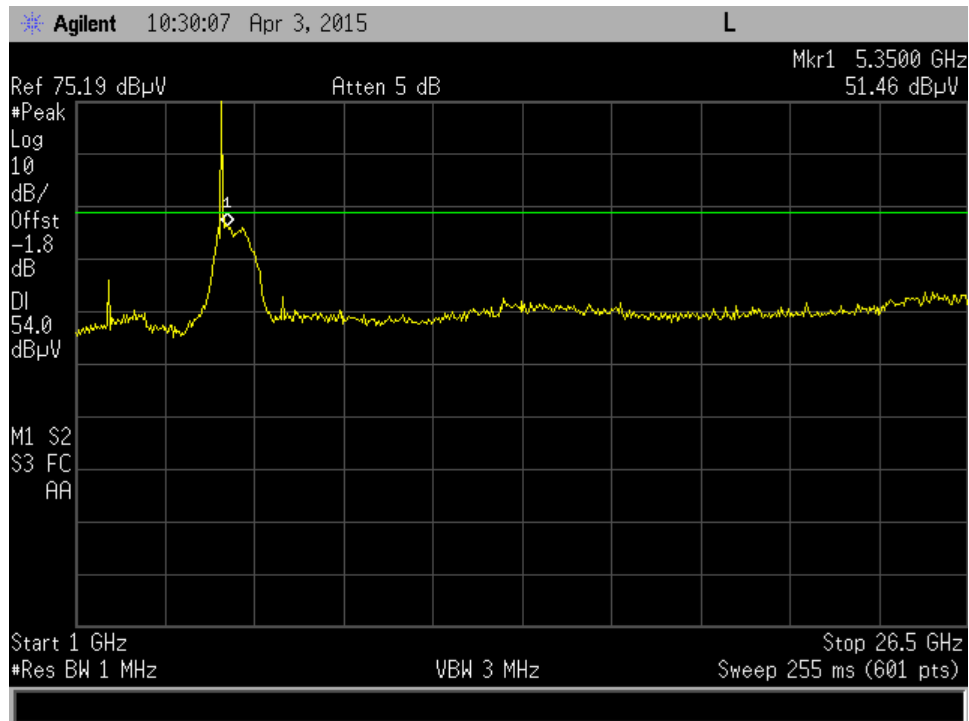
10MHz high ch 30 - 1000MHz J7



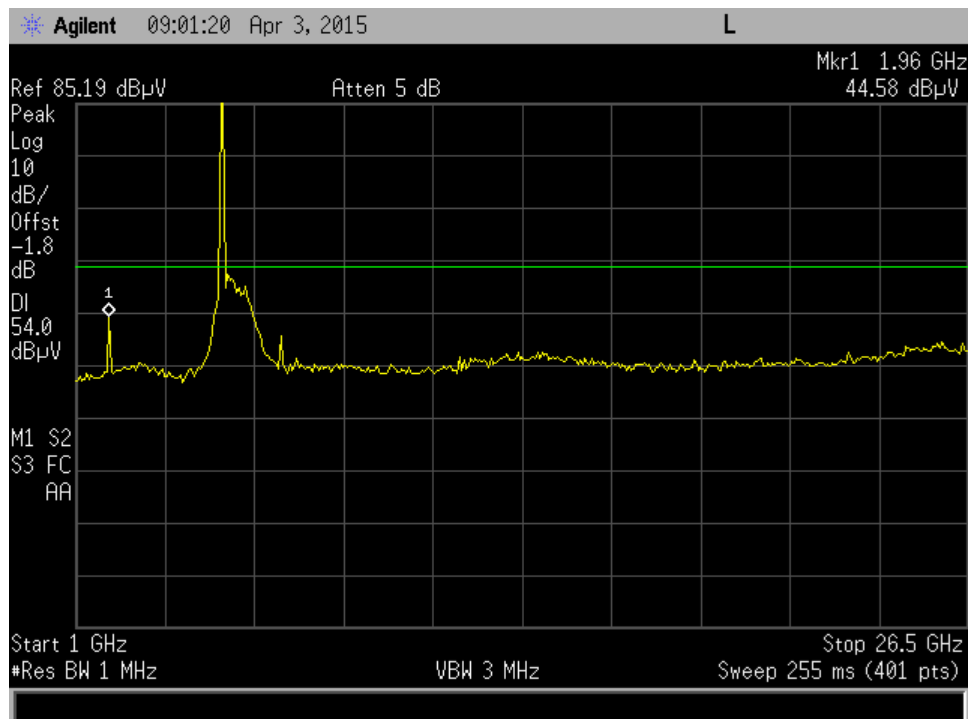
10MHz high ch 5350 - 5460GHz J7



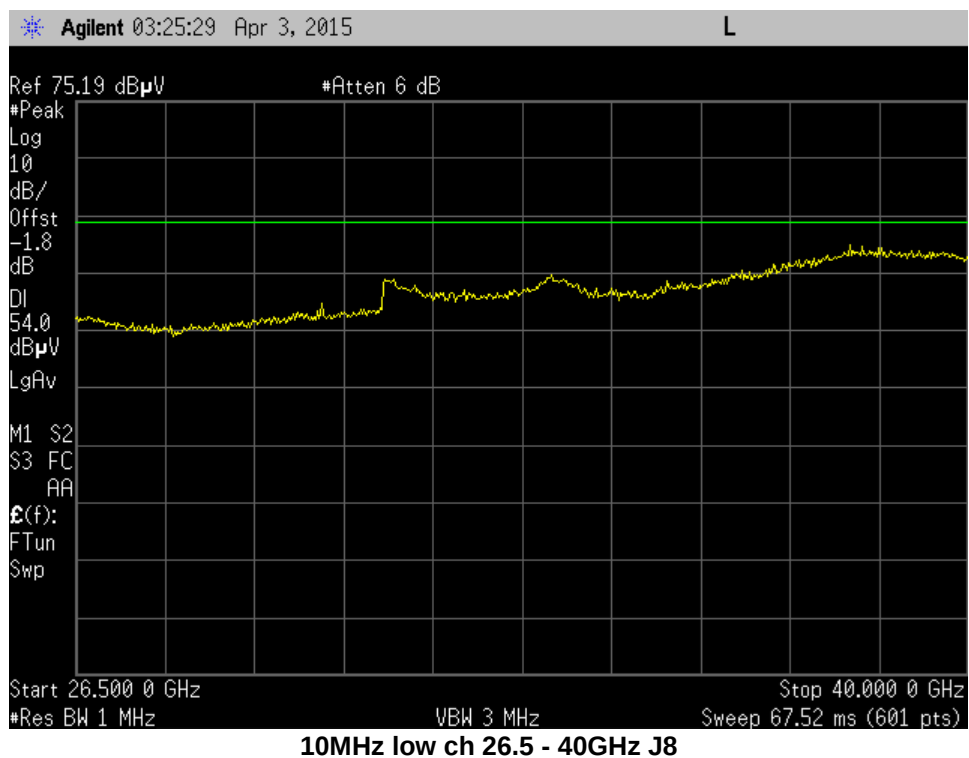
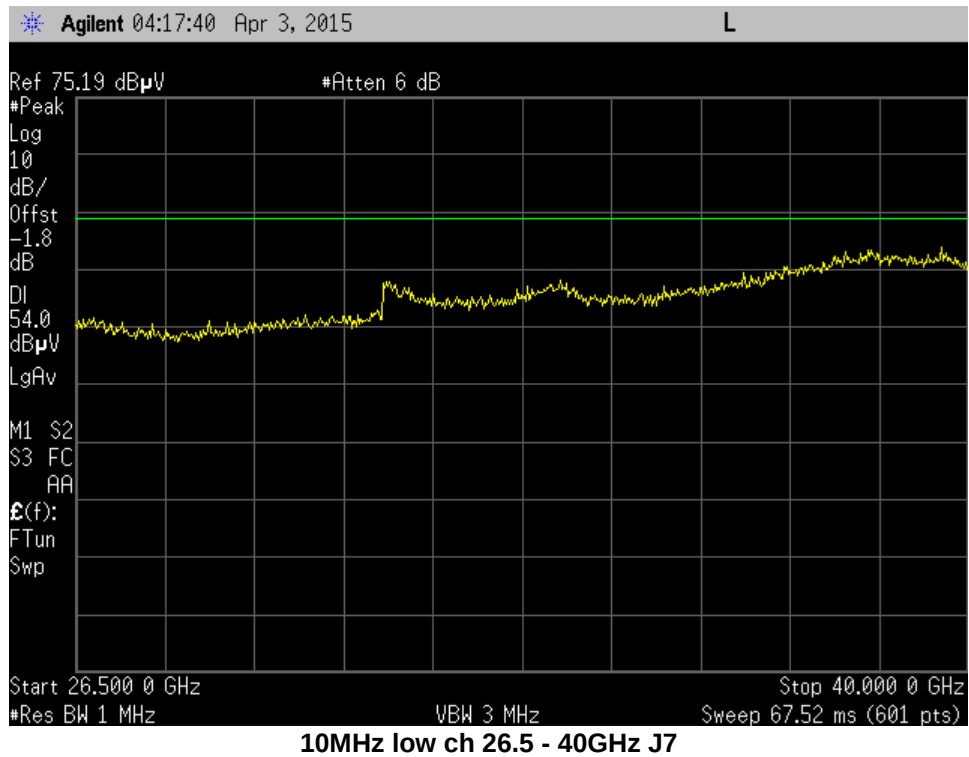
10MHz high ch 5350 - 5460GHz J8

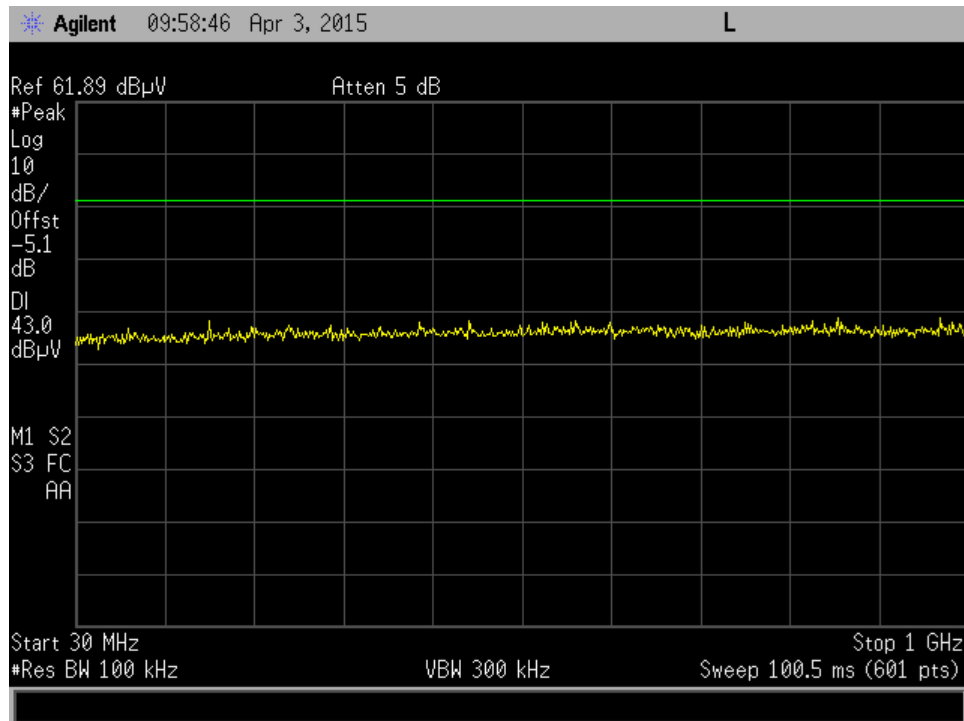


10MHz low ch 1 - 26.5GHz J8

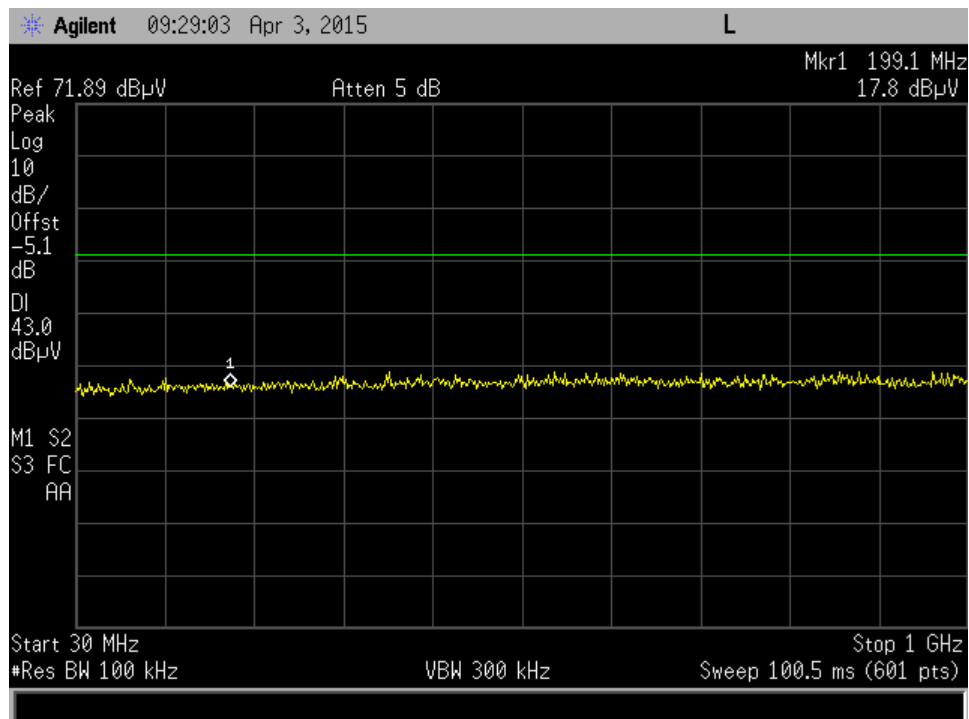


10MHz low ch 1 - 26.5GHz J7

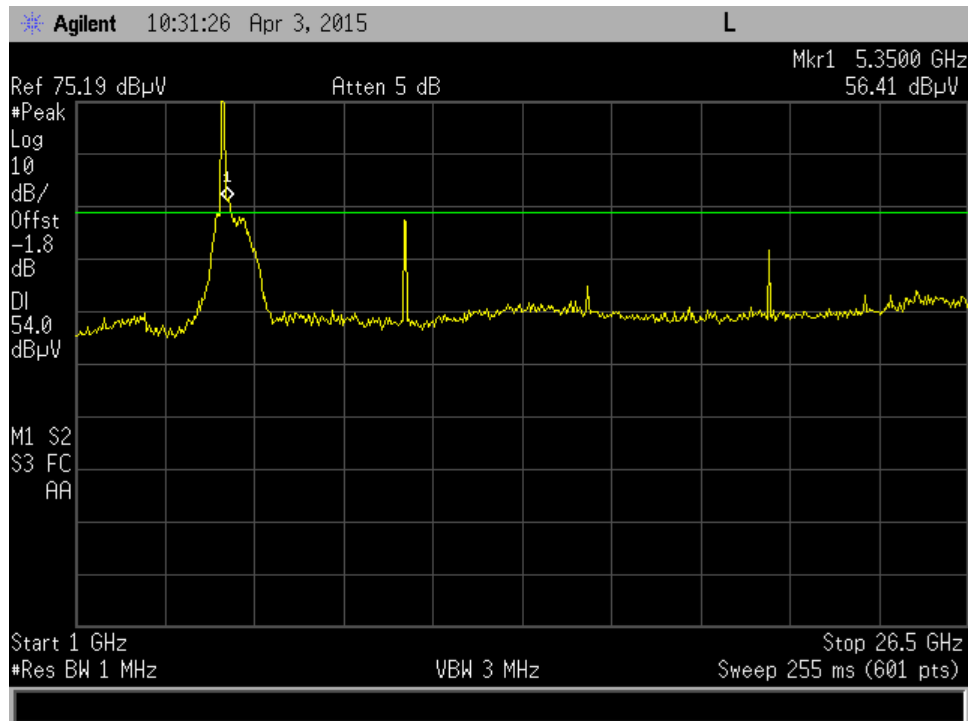




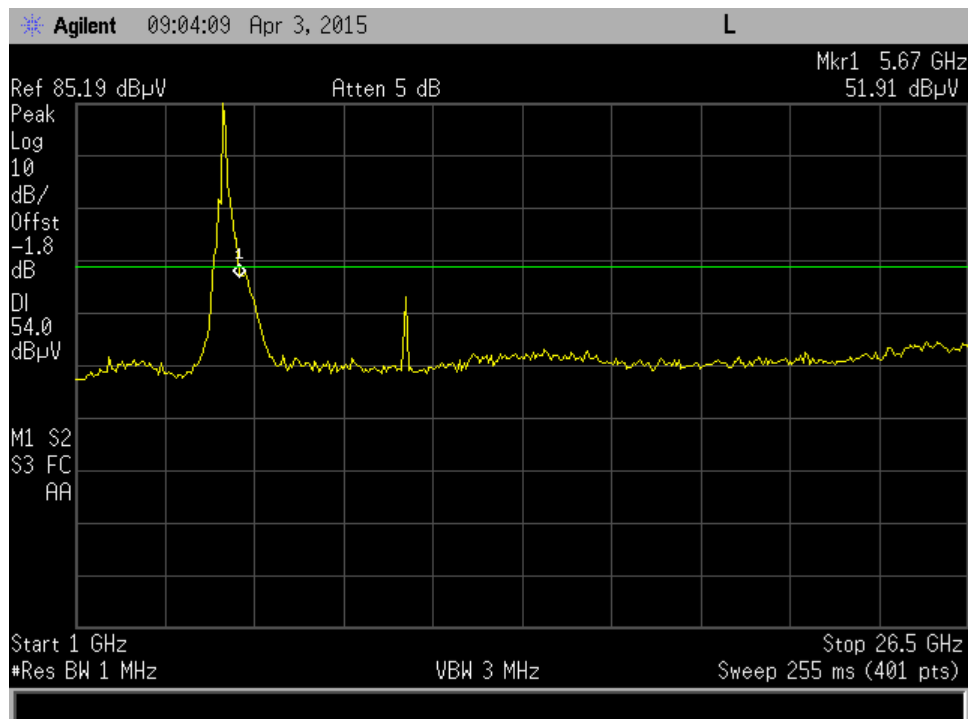
10MHz low ch 30 - 1000MHz J8



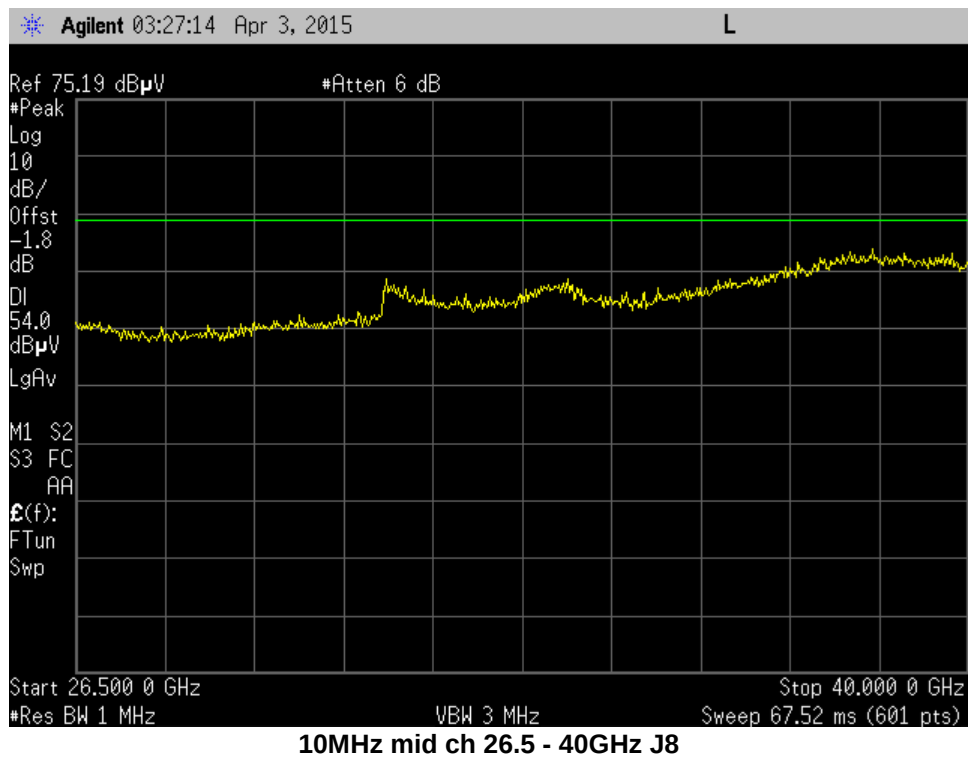
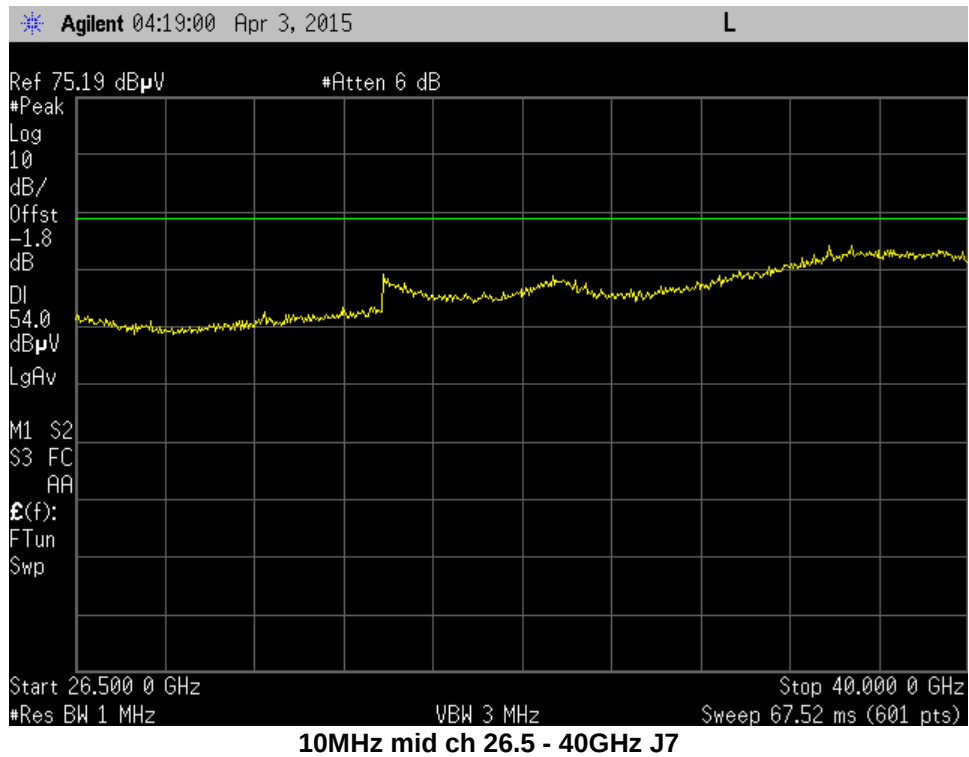
10MHz low ch 30 - 1000MHz J7

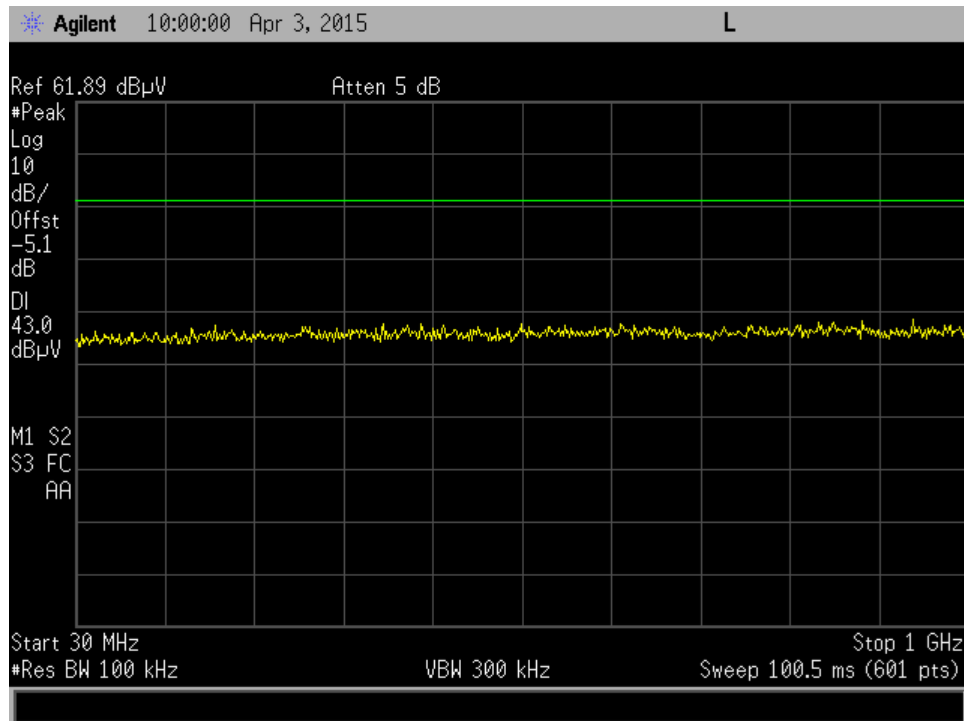


10MHz mid ch 1 - 26.5GHz J8

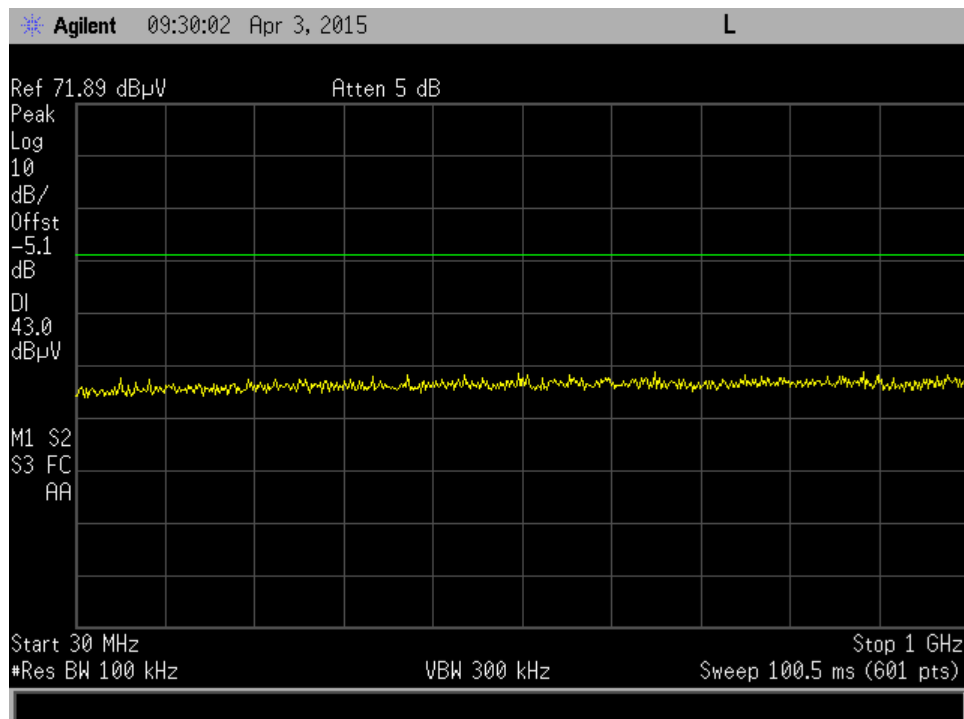


10MHz mid ch 1 - 26.5GHz J7

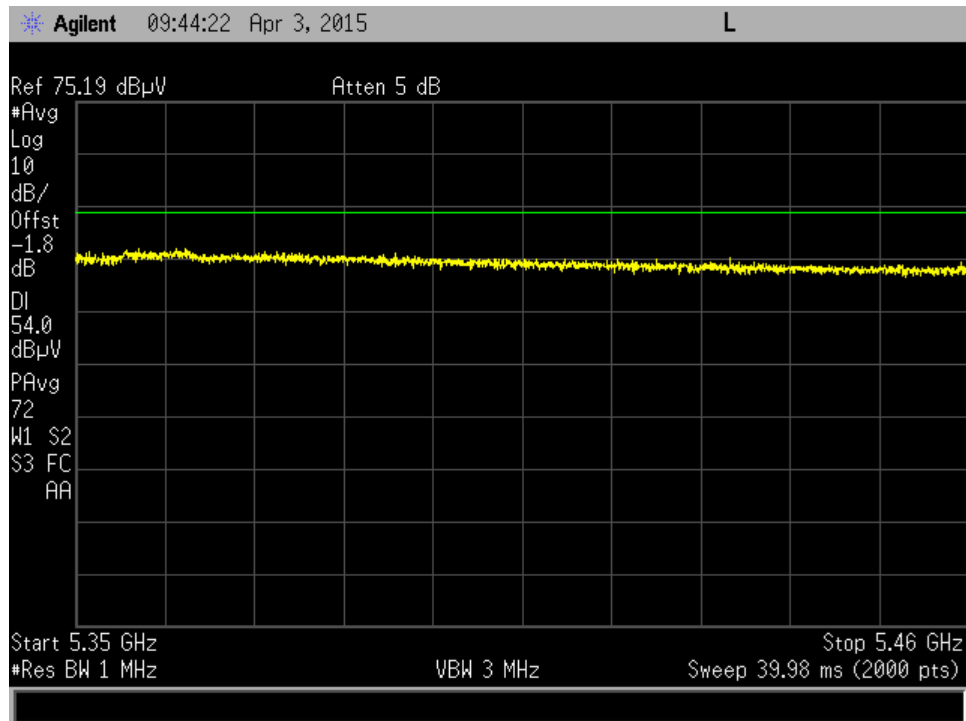




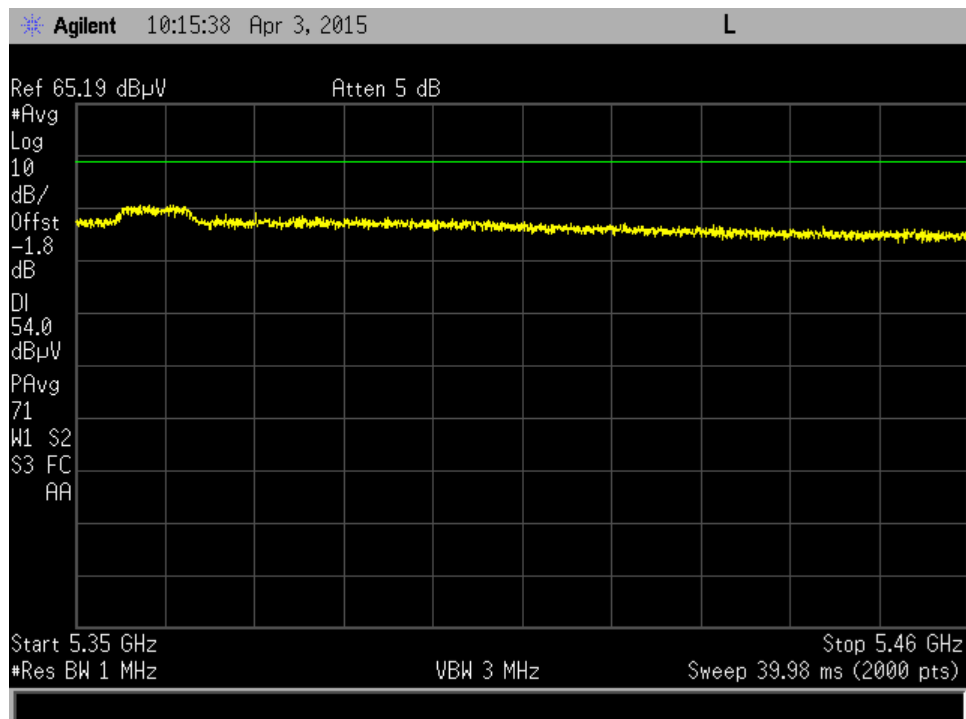
10MHz mid ch 30 - 1000MHz J8



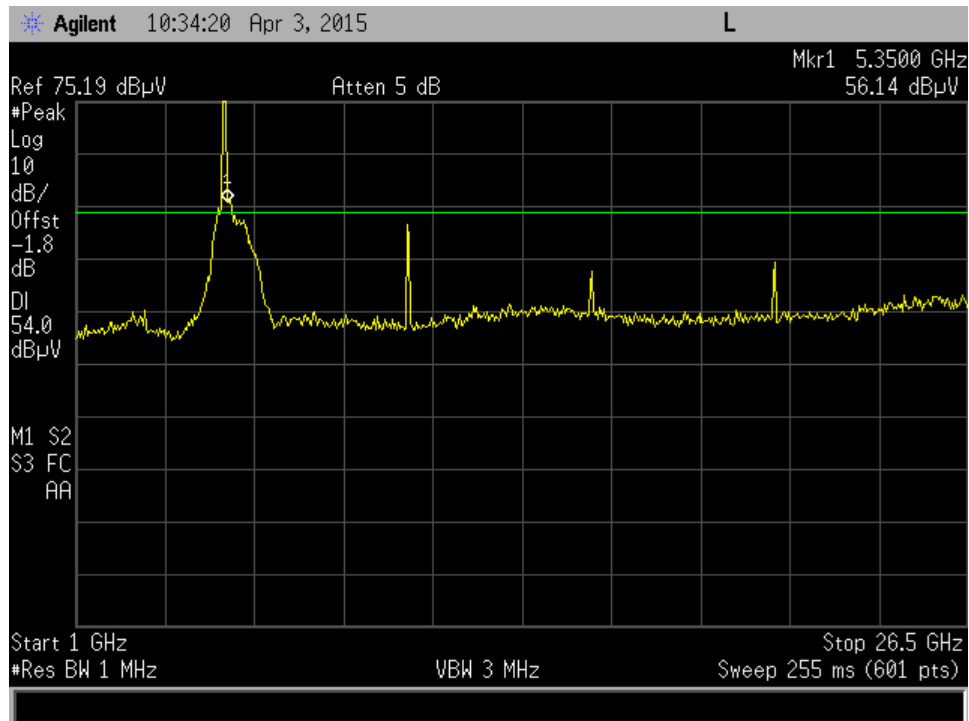
10MHz mid ch 30 - 1000MHz J7



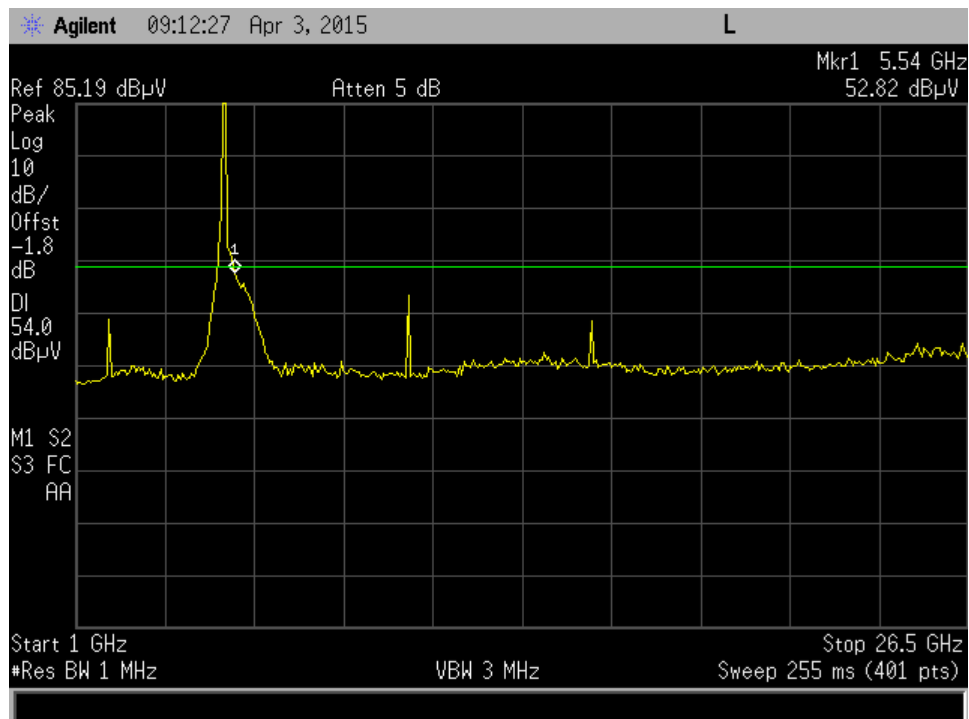
10MHz mid ch 5350 - 5460GHz J7



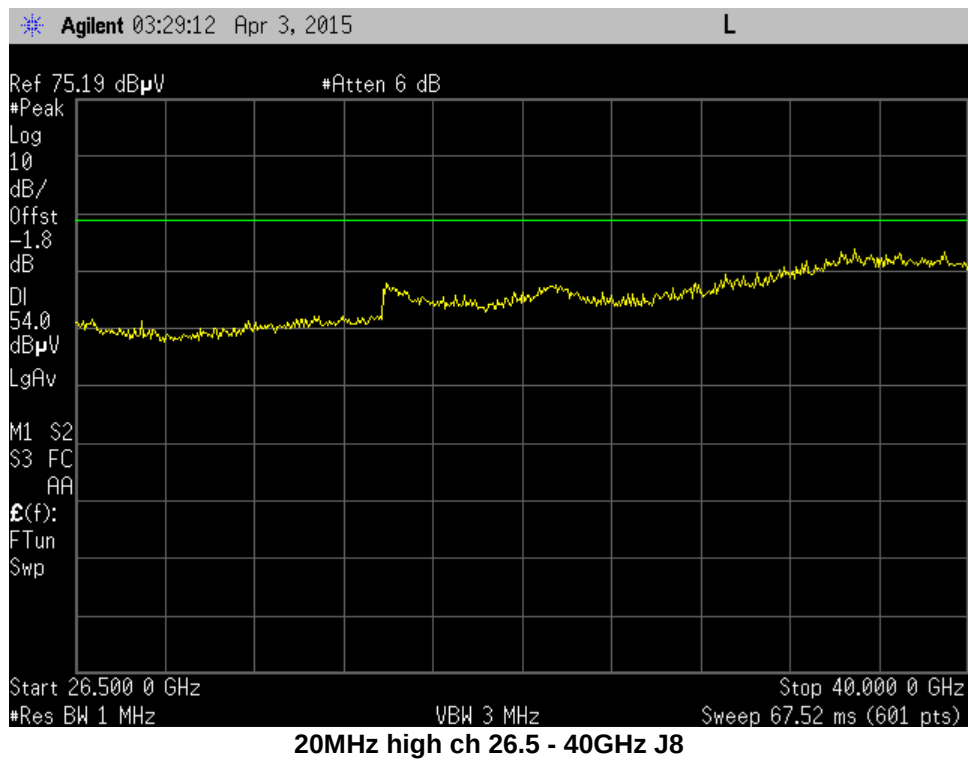
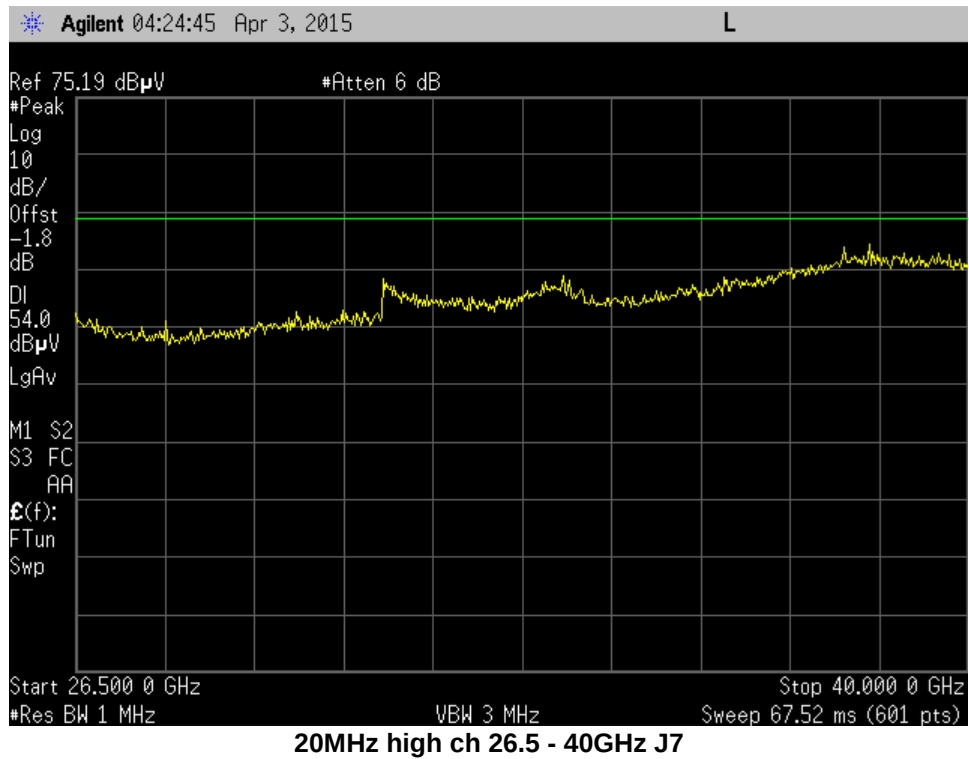
10MHz mid ch 5350 - 5460GHz J8

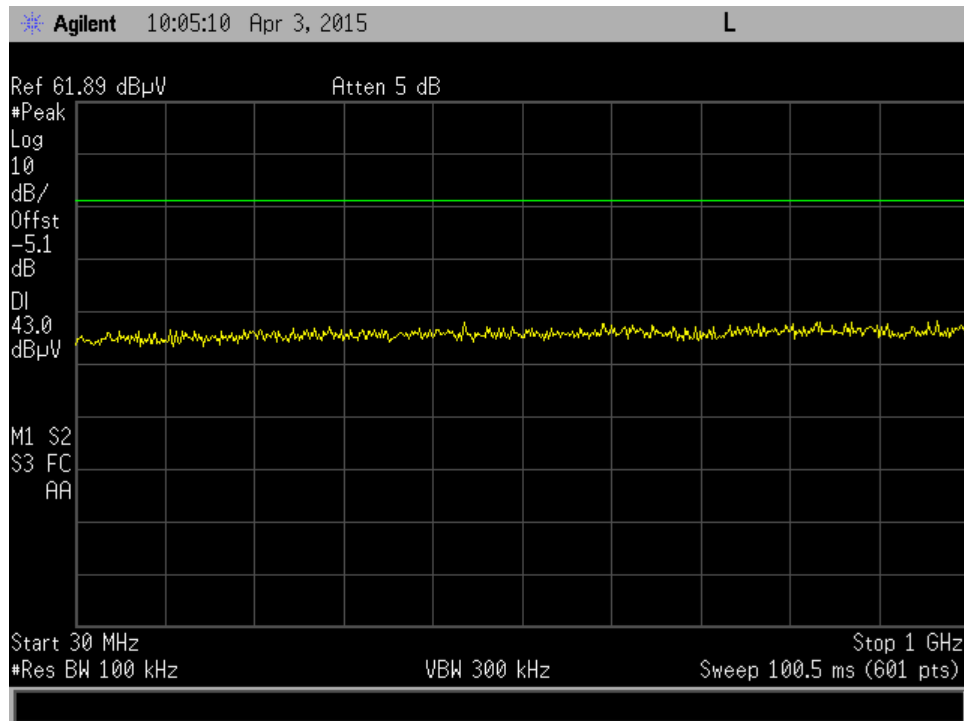


20MHz high ch 1 - 26.5GHz J8

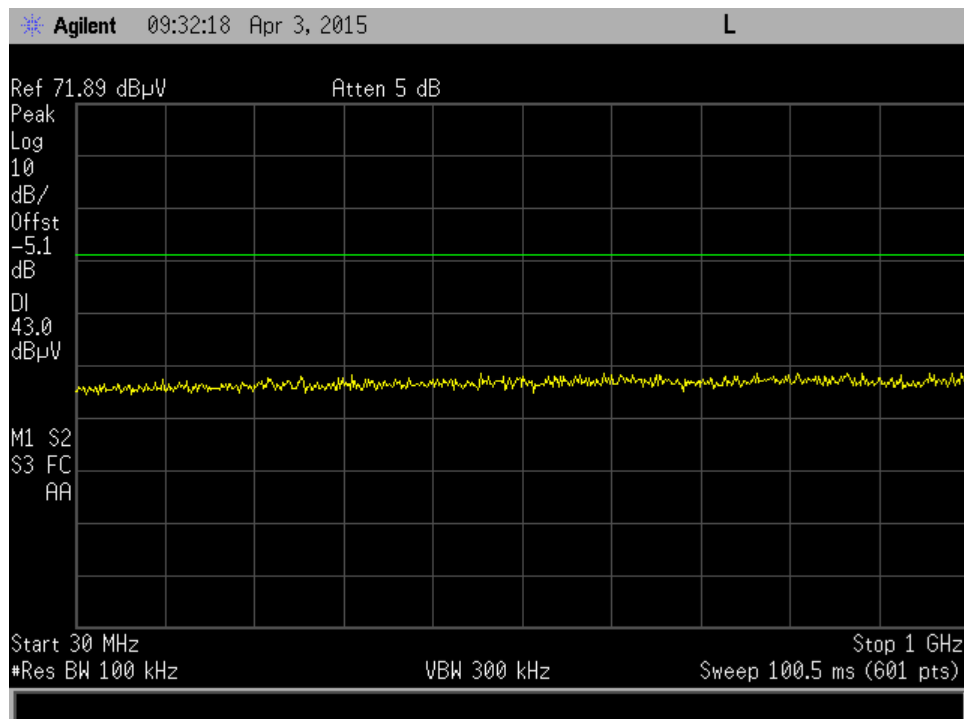


20MHz high ch 1 - 26.5GHz J7

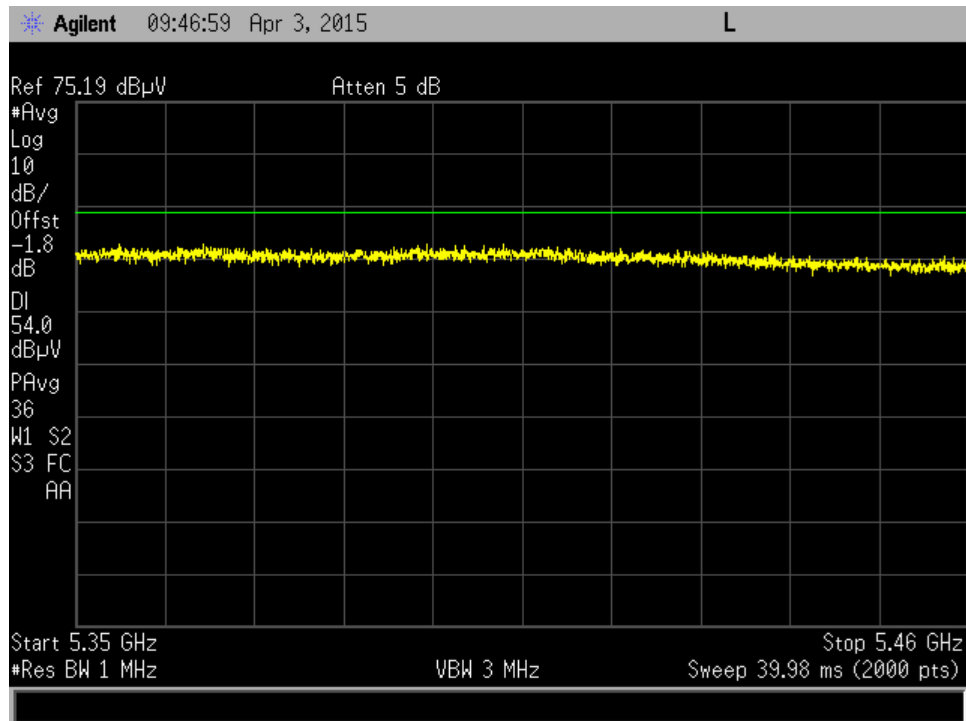




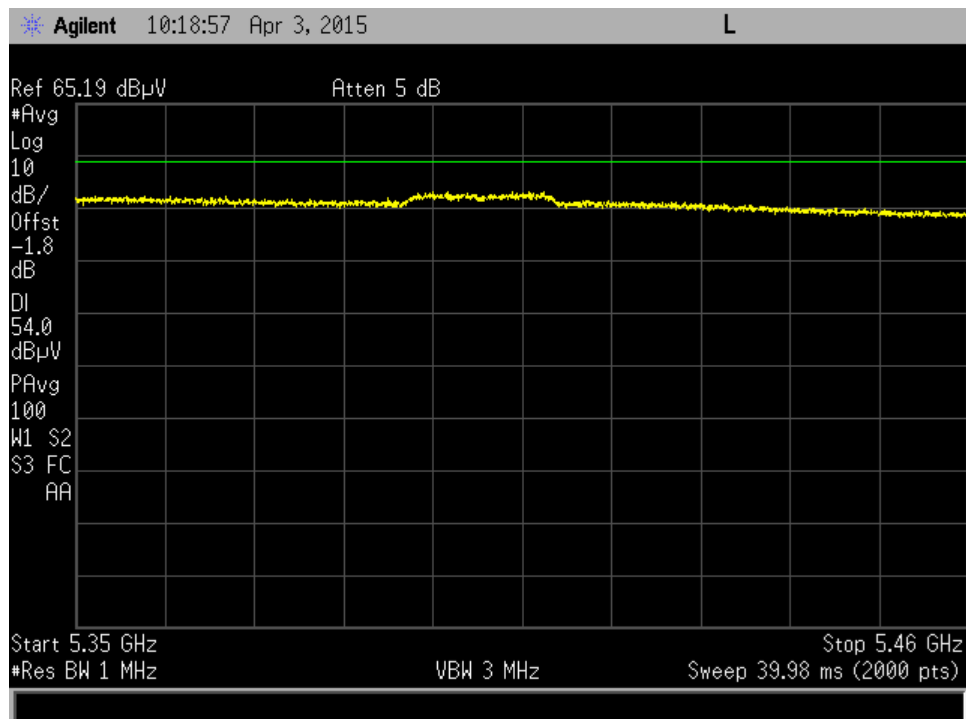
20MHz high ch 30 - 1000MHz J8



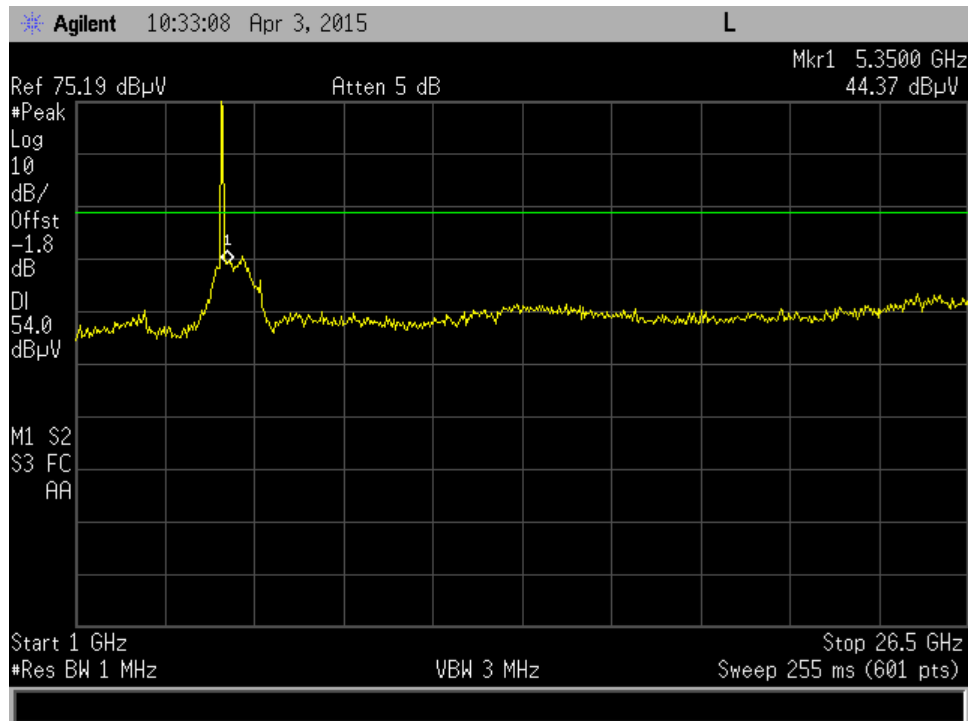
20MHz high ch 30 - 1000MHz J7



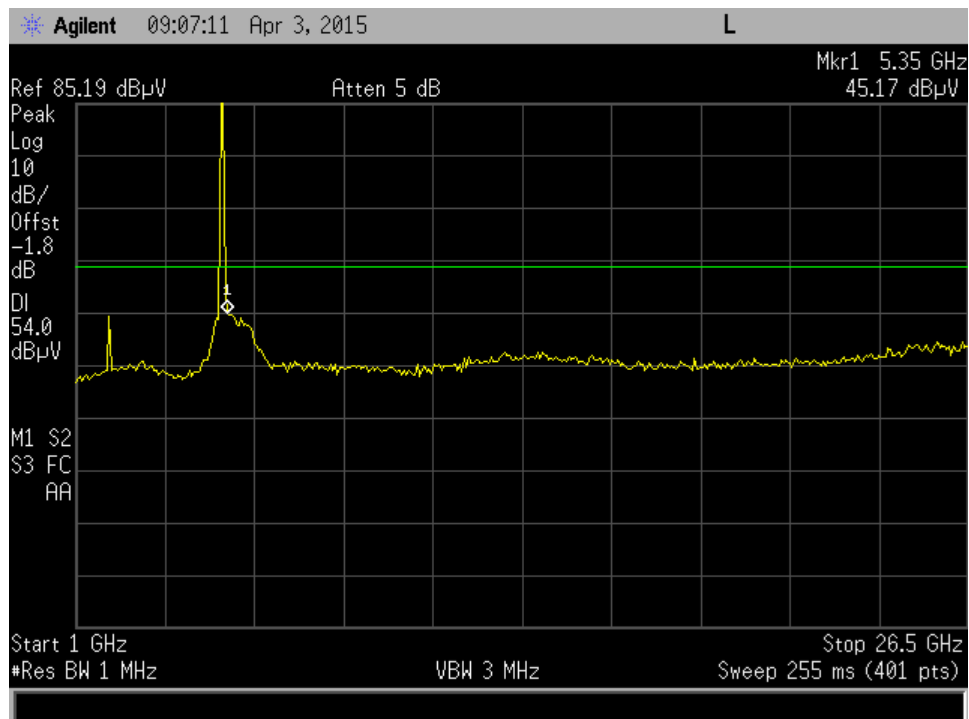
20MHz high ch 5350 - 5460GHz J7



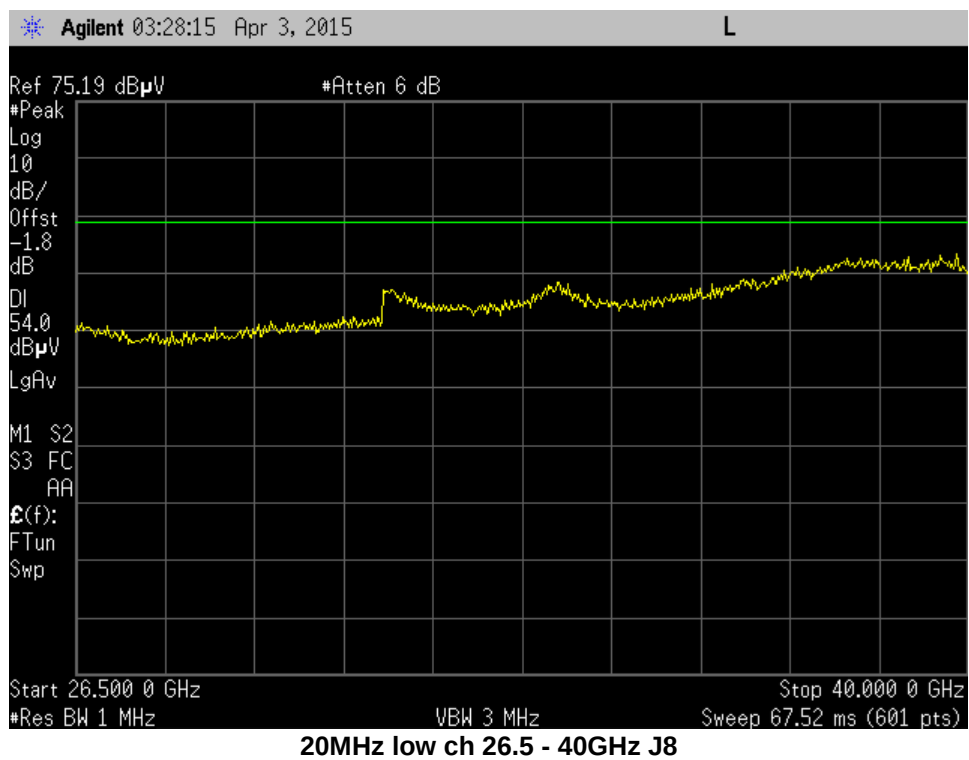
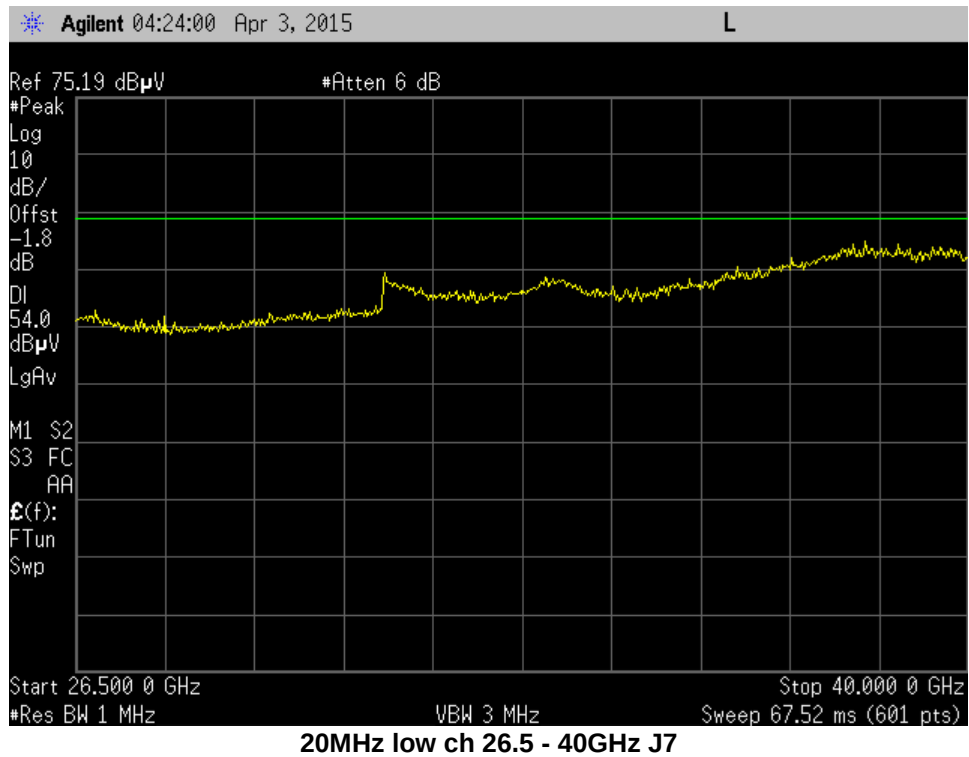
20MHz high ch 5350 - 5460GHz J8

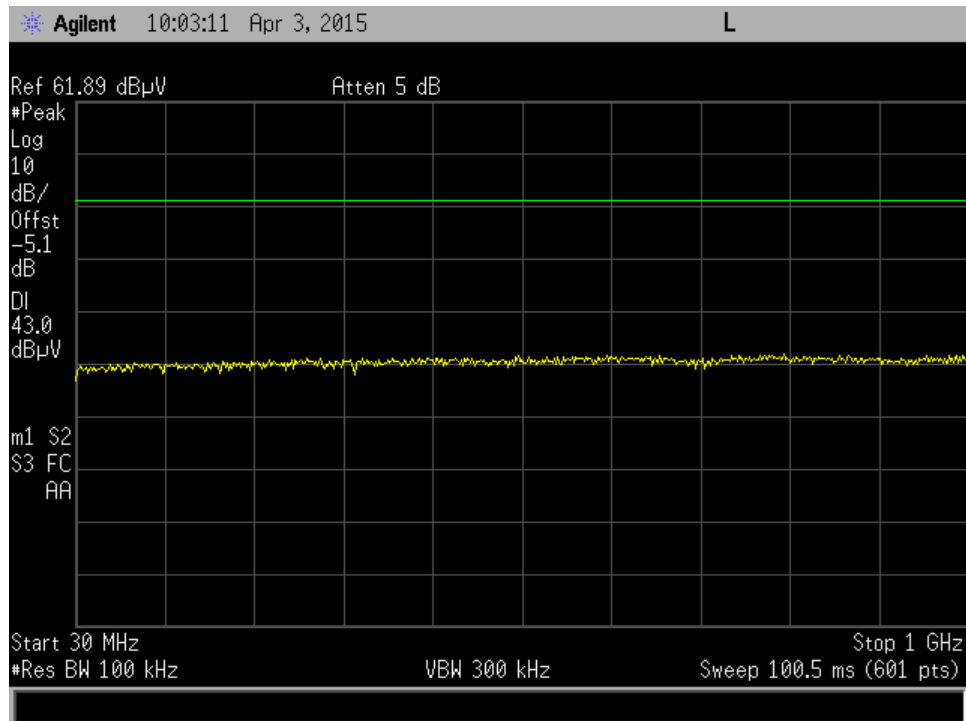


20MHz low ch 1 - 26.5GHz J8

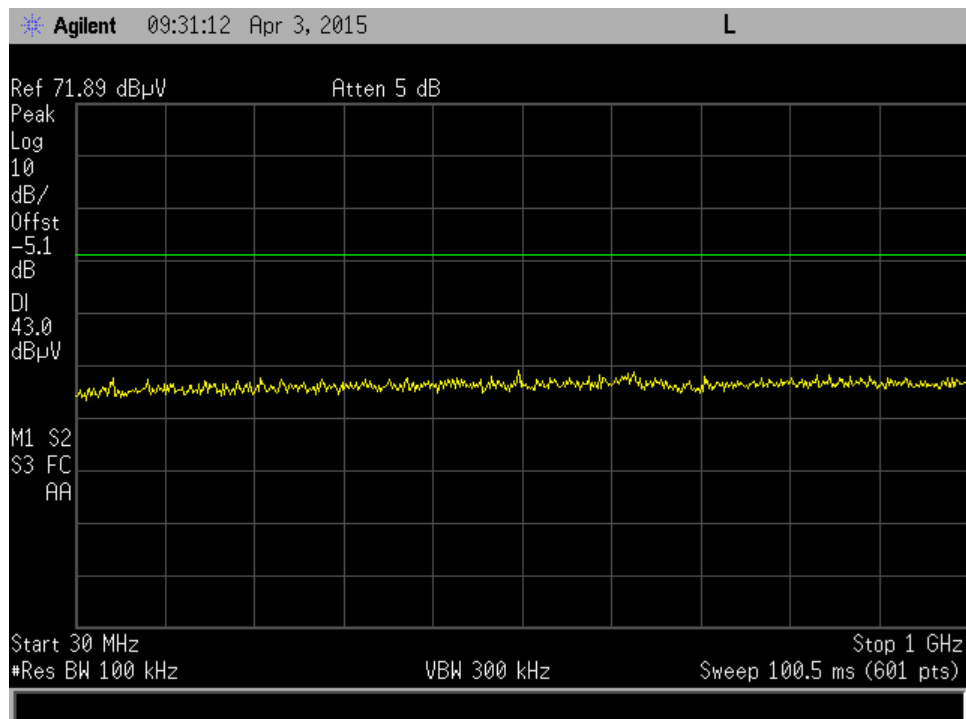


20MHz low ch 1 - 26.5GHz J7

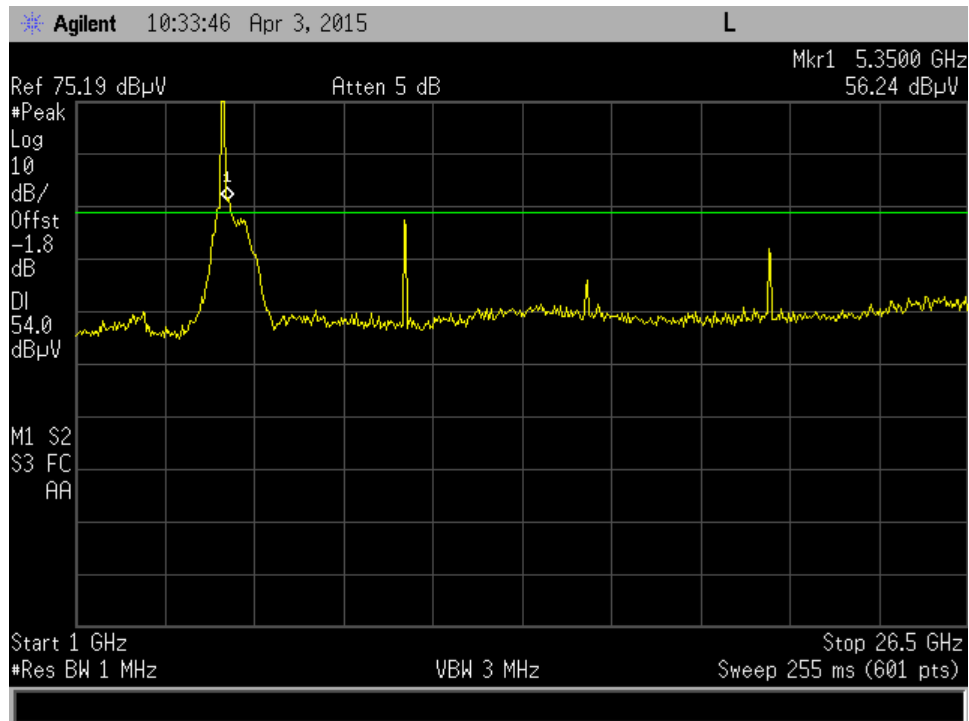




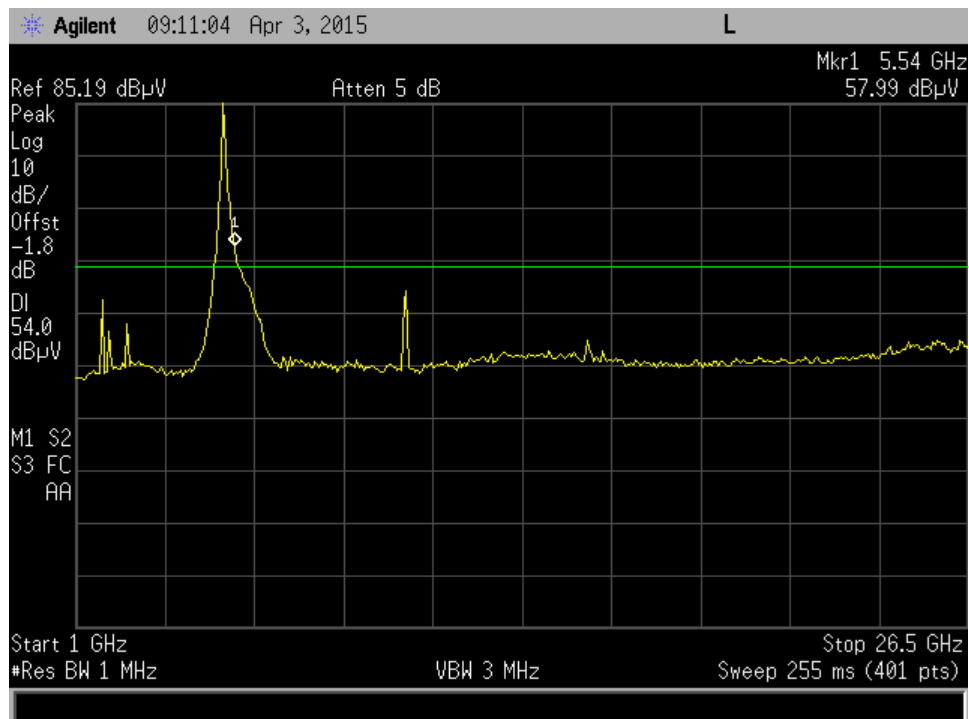
20MHz low ch 30 - 1000MHz J8



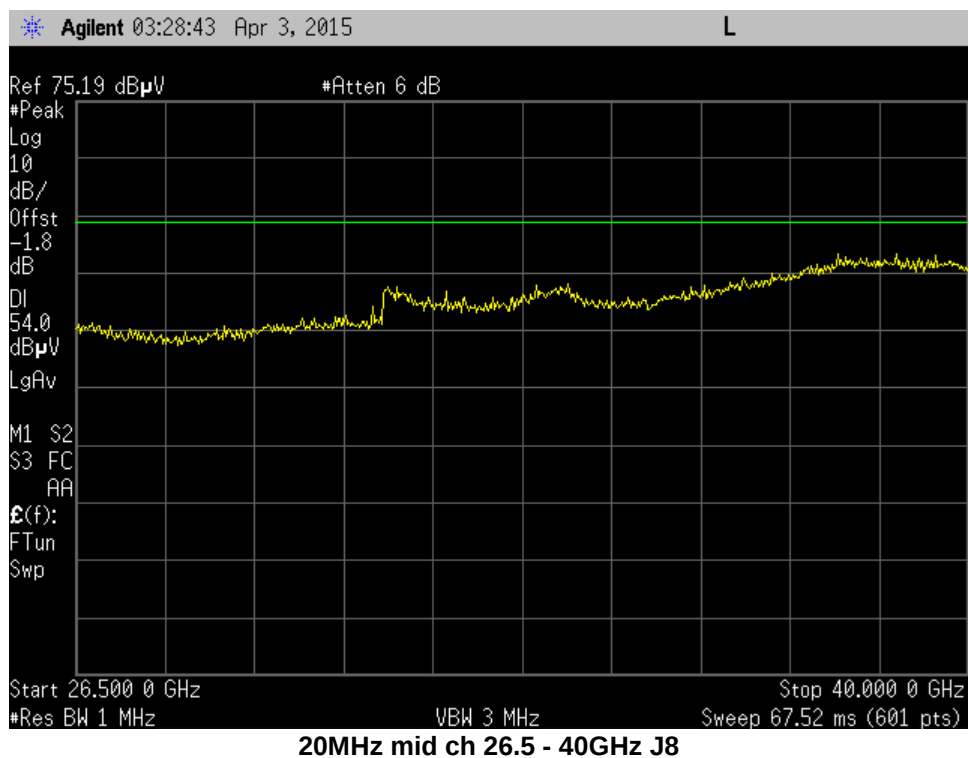
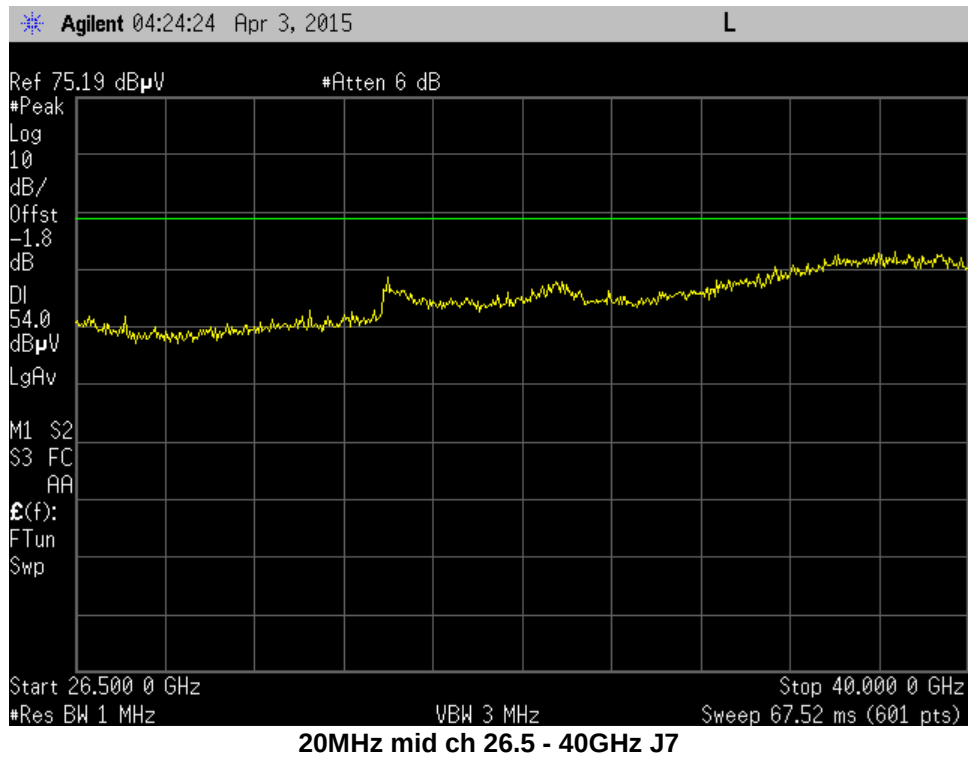
20MHz low ch 30 - 1000MHz J7

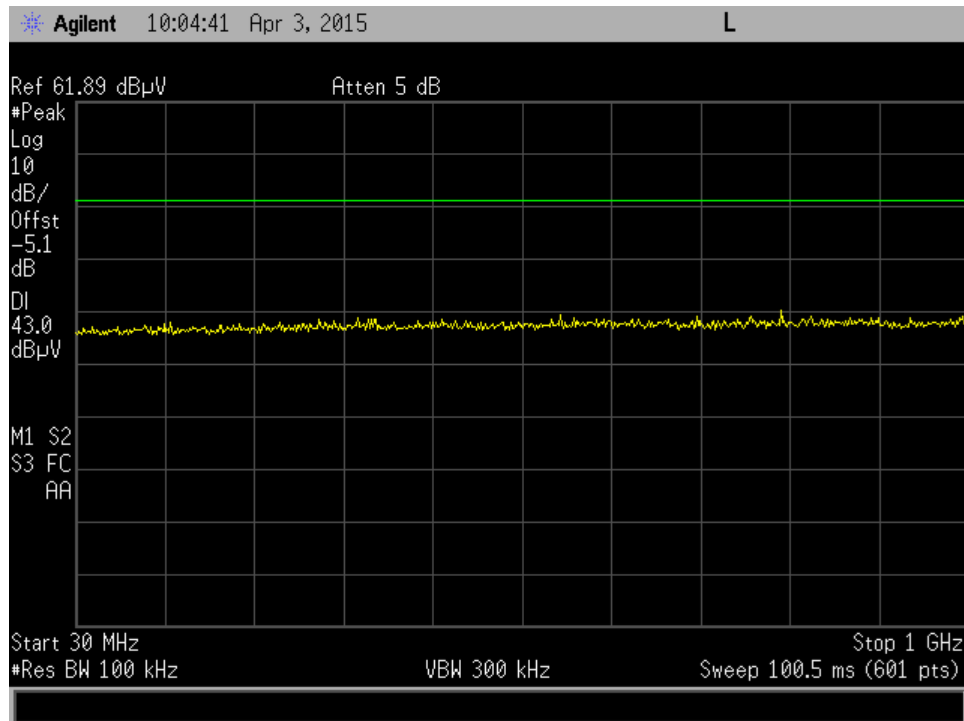


20MHz mid ch 1 - 26.5GHz J8

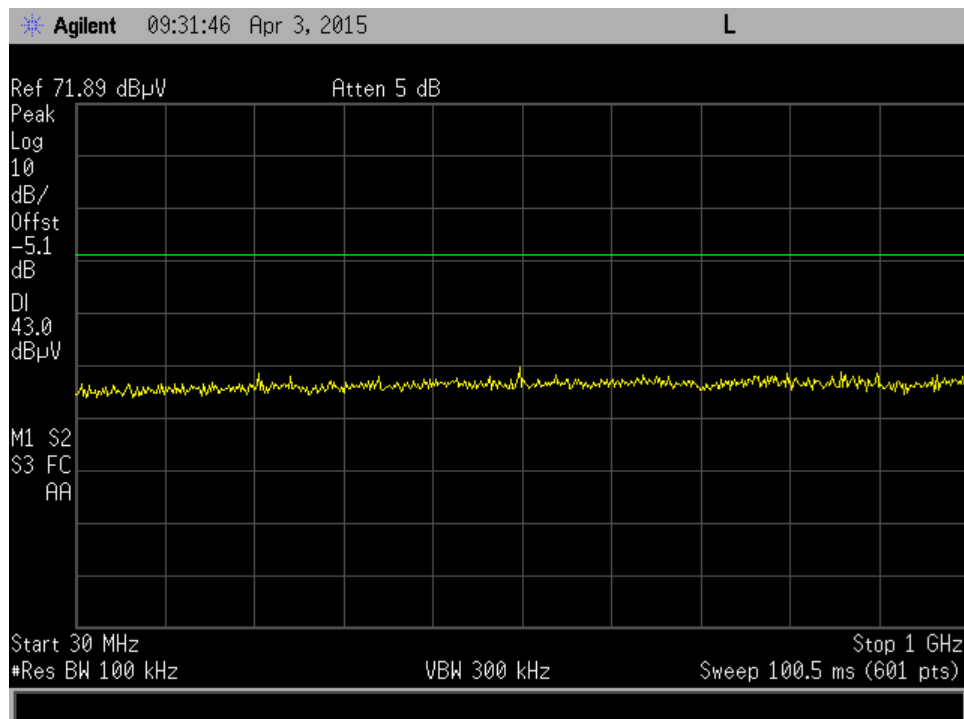


20MHz mid ch 1 - 26.5GHz J7

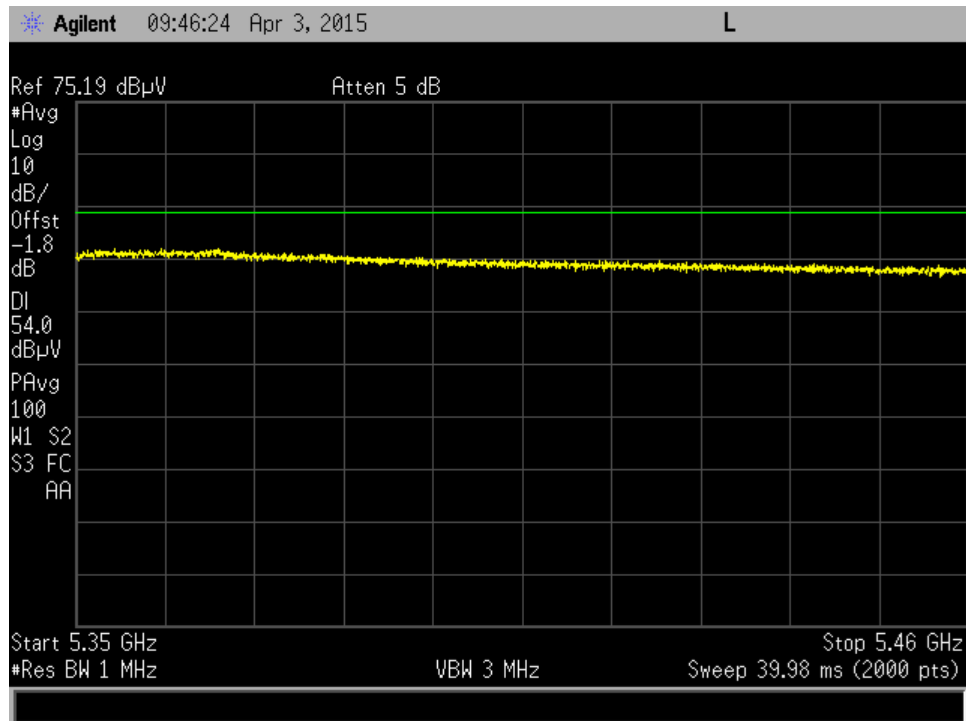




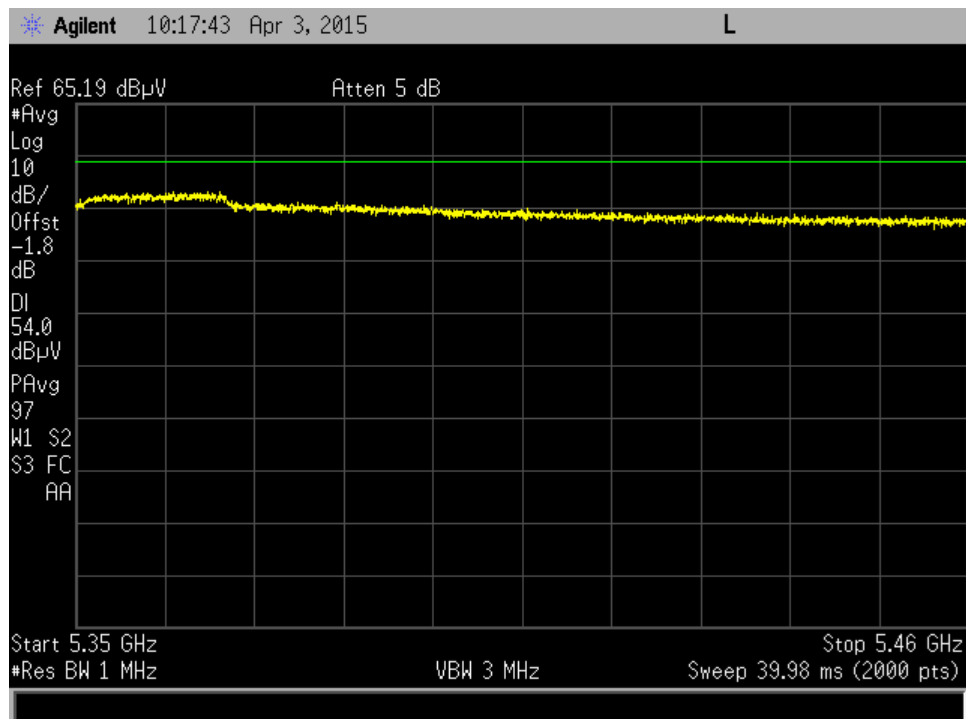
20MHz mid ch 30 - 1000MHz J8



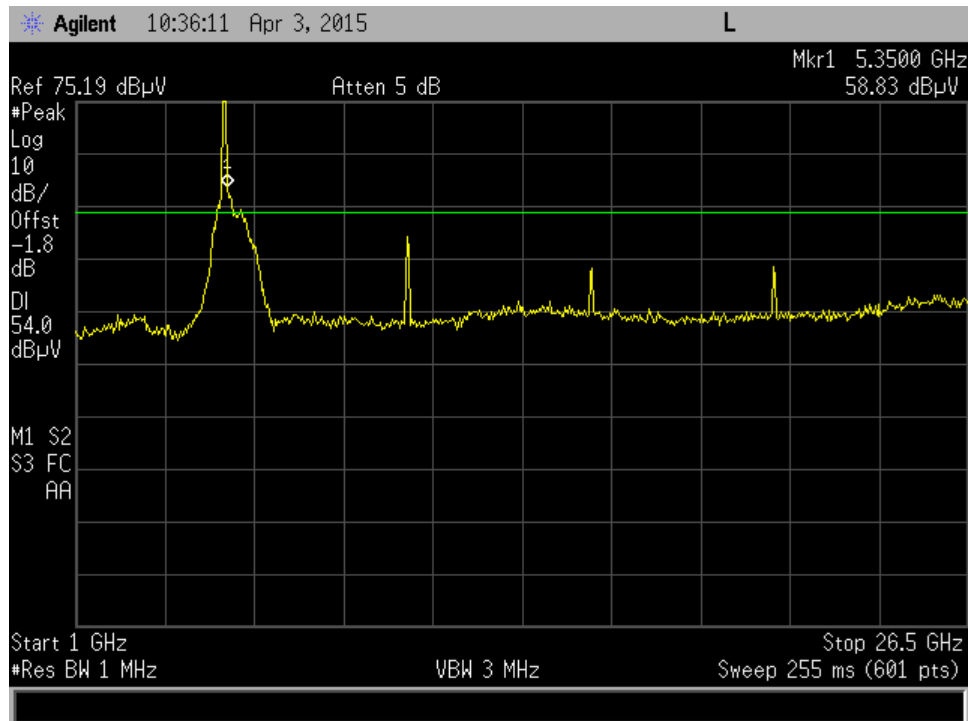
20MHz mid ch 30 - 1000MHz J7



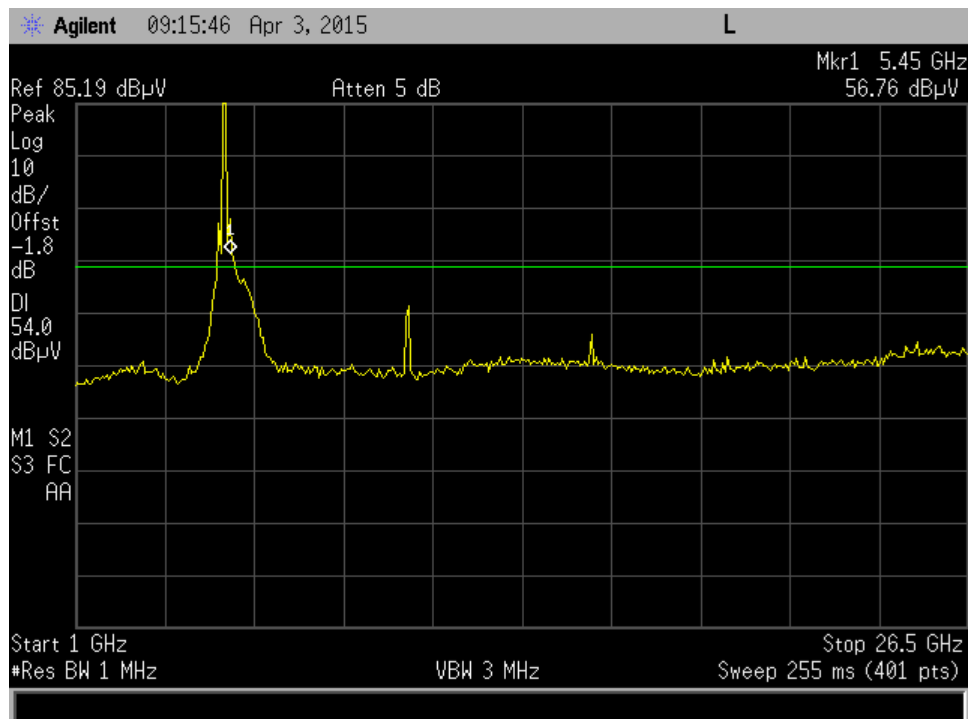
20MHz mid ch 5350 - 5460GHz J7



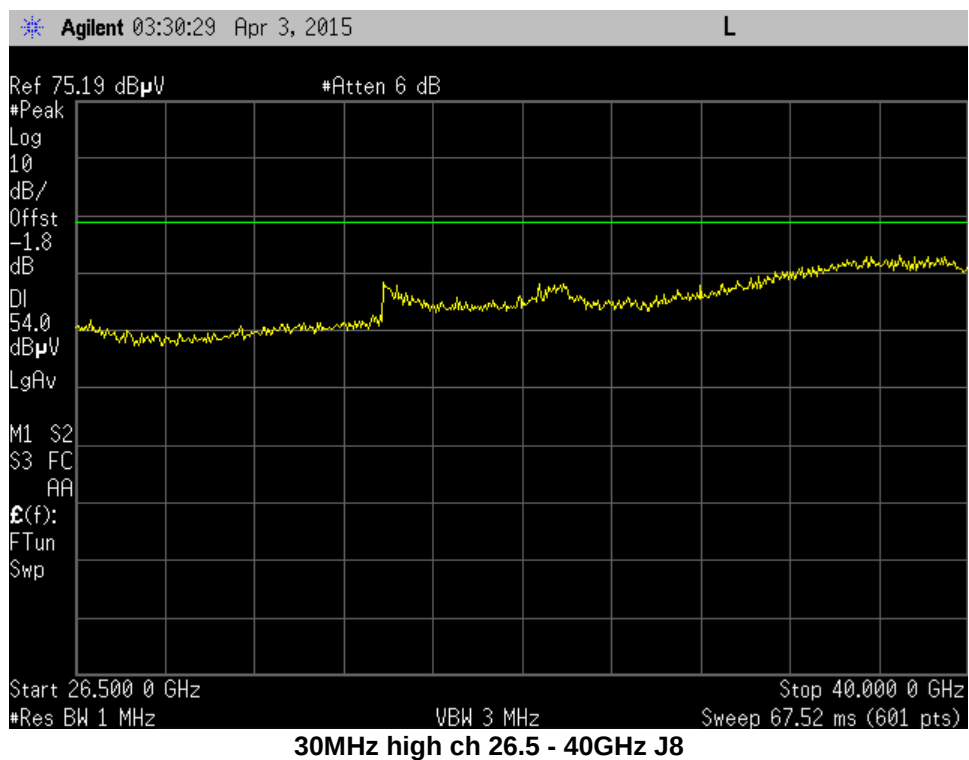
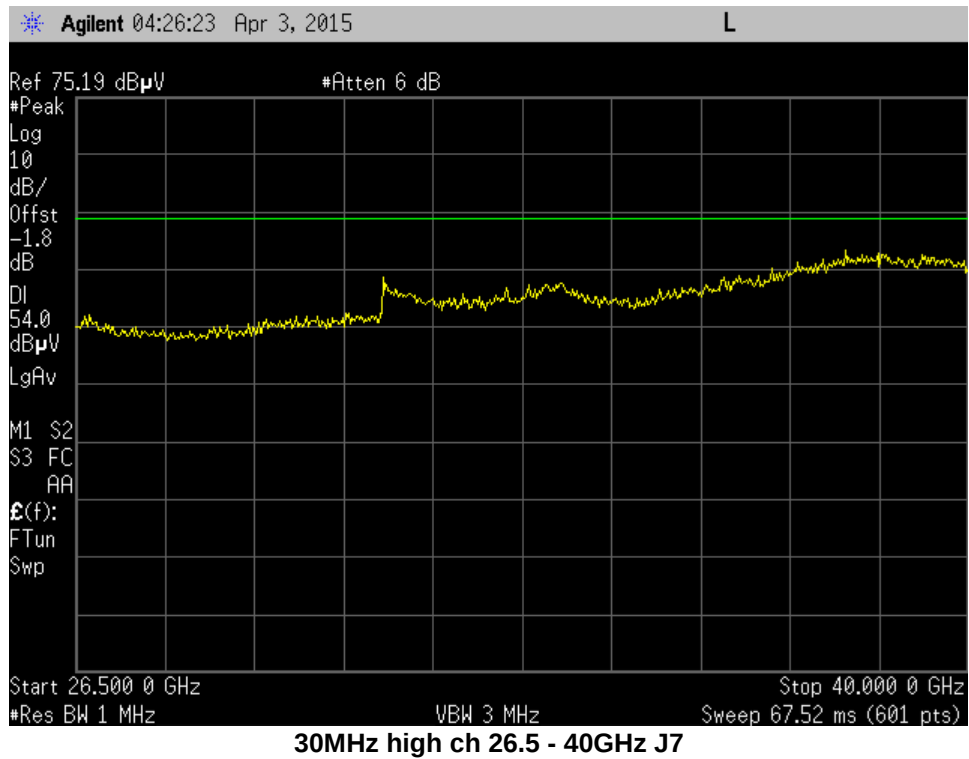
20MHz mid ch 5350 - 5460GHz J8

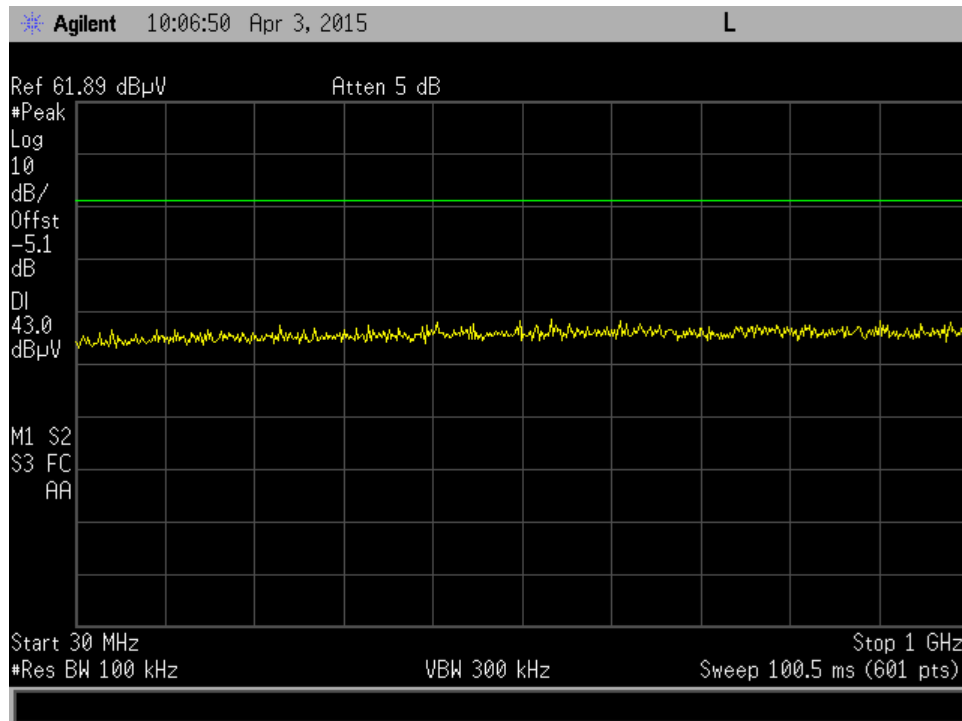


30MHz high ch 1 - 26.5GHz J8

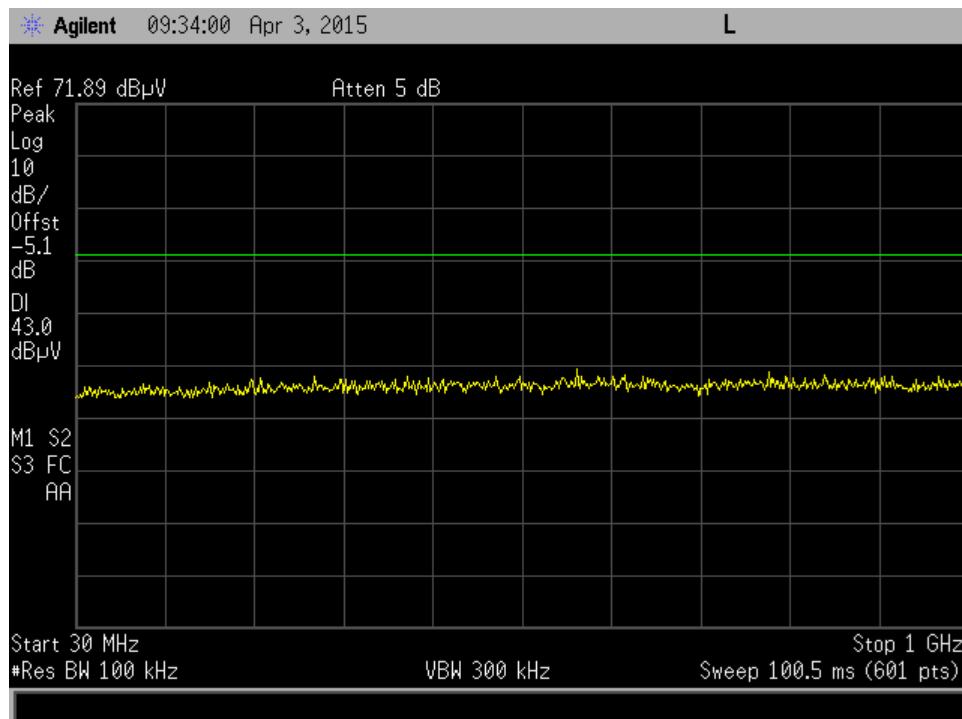


30MHz high ch 1 - 26.5GHz J7

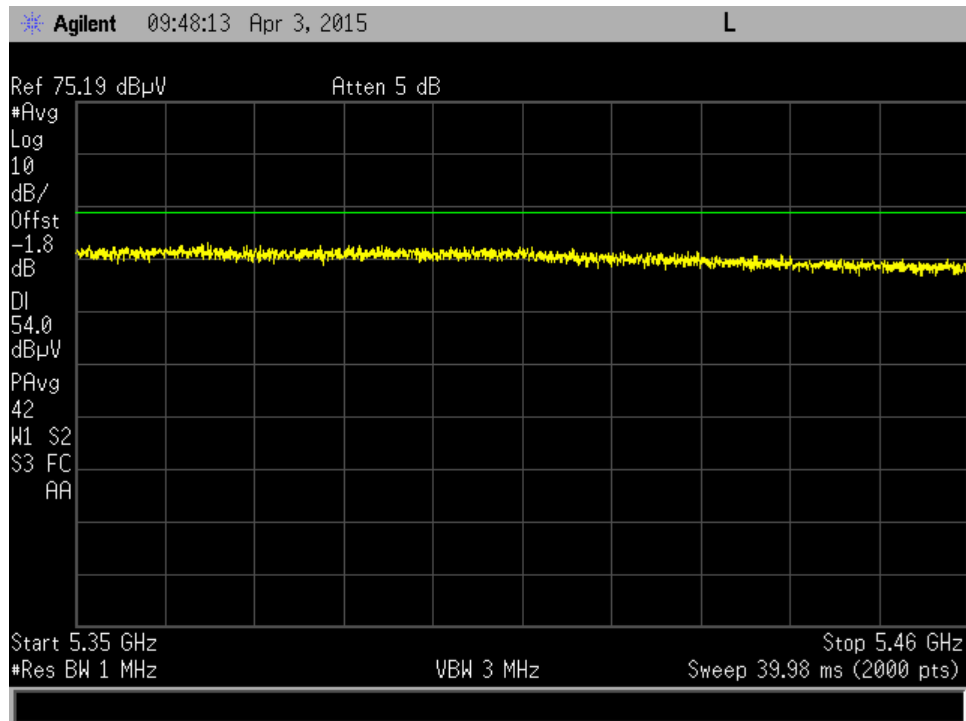




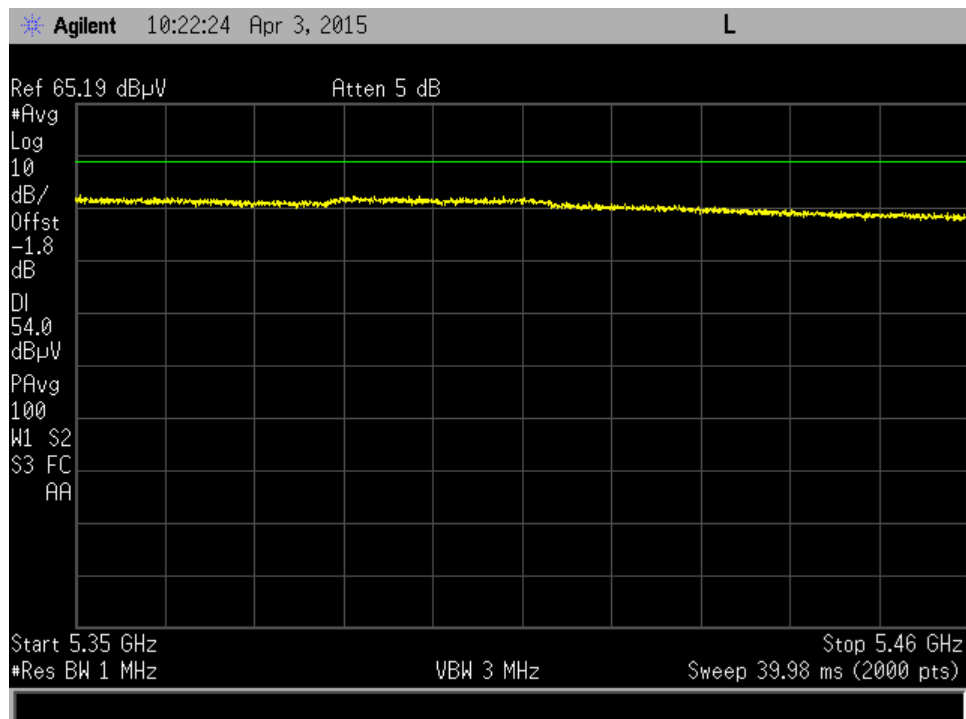
30MHz high ch 30 - 1000MHz J8



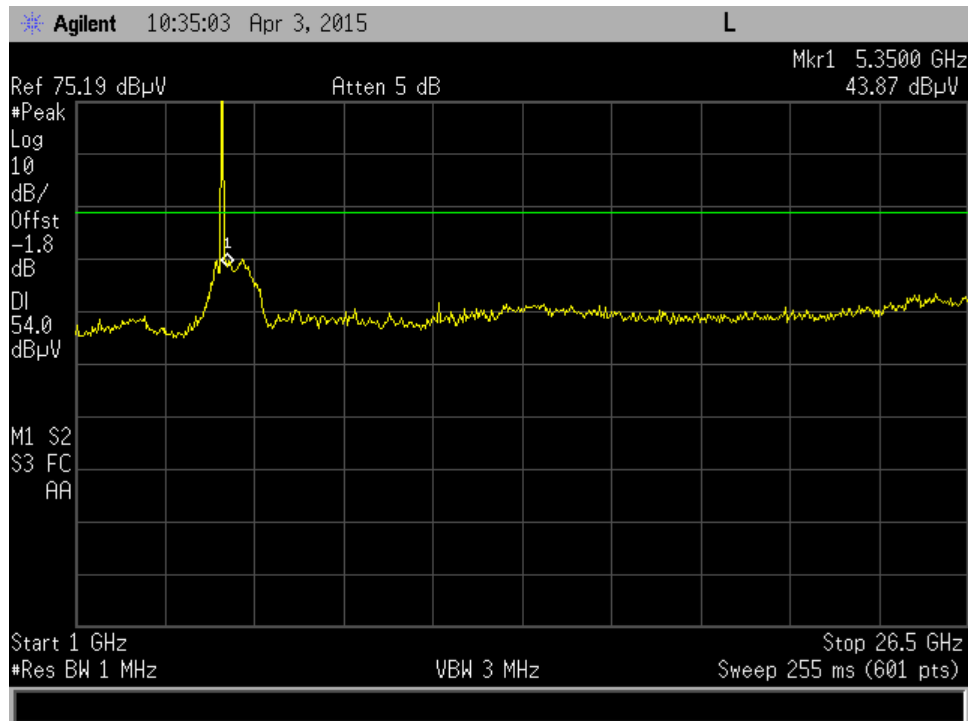
30MHz high ch 30 - 1000MHz J7



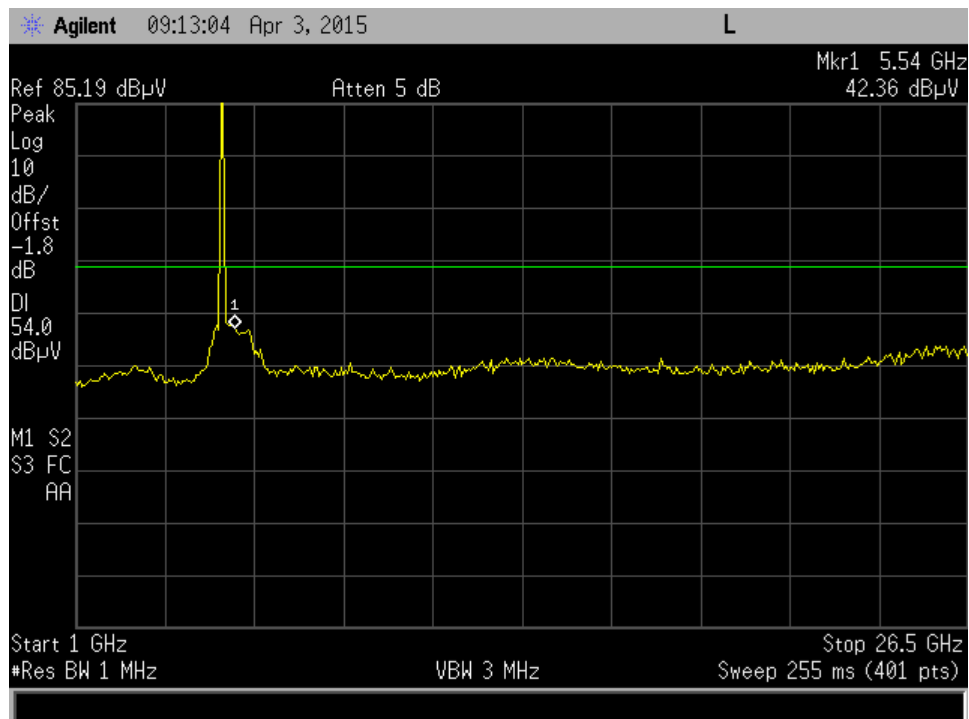
30MHz high ch 5350 - 5460GHz J7



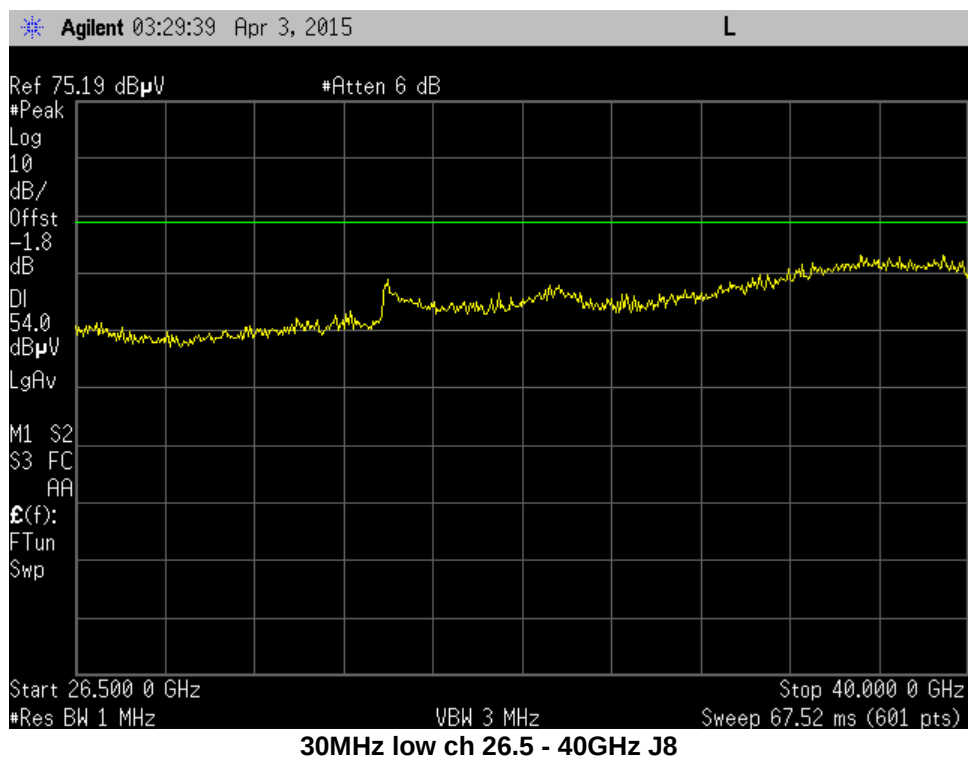
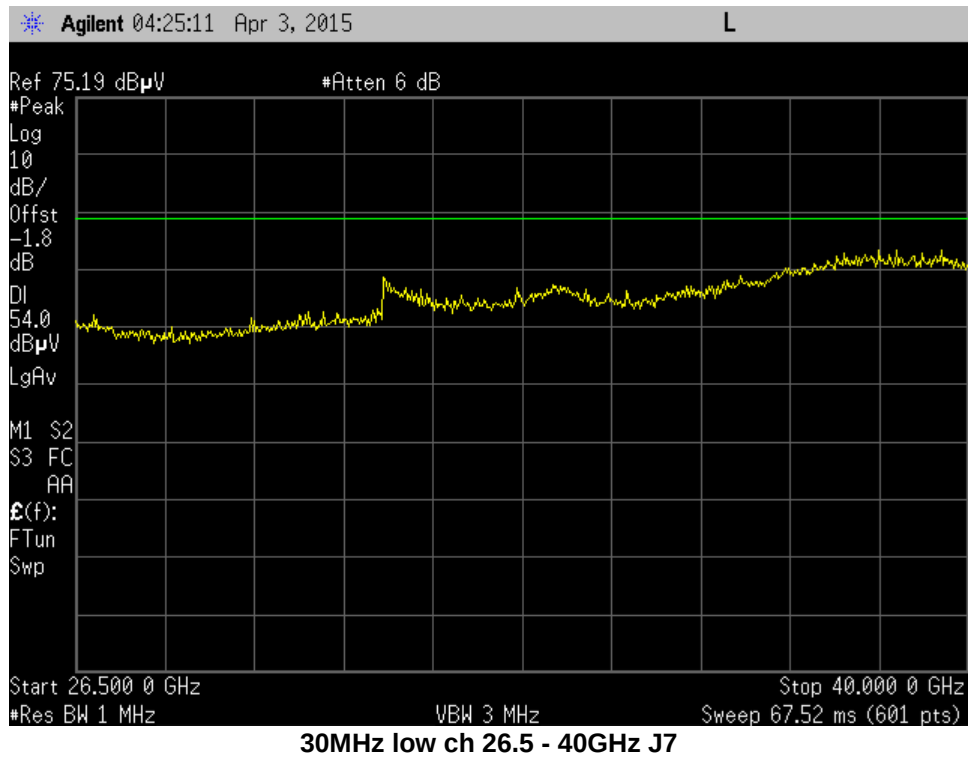
30MHz high ch 5350 - 5460GHz J8

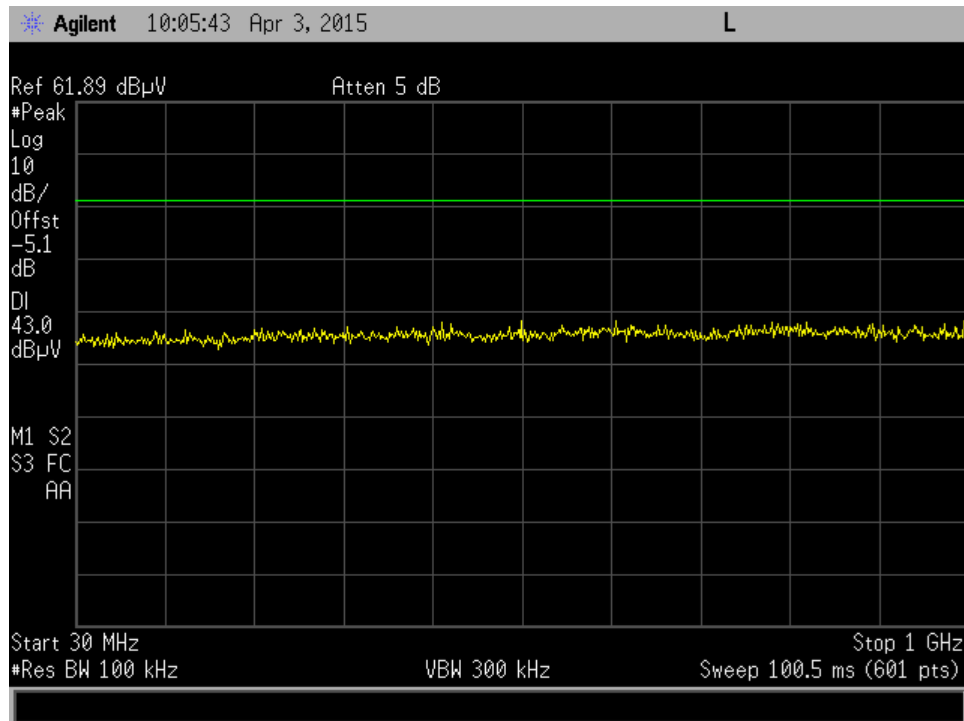


30MHz low ch 1 - 26.5GHz J8

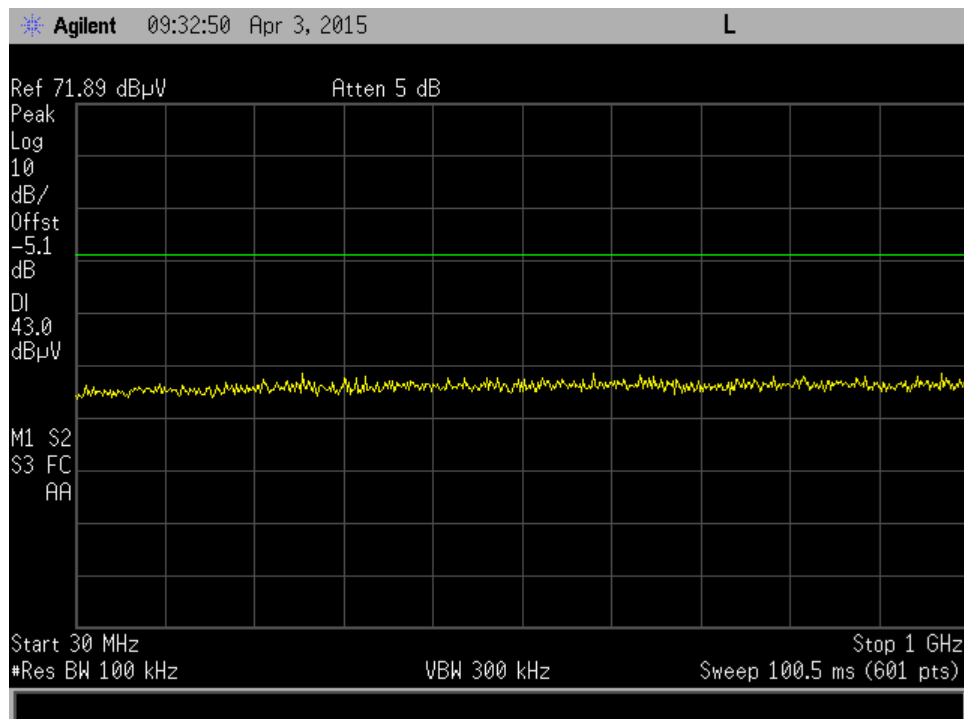


30MHz low ch 1 - 26.5GHz J7

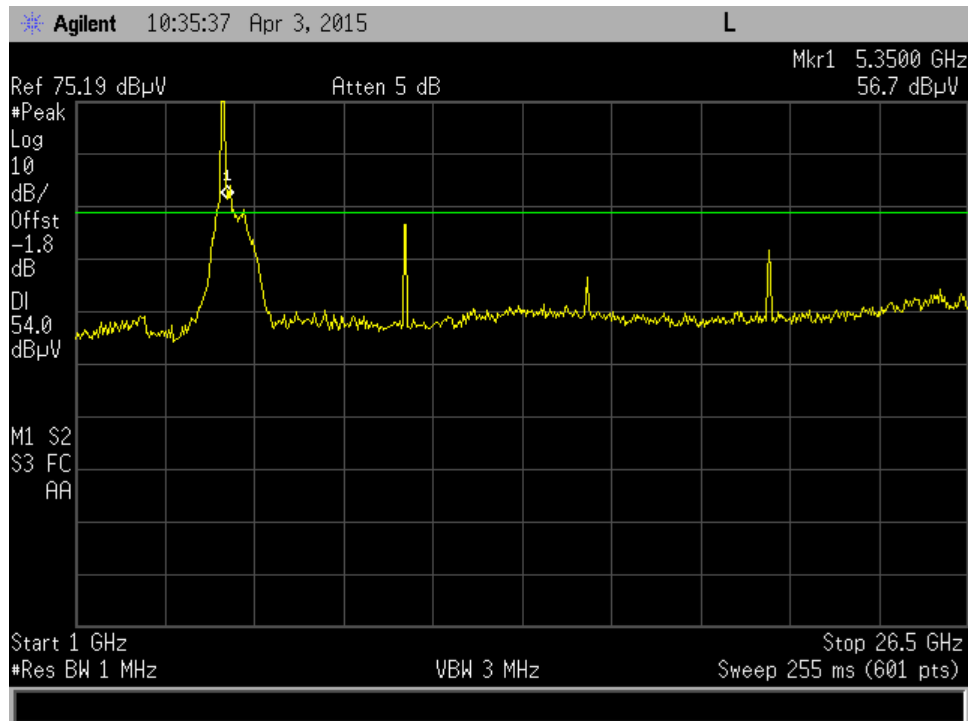




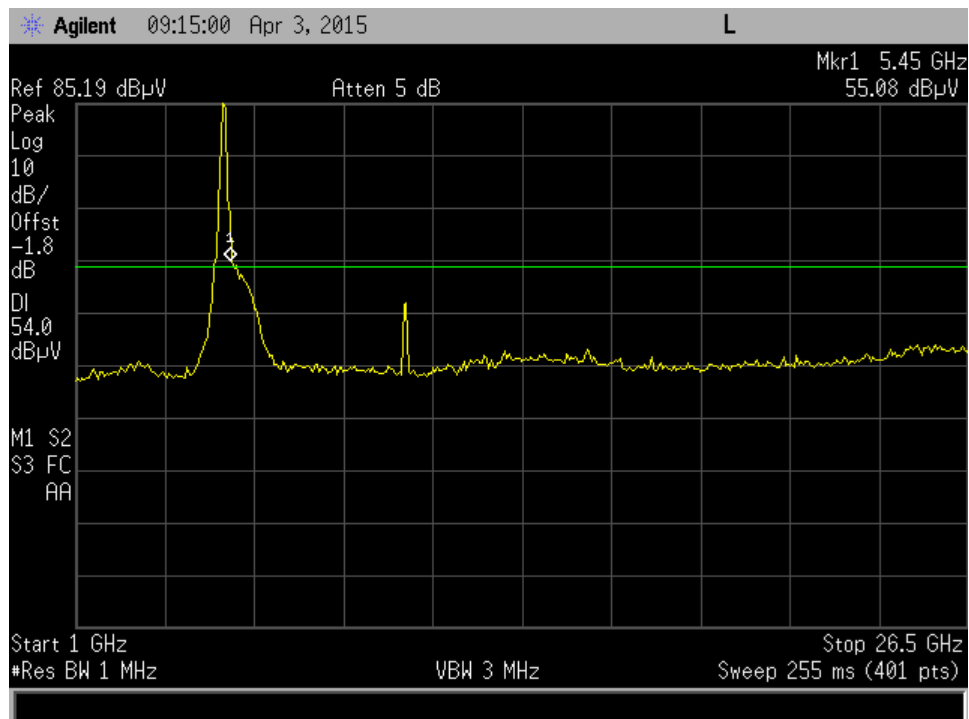
30MHz low ch 30 - 1000MHz J8



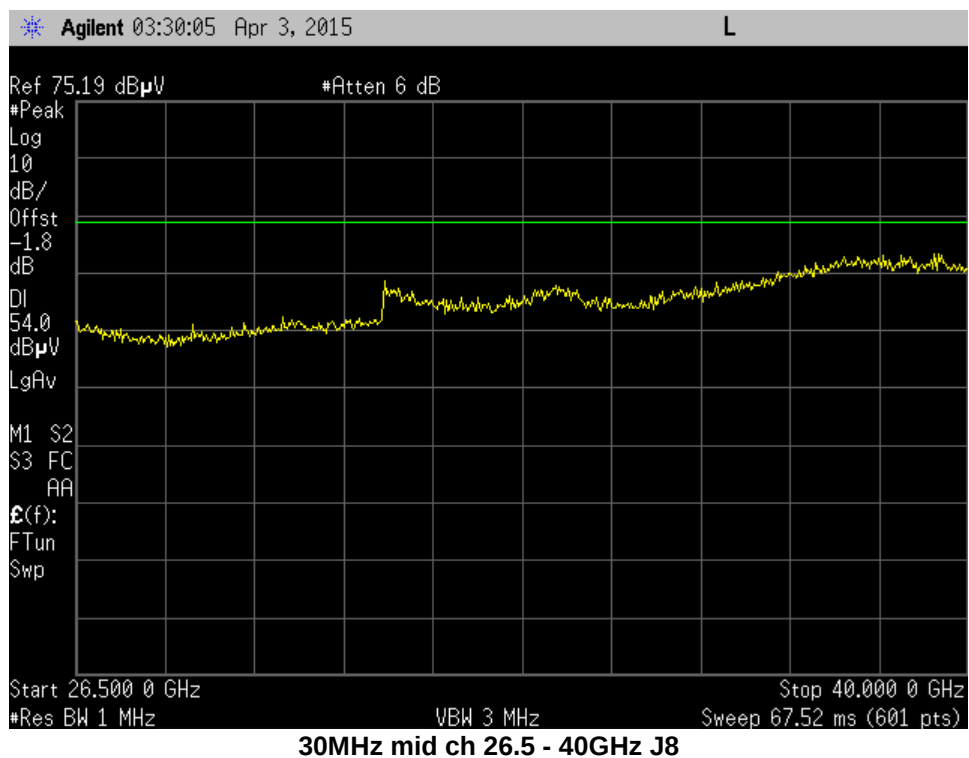
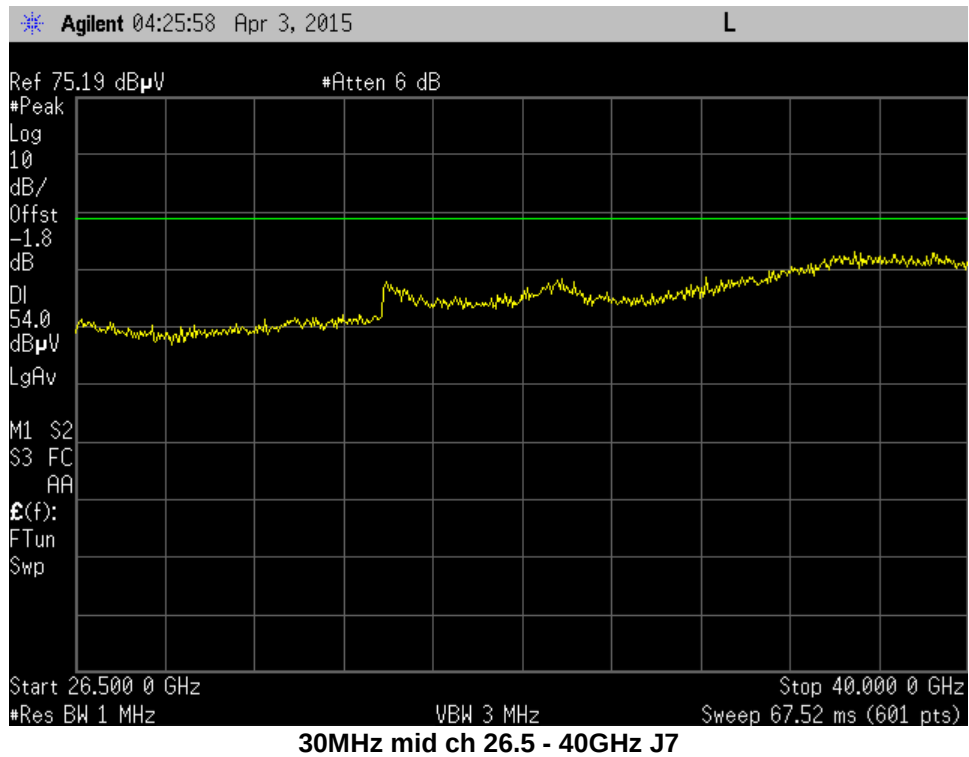
30MHz low ch 30 - 1000MHz J7

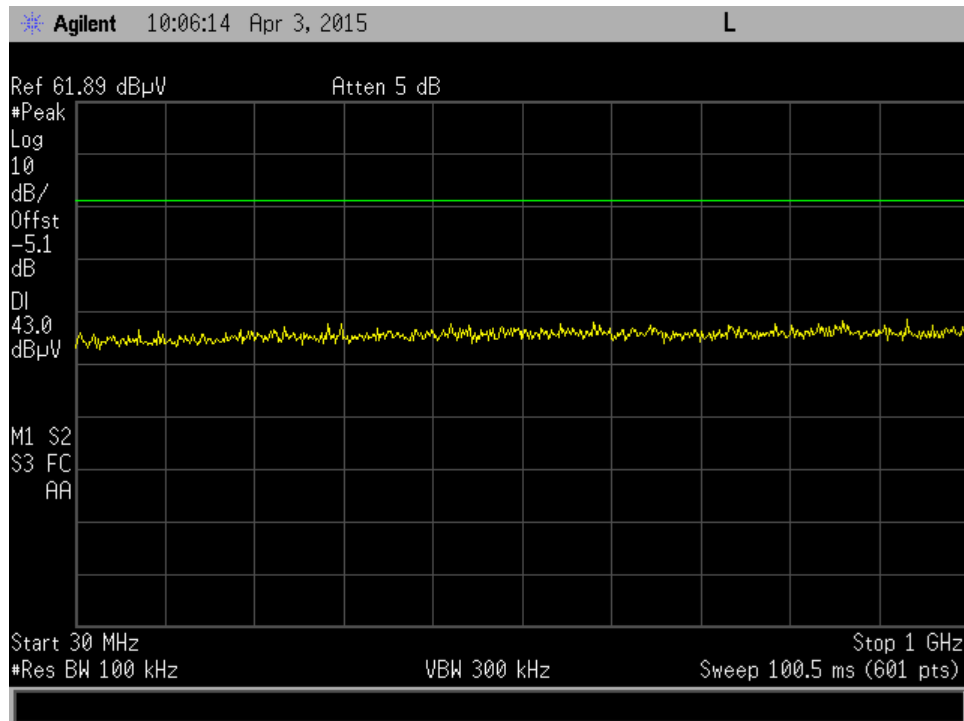


30MHz mid ch 1 - 26.5GHz J8

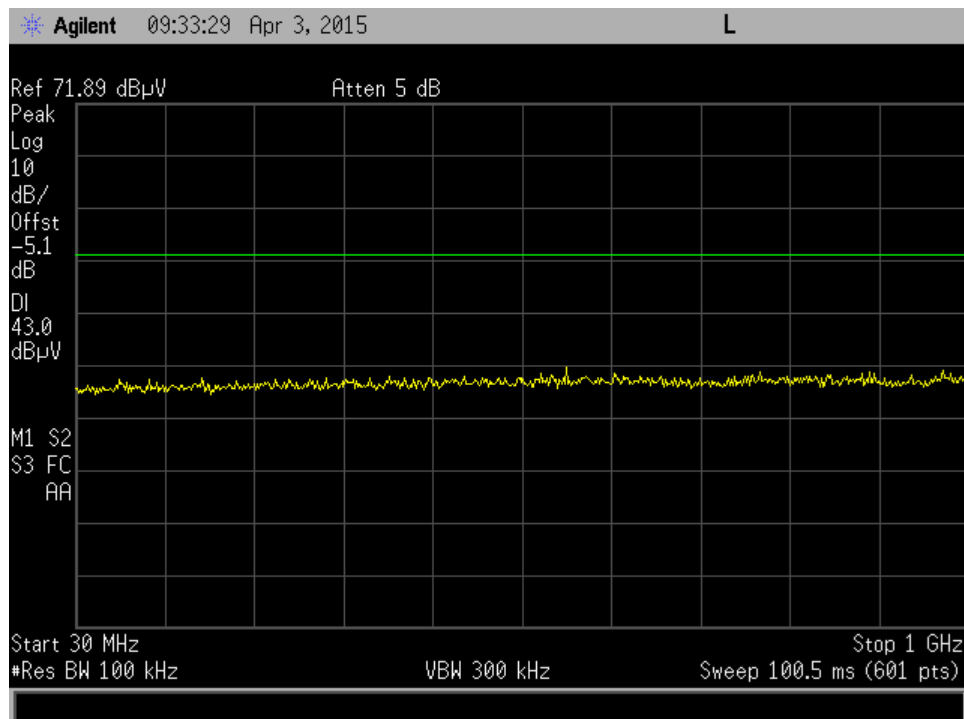


30MHz mid ch 1 - 26.5GHz J7

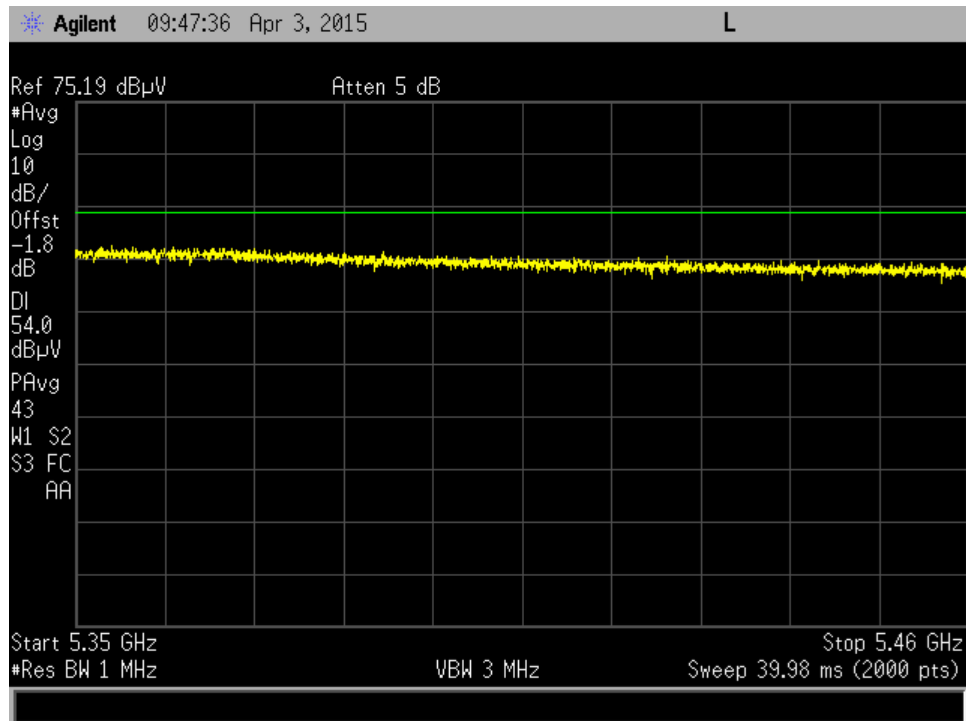




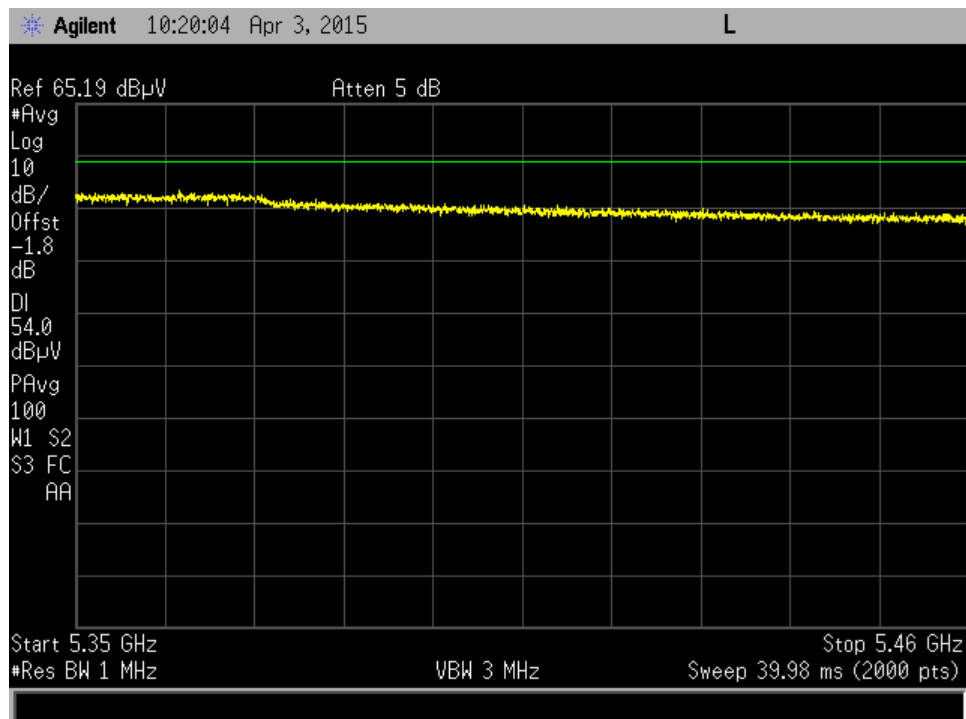
30MHz mid ch 30 - 1000MHz J8



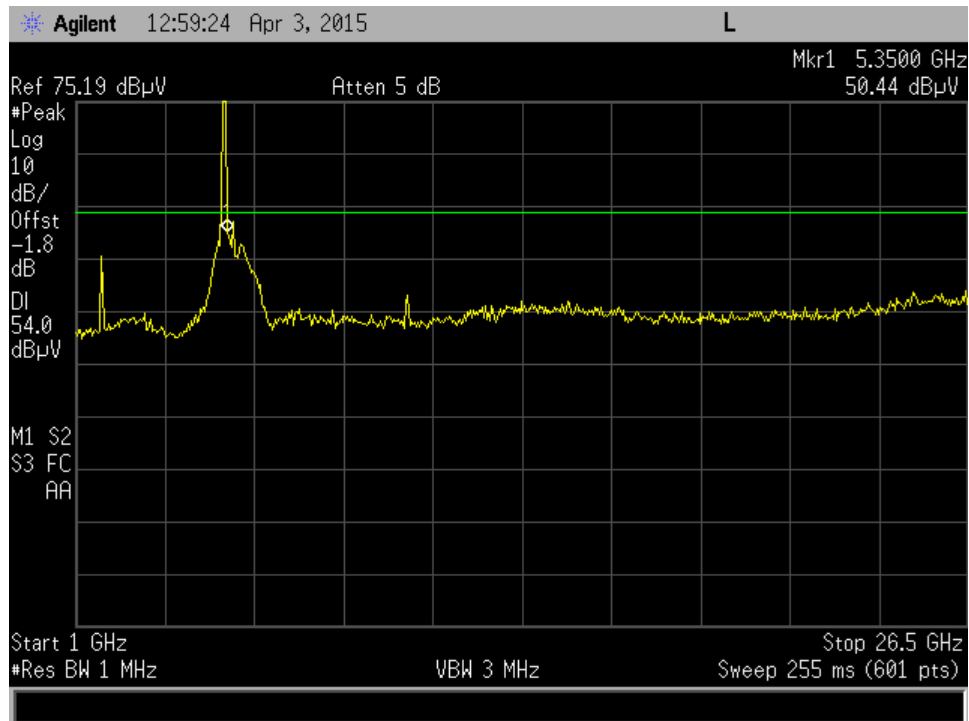
30MHz mid ch 30 - 1000MHz J7



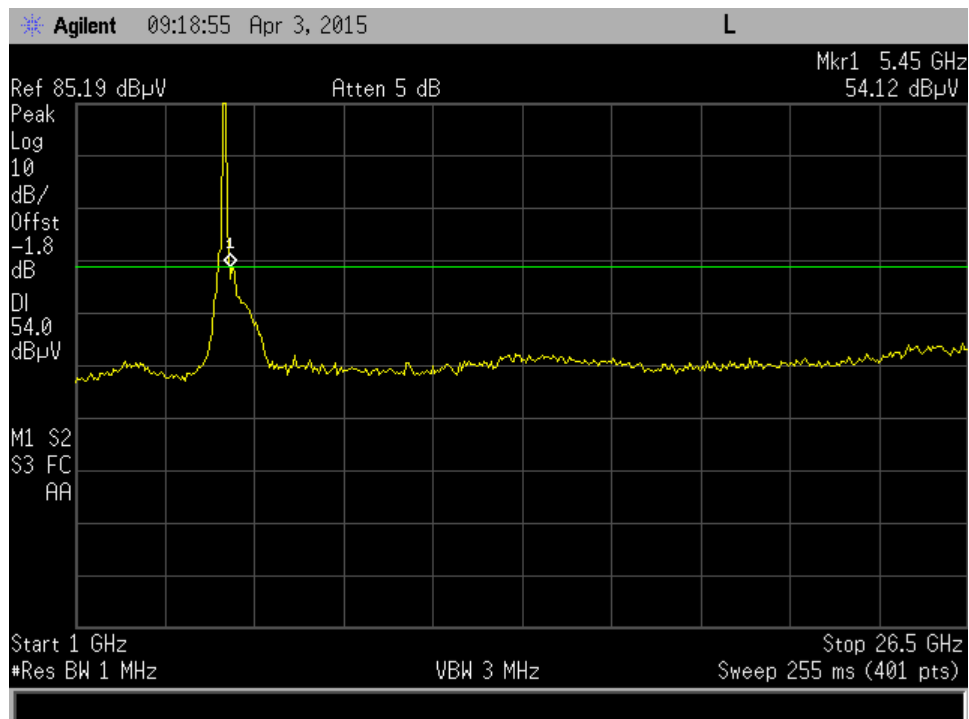
30MHz mid ch 5350 - 5460GHz J7



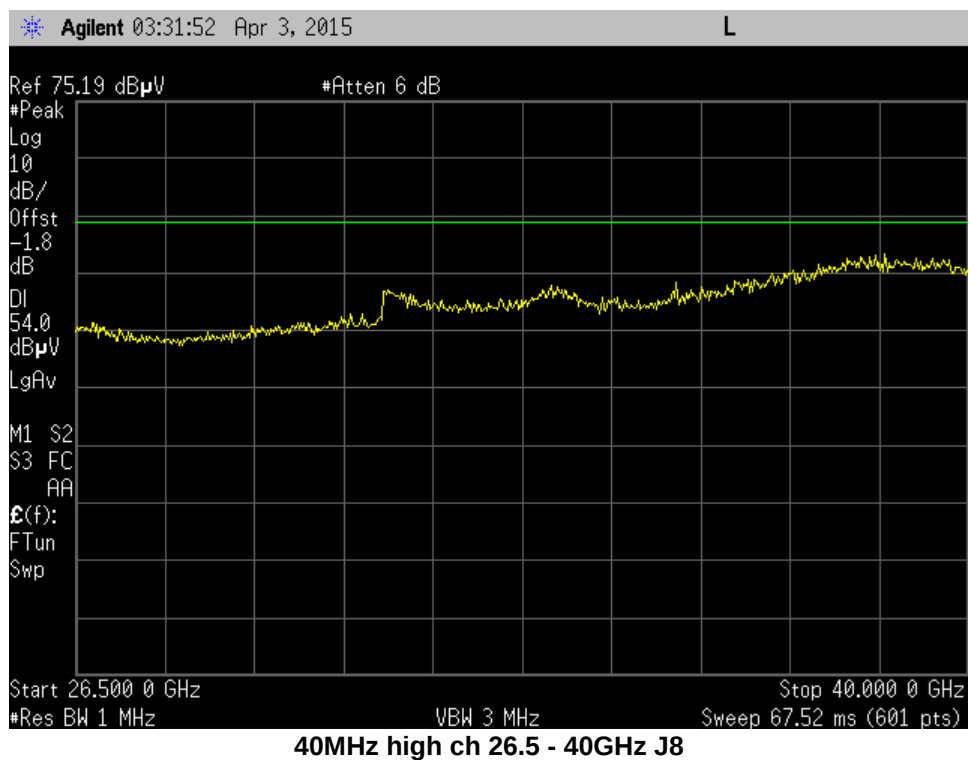
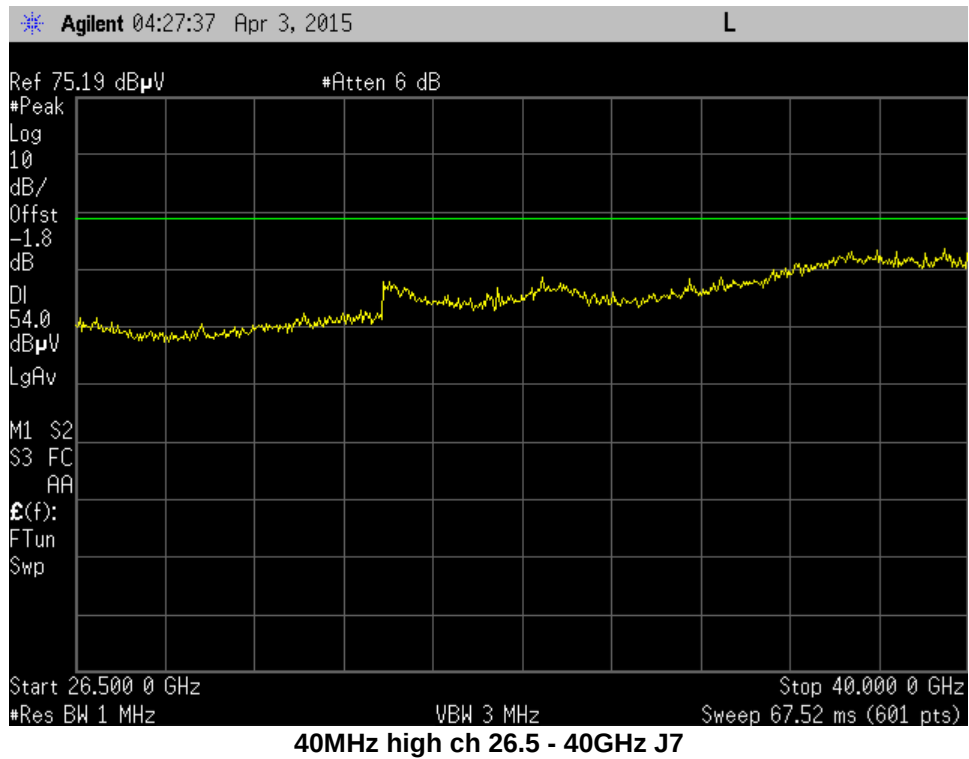
30MHz mid ch 5350 - 5460GHz J8

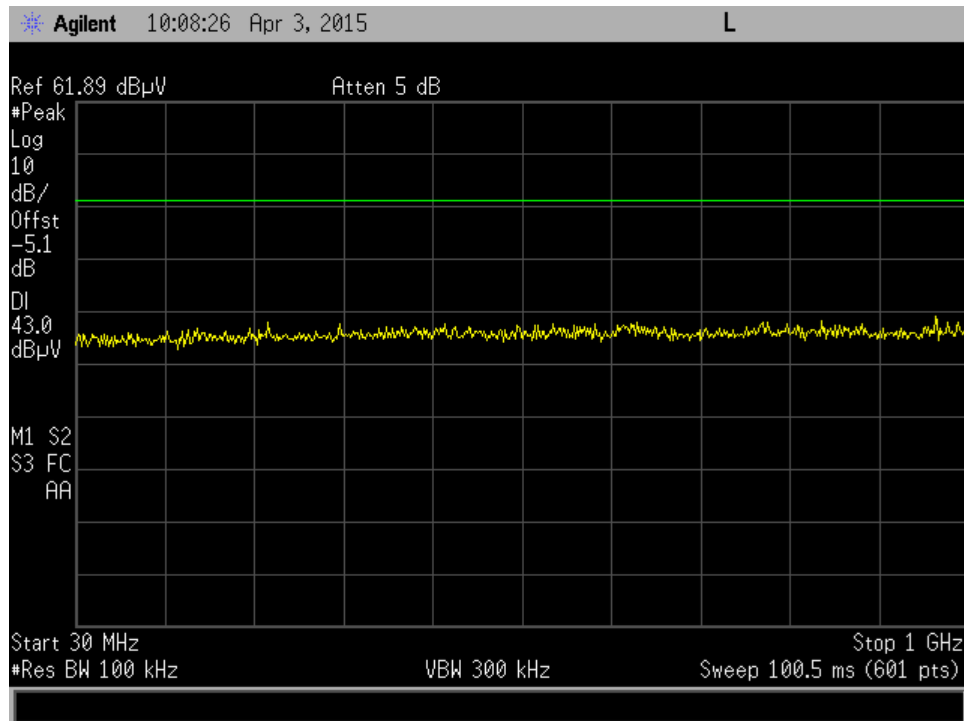


40MHz high ch 1 - 26.5GHz J8

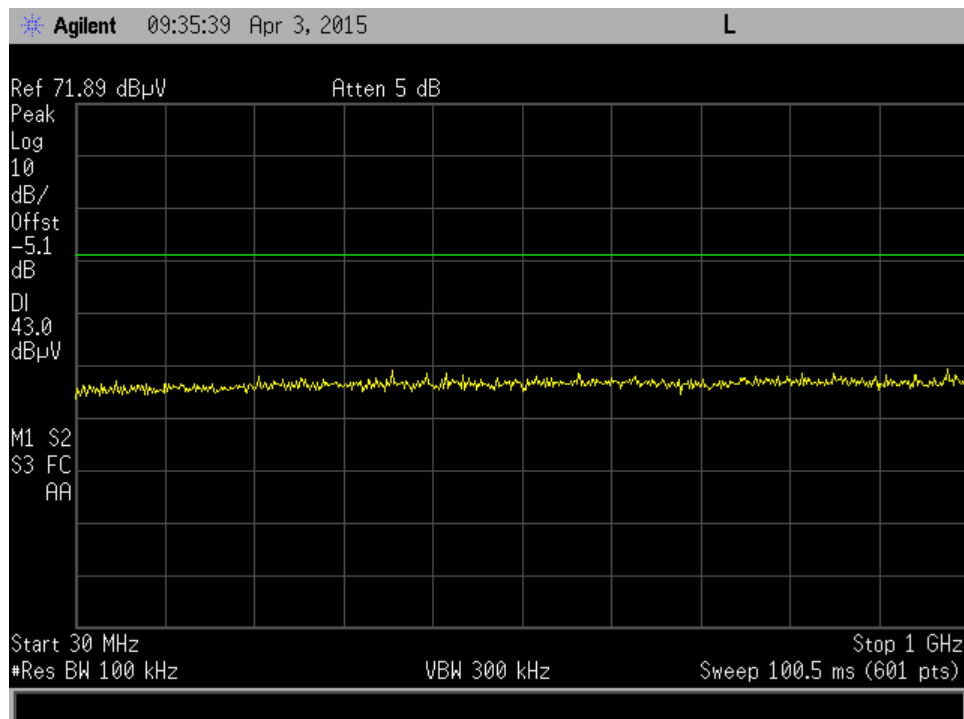


40MHz high ch 1 - 26.5GHz J7

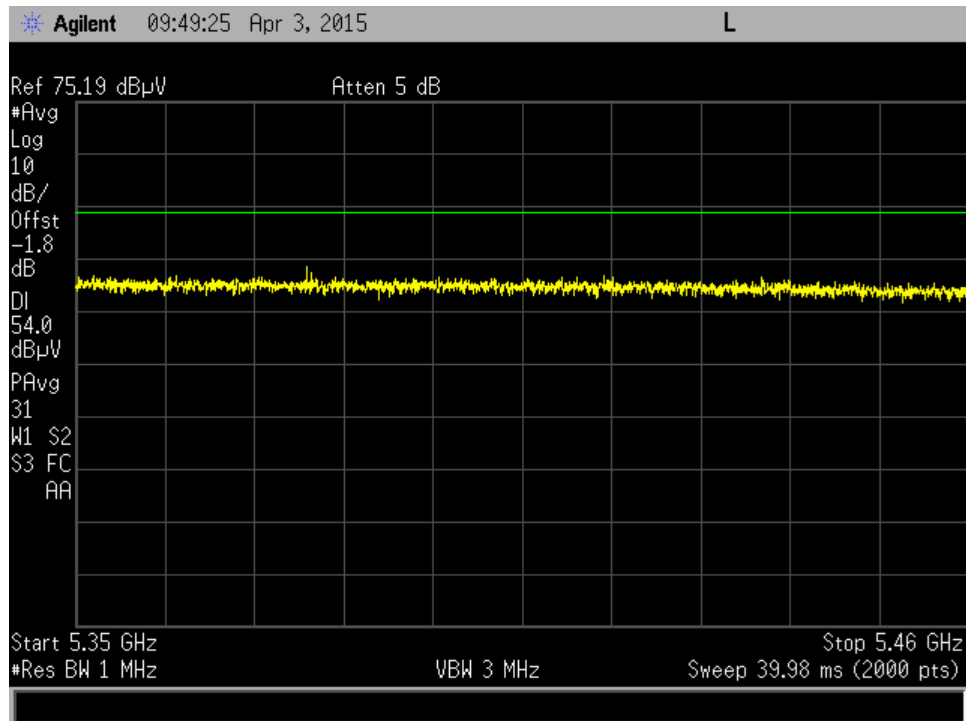




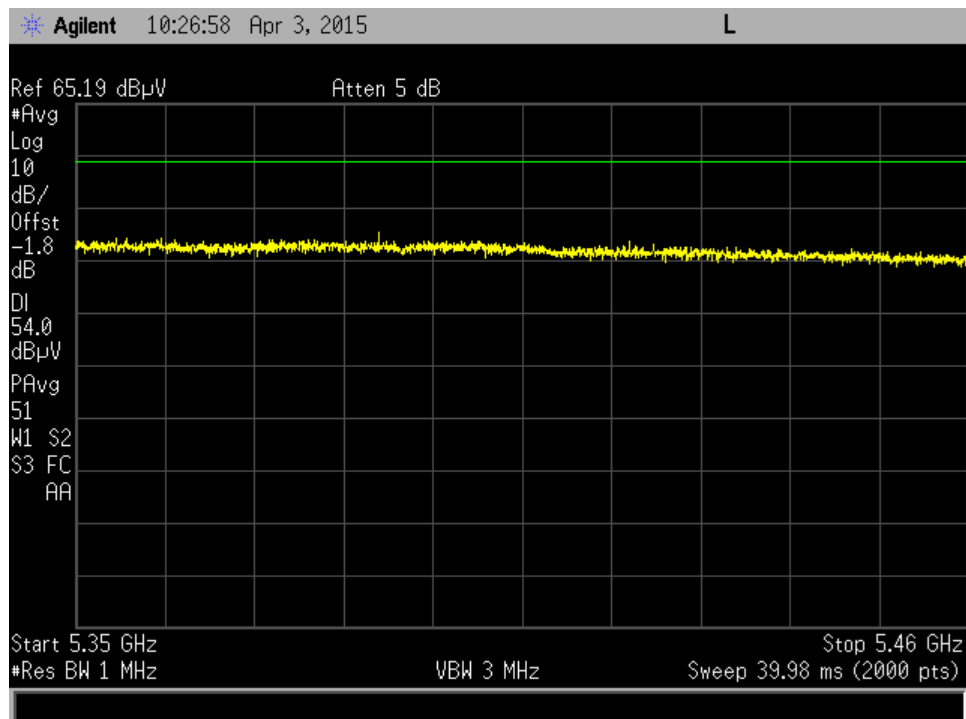
40MHz high ch 30 - 1000MHz J8



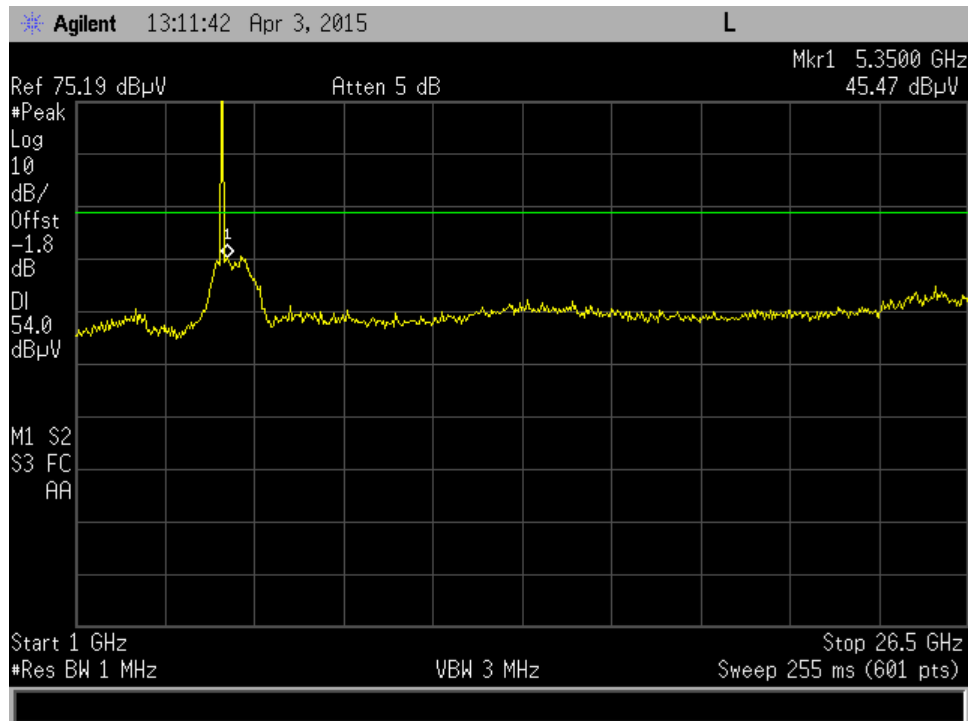
40MHz high ch 30 - 1000MHz J7



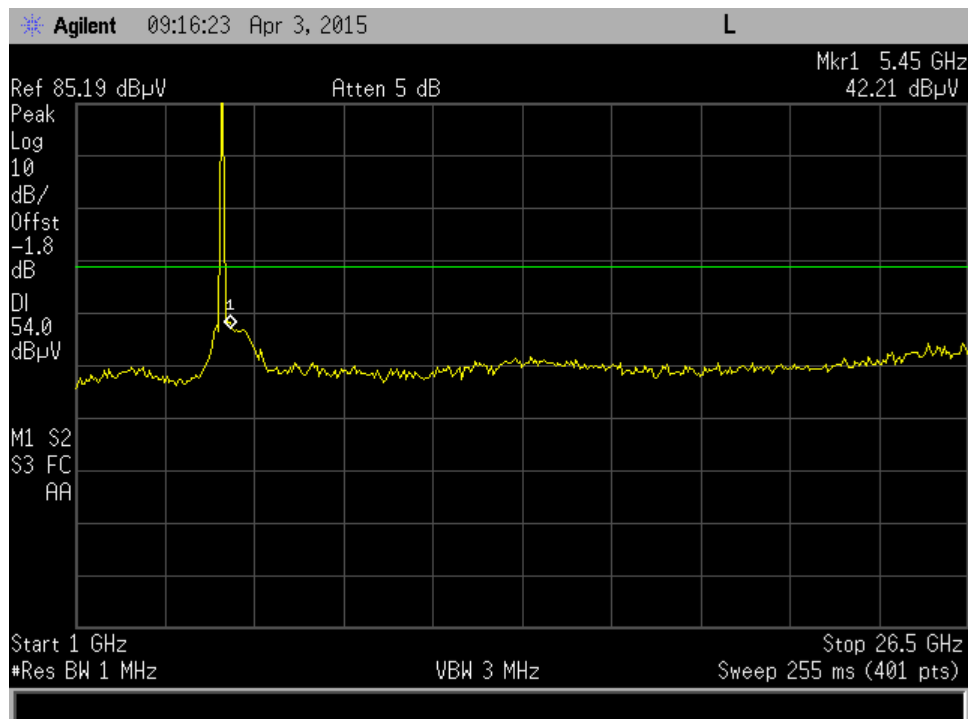
40MHz high ch 5350 - 5460GHz J7



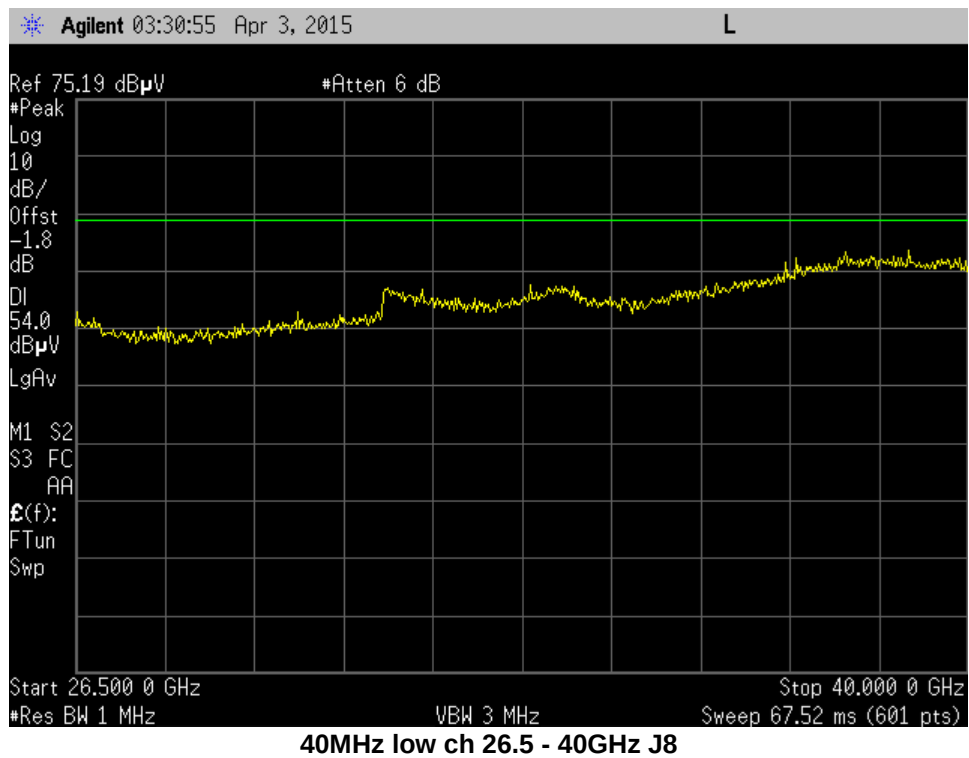
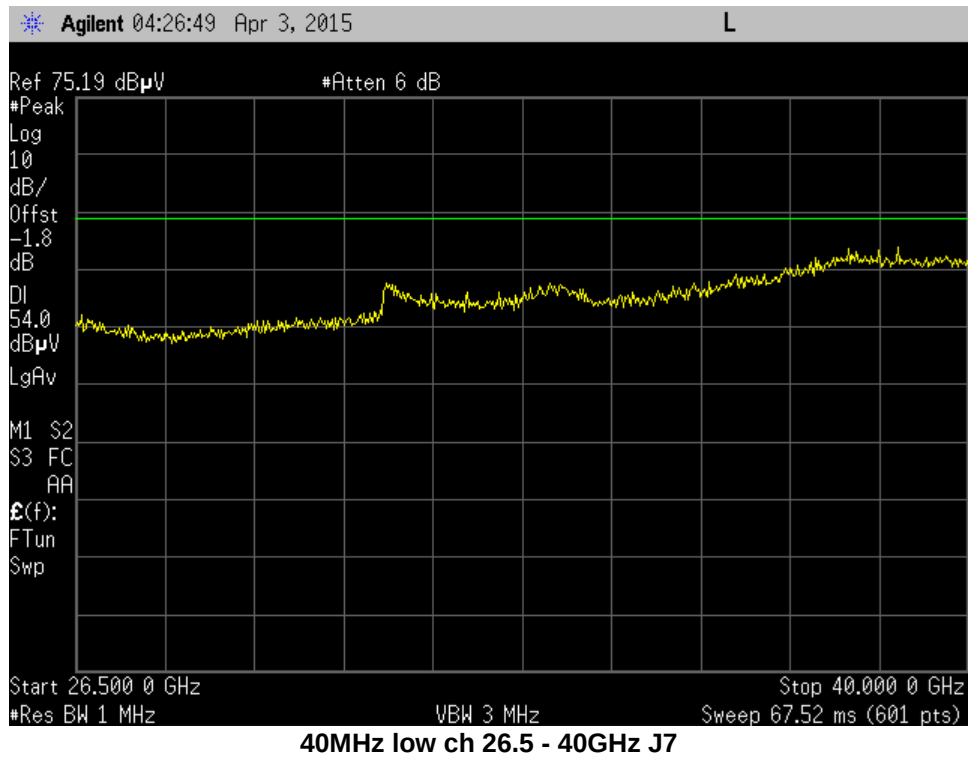
40MHz high ch 5350 - 5460GHz J8

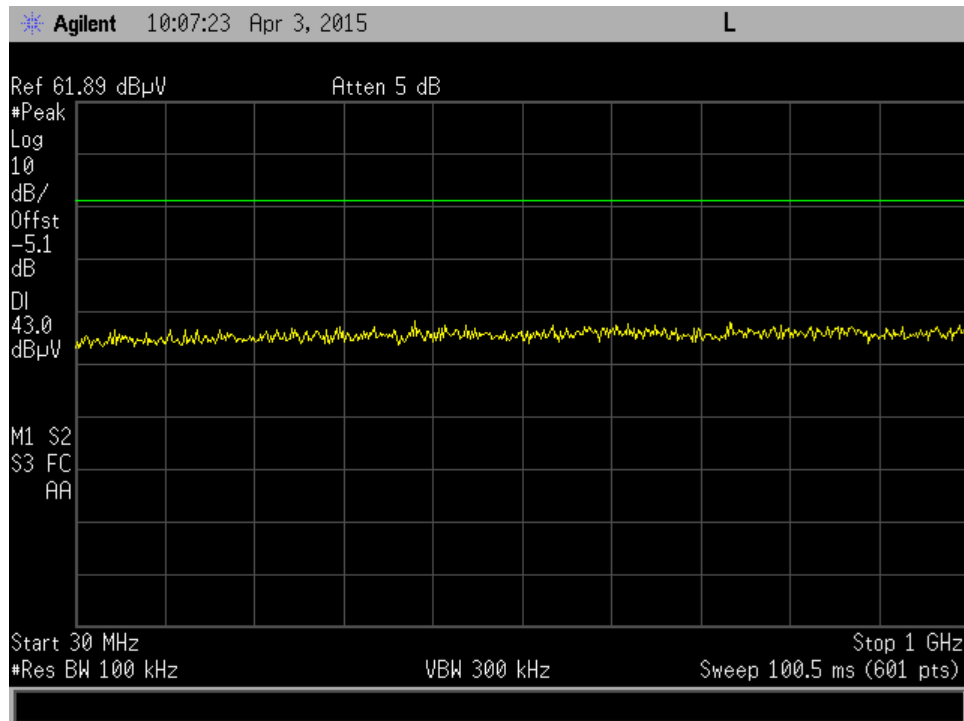


40MHz low ch 1 - 26.5GHz J8

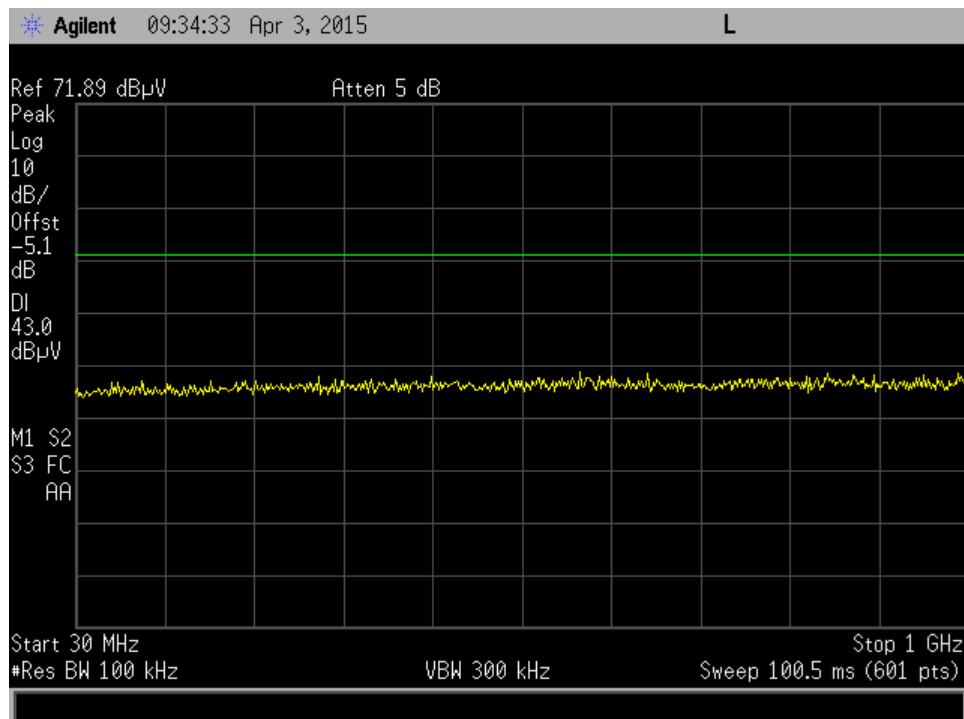


40MHz low ch 1 - 26.5GHz J7

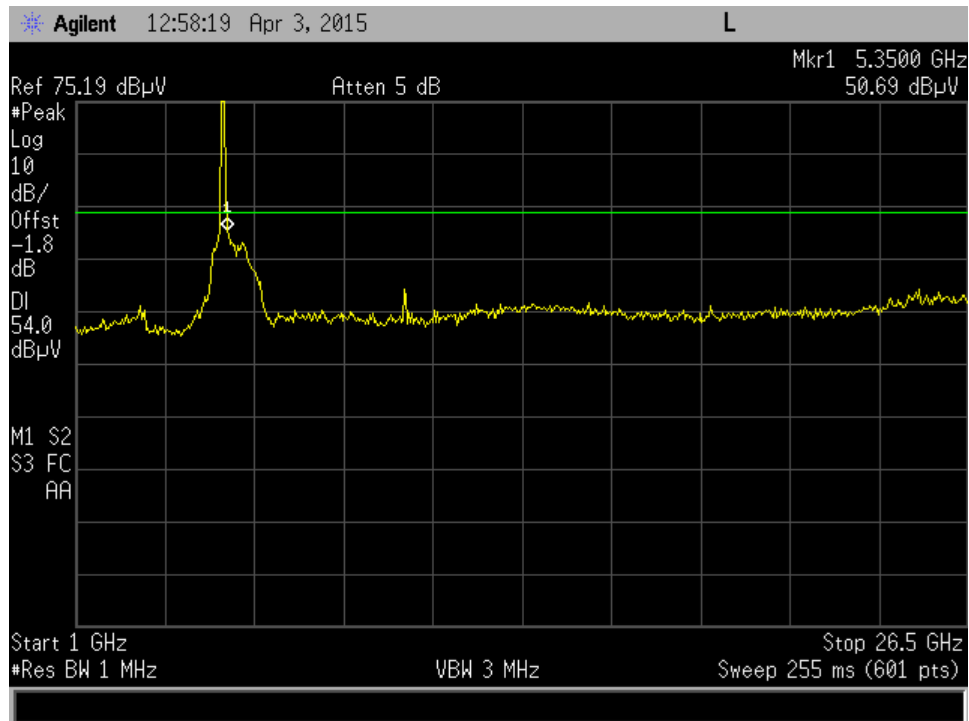




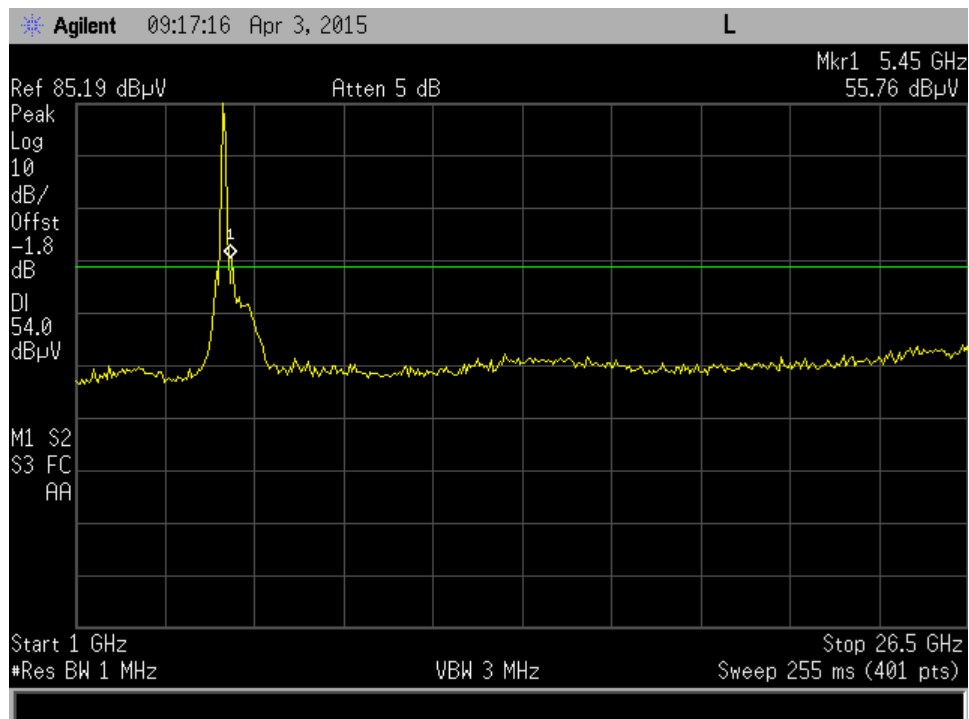
40MHz low ch 30 - 1000MHz J8



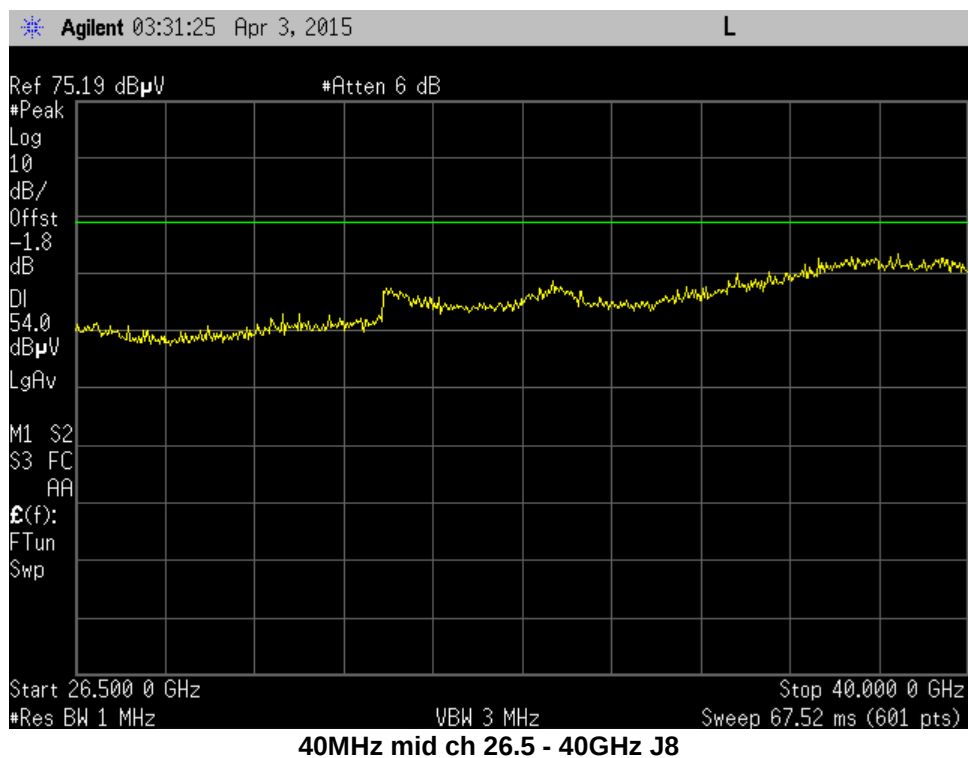
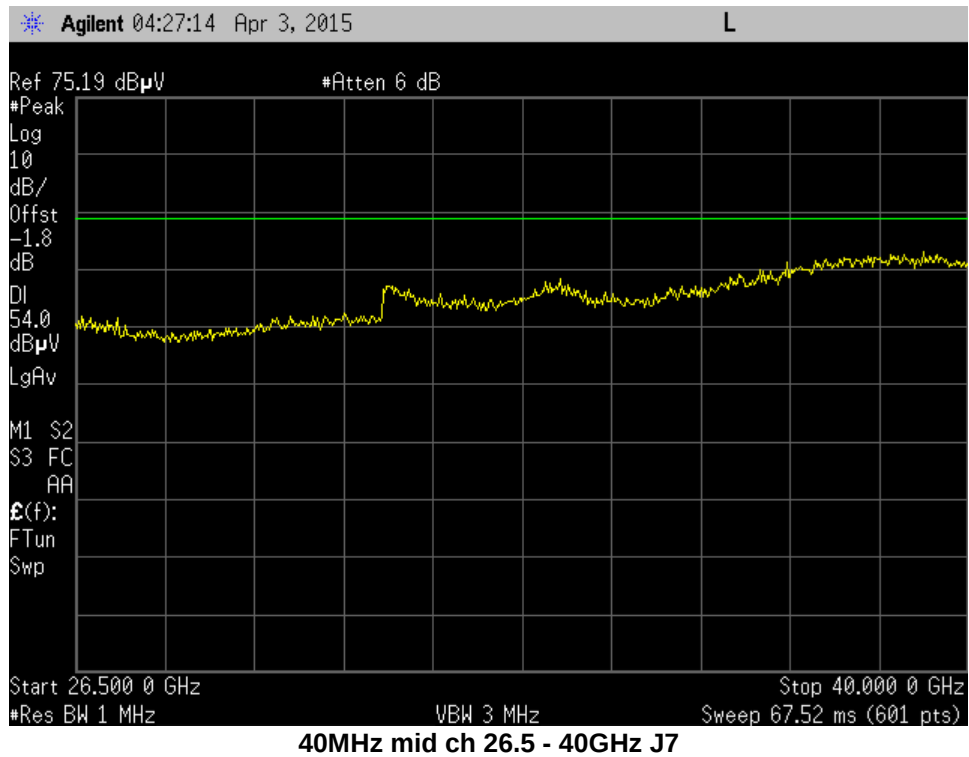
40MHz low ch 30 - 1000MHz J7

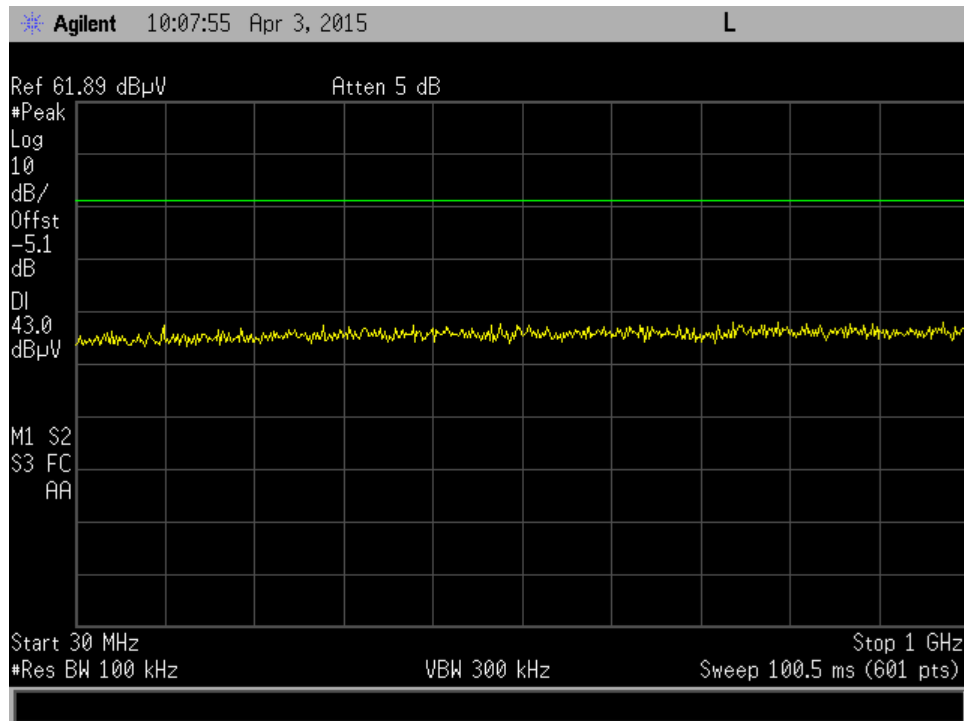


40MHz mid ch 1 - 26.5GHz J8

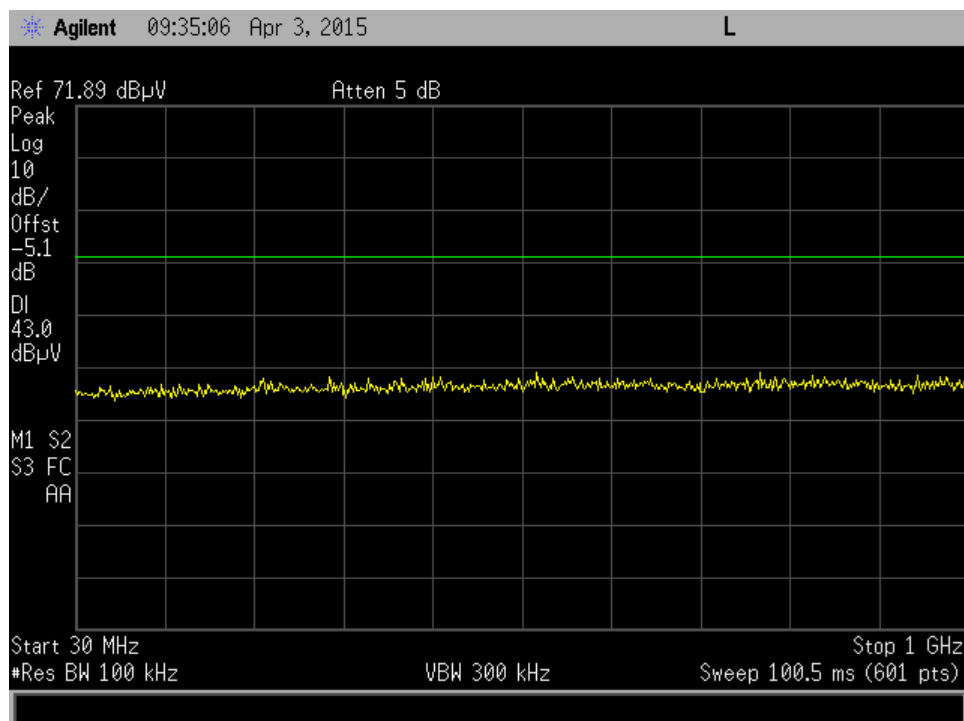


40MHz mid ch 1 - 26.5GHz J7

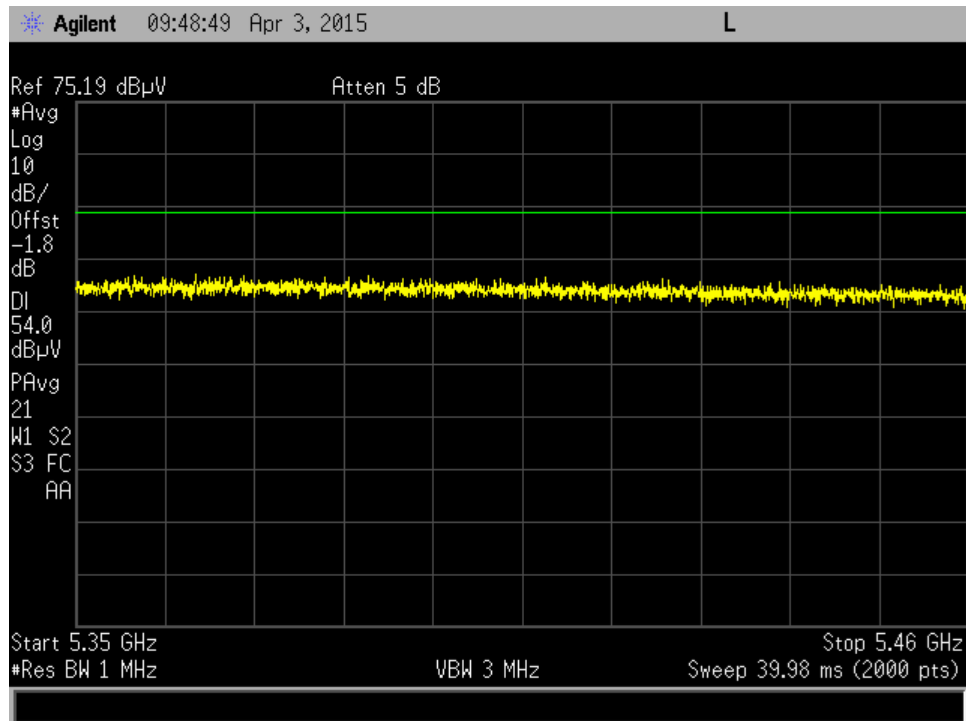




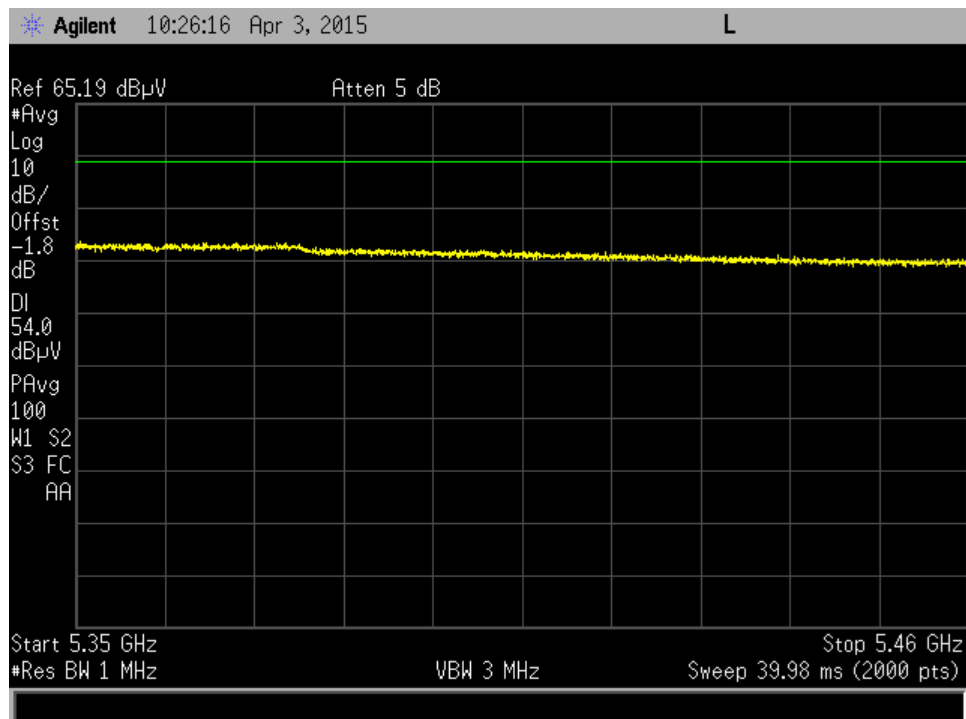
40MHz mid ch 30 - 1000MHz J8



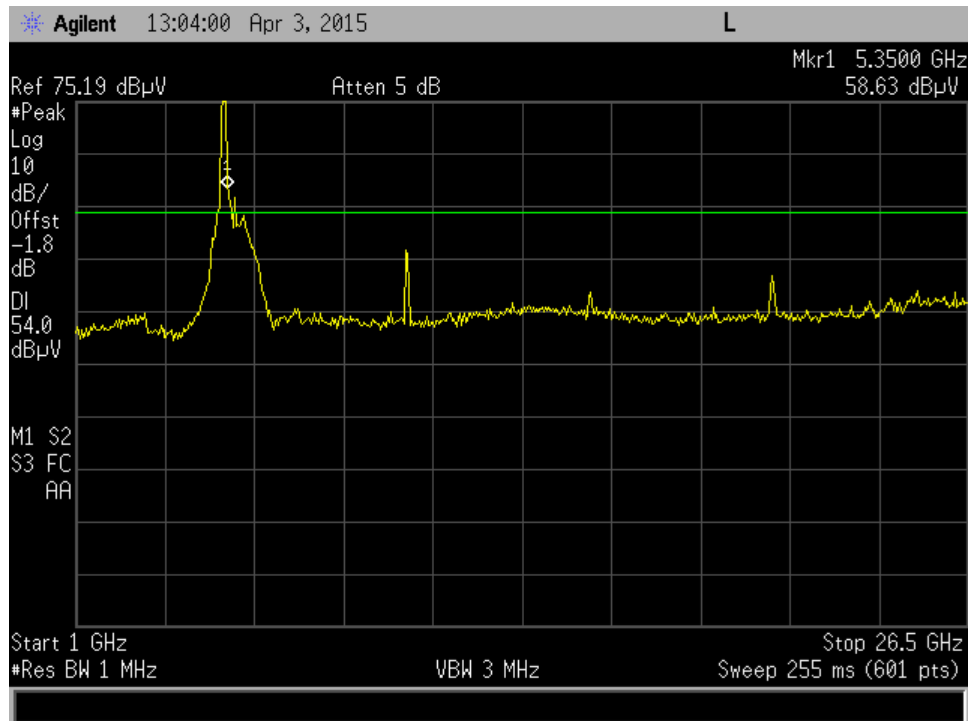
40MHz mid ch 30 - 1000MHz J7



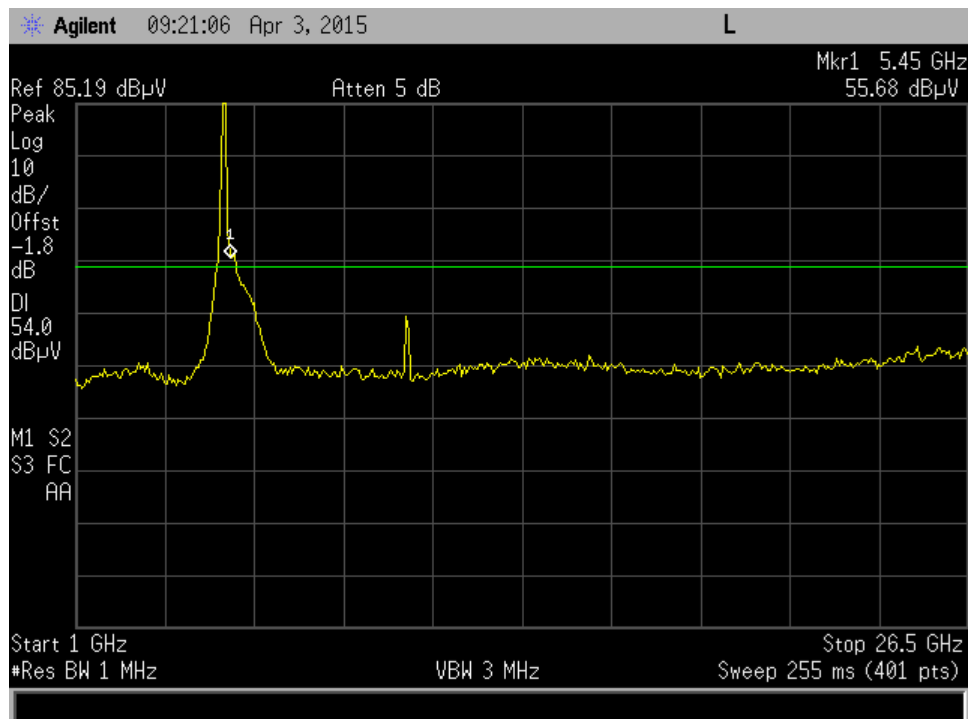
40MHz mid ch 5350 - 5460GHz J7



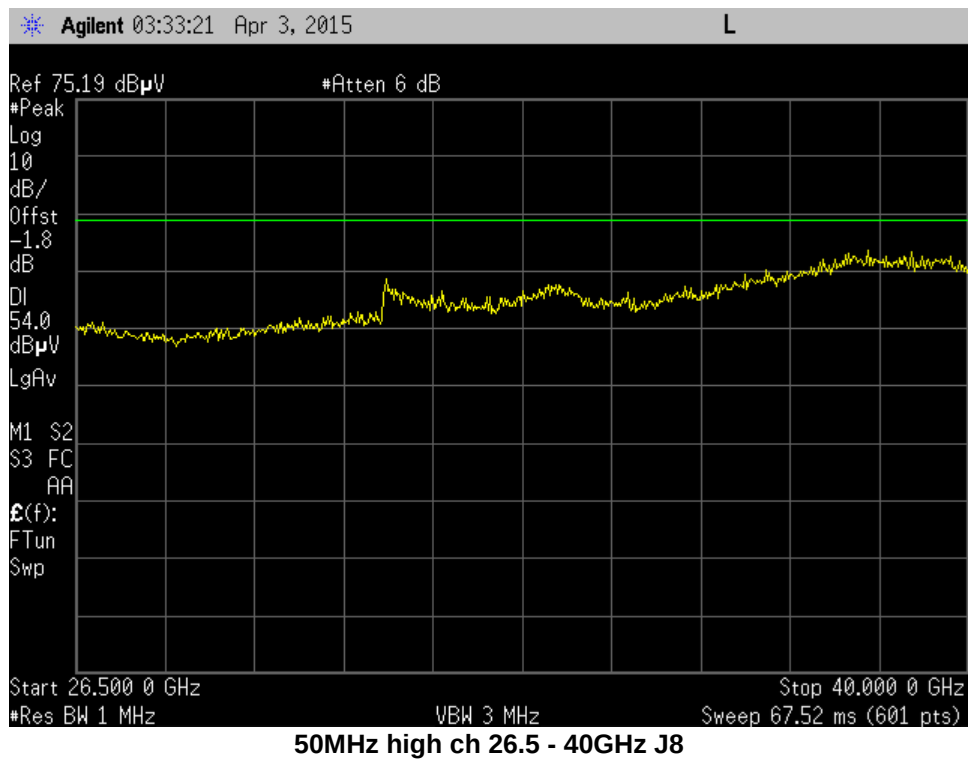
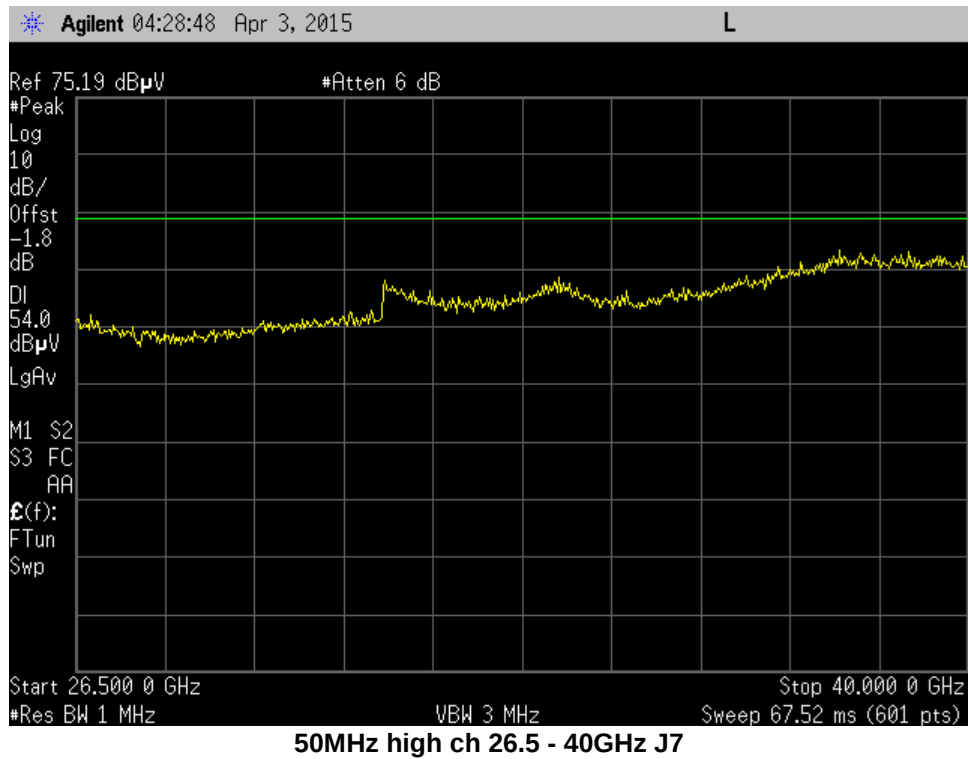
40MHz mid ch 5350 - 5460GHz J8

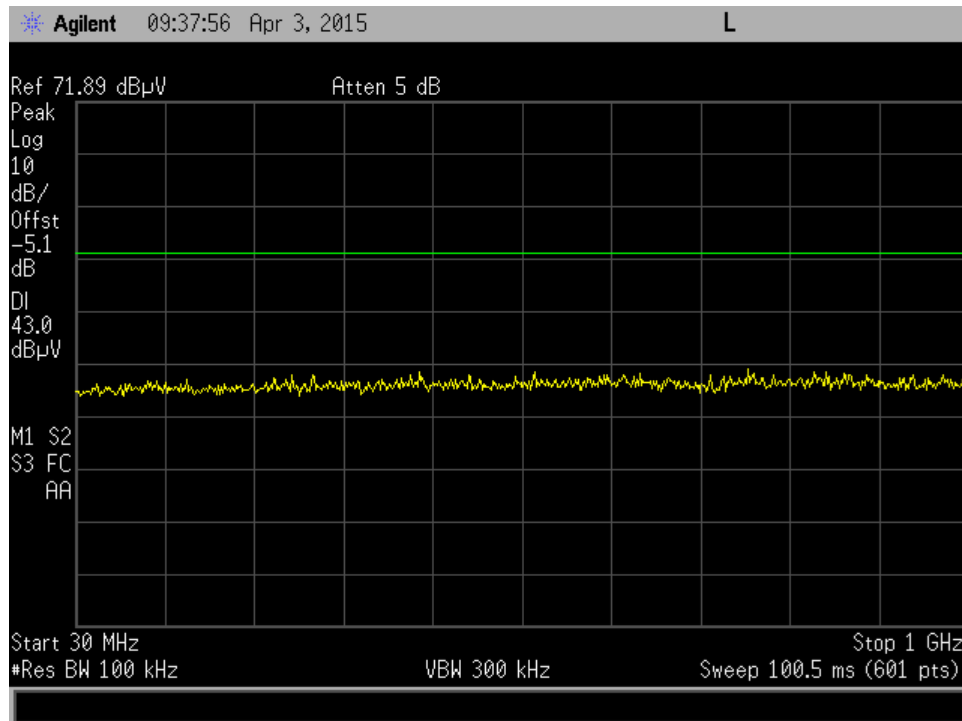


50MHz high ch 1 - 26.5GHz J8

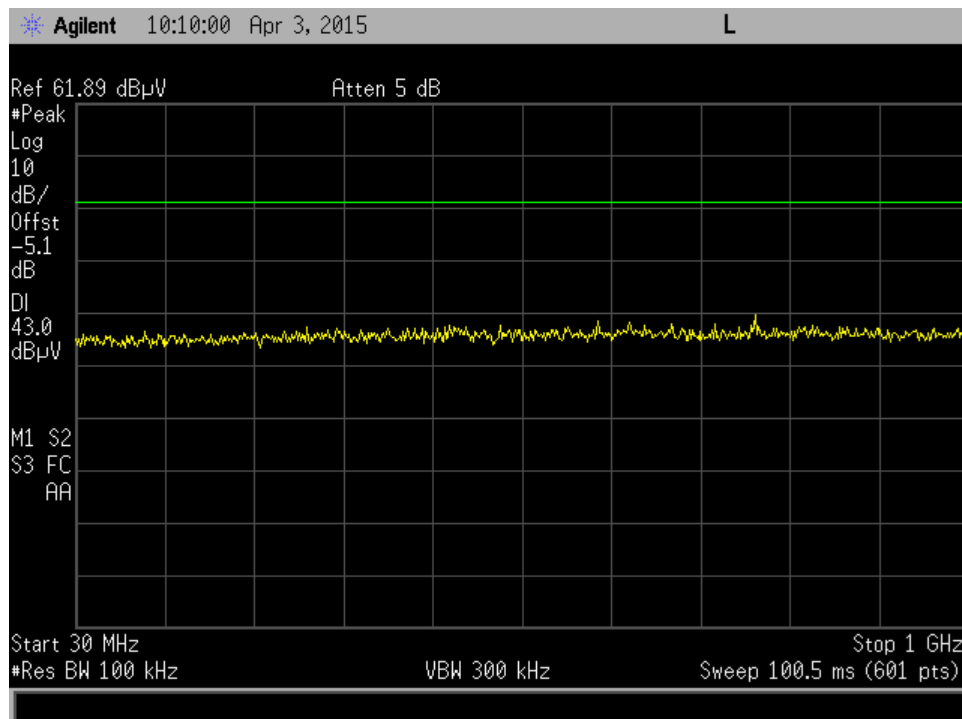


50MHz high ch 1 - 26.5GHz J7

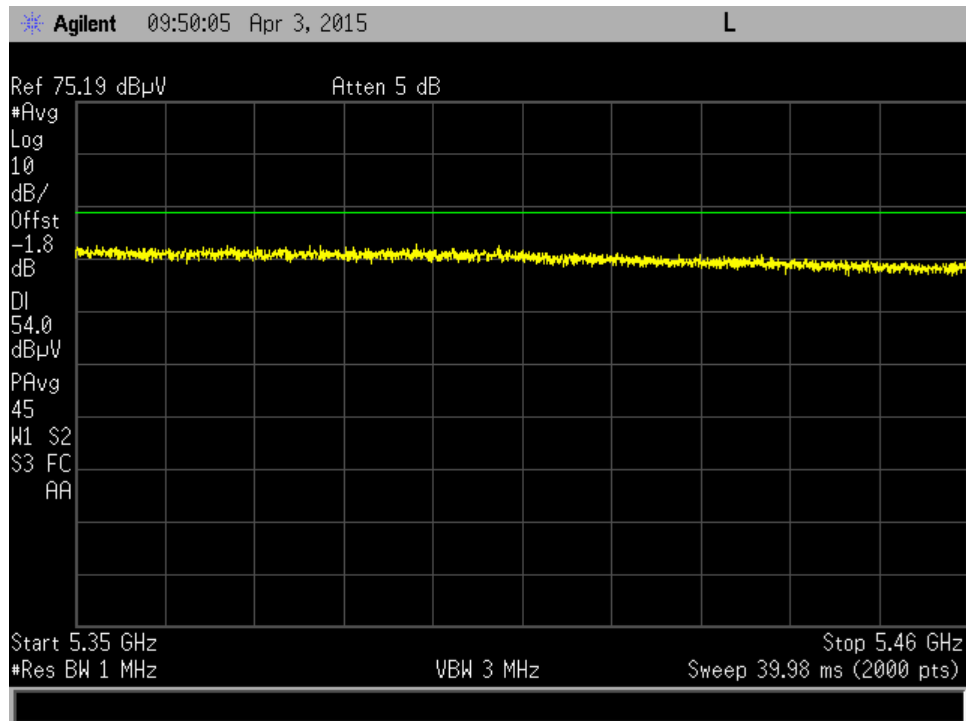




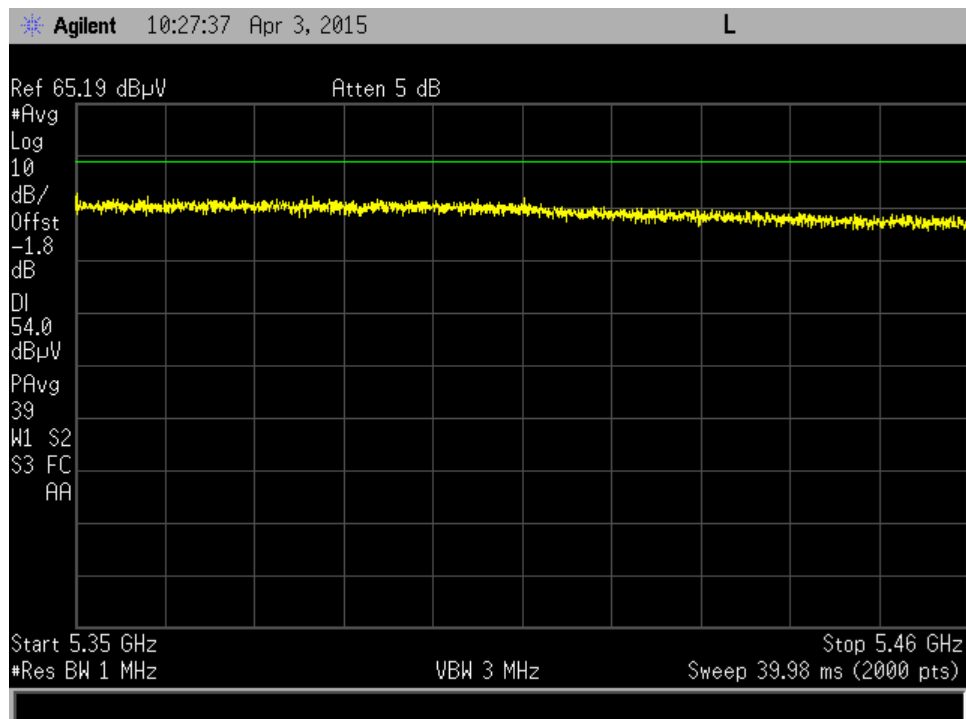
50MHz high ch 30 - 1000MHz J7



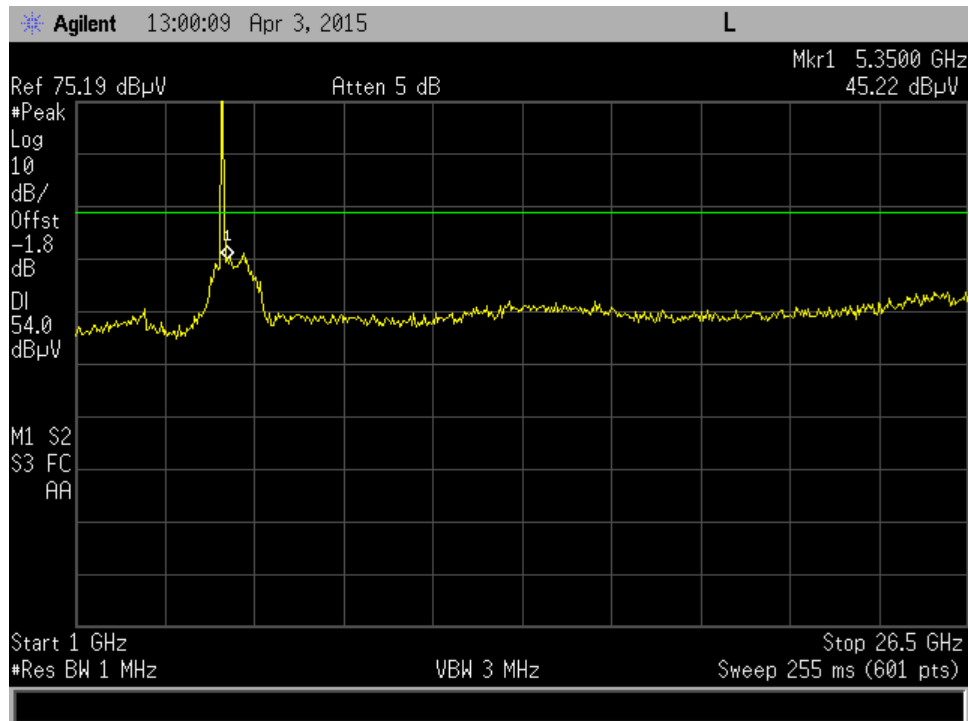
50MHz high ch 30 - 1000MHz J8



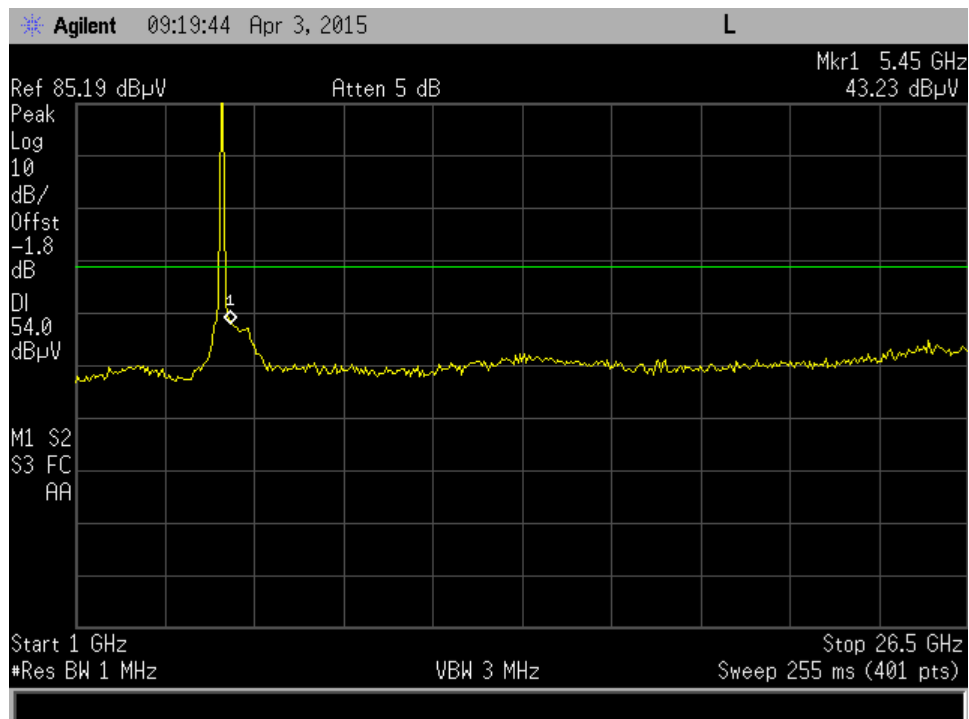
50MHz high ch 5350 - 5460GHz J7



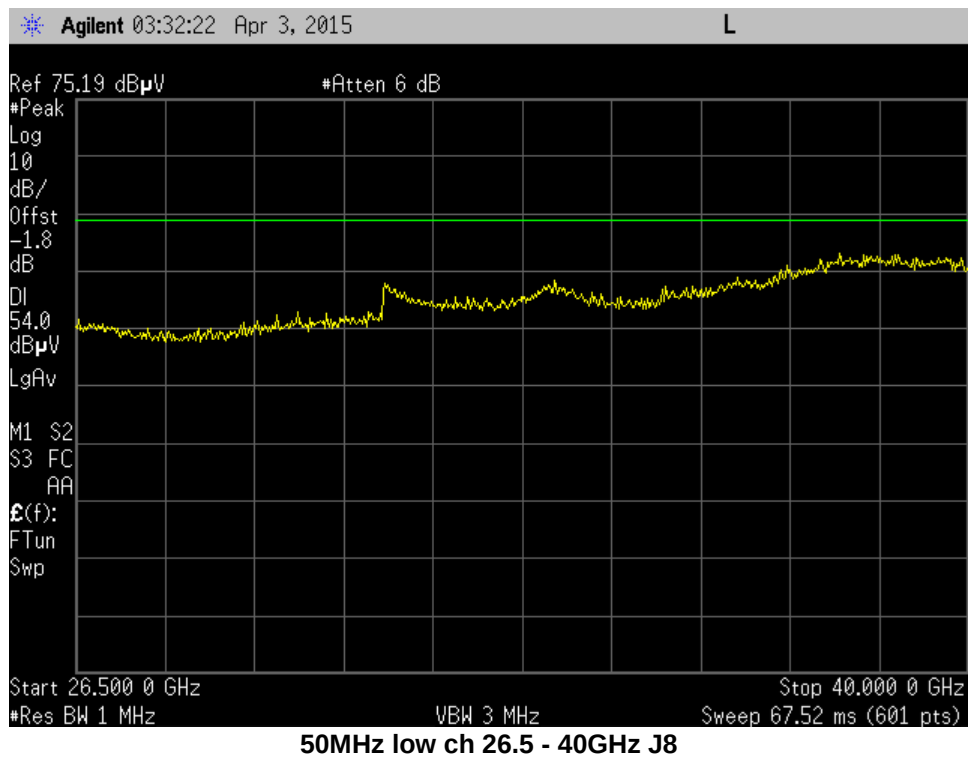
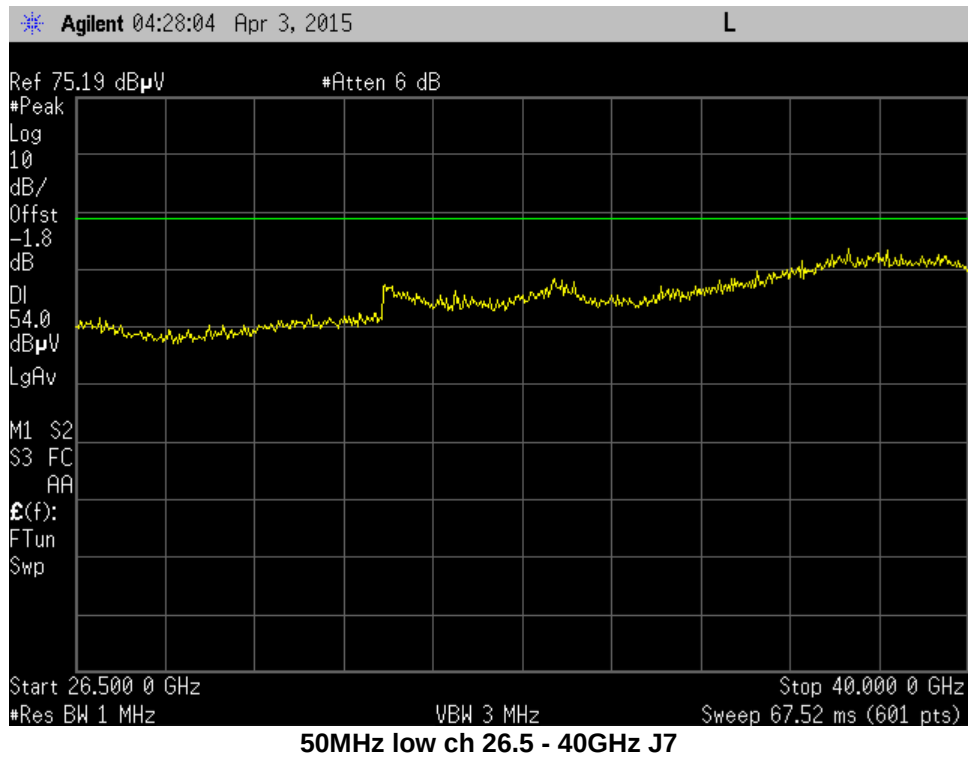
50MHz high ch 5350 - 5460GHz J8

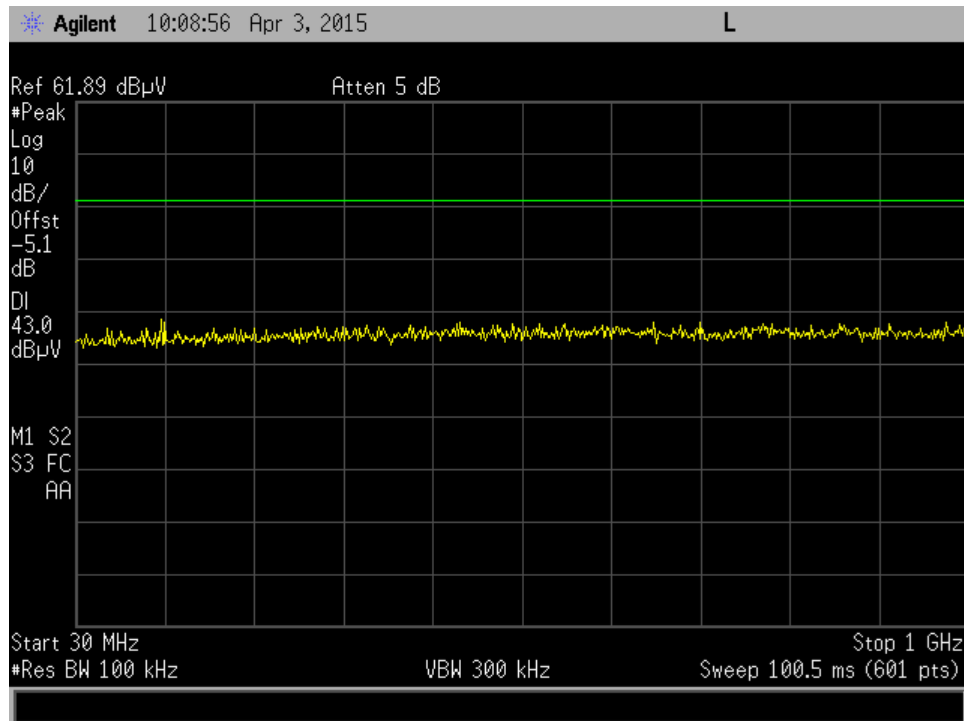


50MHz low ch 1 - 26.5GHz J8

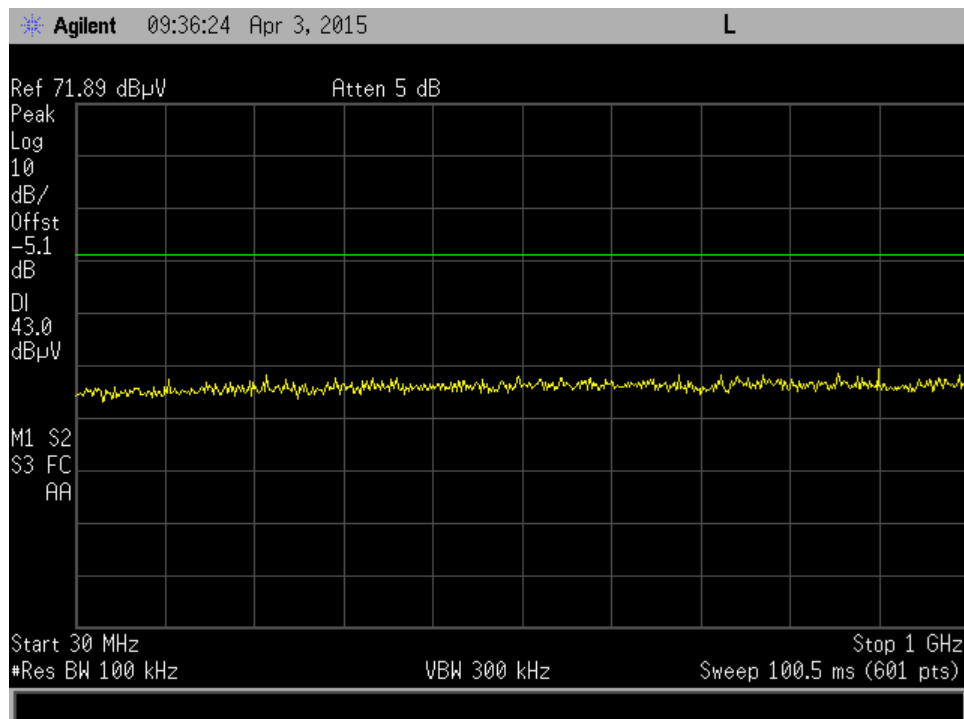


50MHz low ch 1 - 26.5GHz J7

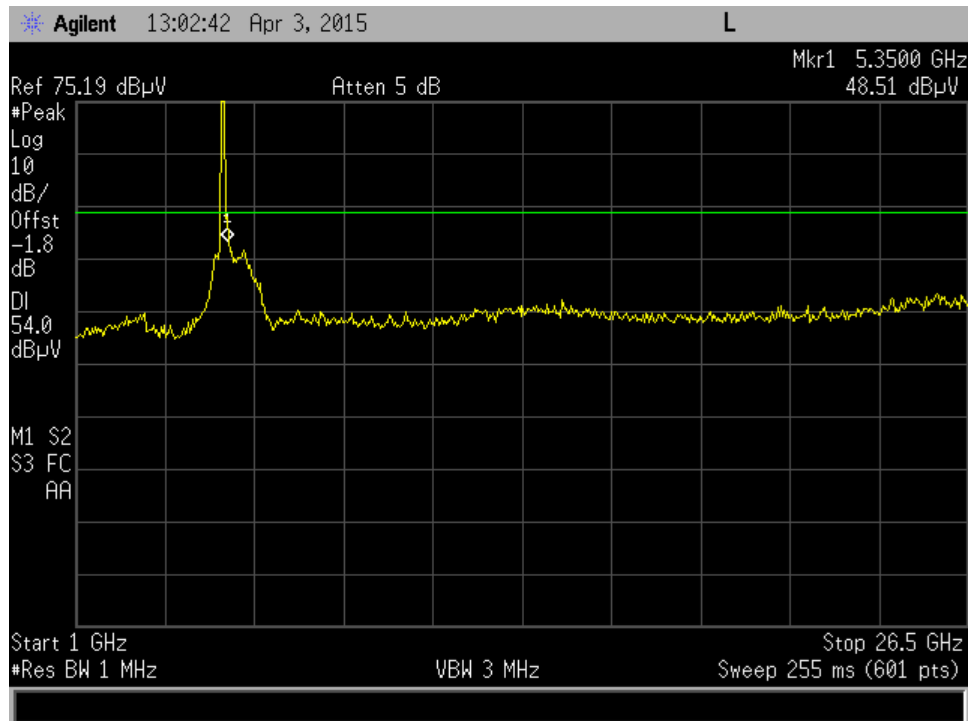




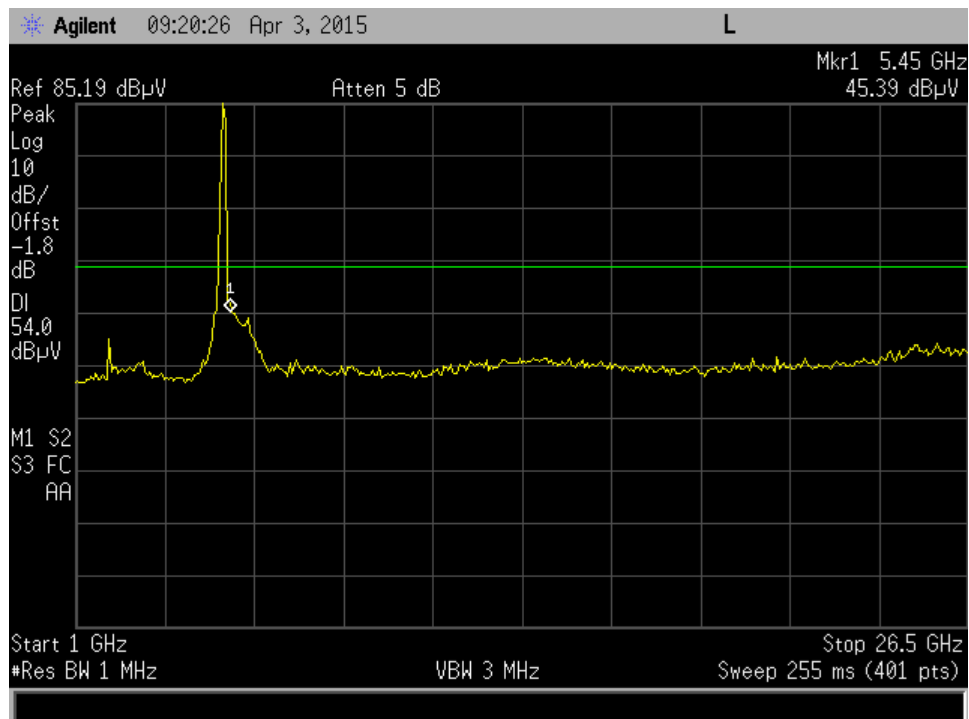
50MHz low ch 30 - 1000MHz J8



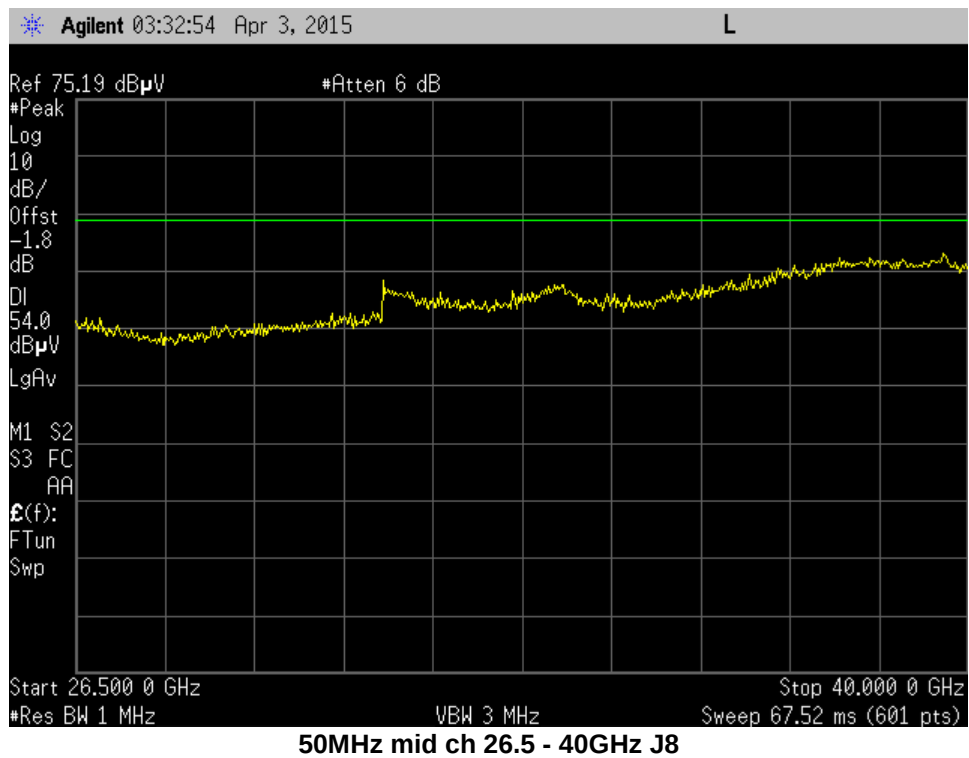
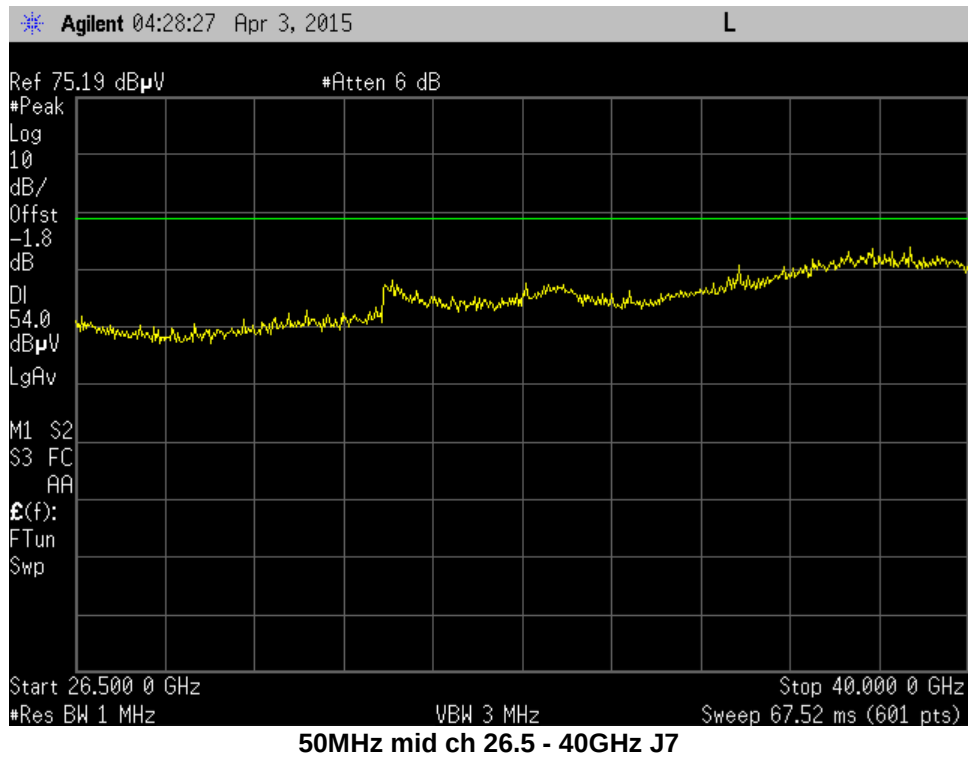
50MHz low ch 30 - 1000MHz J7

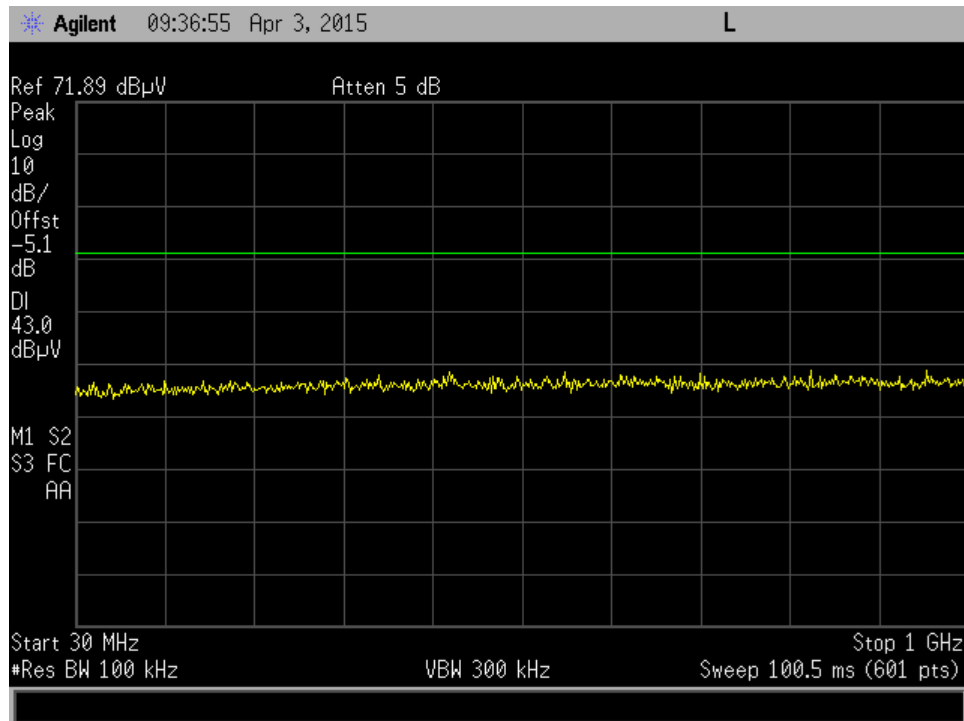


50MHz mid ch 1 - 26.5GHz J8

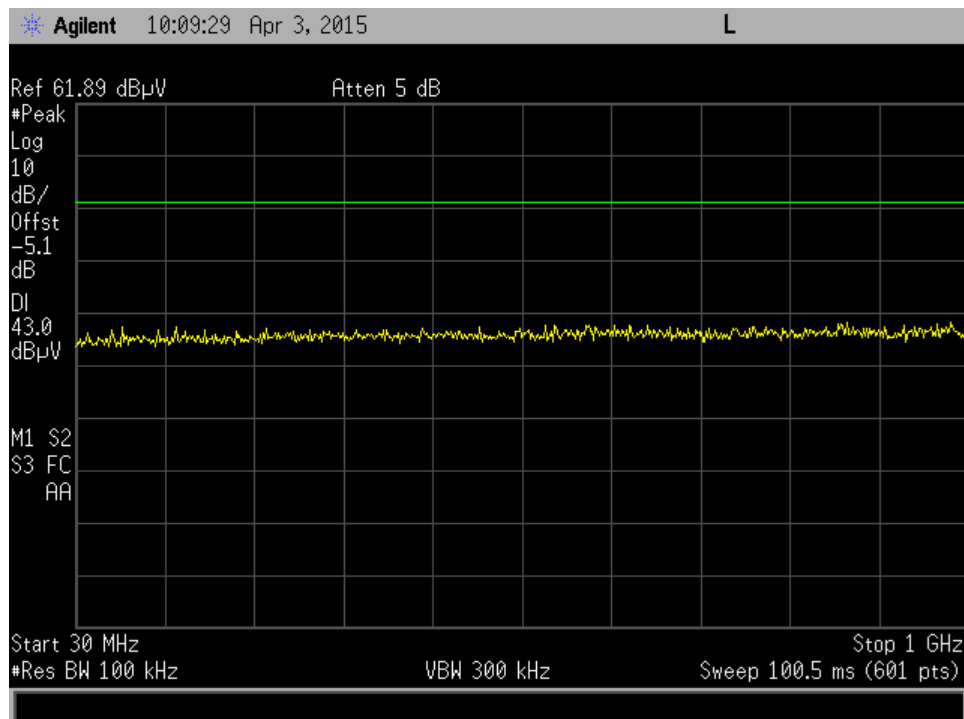


50MHz mid ch 1 - 26.5GHz J7

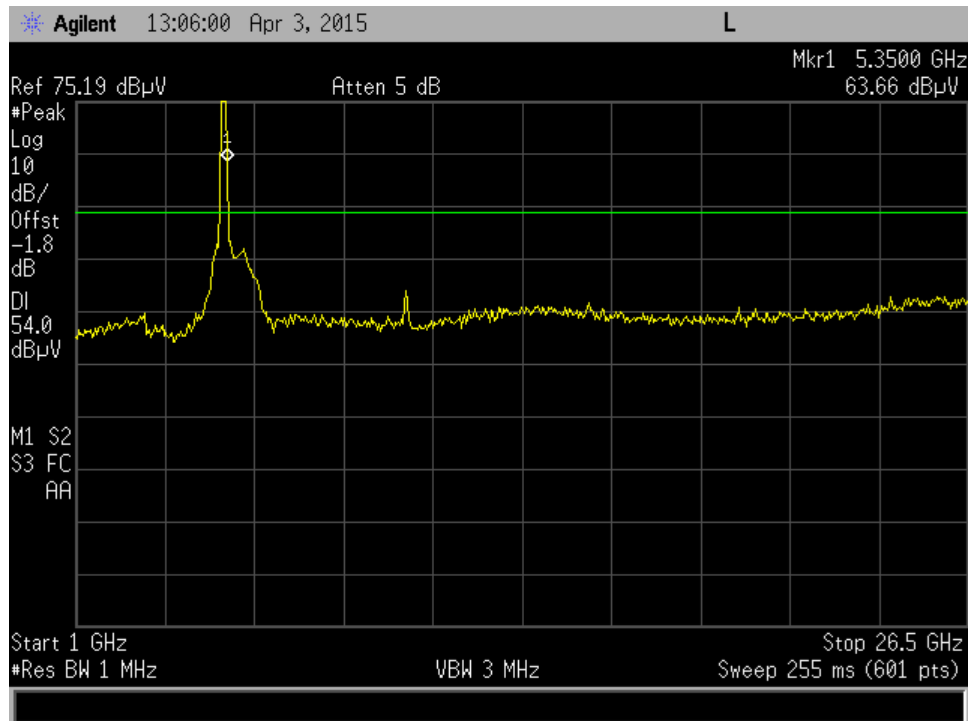




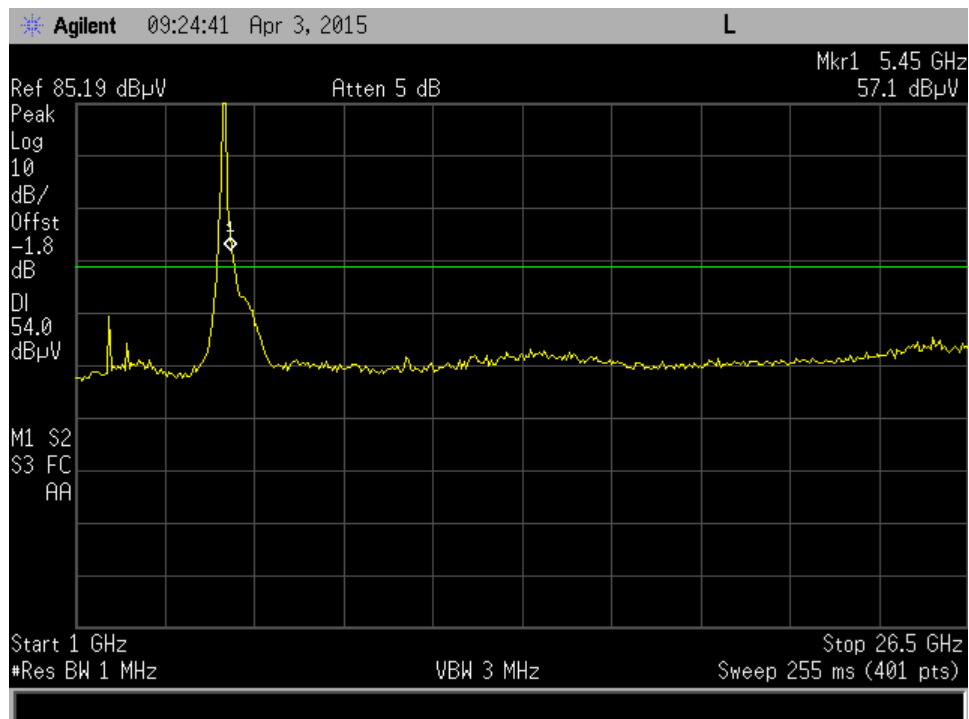
50MHz mid ch 30 - 1000MHz J7



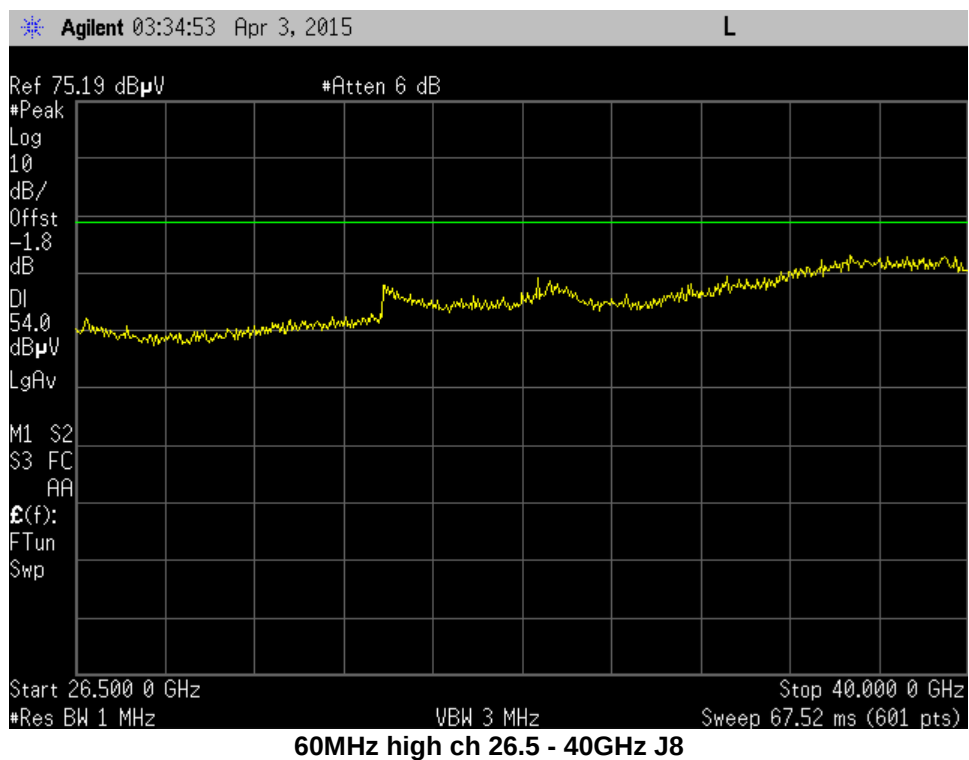
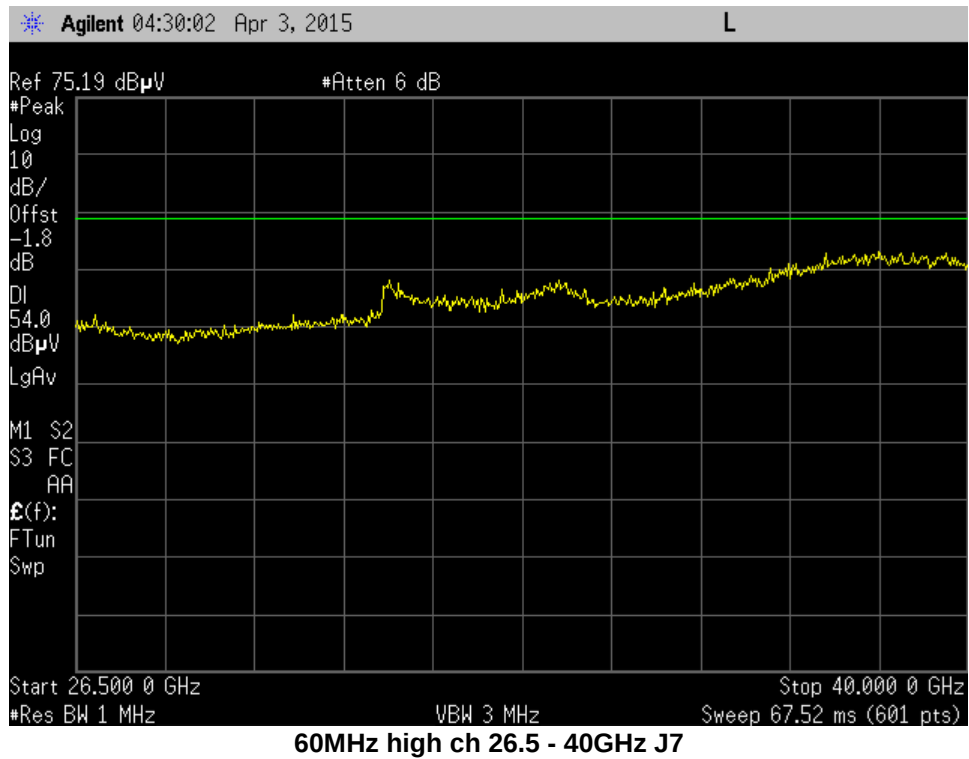
50MHz mid ch 30 - 1000MHz J8

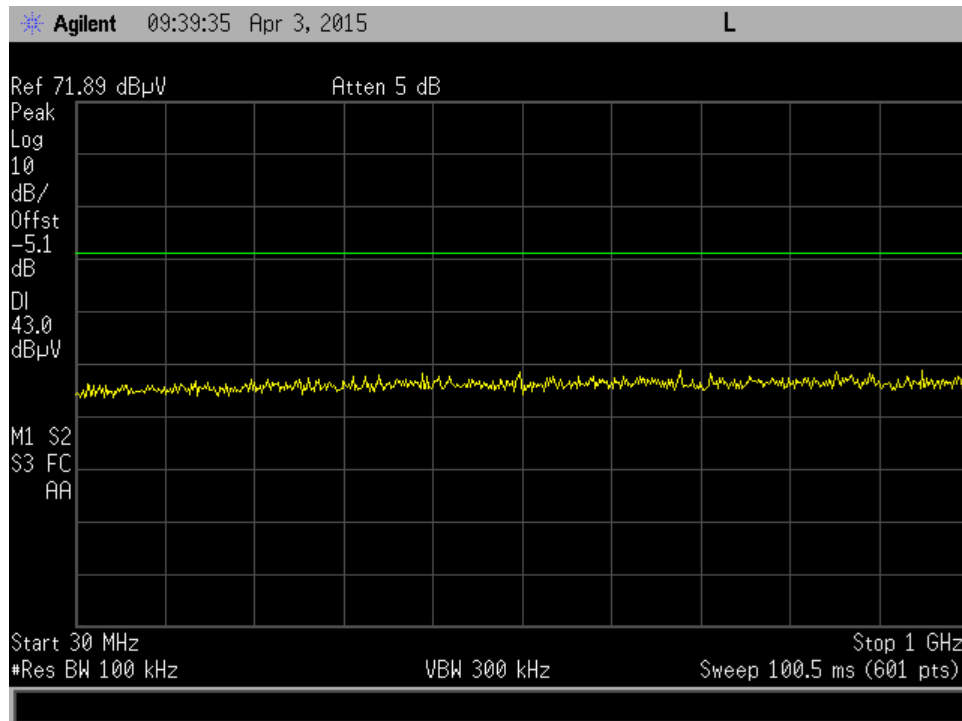


60MHz high ch 1 - 26.5GHz J8

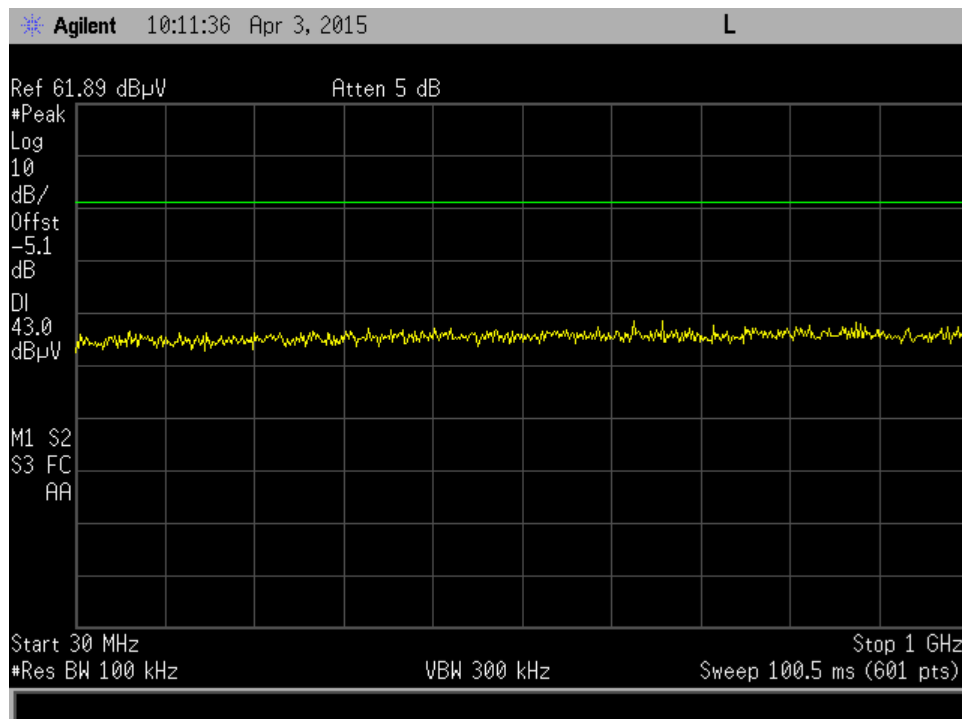


60MHz high ch 1 - 26.5GHz J7

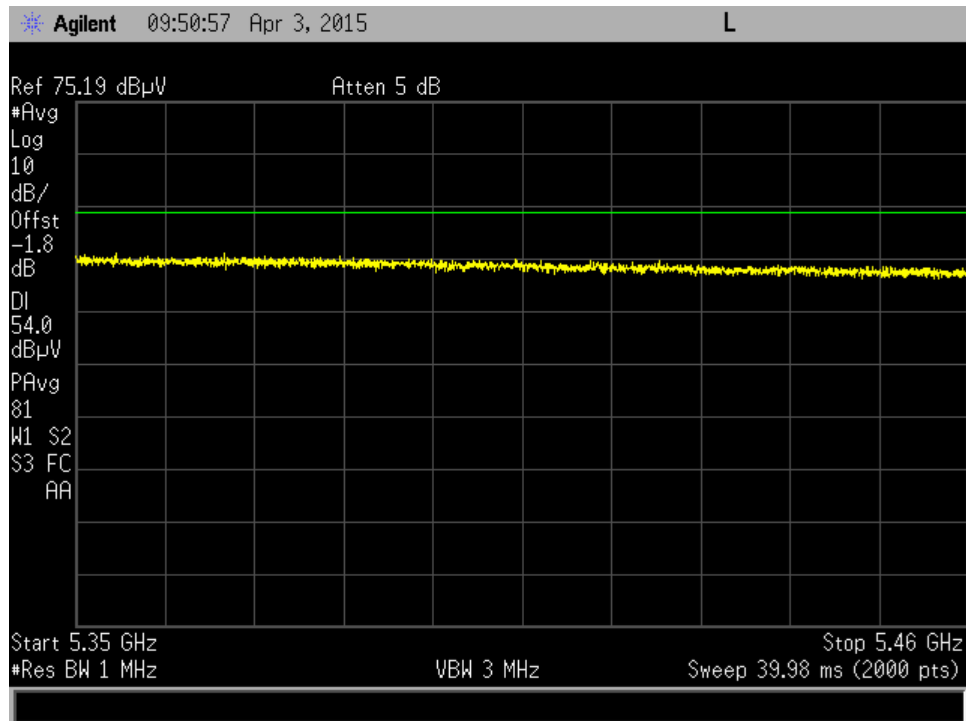




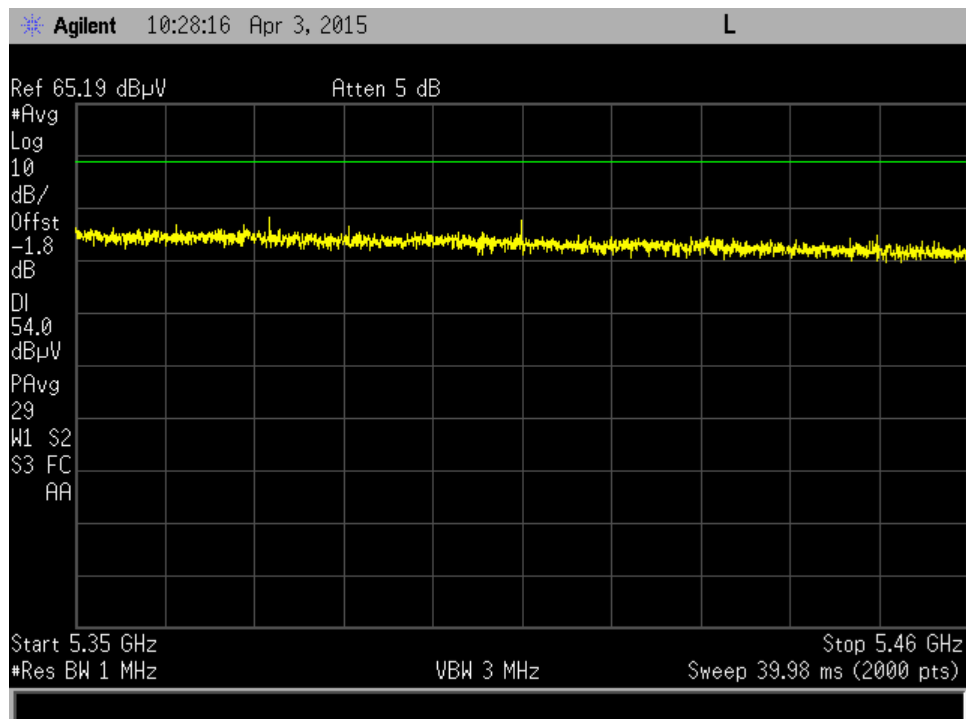
60MHz high ch 30 - 1000MHz J7



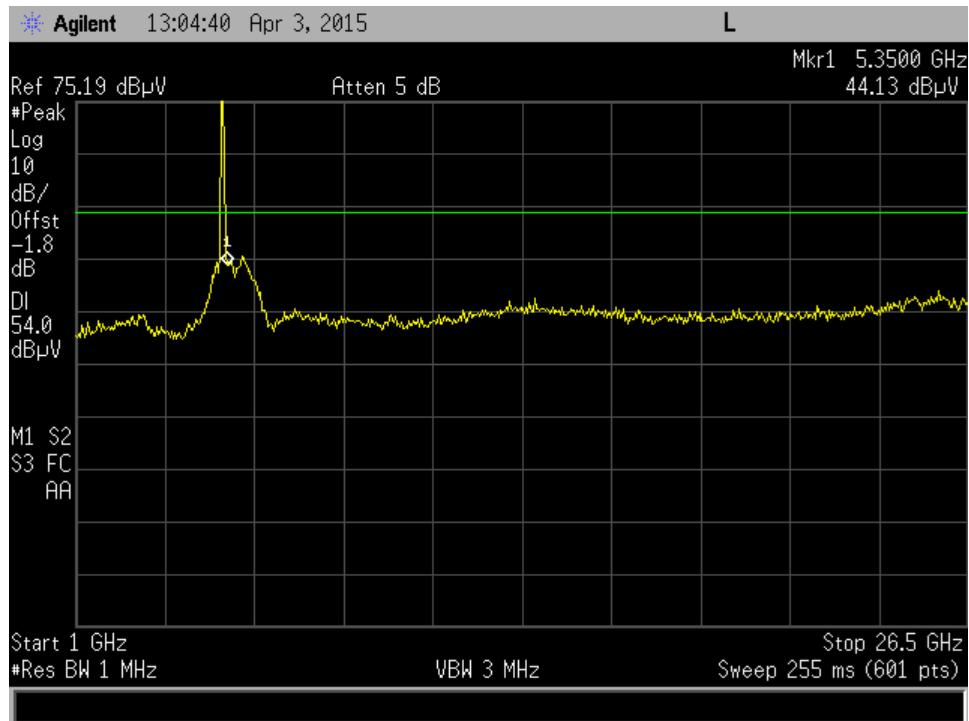
60MHz high ch 30 - 1000MHz J8



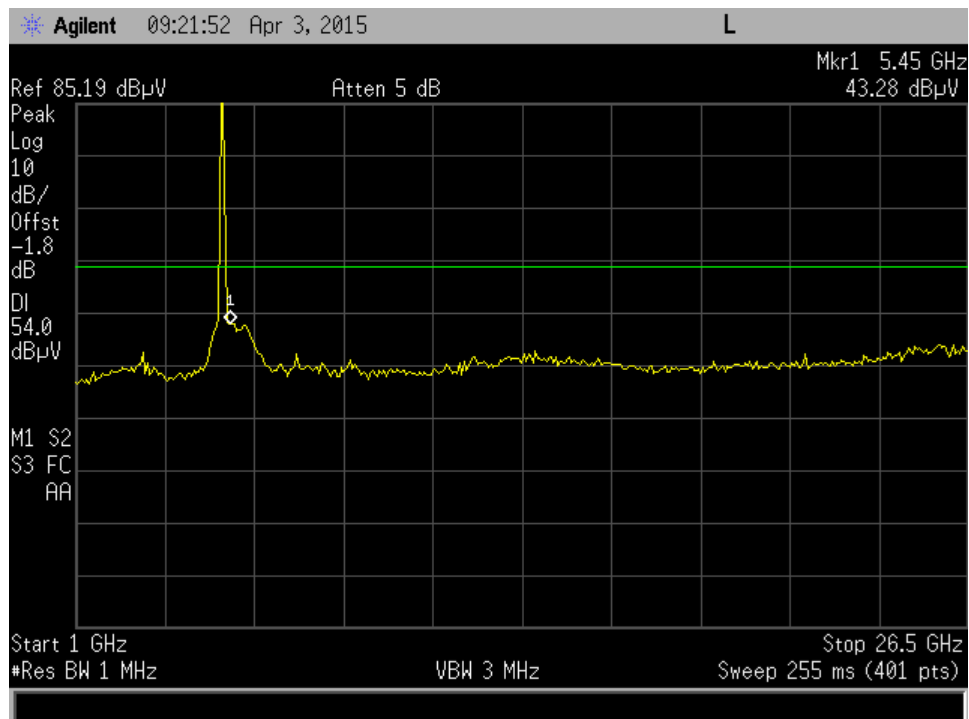
60MHz high ch 5350 - 5460GHz J7



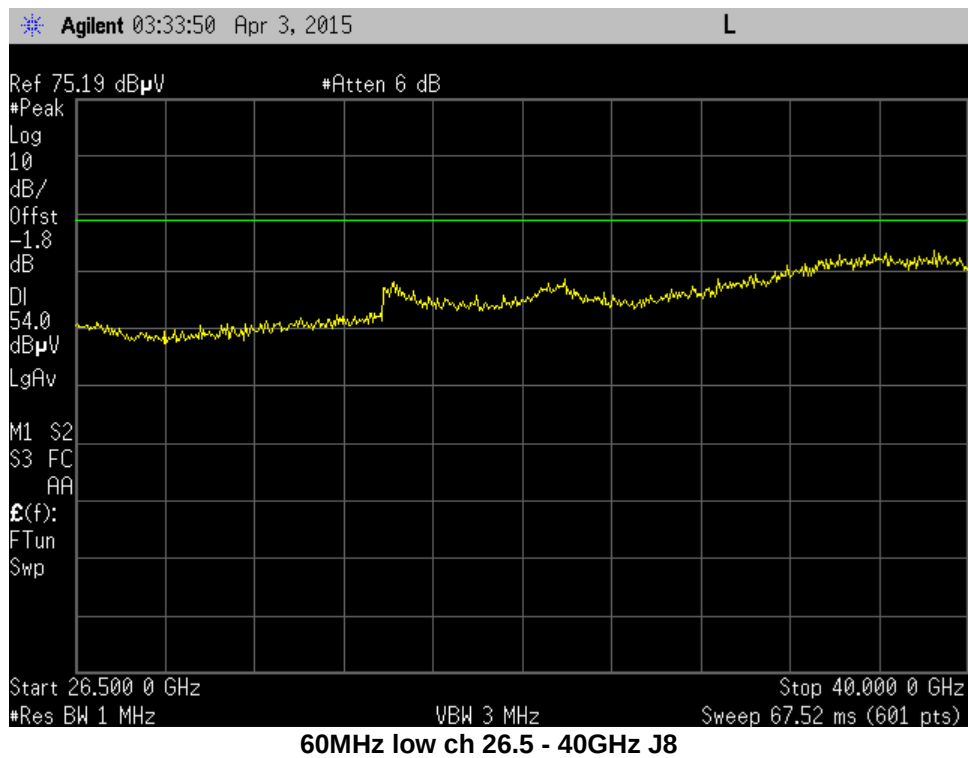
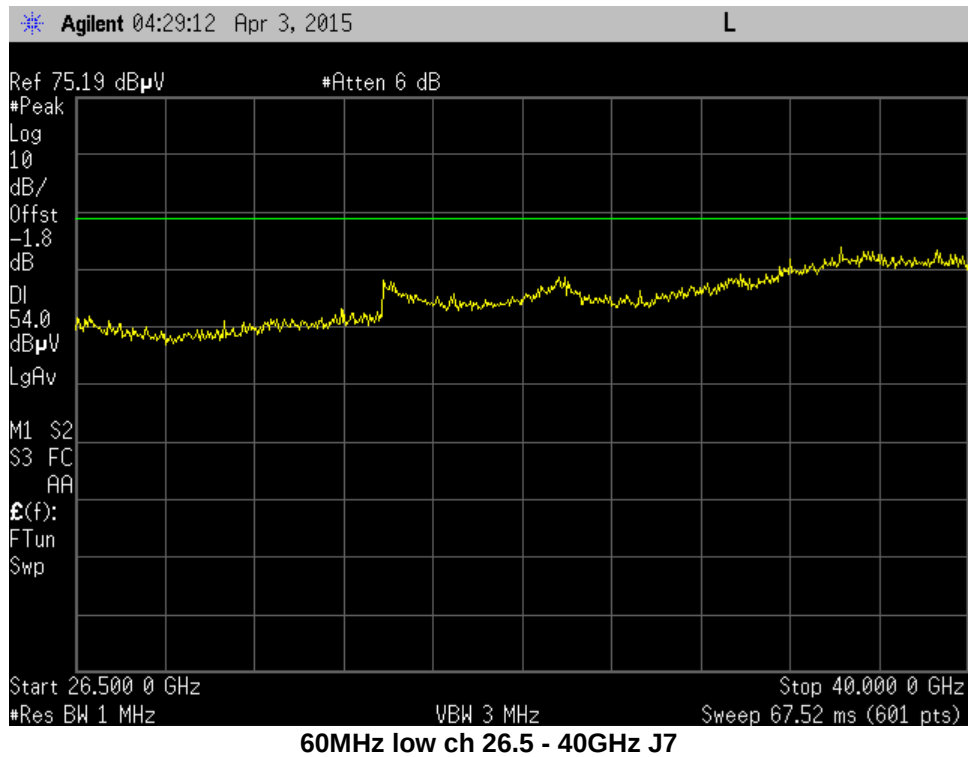
60MHz high ch 5350 - 5460GHz J8

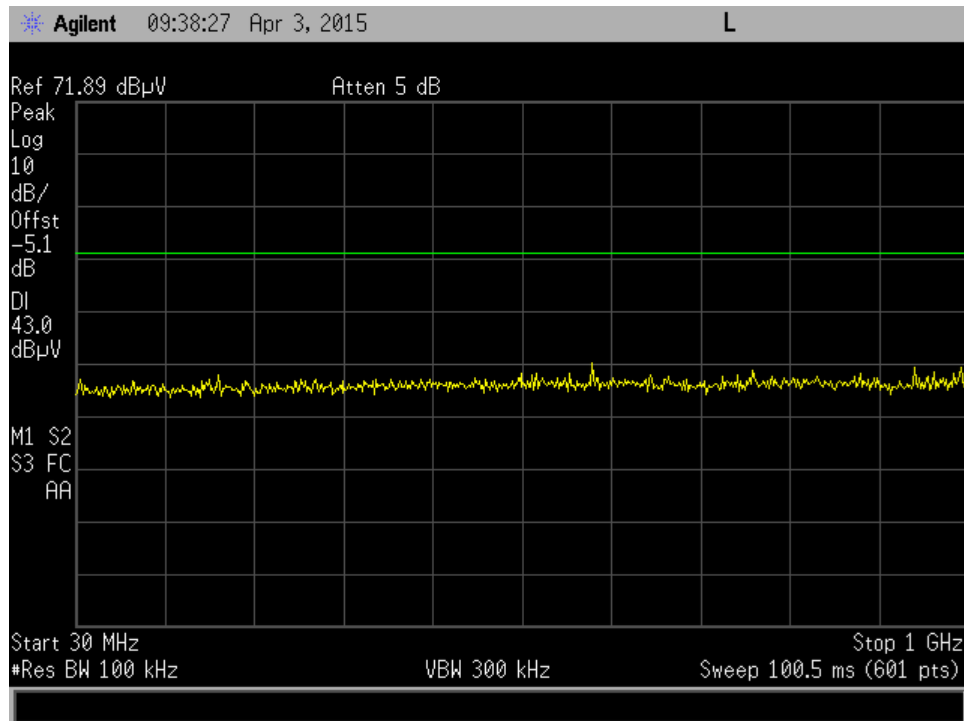


60MHz low ch 1 - 26.5GHz J8

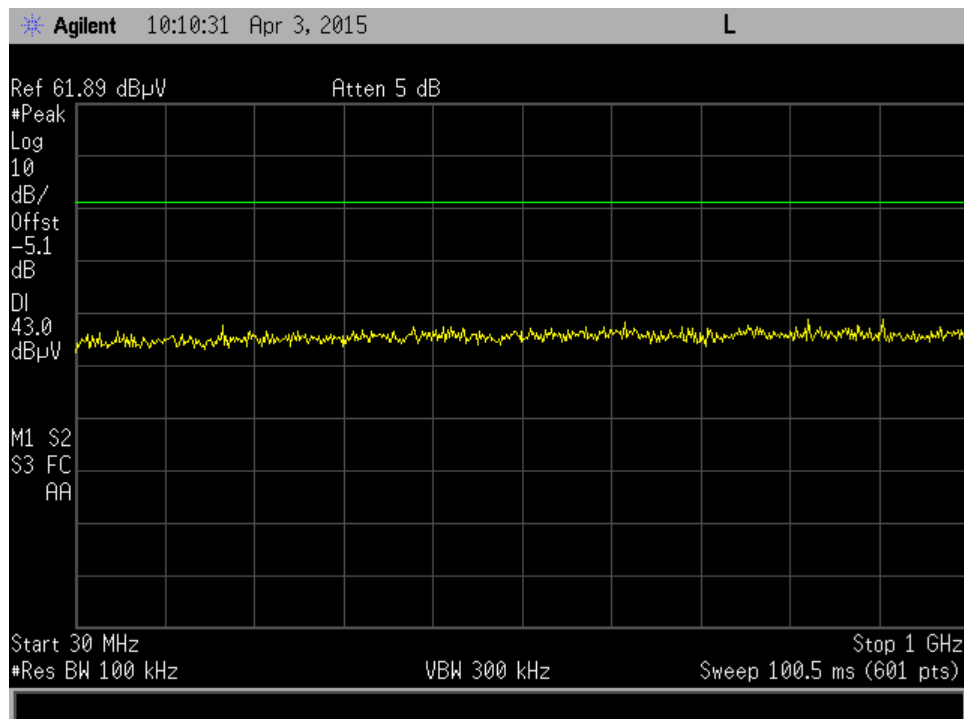


60MHz low ch 1 - 26.5GHz J7

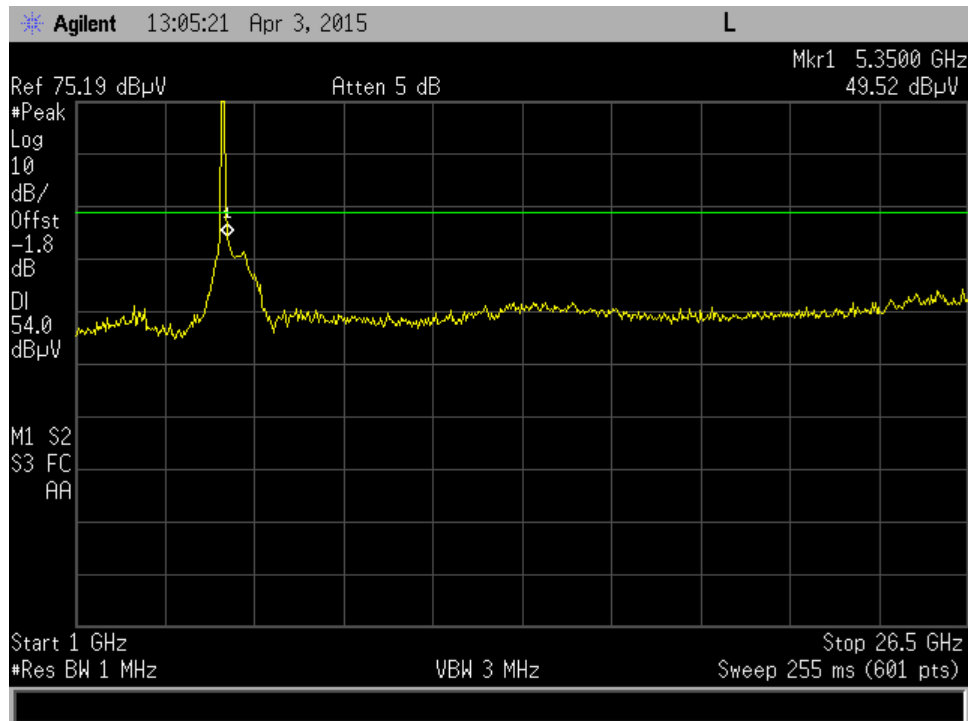




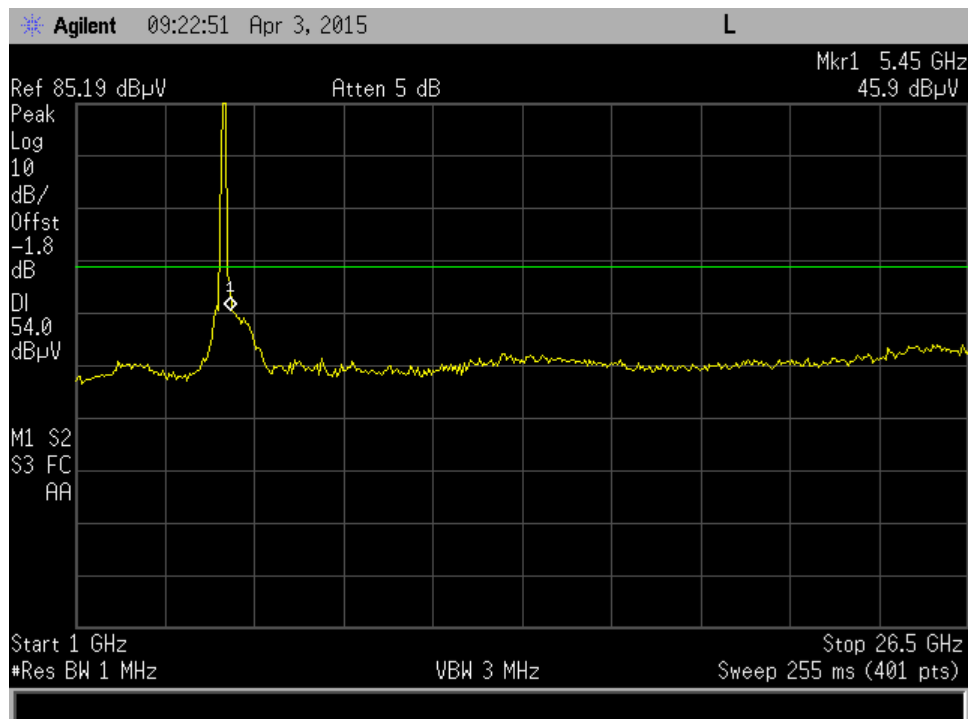
60MHz low ch 30 - 1000MHz J7



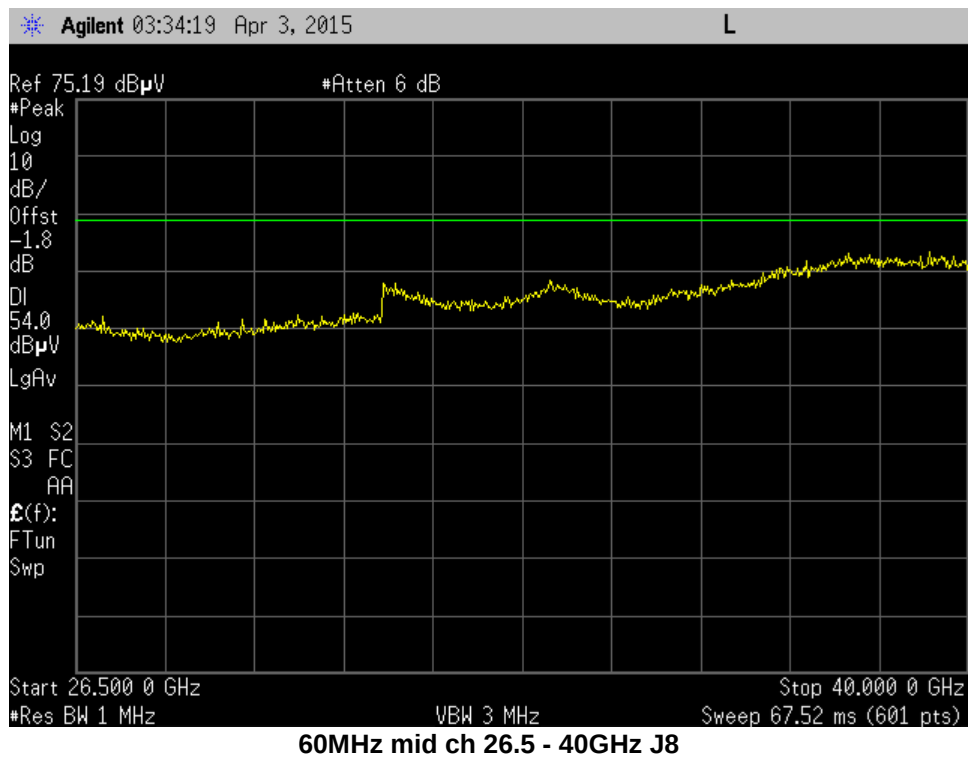
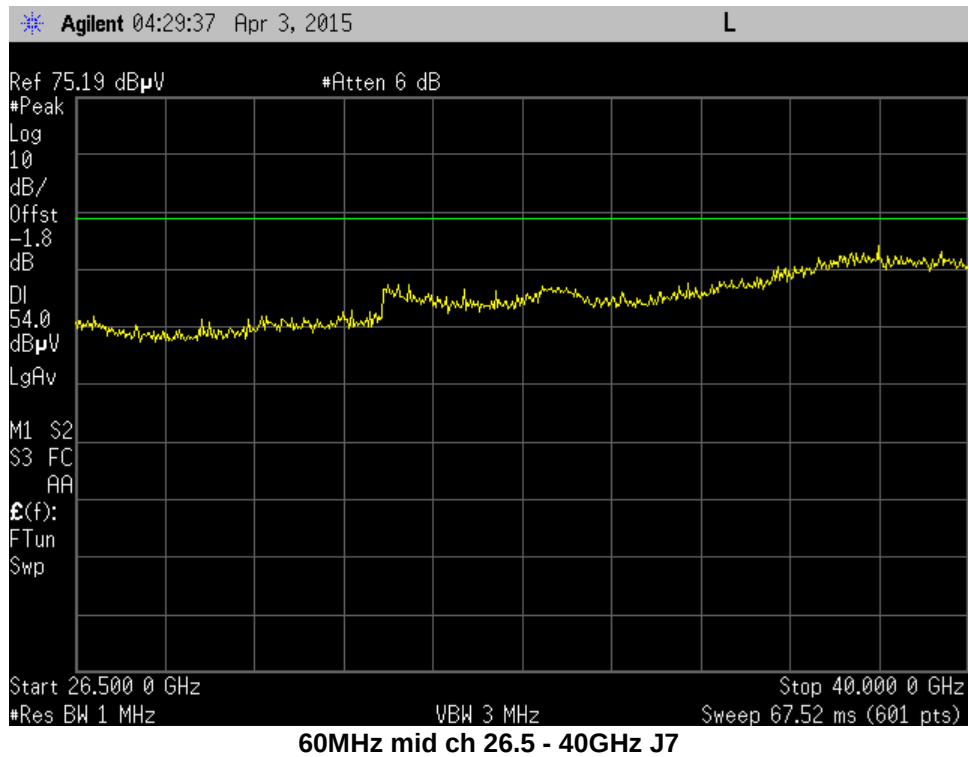
60MHz low ch 30 - 1000MHz J8

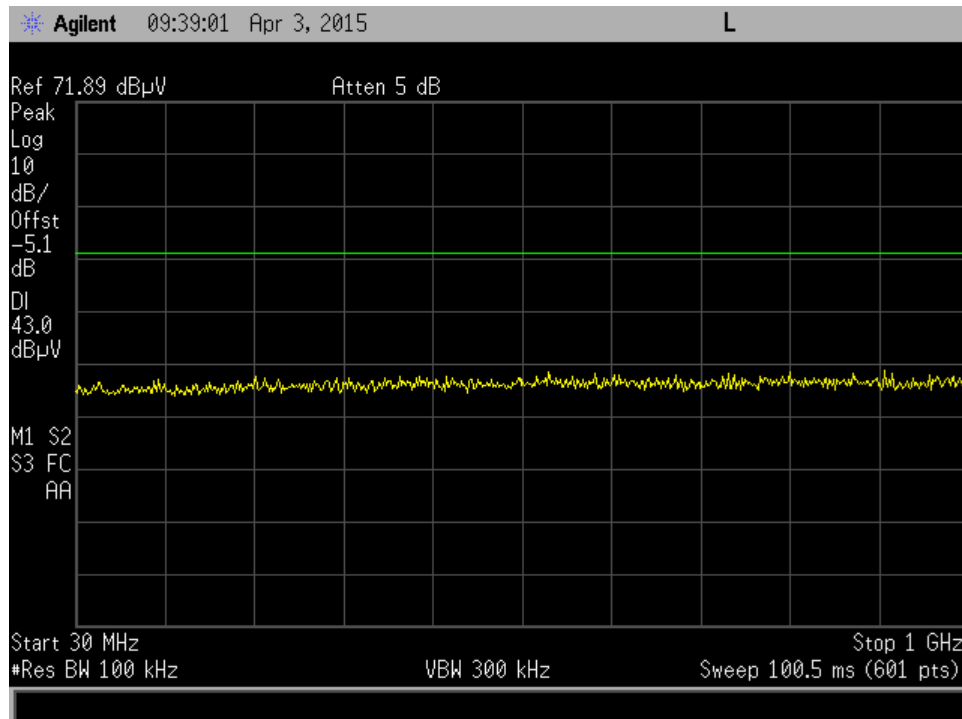


60MHz mid ch 1 - 26.5GHz J8

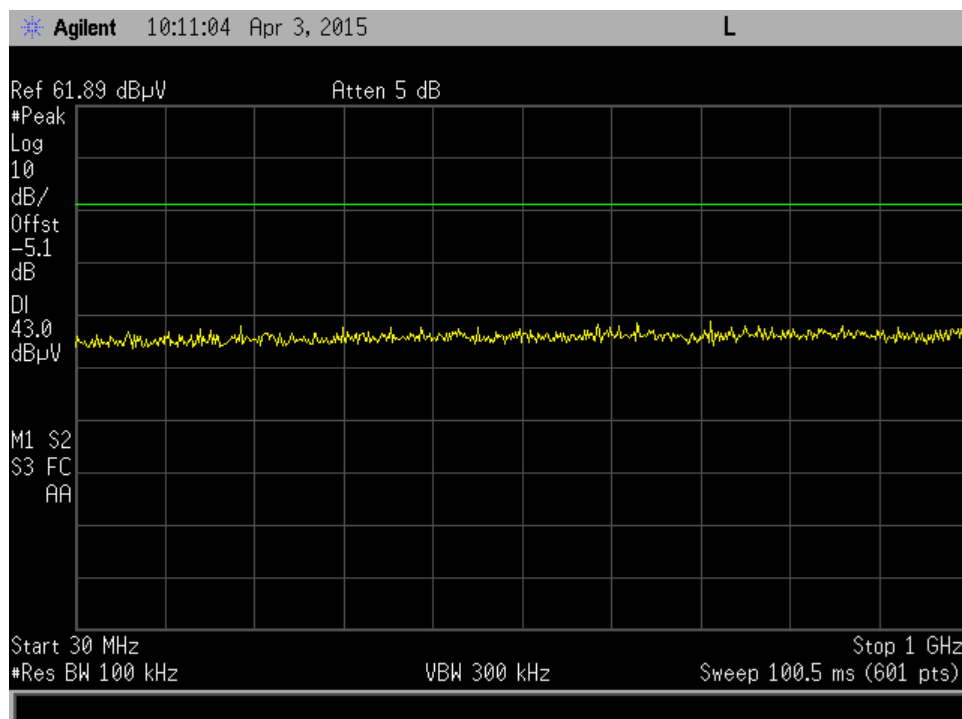


60MHz mid ch 1 - 26.5GHz J7

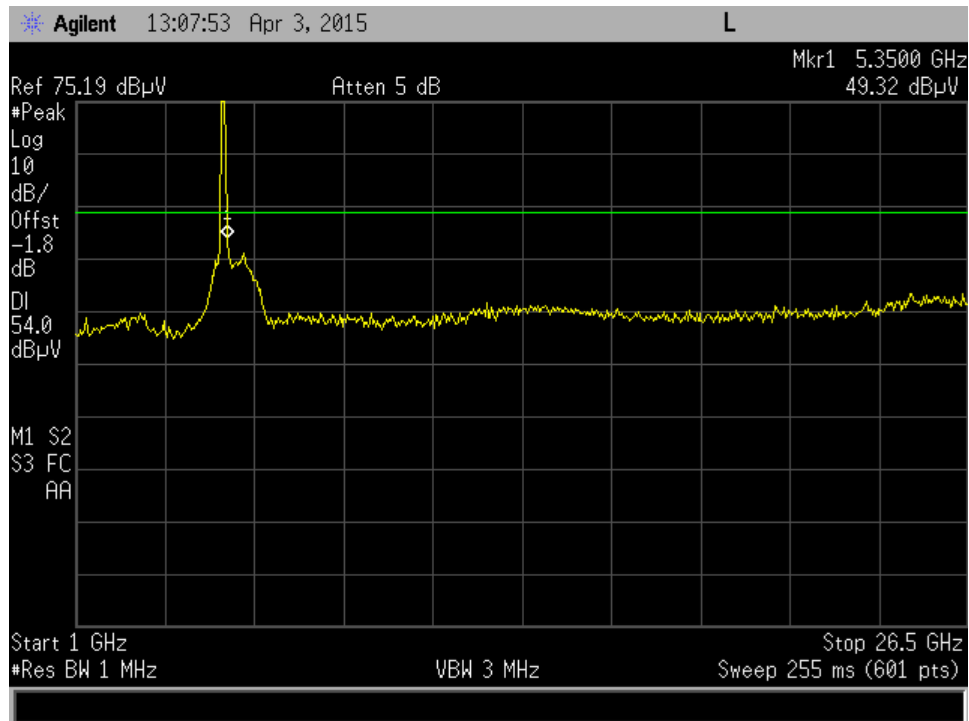




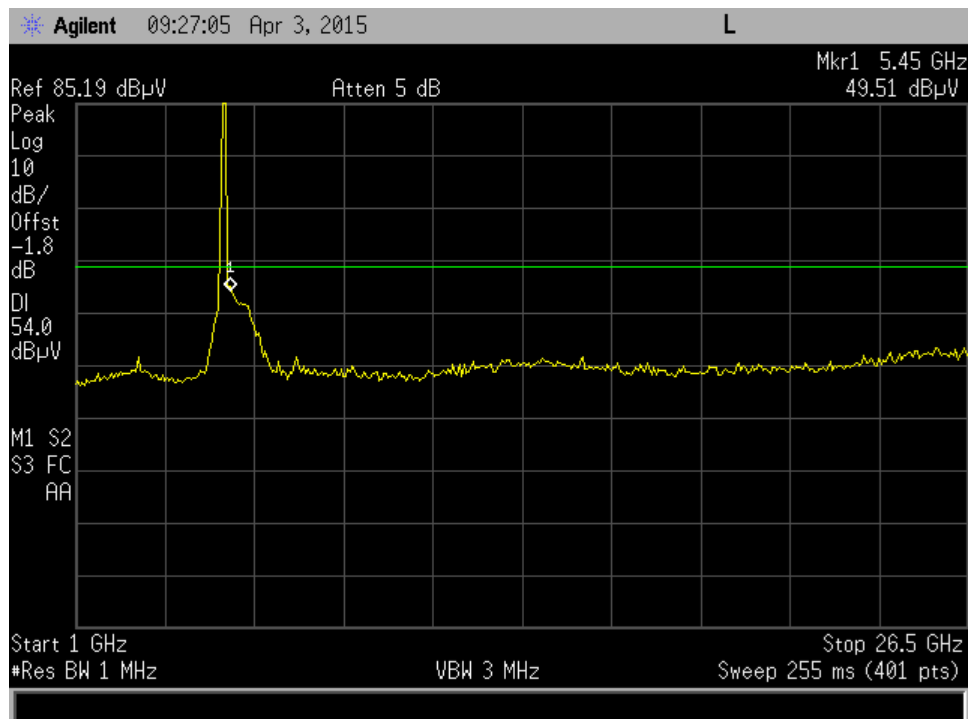
60MHz mid ch 30 - 1000MHz J7



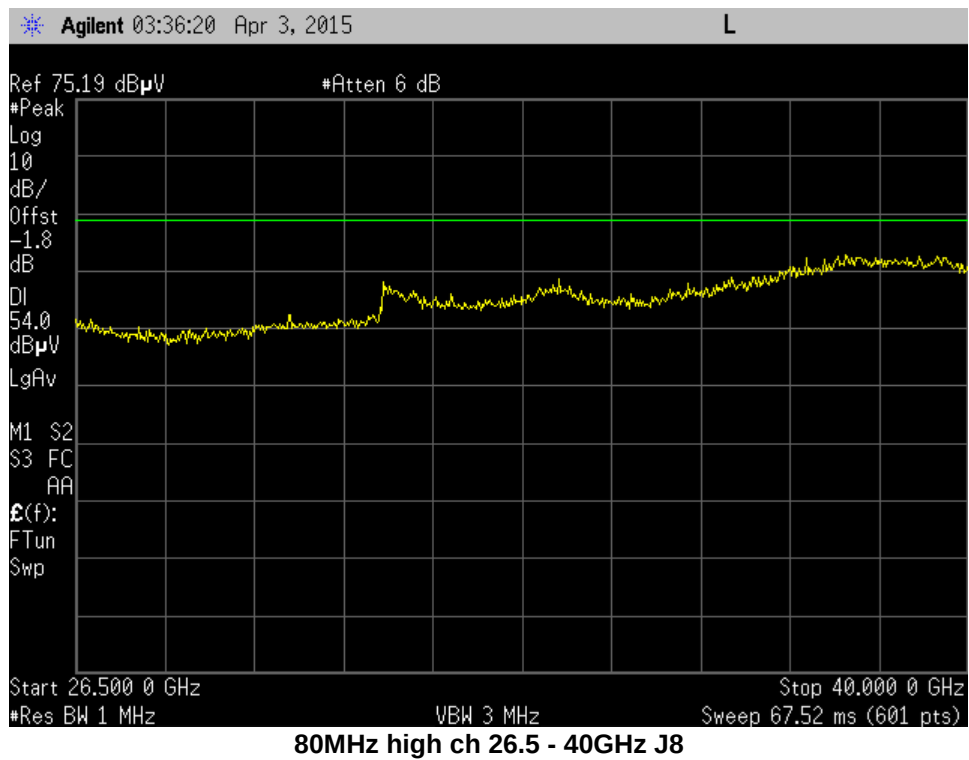
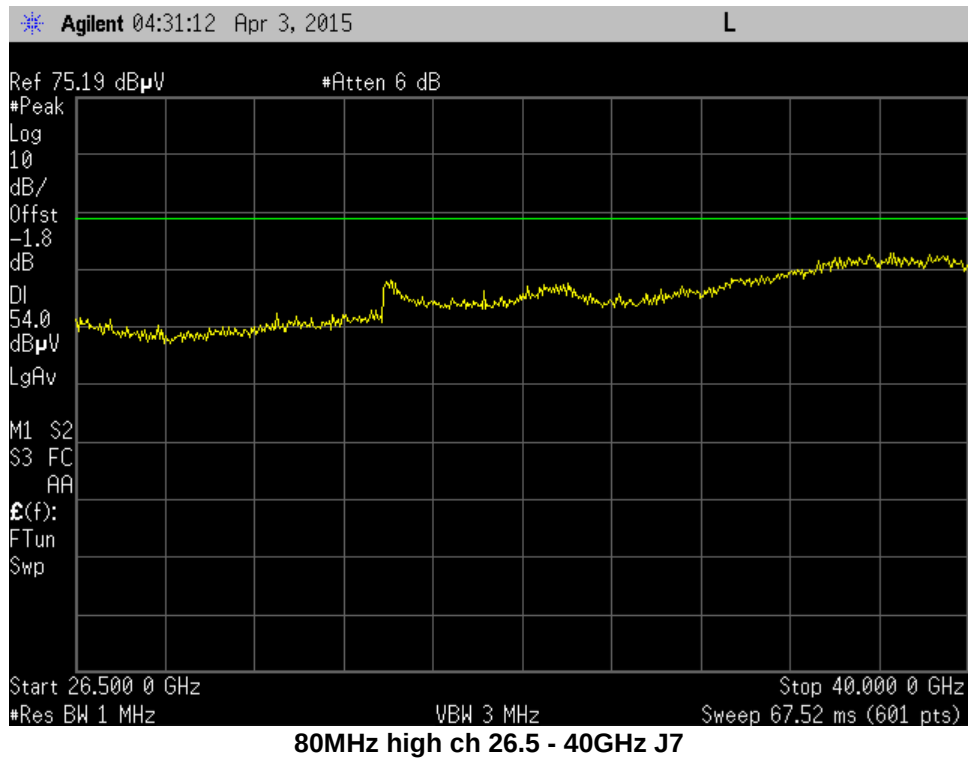
60MHz mid ch 30 - 1000MHz J8

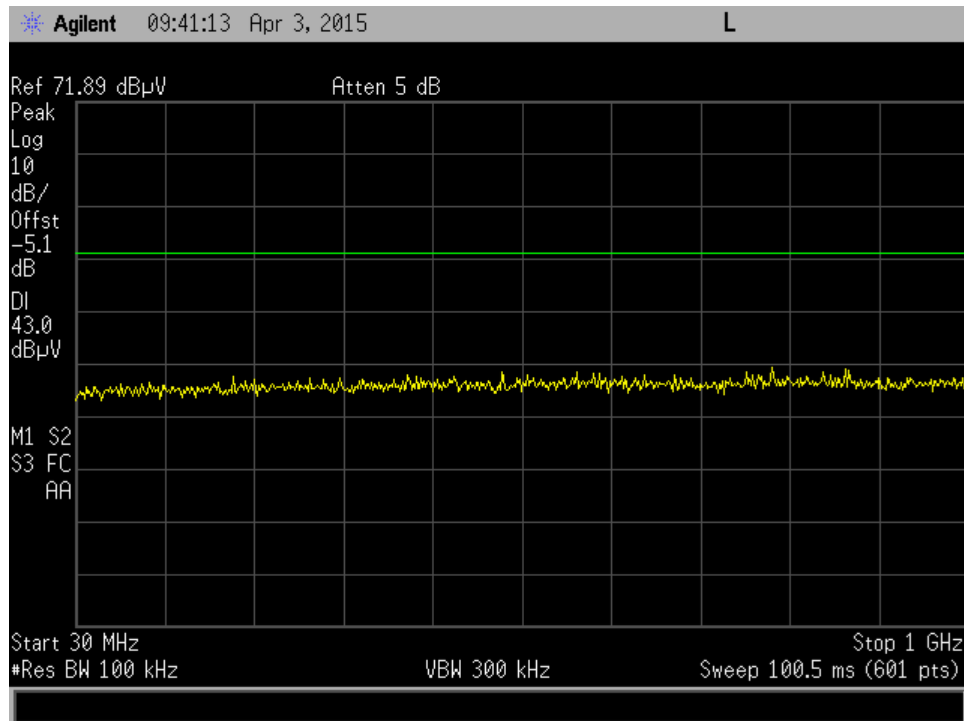


80MHz high ch 1 - 26.5GHz J8

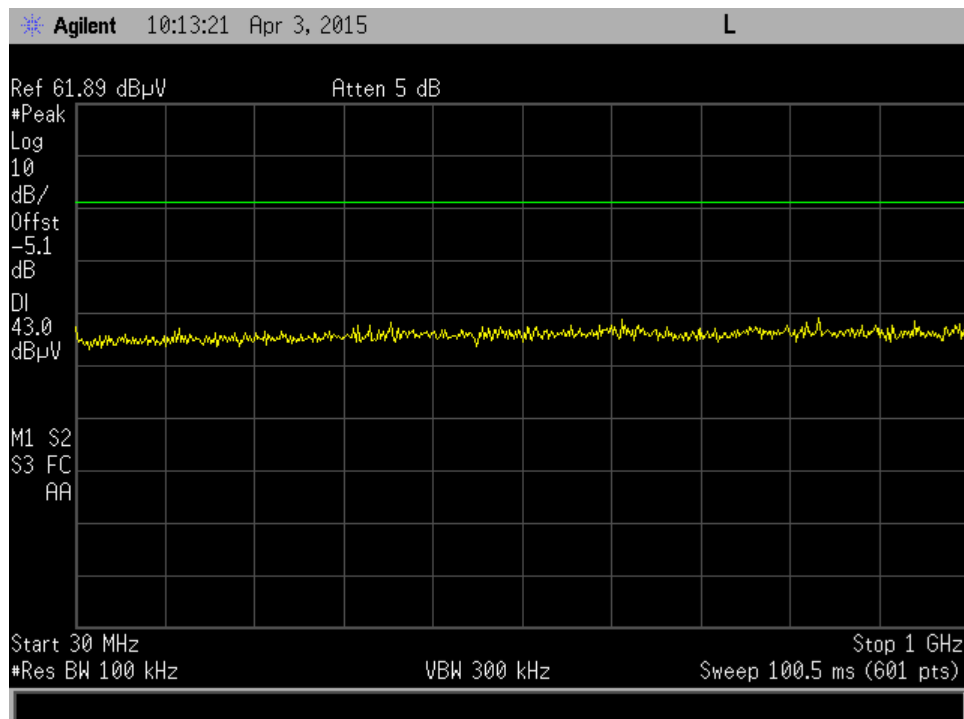


80MHz high ch 1 - 26.5GHz J7

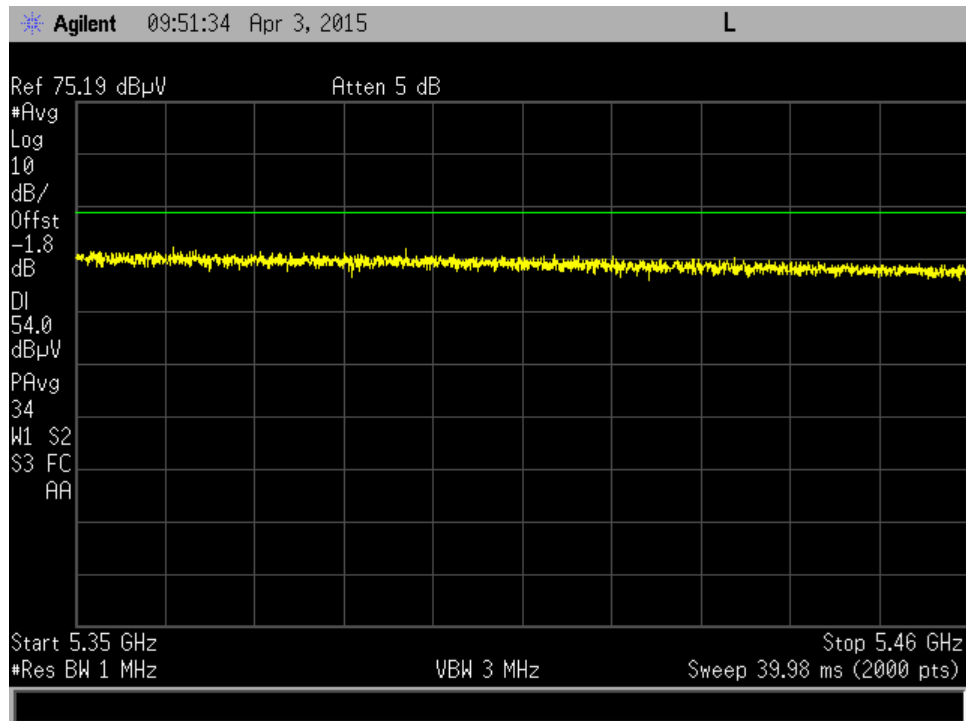




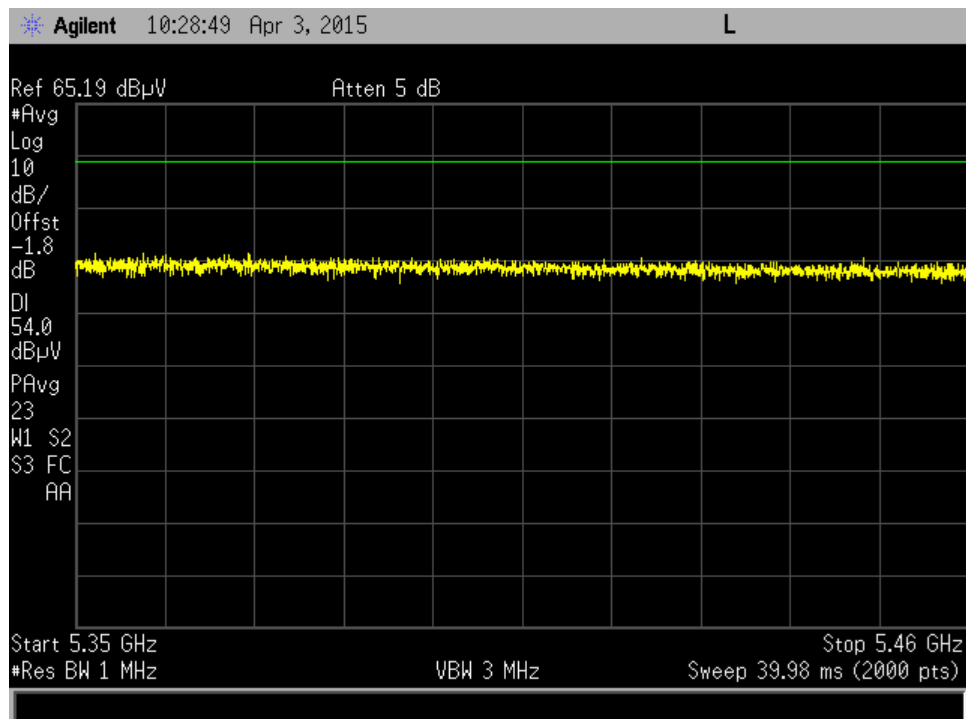
80MHz high ch 30 - 1000MHz J7



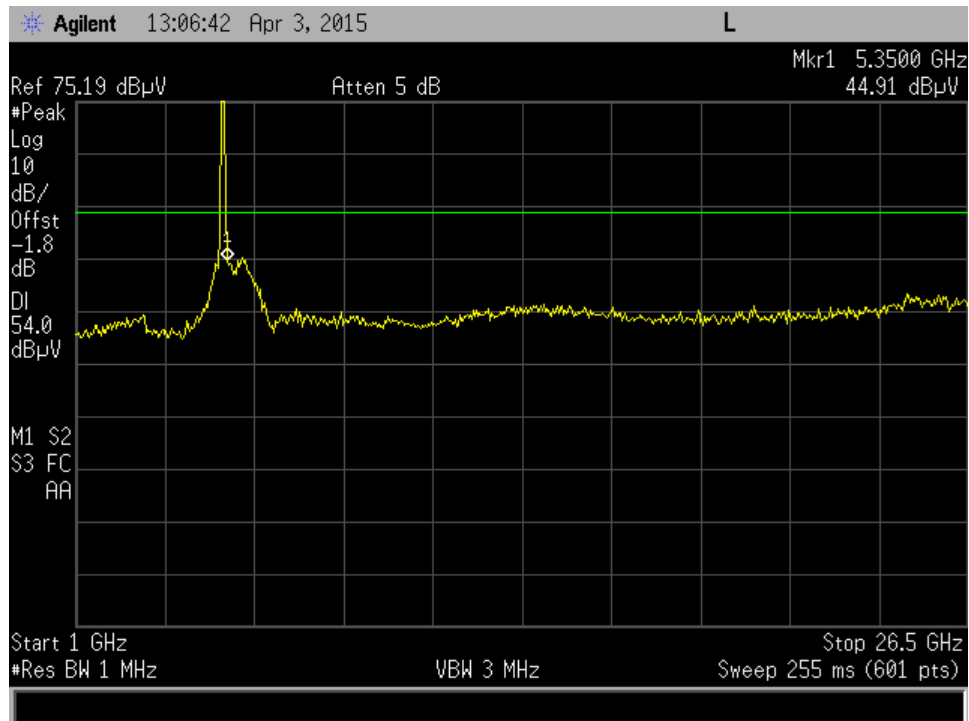
80MHz high ch 30 - 1000MHz J8



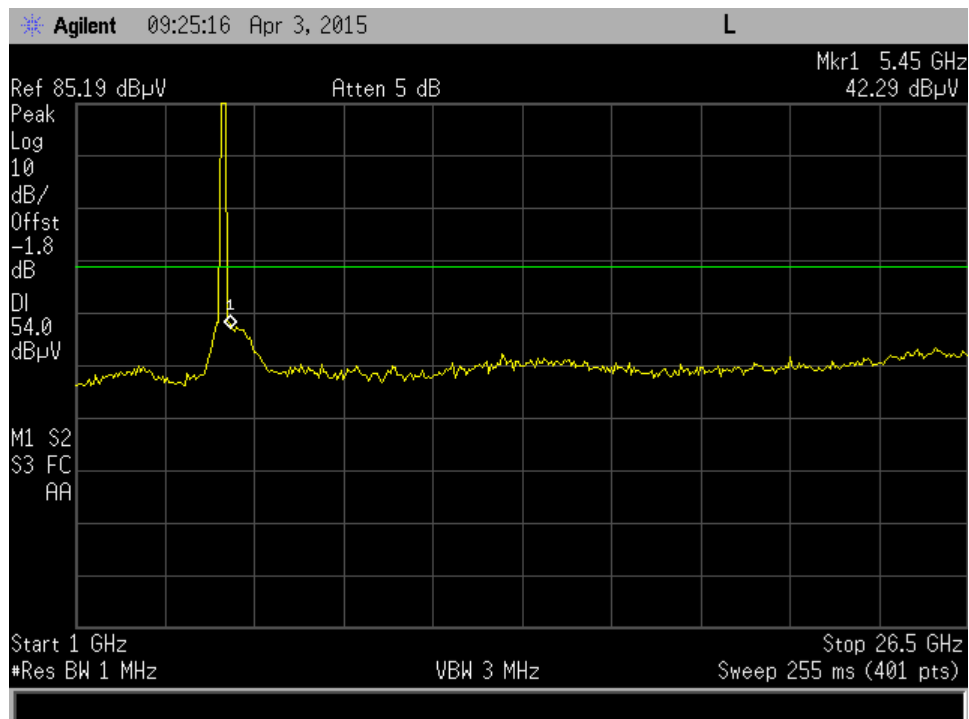
80MHz high ch 5350 - 5460GHz J7



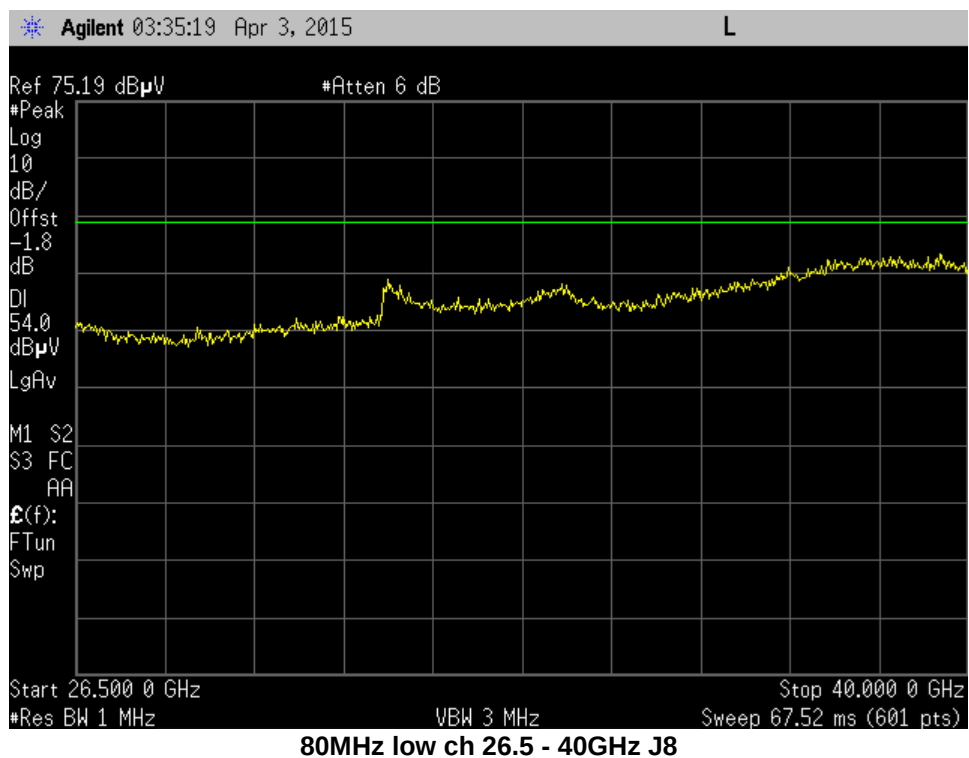
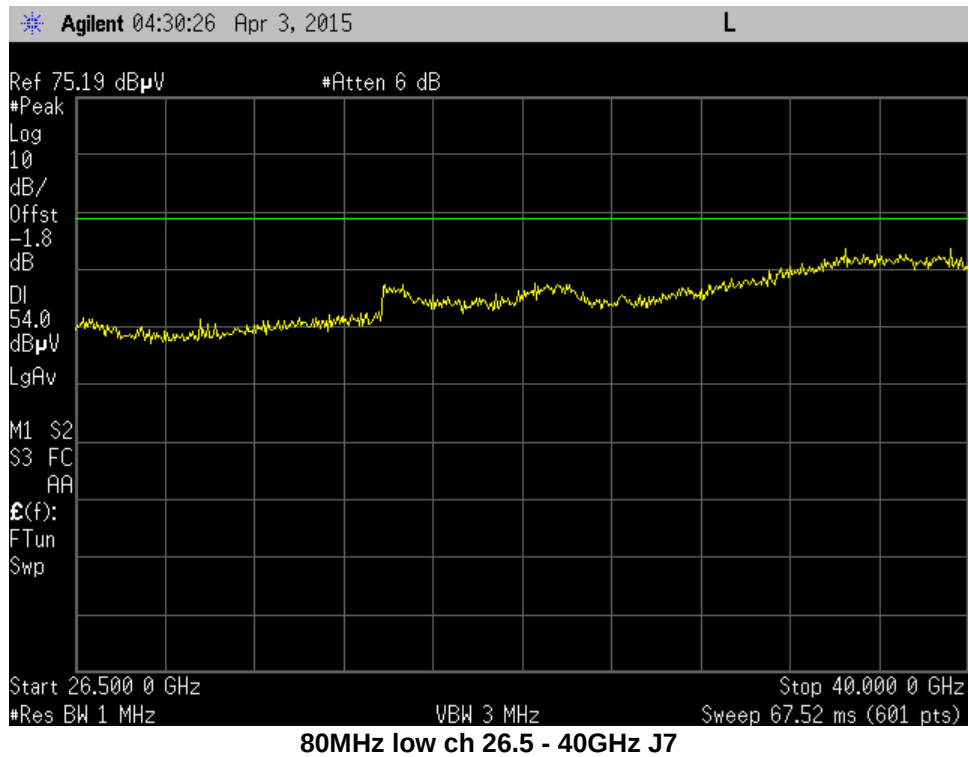
80MHz high ch 5350 - 5460GHz J8

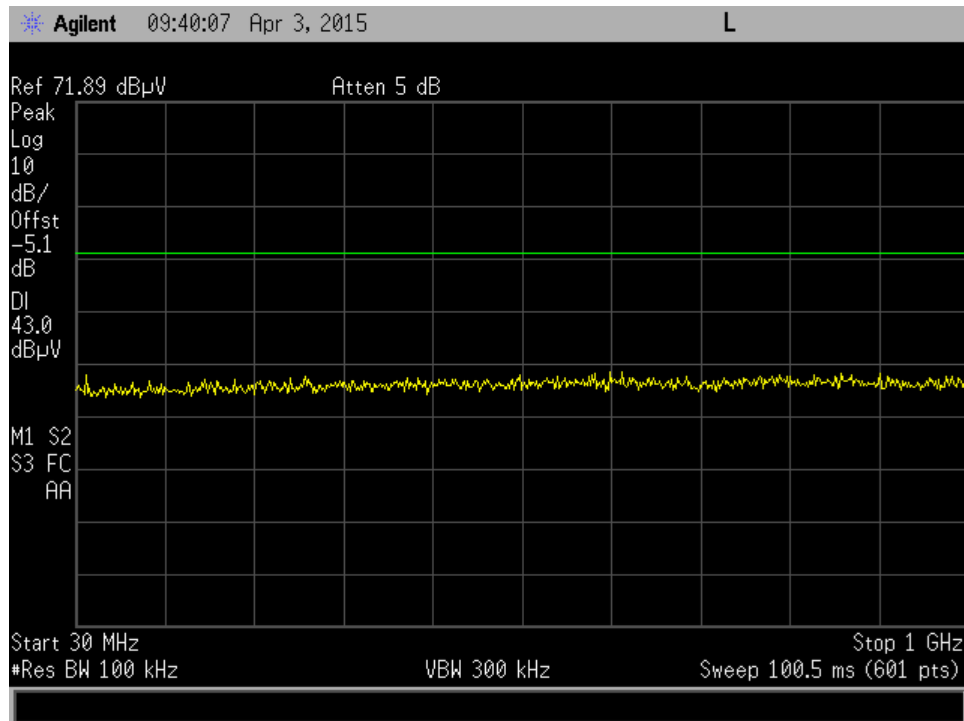


80MHz low ch 1 - 26.5GHz J8

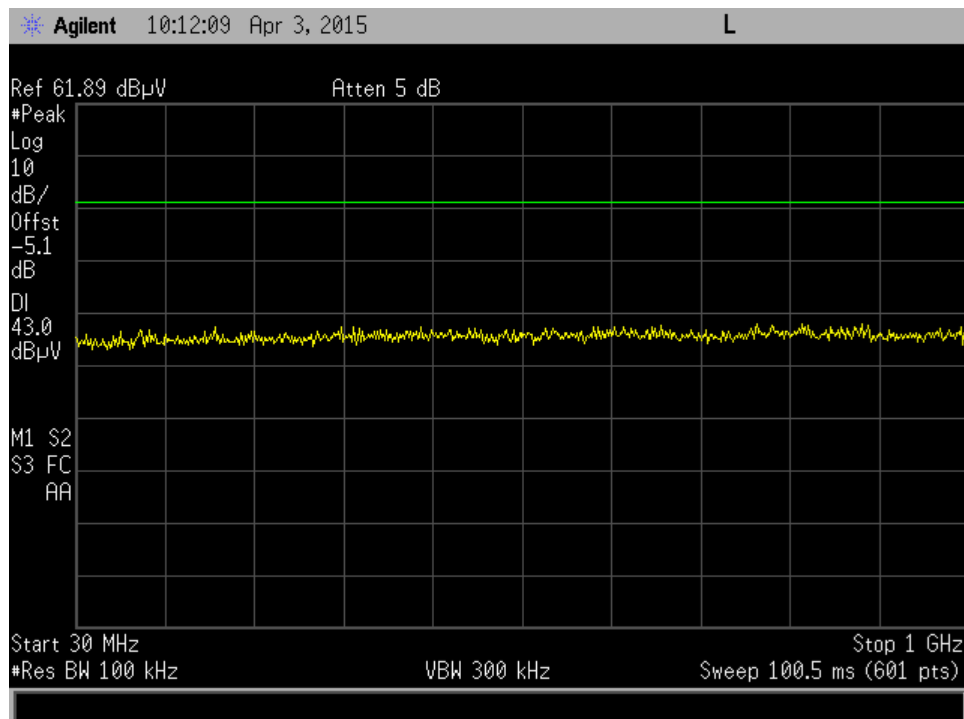


80MHz low ch 1 - 26.5GHz J7

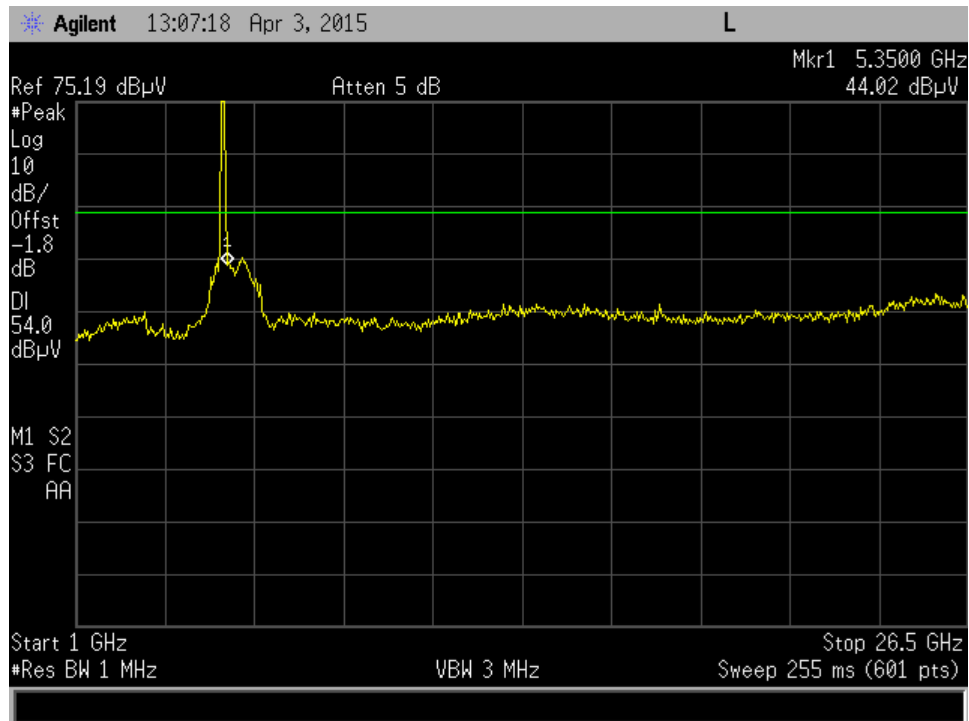




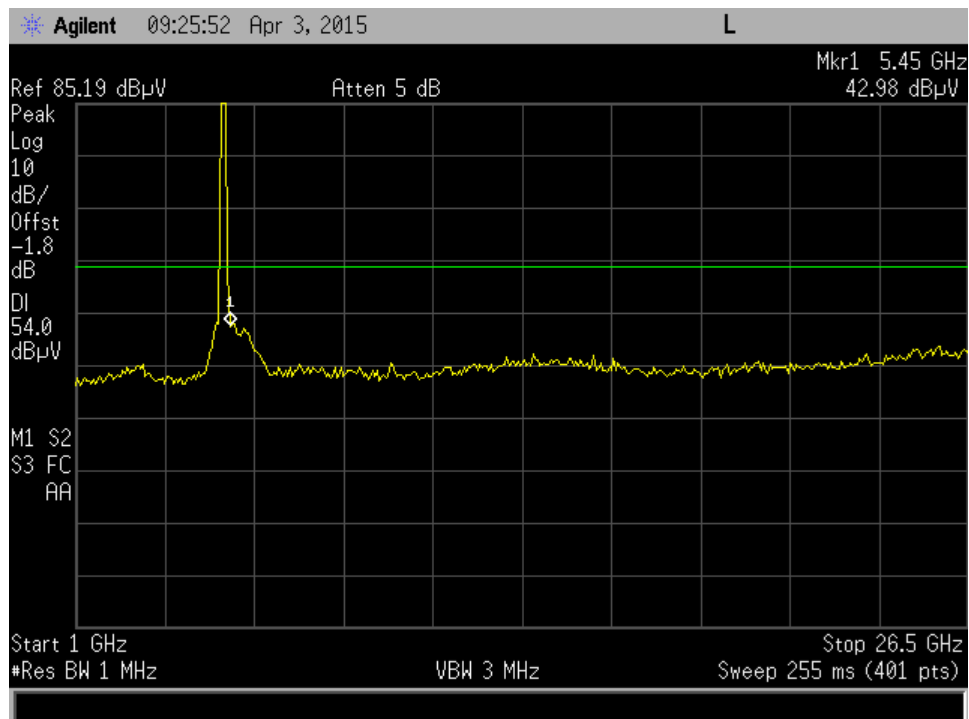
80MHz low ch 30 - 1000MHz J7



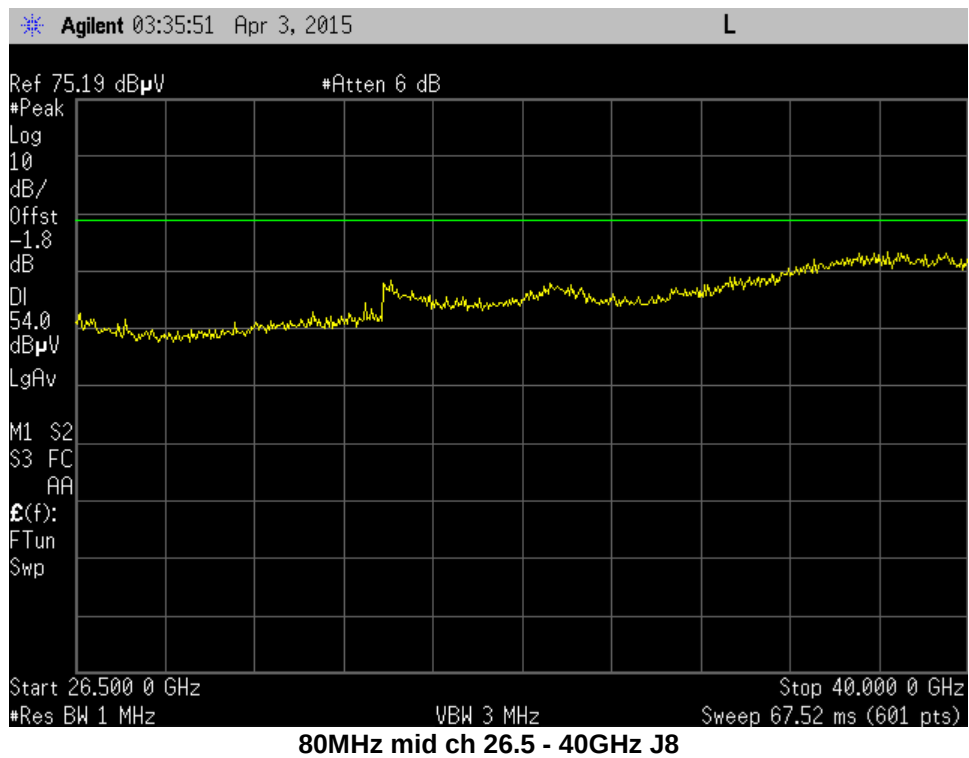
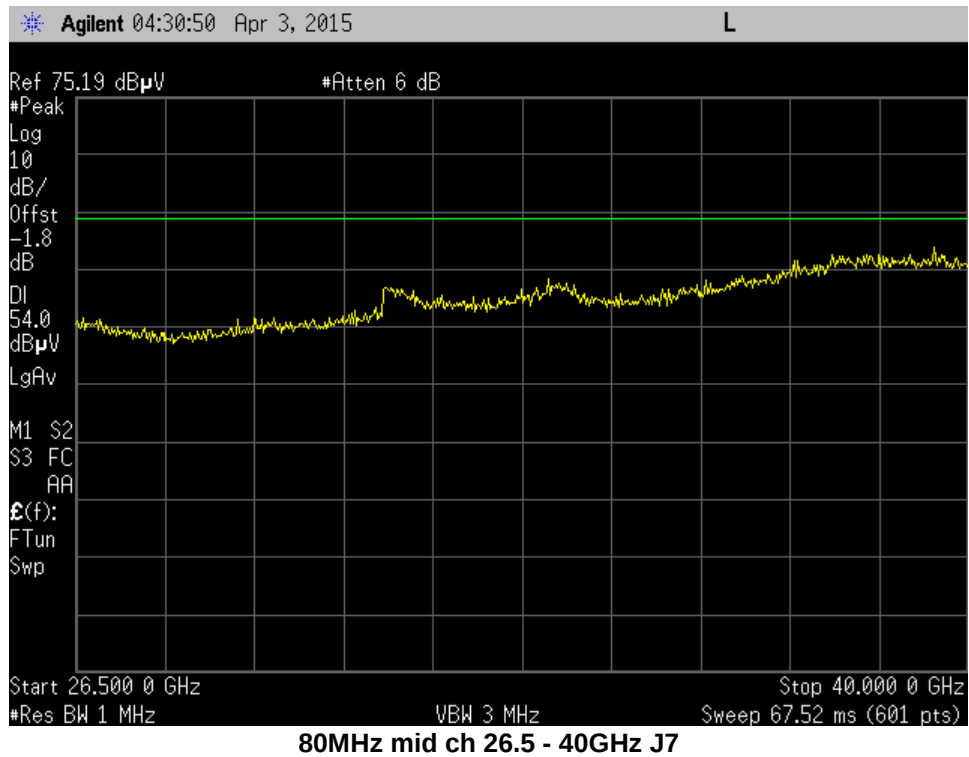
80MHz low ch 30 - 1000MHz J8

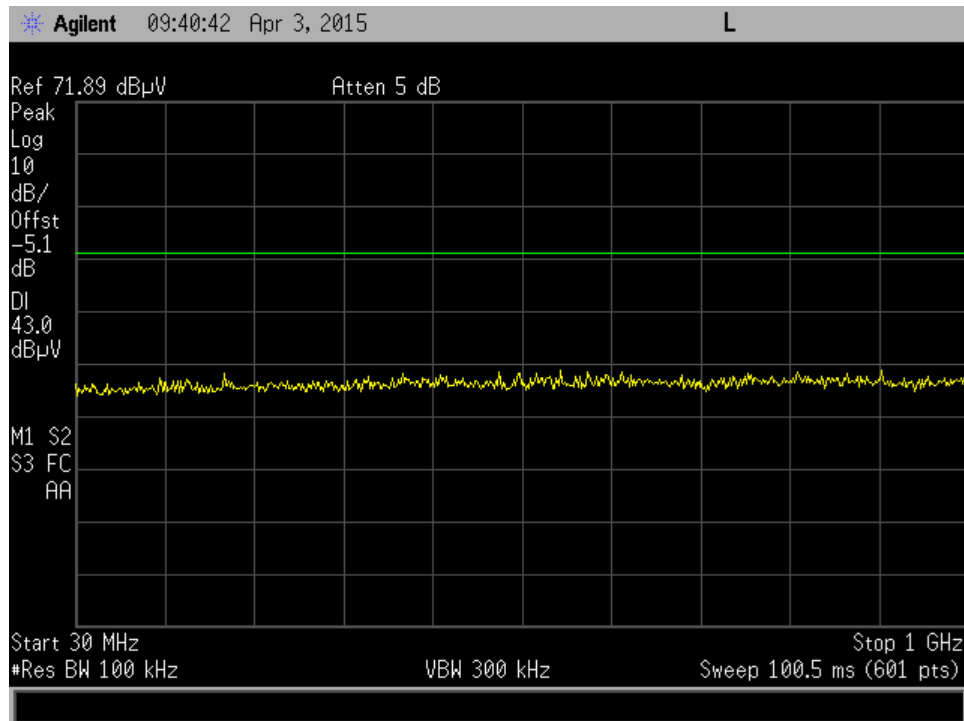


80MHz mid ch 1 - 26.5GHz J8

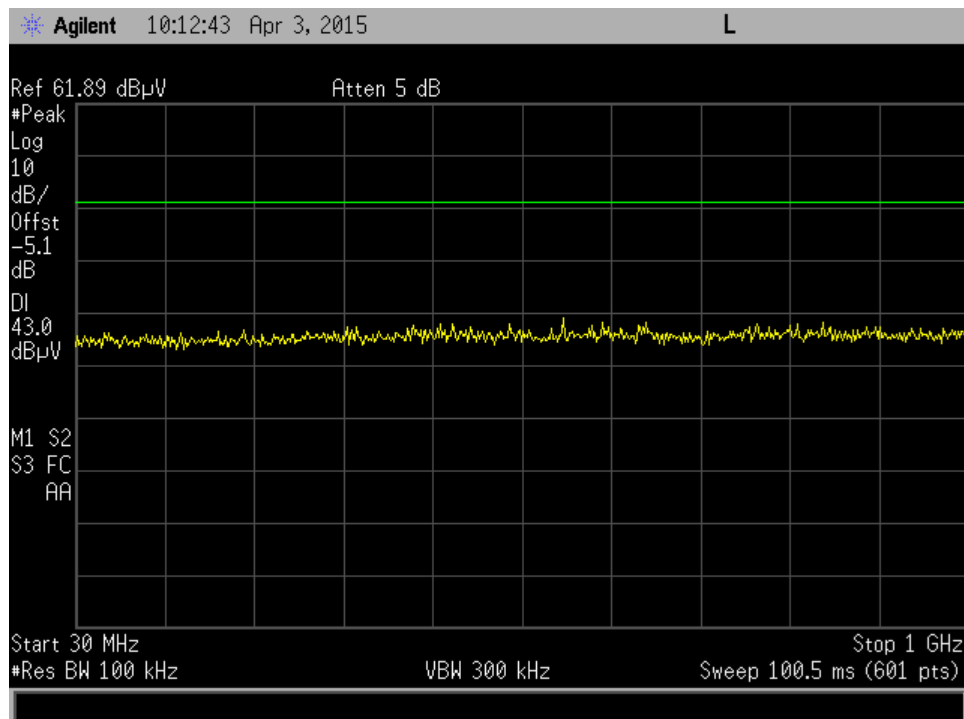


80MHz mid ch 1 - 26.5GHz J7





80MHz mid ch 30 - 1000MHz J7



80MHz mid ch 30 - 1000MHz J8

Band Edge_10dBi

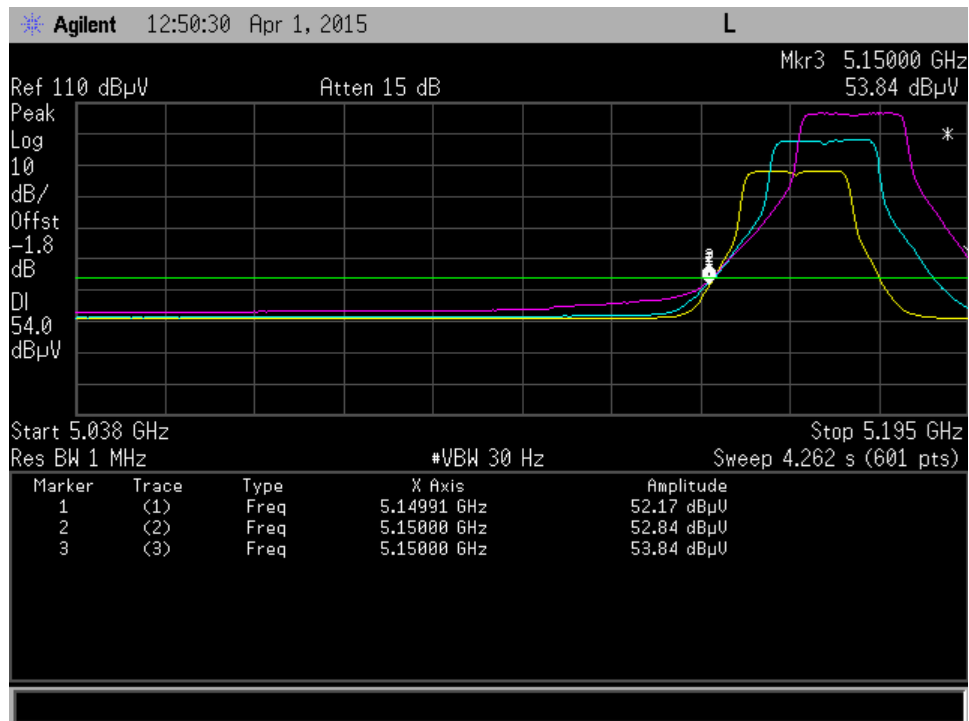
Bandwidth	Frequency	Data Rate	TP	J7 Level	J8 Level	Limit	J7 Margin	J8 Margin
MHz	MHz			dBm	dBm	dBm/MHz	dB	dB
10	5160	vt0	4	-37.57	-39.6	-27	-10.57	-12.6
10	5165	vt0	16	-40.53	-40.3	-27	-13.53	-13.3
10	5170	vt0	20	-38.13	-40.16	-27	-11.13	-13.16
20	5165	vt0	-6	-39.96	-42.12	-27	-12.96	-15.12
20	5170	vt0	8	-38.88	-40.33	-27	-11.88	-13.33
20	5175	vt0	15	-40.94	-38.4	-27	-13.94	-11.4
30	5170	vt0	-8	-37.83	-40.7	-27	-10.83	-13.7
30	5175	vt0	4	-38.59	-40.45	-27	-11.59	-13.45
30	5180	vt0	8	-39.86	-42.24	-27	-12.86	-15.24
40	5175	vf0	-8	-38.48	-40.77	-27	-11.48	-13.77
40	5180	vf0	2	-38.59	-39.43	-27	-11.59	-12.43
40	5185	vf0	7	-39.2	-41.14	-27	-12.2	-14.14
50	5180	vf0	-8	-39.14	-40.71	-27	-12.14	-13.71
50	5185	vf0	-3	-38.57	-43.02	-27	-11.57	-16.02
50	5190	vf0	6	-39.17	-40.44	-27	-12.17	-13.44
60	5185	vf0	-9	-39.02	-40.99	-27	-12.02	-13.99
60	5190	vf0	-4	-38.93	-40.25	-27	-11.93	-13.25
60	5195	vf0	4	-38.77	-39.32	-27	-11.77	-12.32
80	5195	ve00	-8	-38.75	-40.29	-27	-11.75	-13.29
80	5200	ve00	-5	-39.54	-40.76	-27	-12.54	-13.76

10MHz low band edge J7

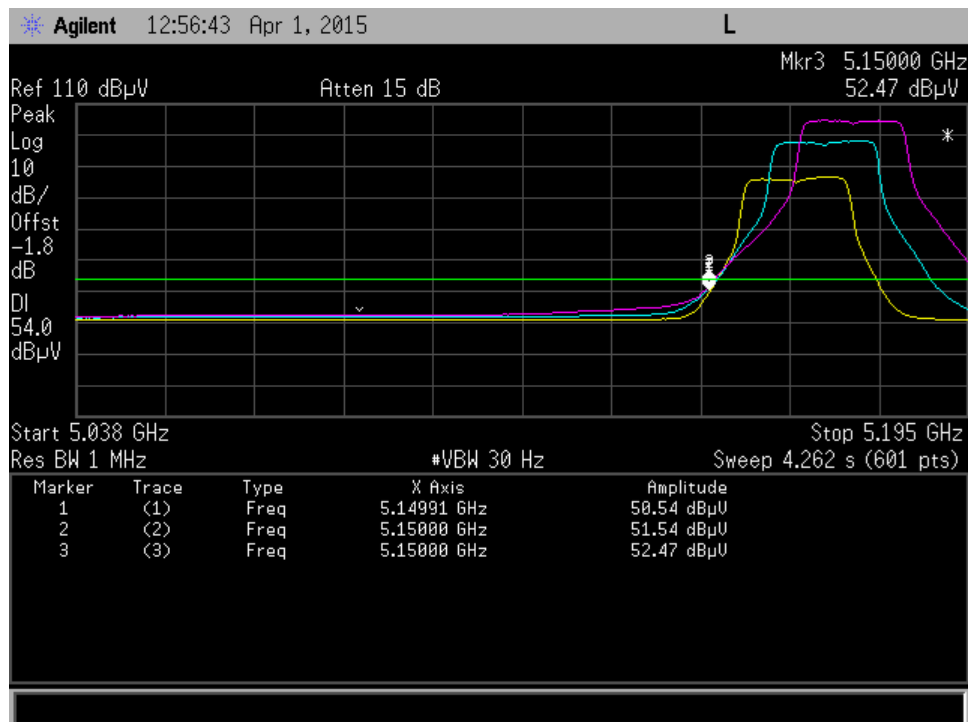




10MHz low band edge J8

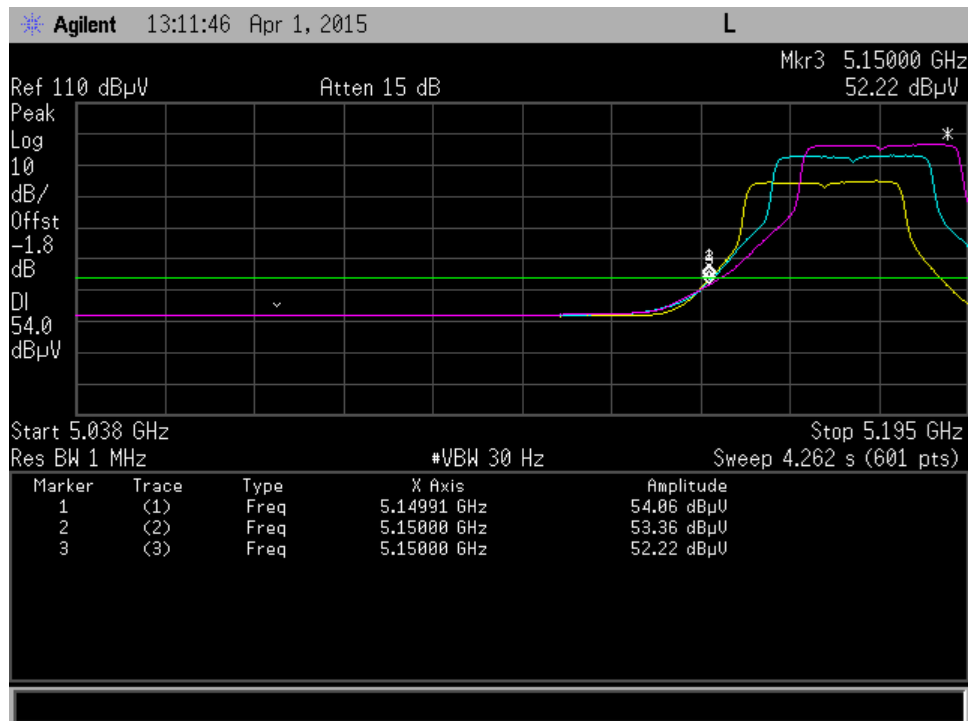


20MHz low band edge J7

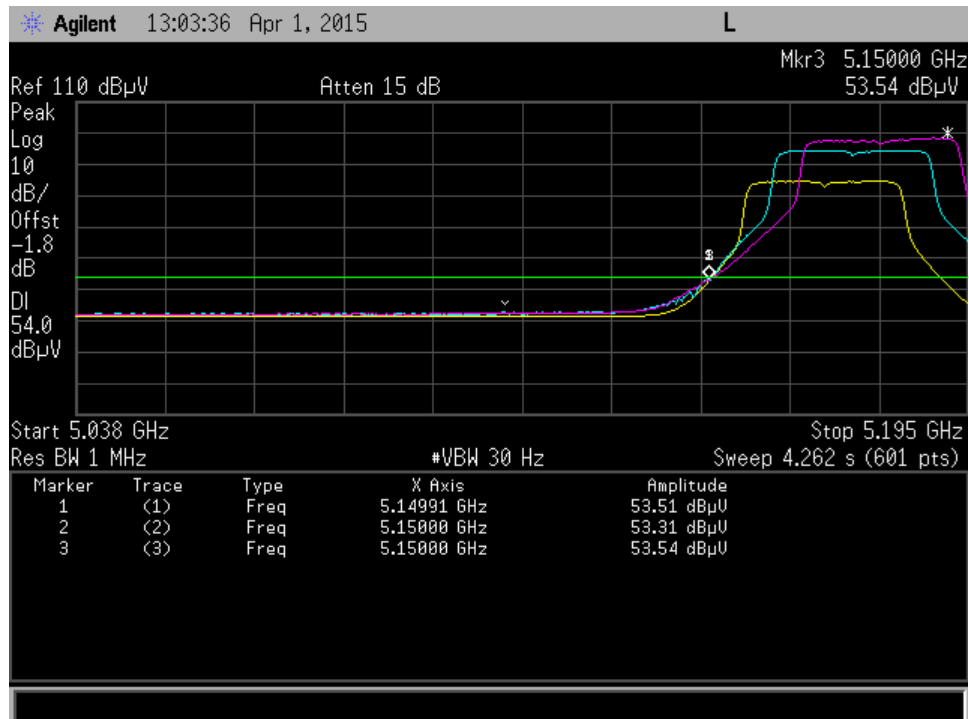




20MHz low band edge J8

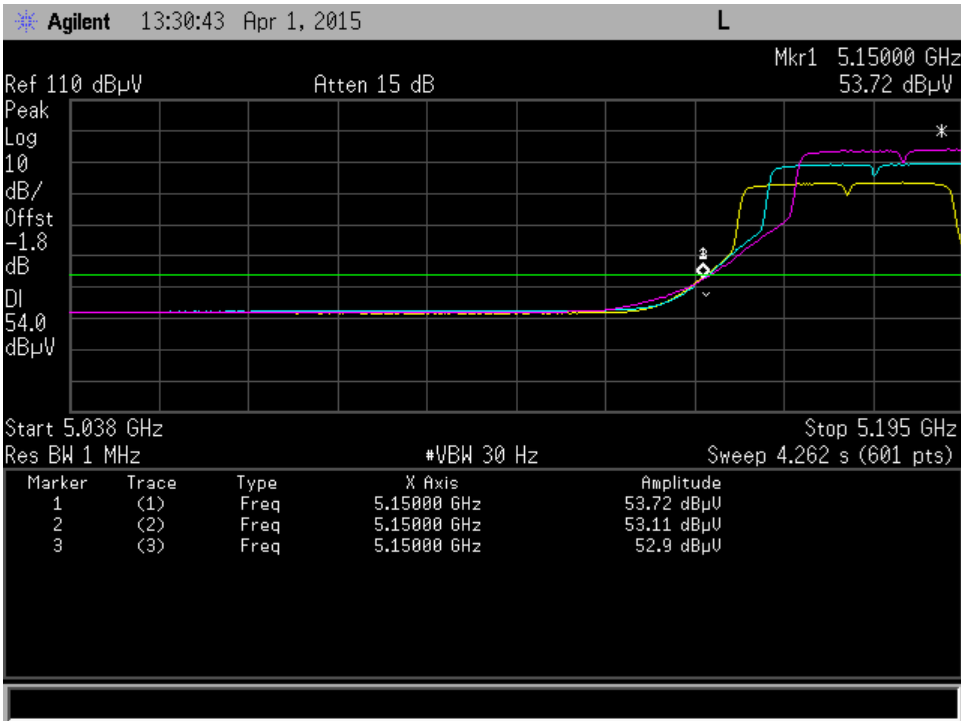


30MHz low band edge J7



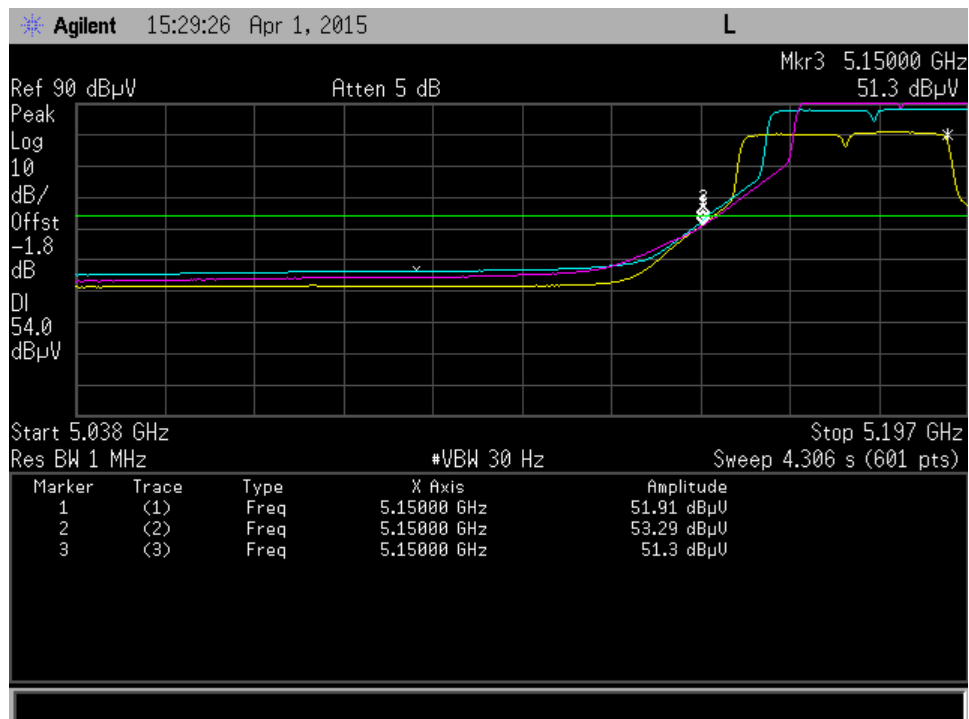


30MHz low band edge J8

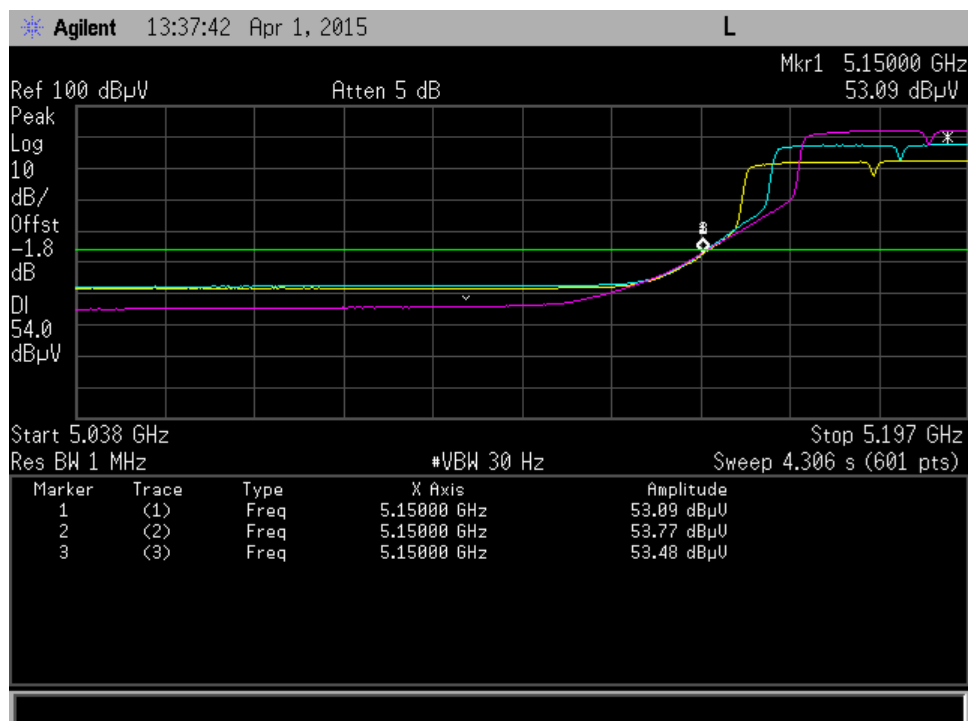




40MHz low band edge J7

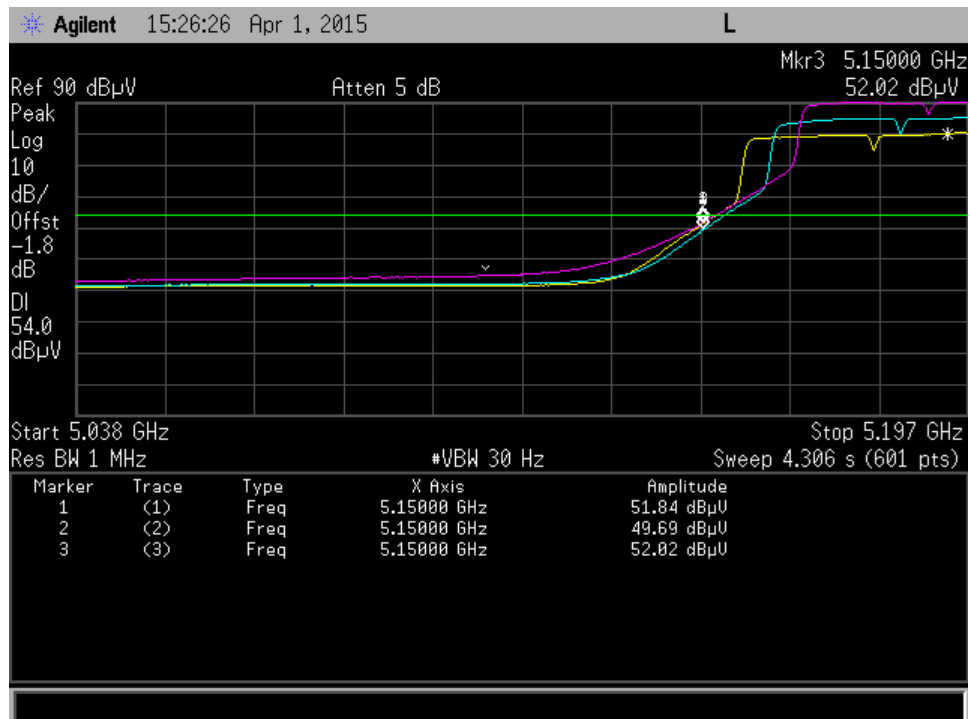


40MHz low band edge J8

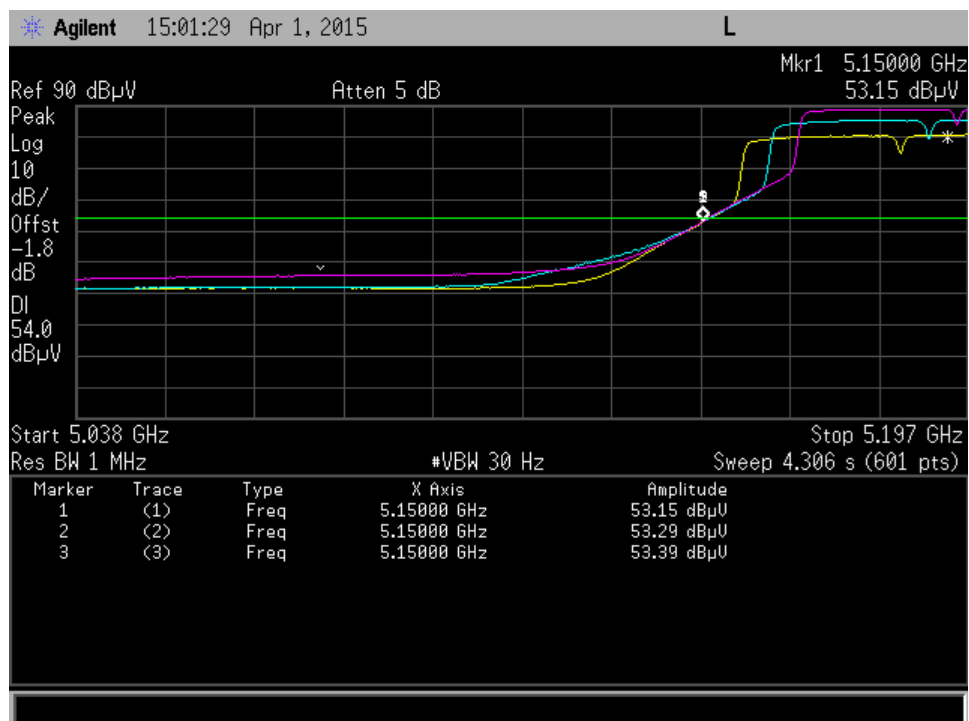




50MHz low band edge J7

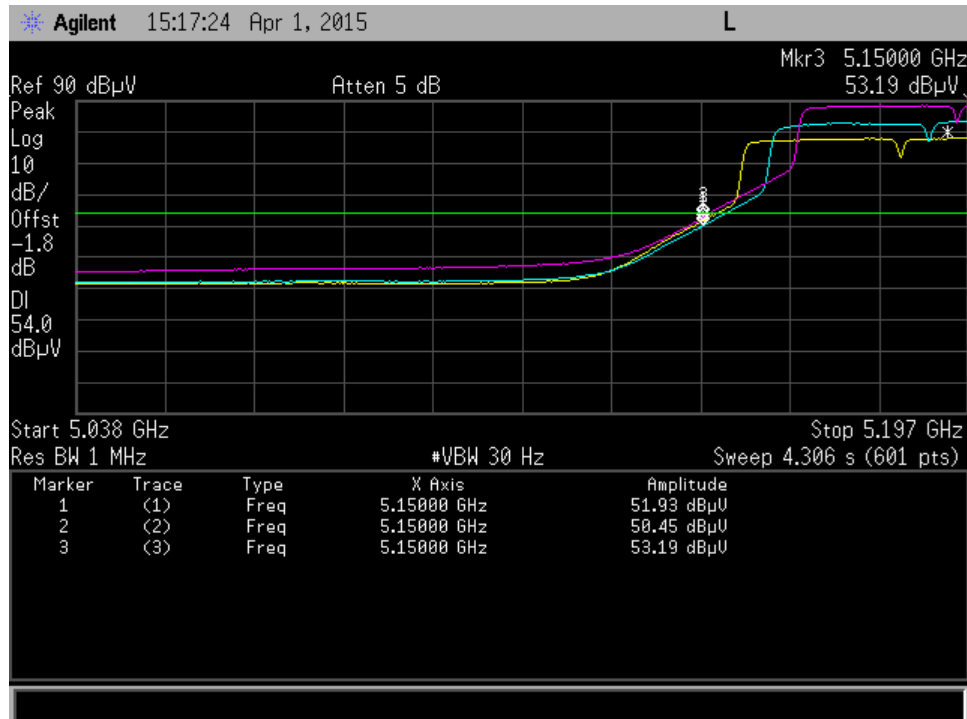


50MHz low band edge J8

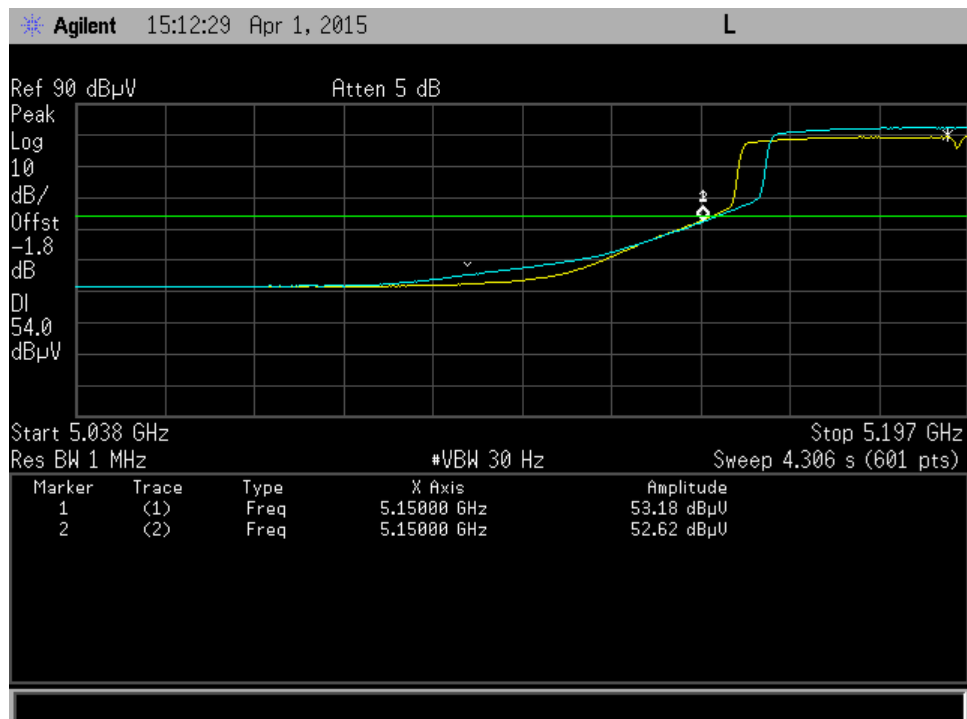




60MHz low band edge J7

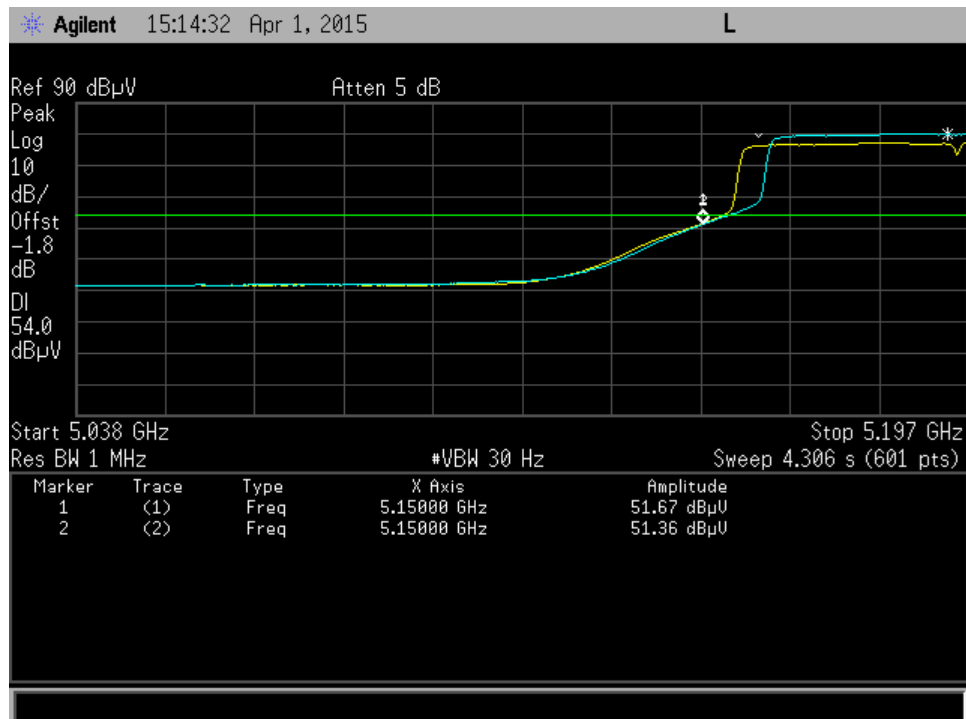


60MHz low band edge J8





80MHz low band edge J7



80MHz low band edge J8

