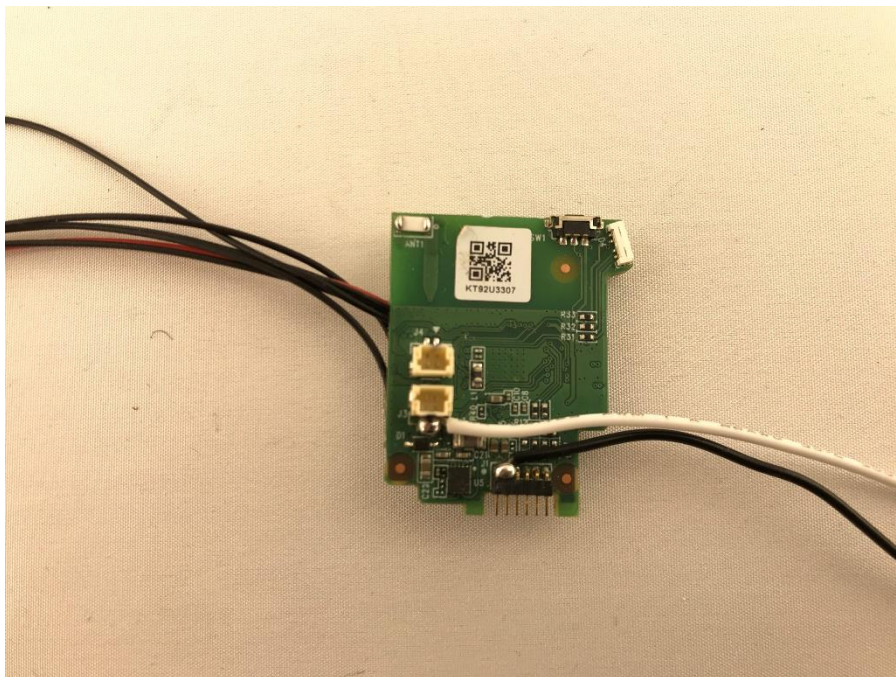


Photograph 1: Top View of Circuit Board



Photograph 2: Bottom View of Circuit Board