

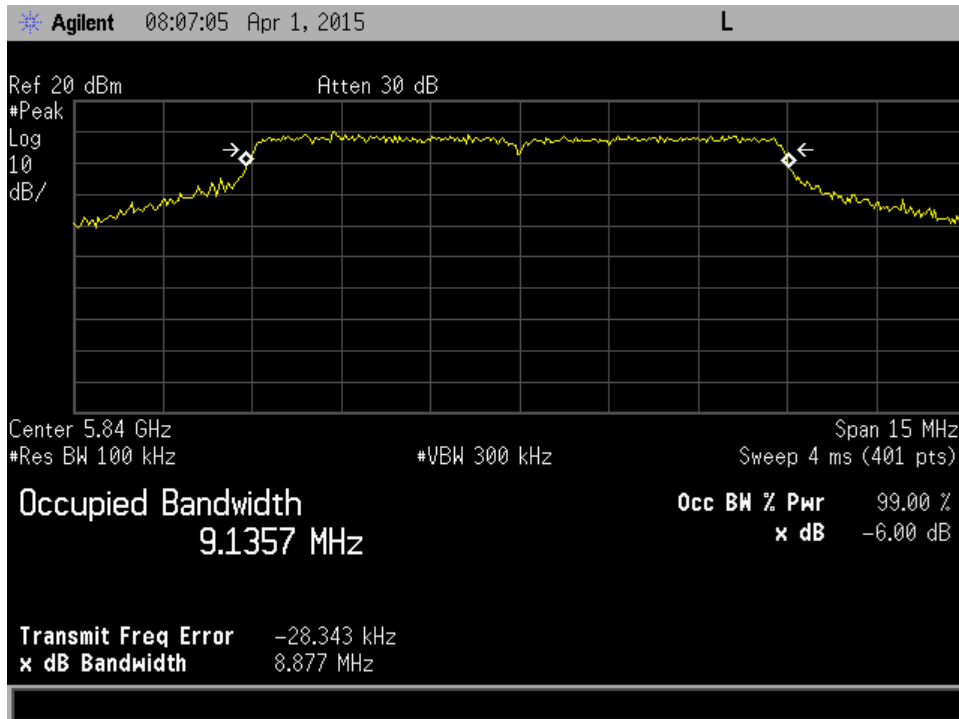


# **Annex D**

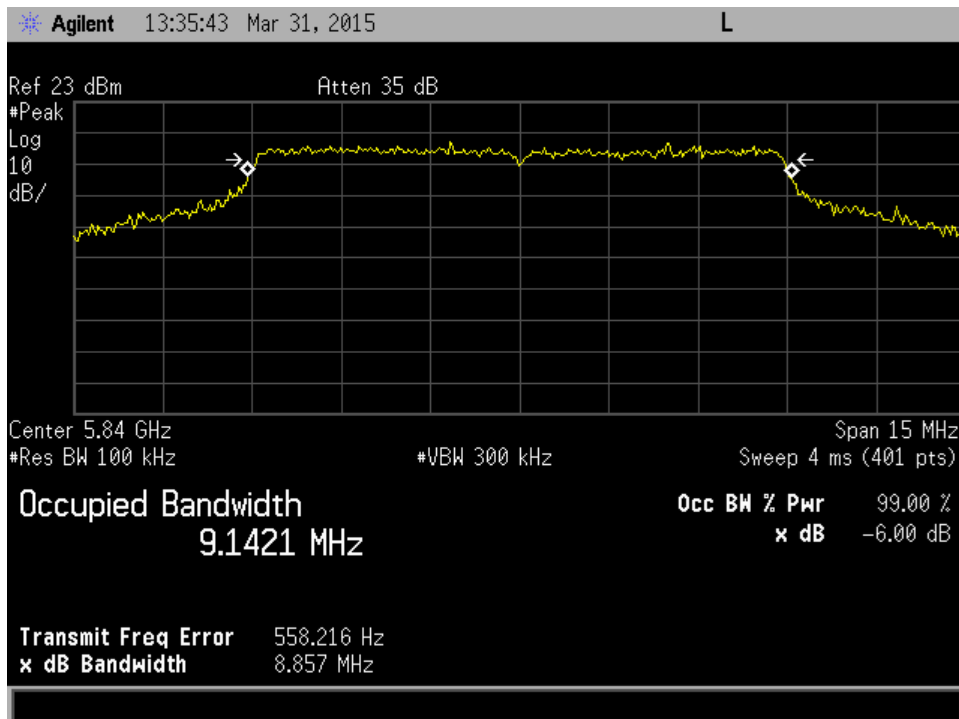
## **Occupied Bandwidth**



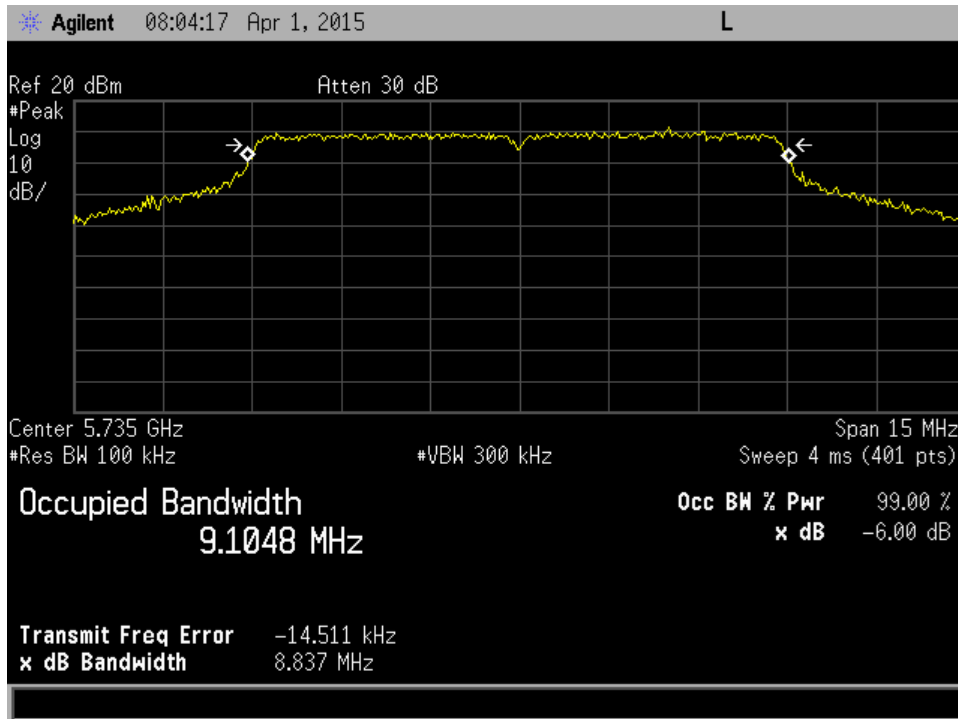
### 10Mhz high ch\_J13



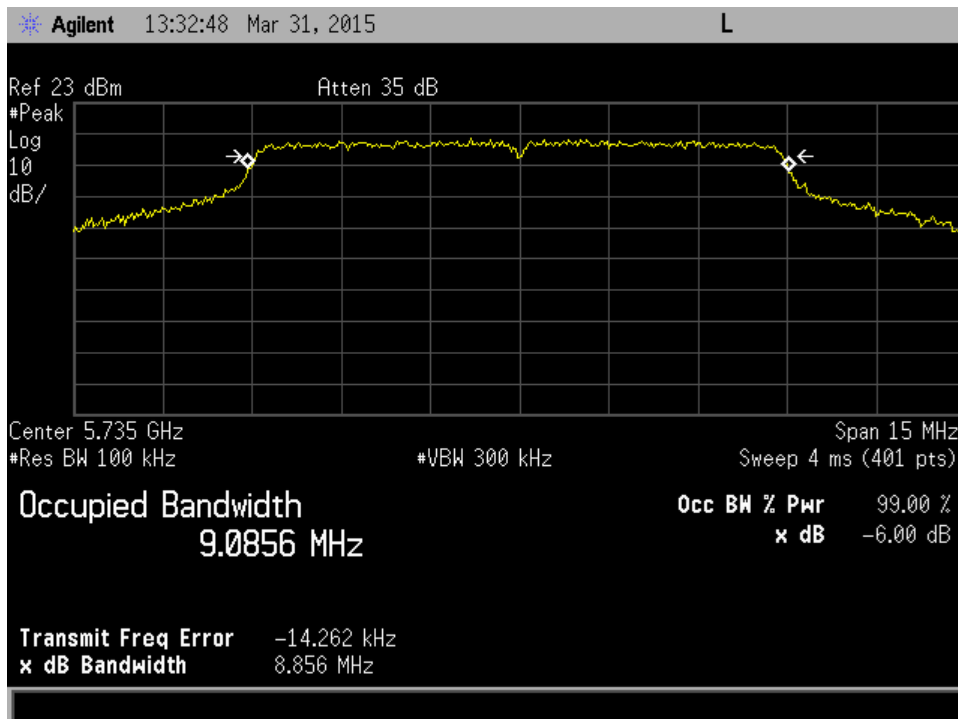
### 10Mhz high ch\_J7



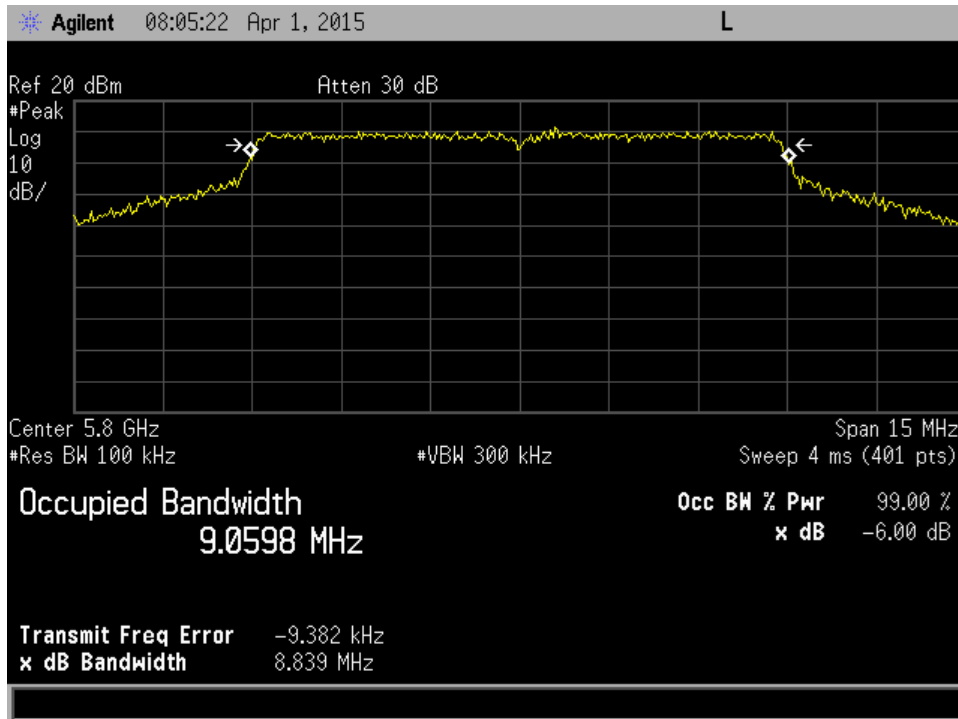
### 10Mhz low ch\_J13



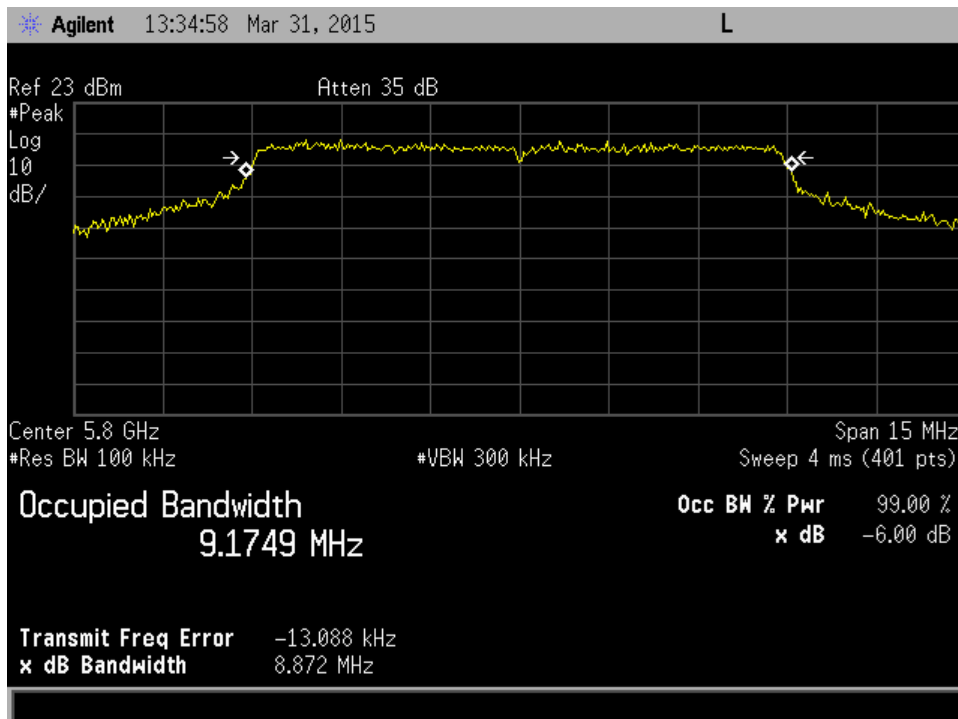
**10Mhz low ch\_J7**



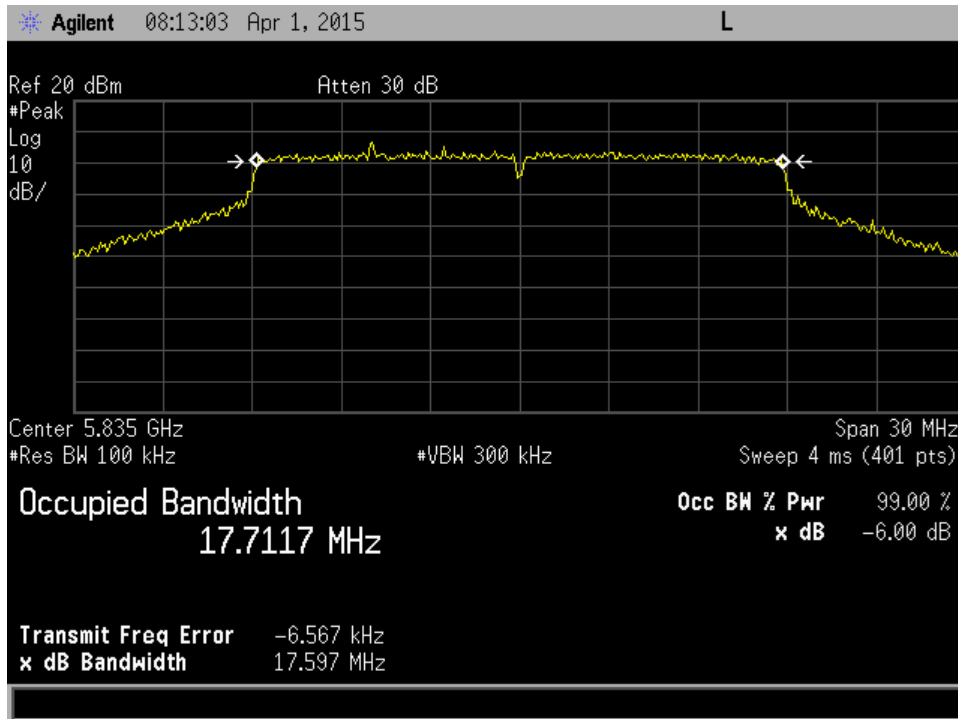
**10Mhz mid ch\_J13**



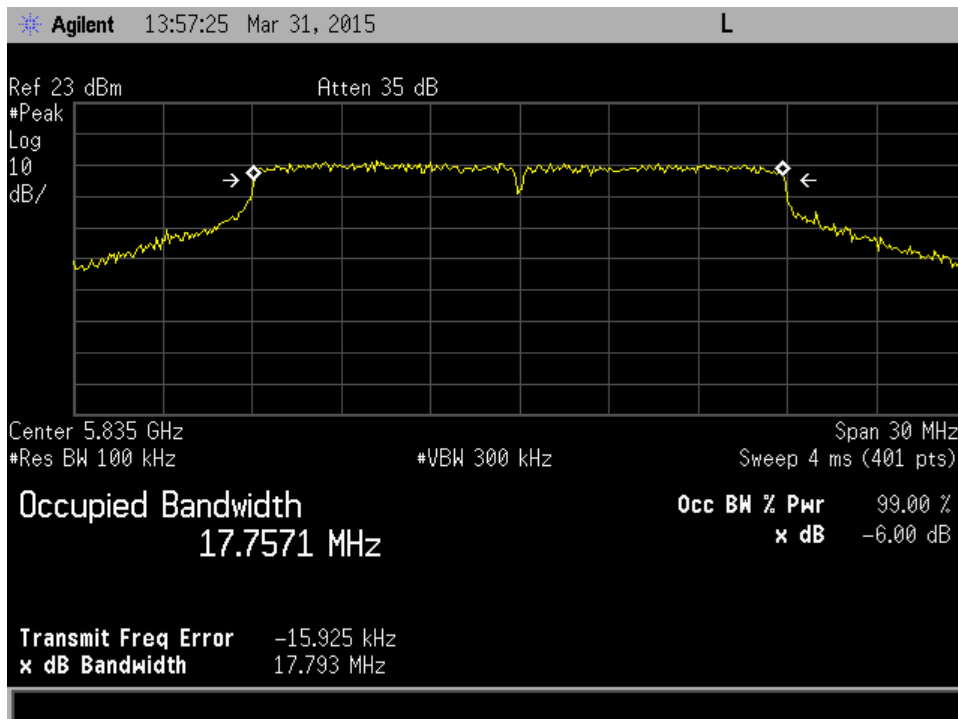
**10Mhz mid ch\_J7**



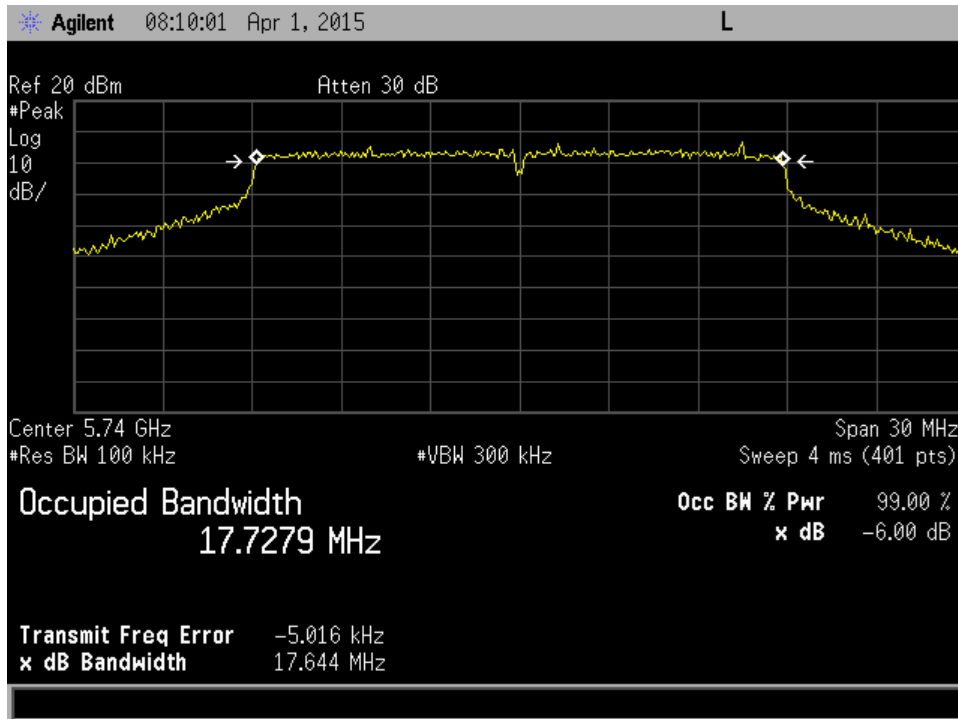
**20Mhz high ch\_J13**



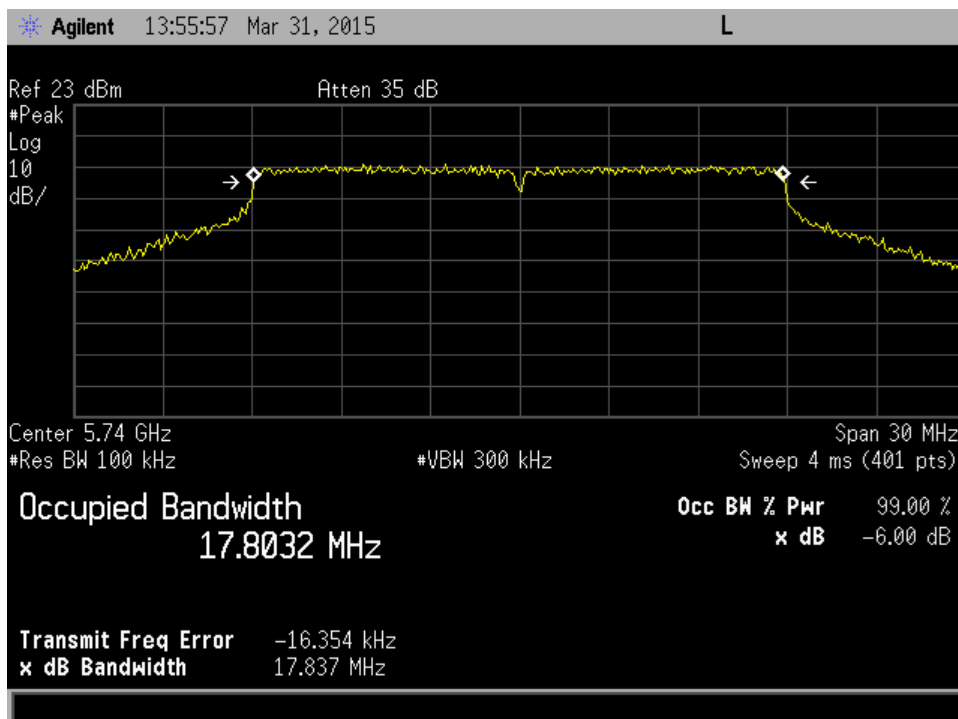
20Mhz high ch\_J7



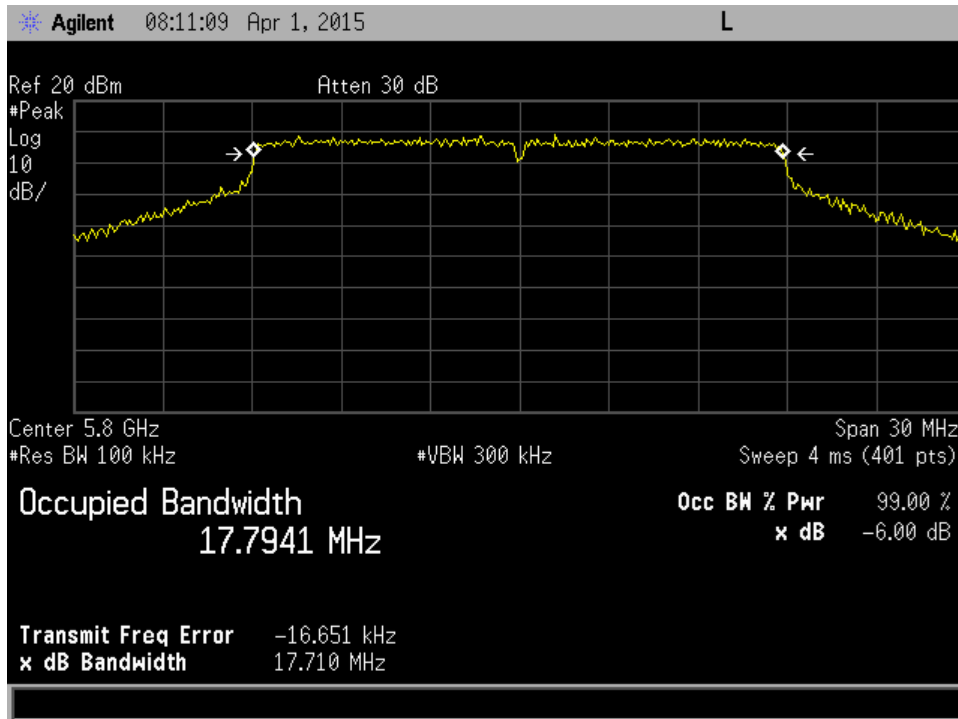
20Mhz low ch\_J13



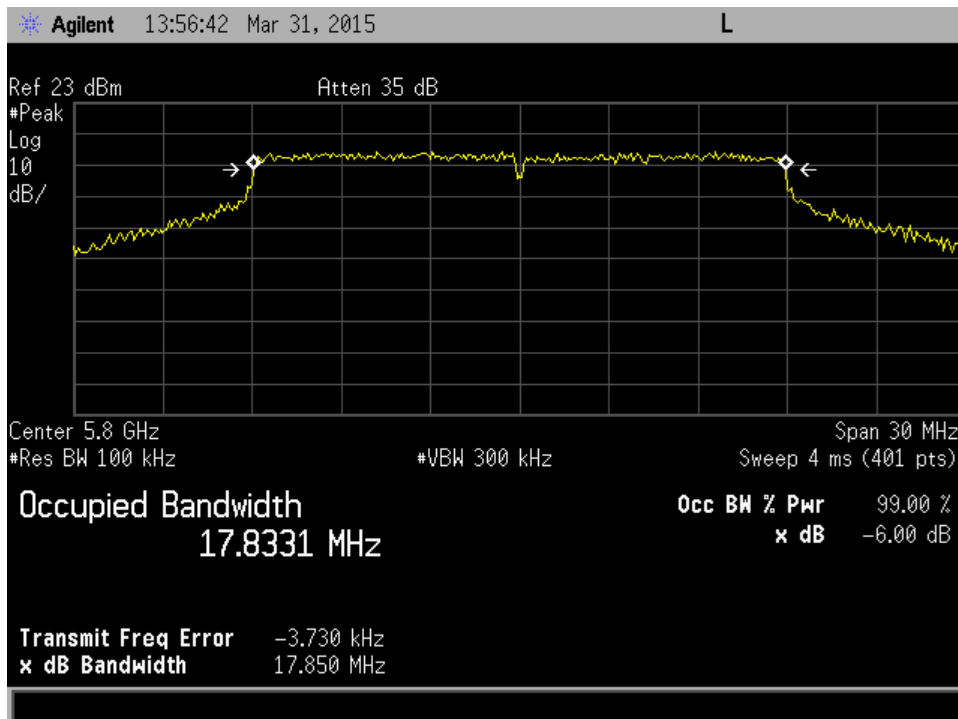
20Mhz low ch\_J7



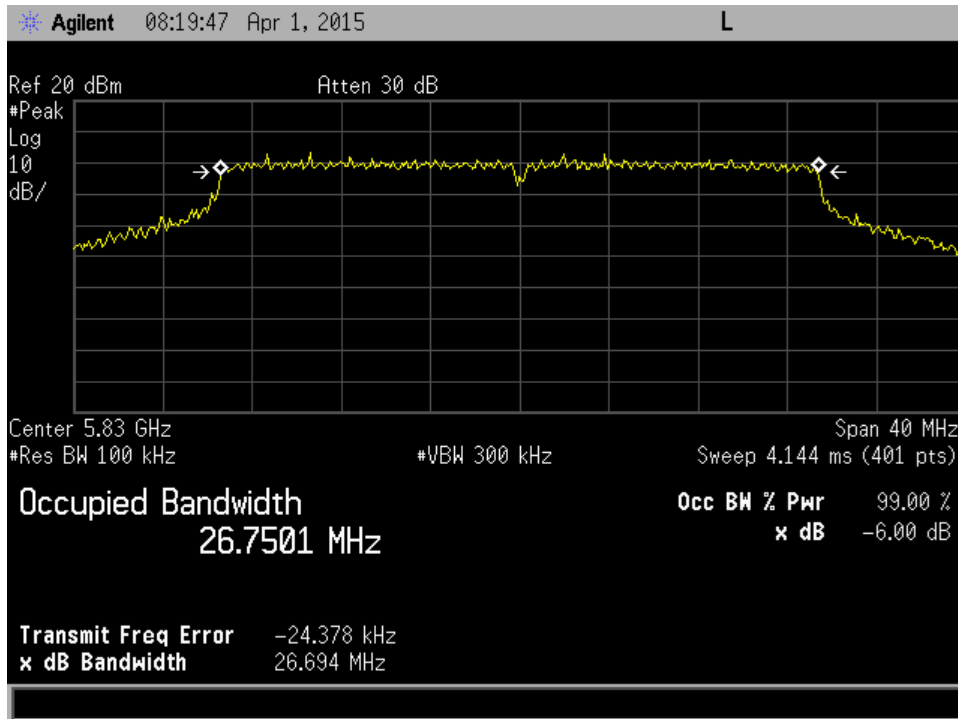
20Mhz mid ch\_J13



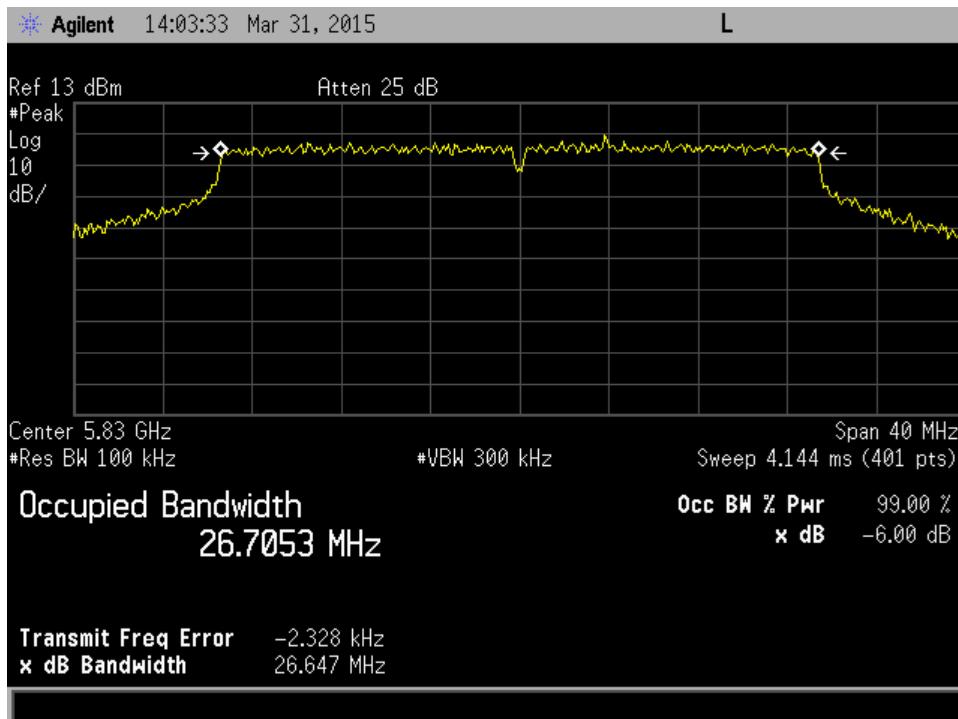
20Mhz mid ch\_J7



30Mhz high ch\_J13

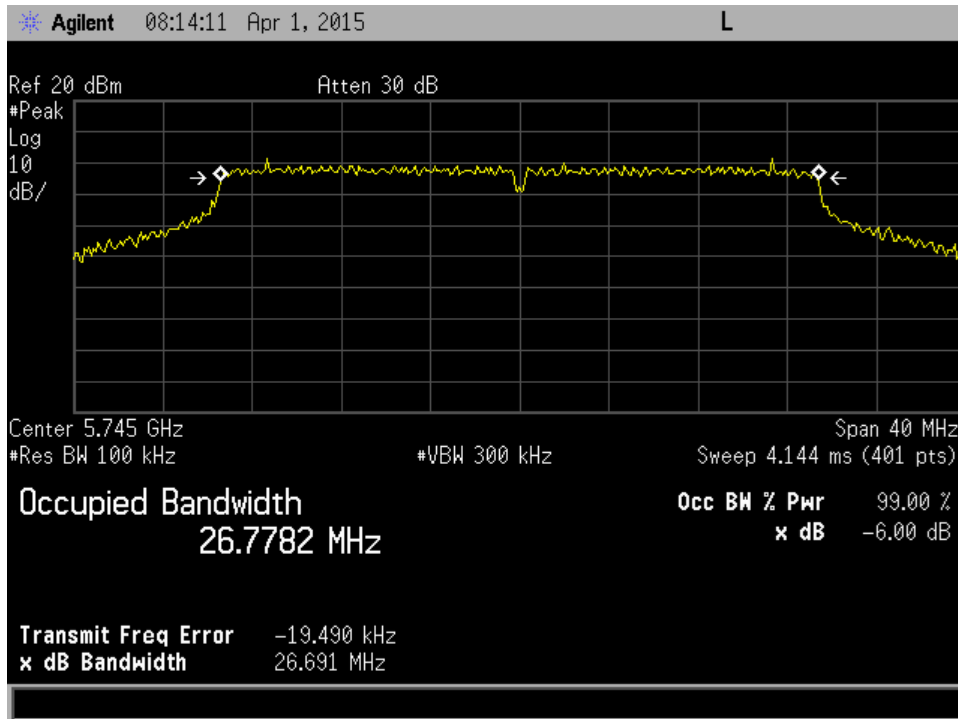


### 30Mhz high ch\_J7

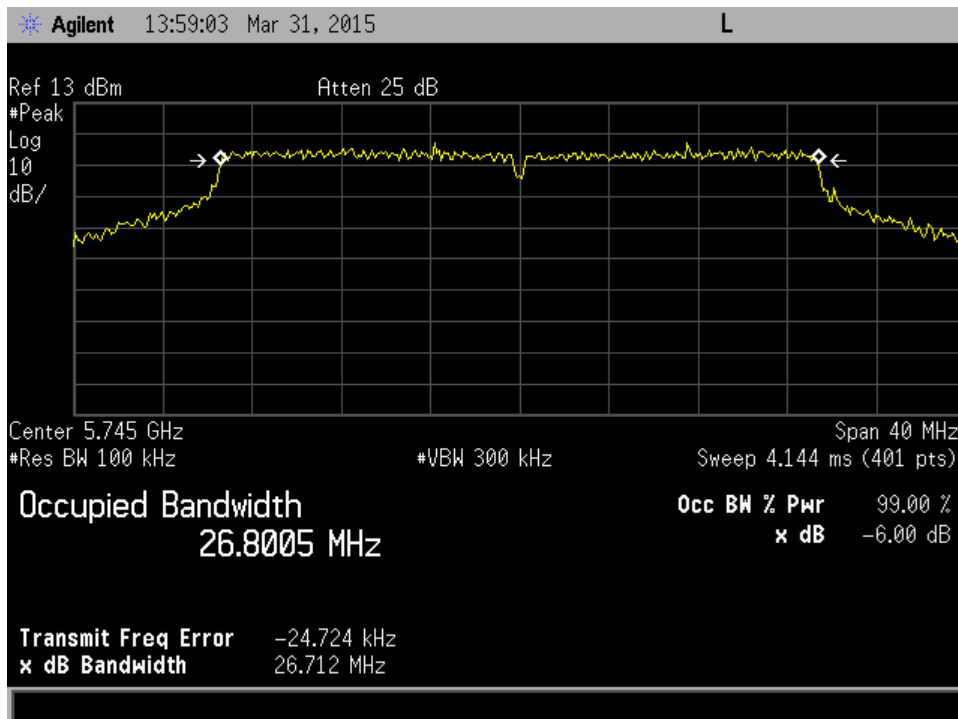


### 30Mhz low ch\_J13

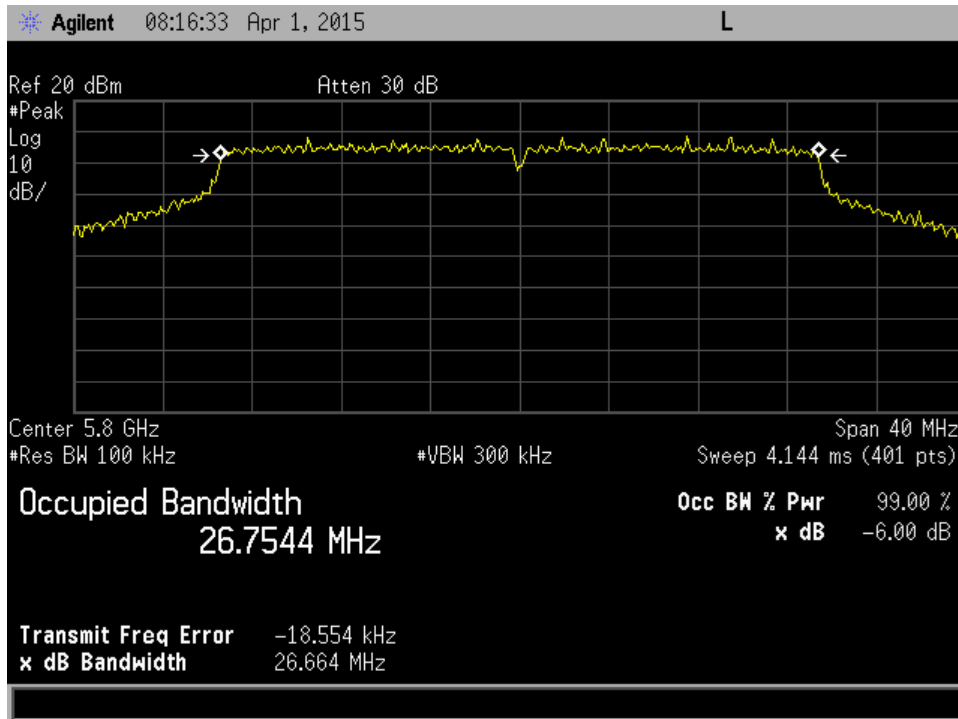




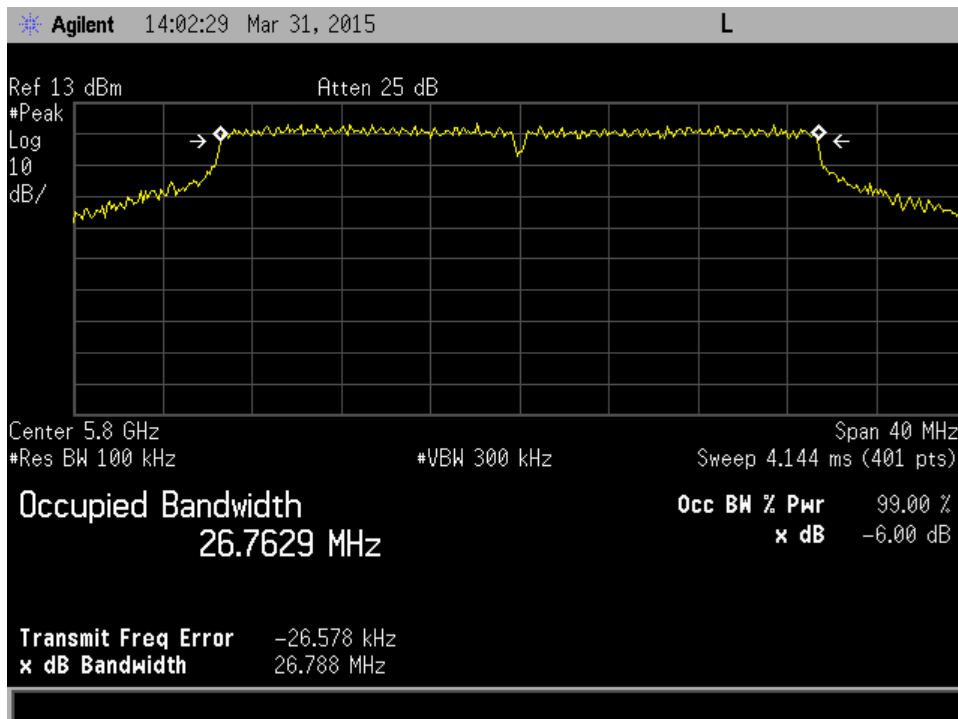
### 30Mhz low ch\_J7



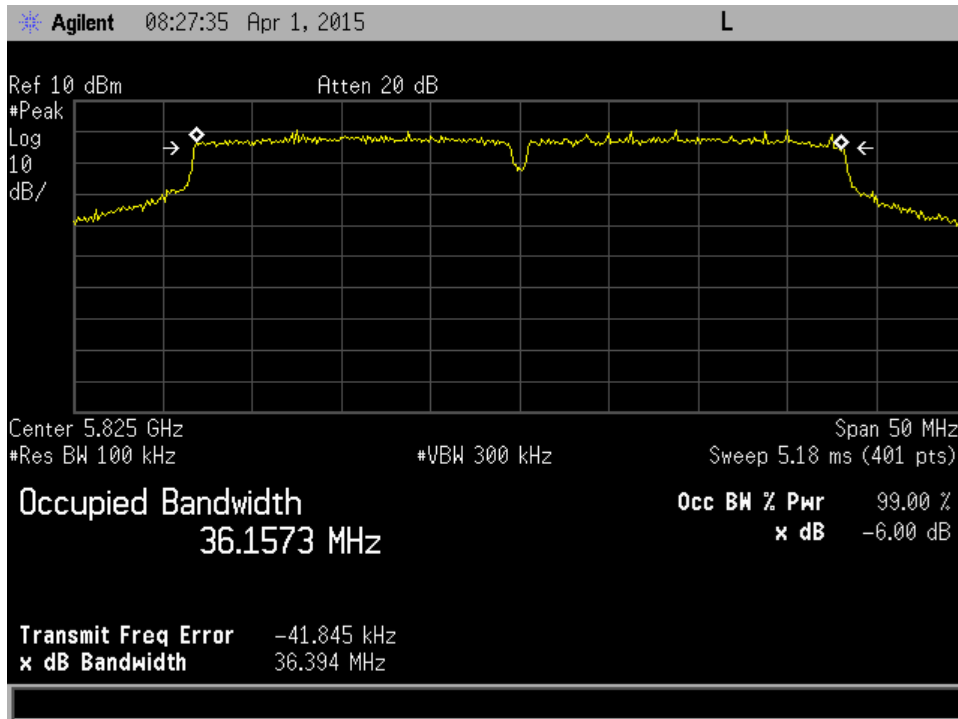
### 30Mhz mid ch\_J13



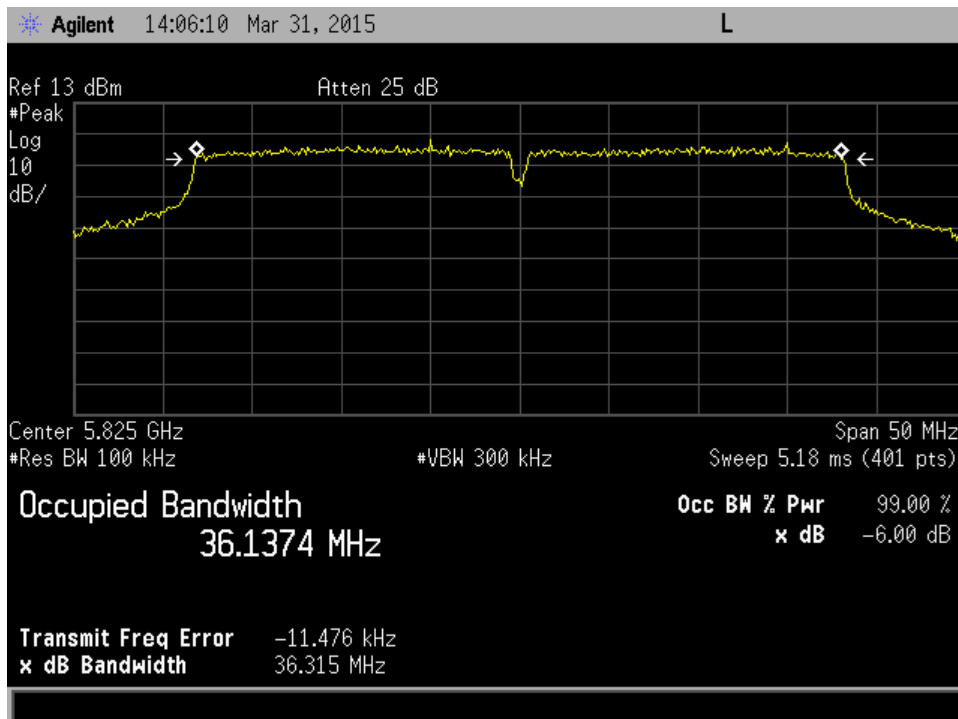
### 30Mhz mid ch\_J7



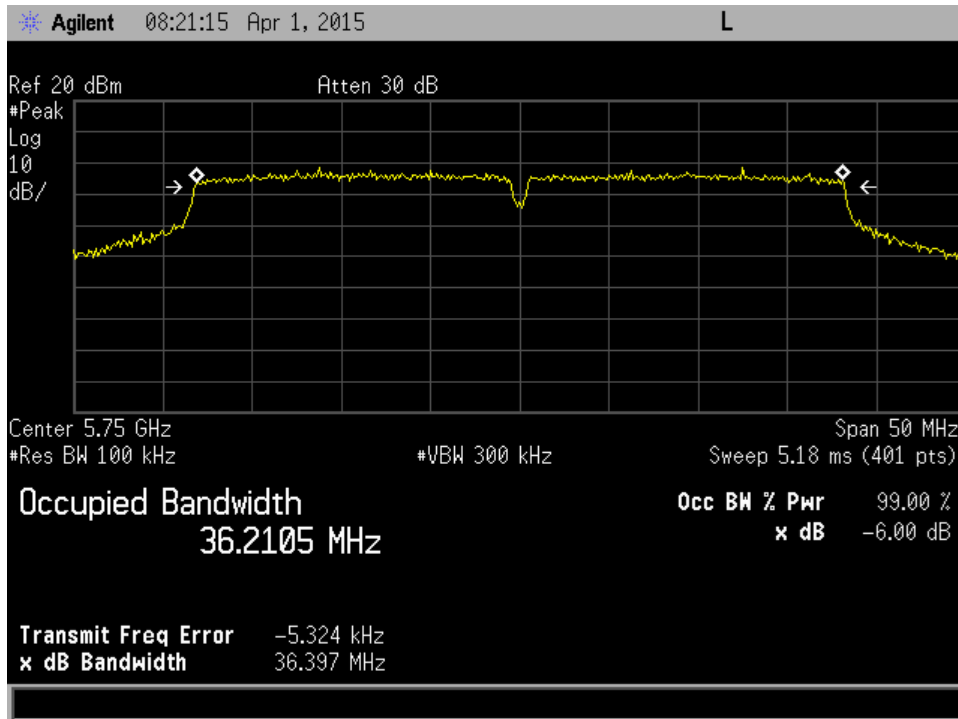
### 40Mhz high ch\_J13



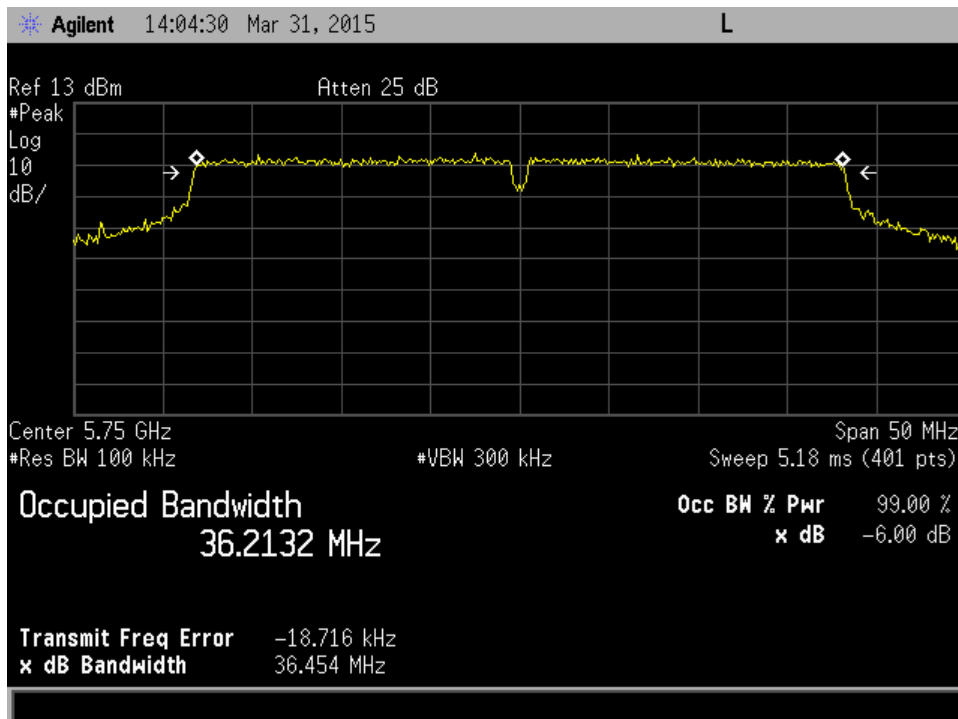
40MHz high ch\_J7



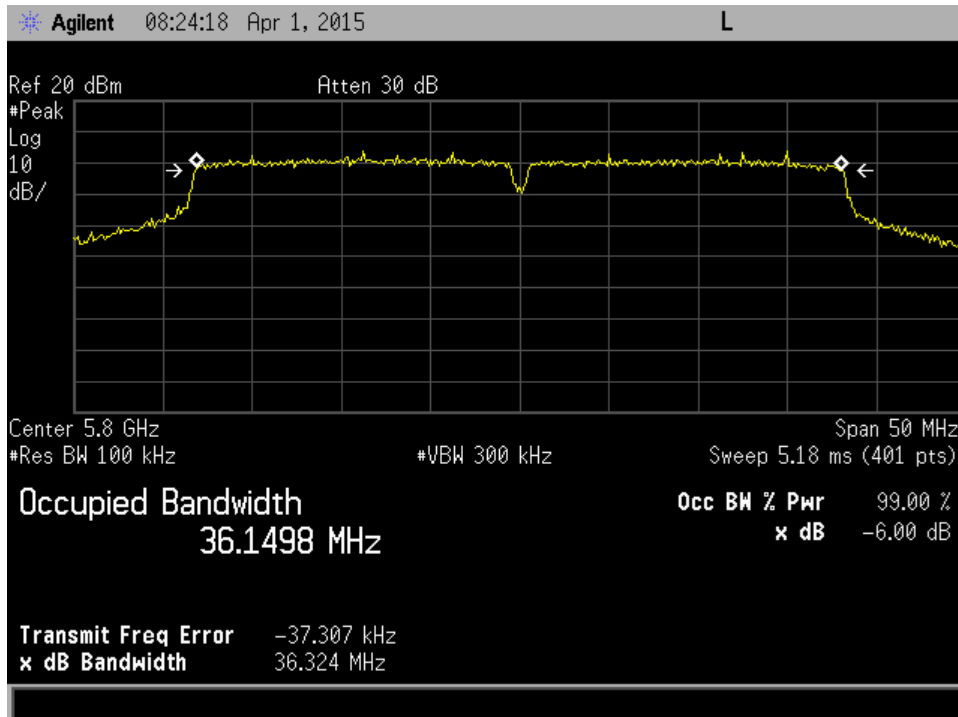
40MHz low ch\_J13



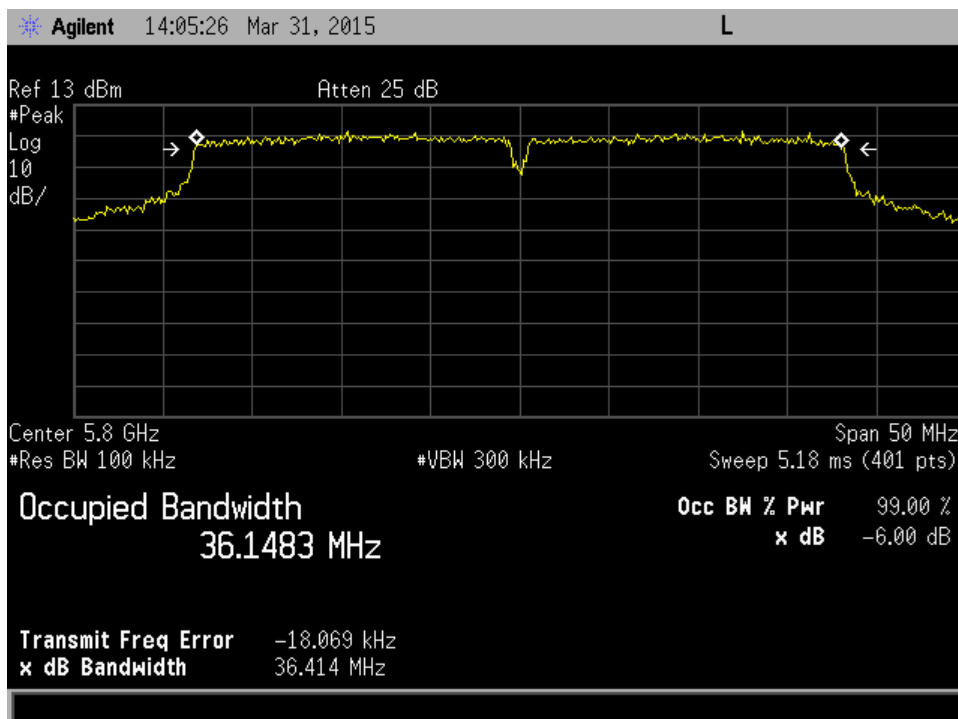
40Mhz low ch\_J7



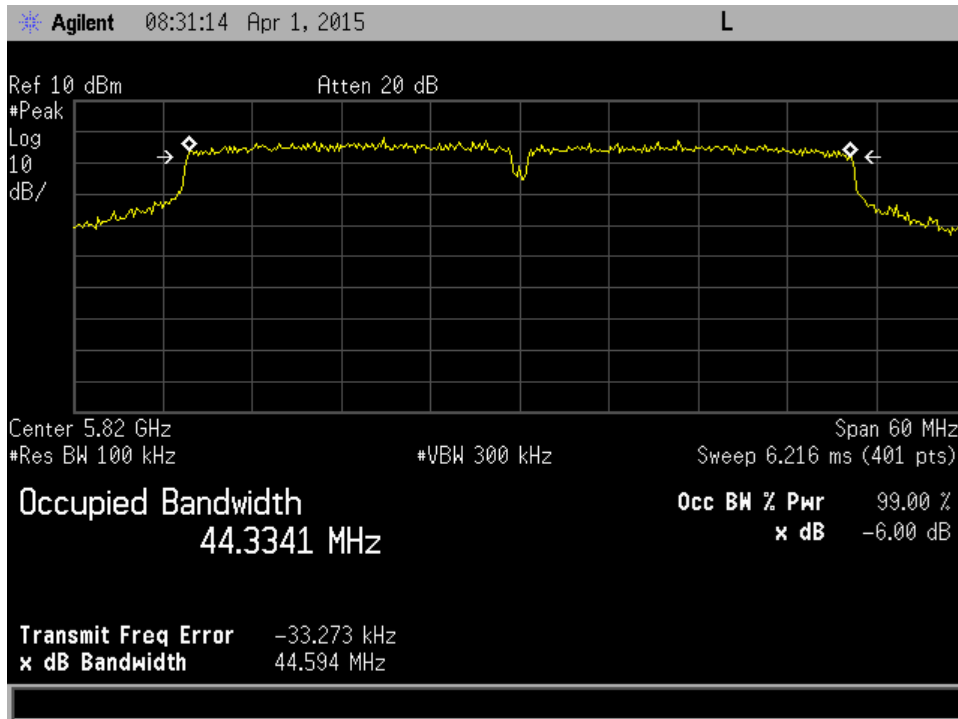
40Mhz mid ch\_J13



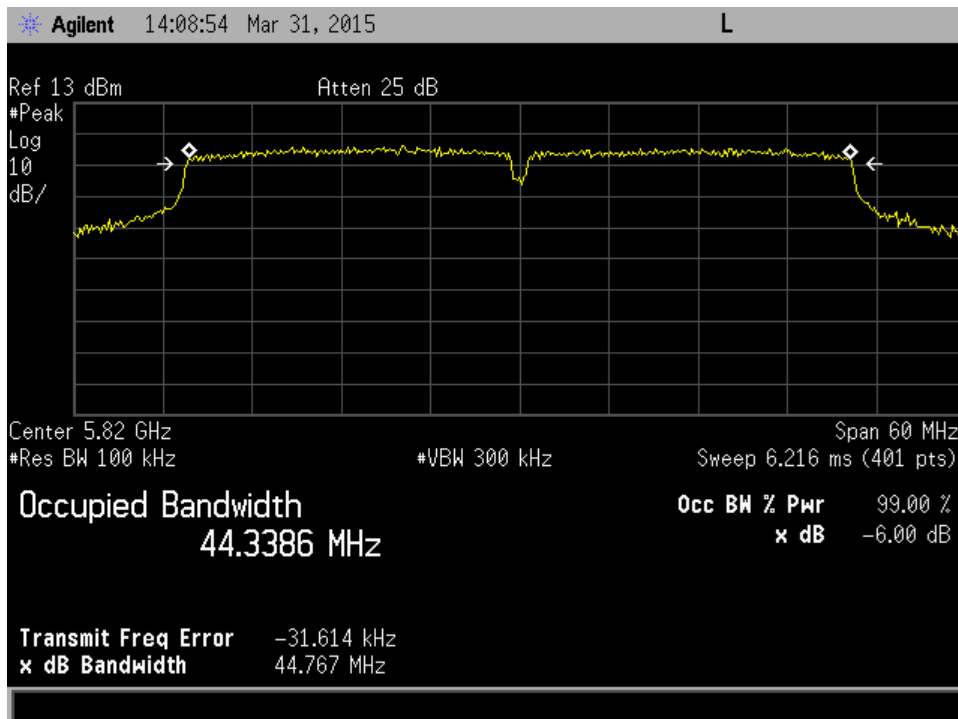
40Mhz mid ch\_J7



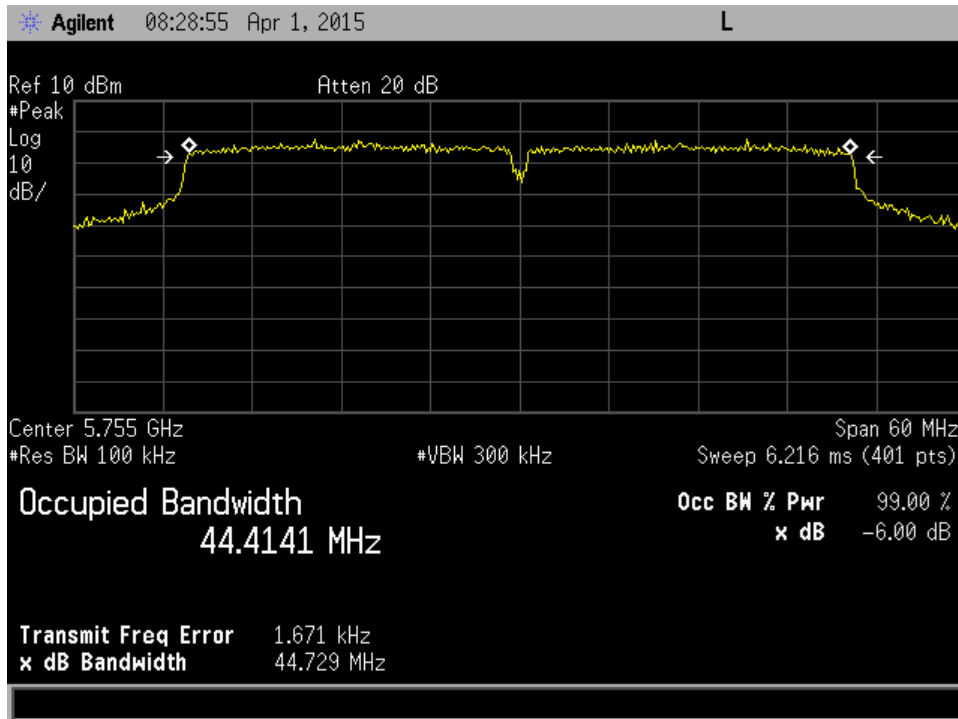
50Mhz high ch\_J13



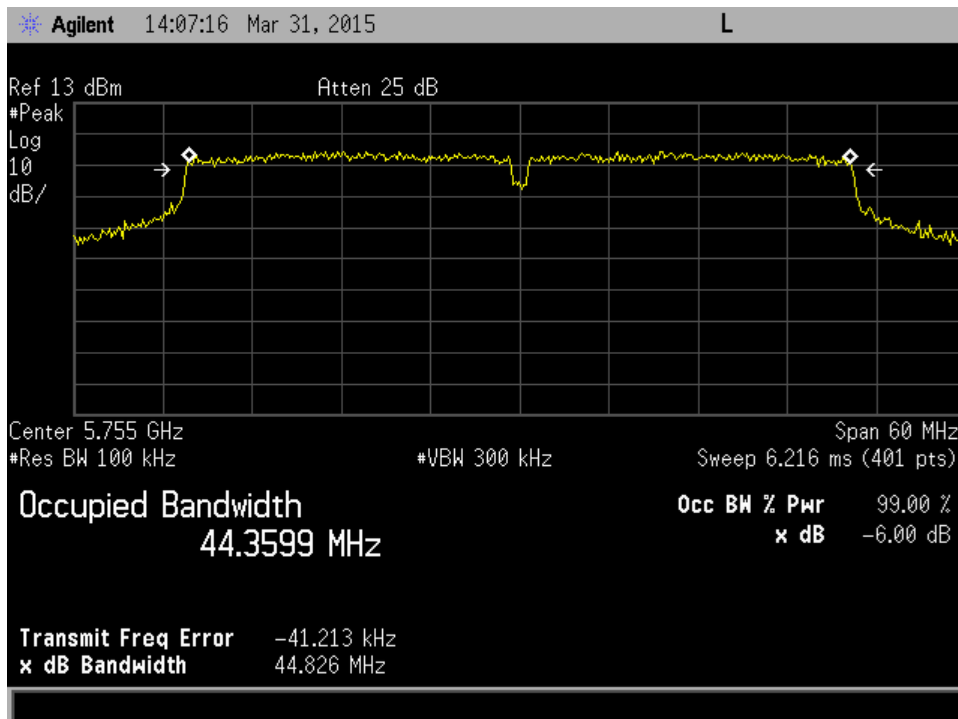
**50Mhz high ch\_J7**



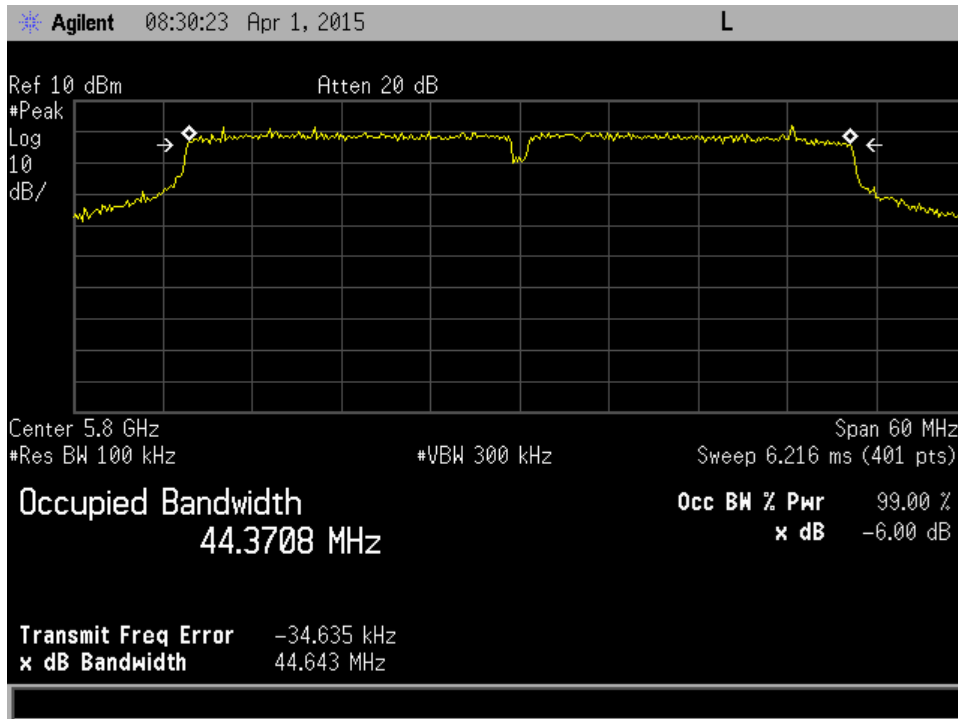
**50Mhz low ch\_J13**



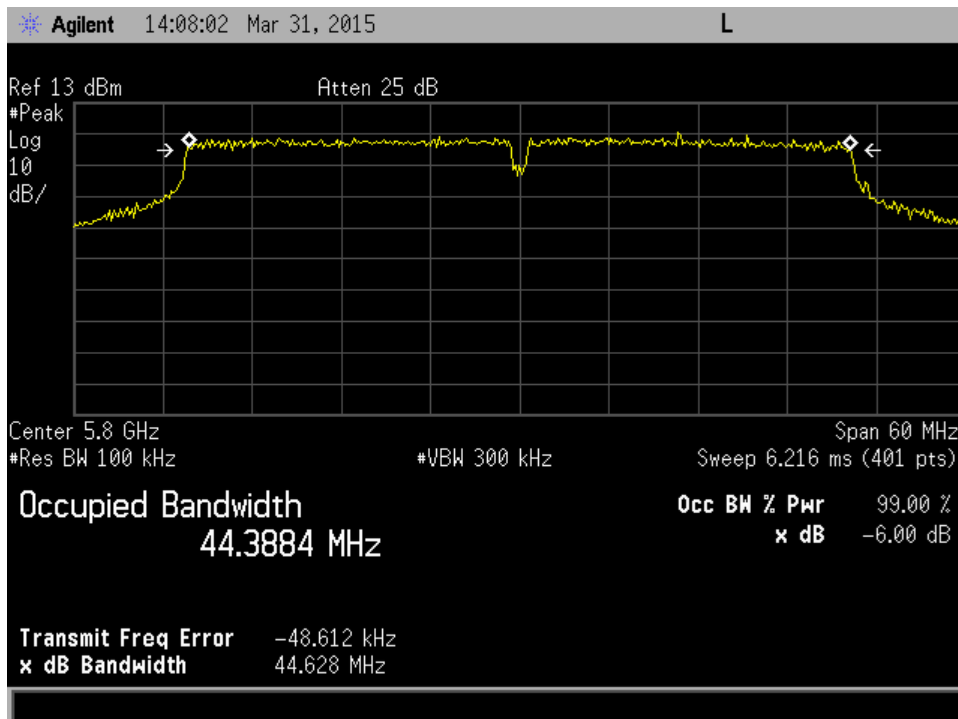
50Mhz low ch\_J7



50Mhz mid ch\_J13

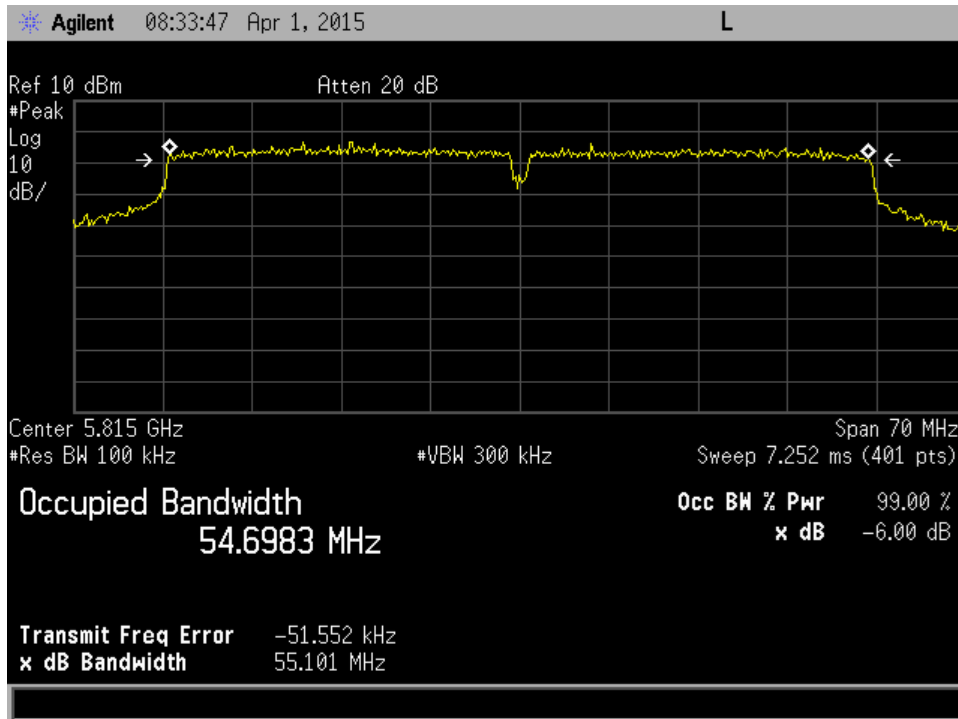


50Mhz mid ch\_J7

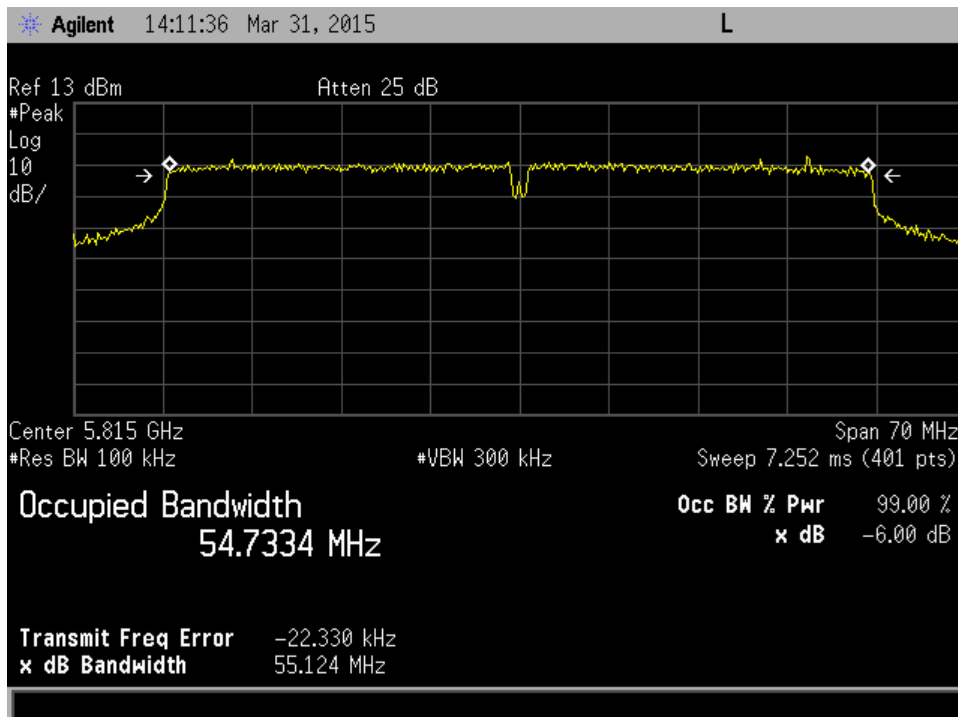


60Mhz high ch\_J13

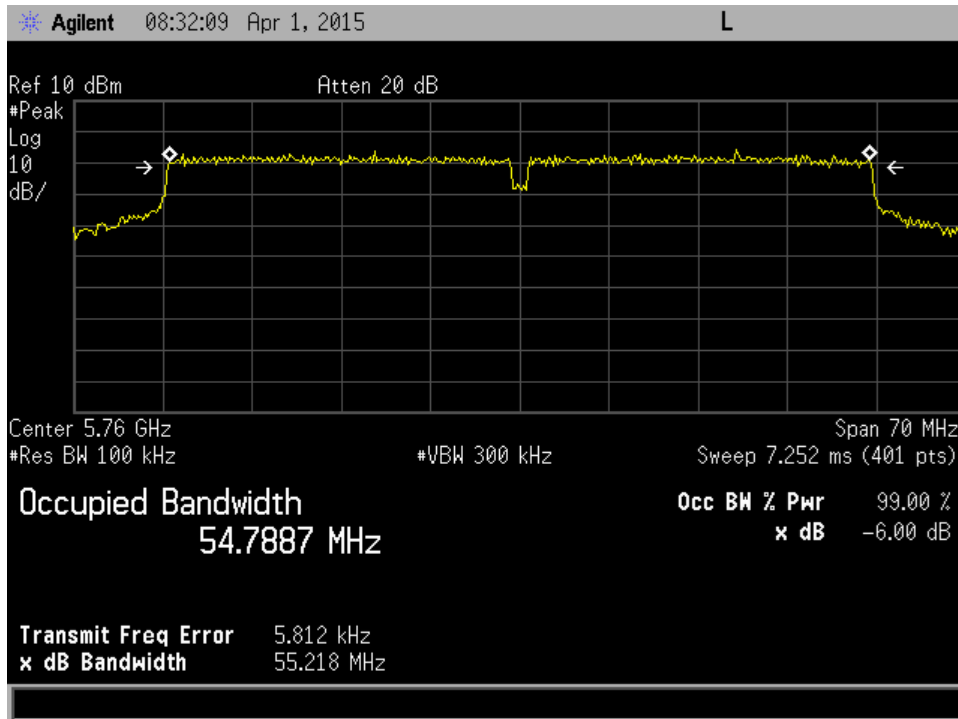




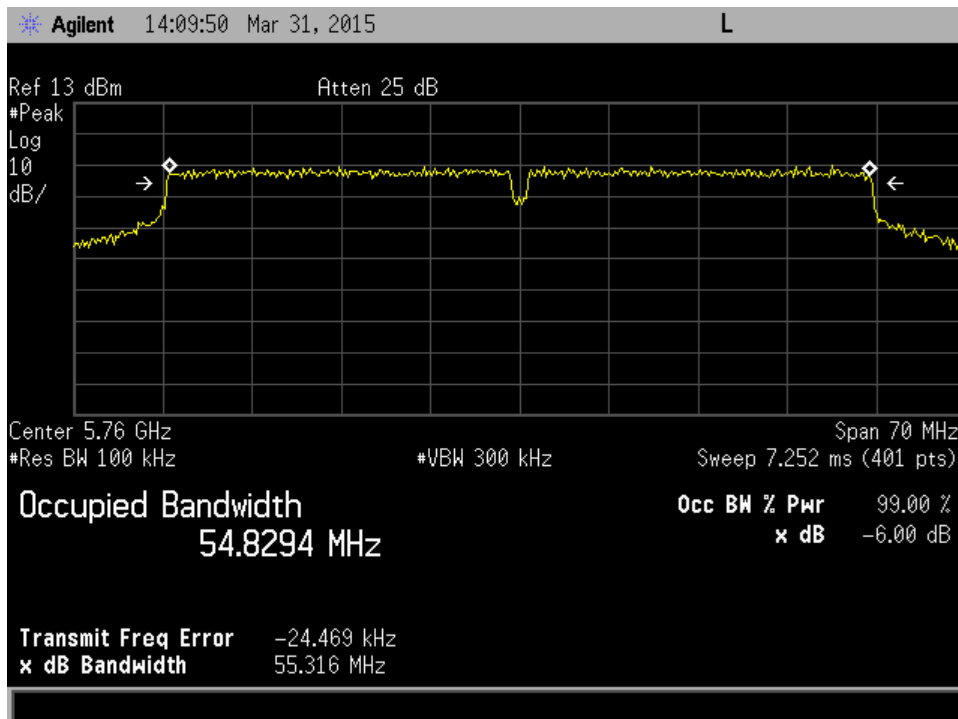
60Mhz high ch\_J7



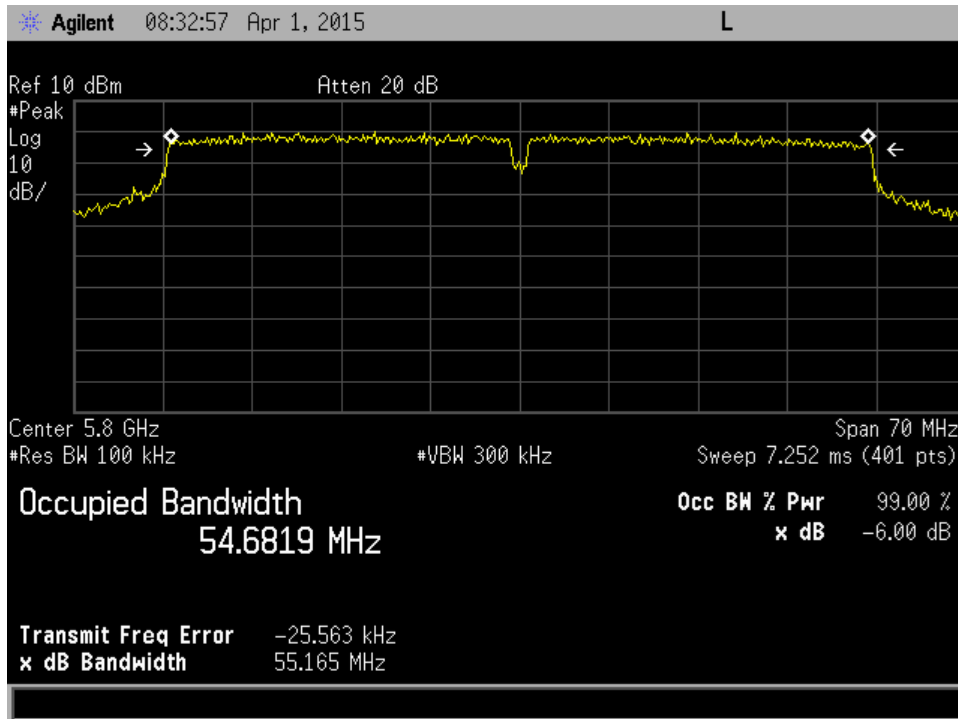
60Mhz low ch\_J13



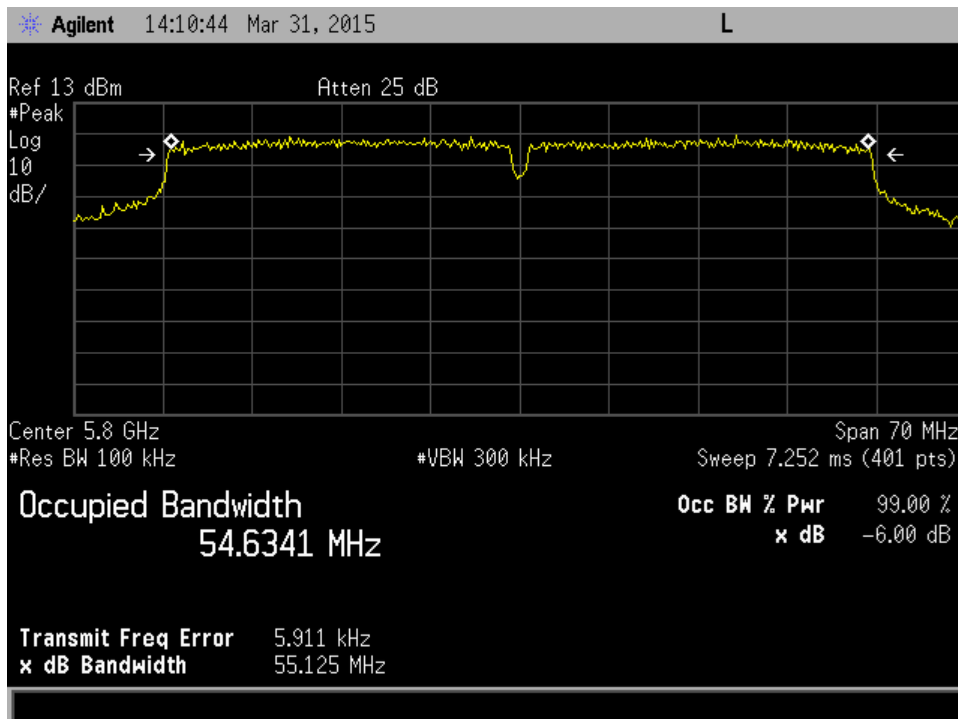
60Mhz low ch\_J7



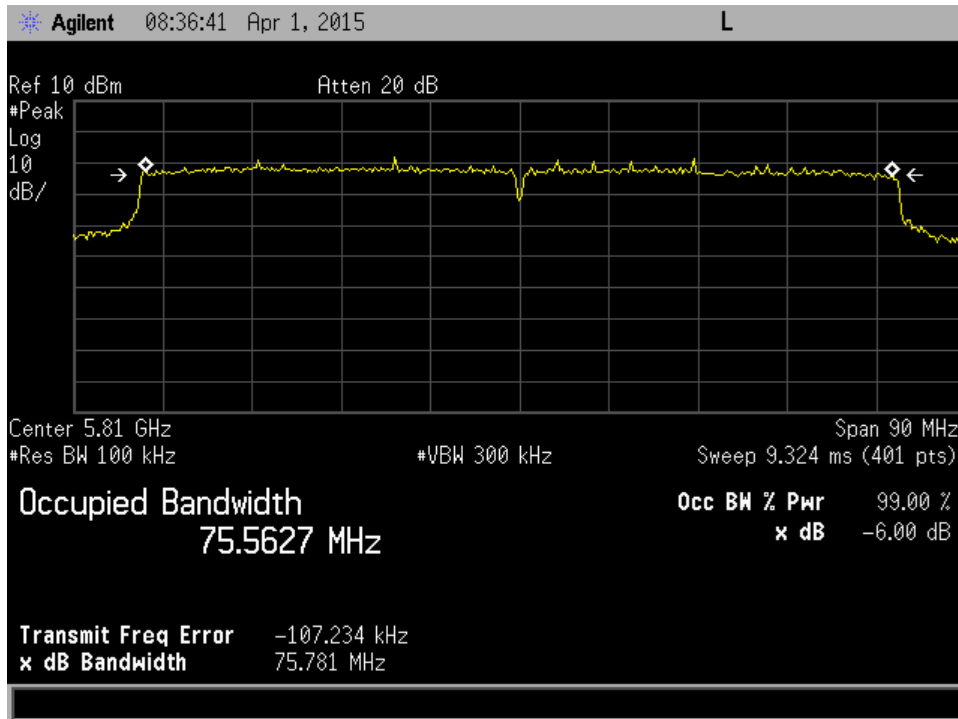
60Mhz mid ch\_J13



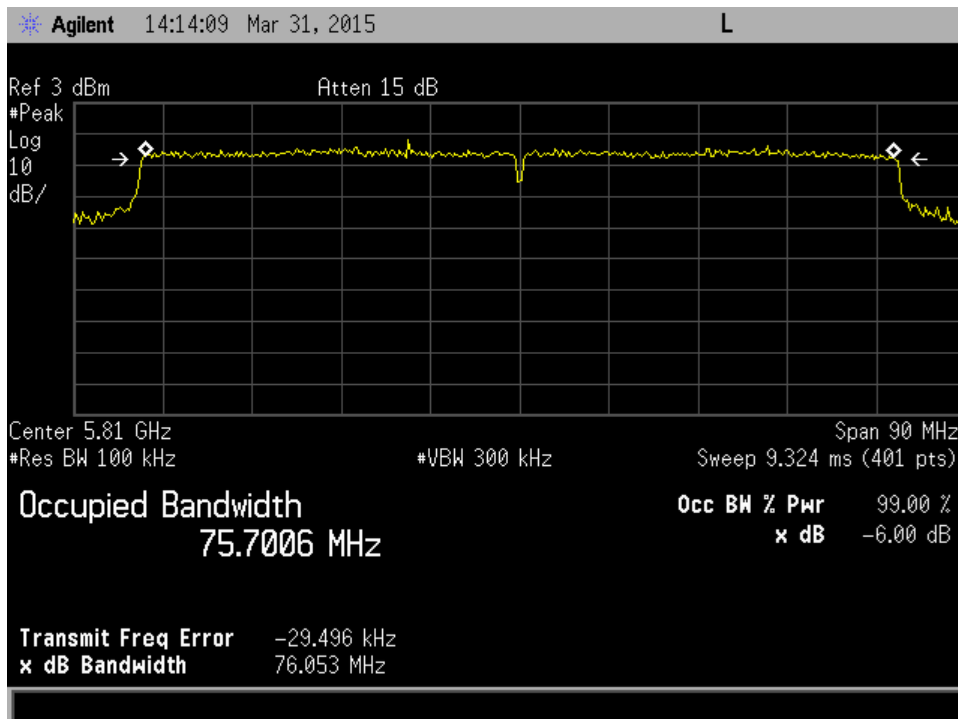
60Mhz mid ch\_J7



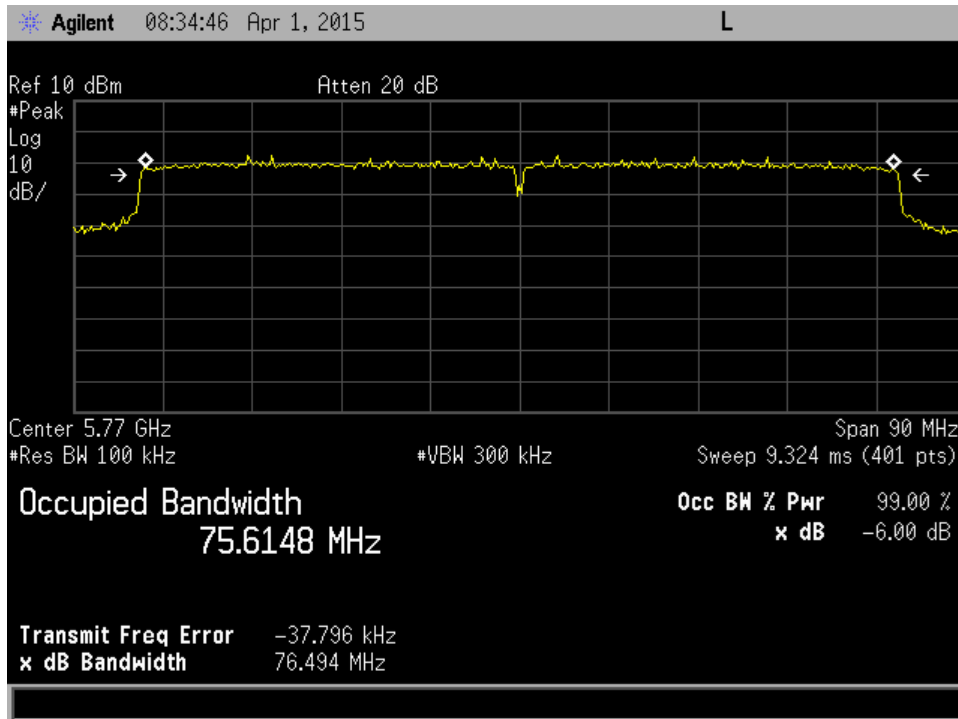
80Mhz high ch\_J13



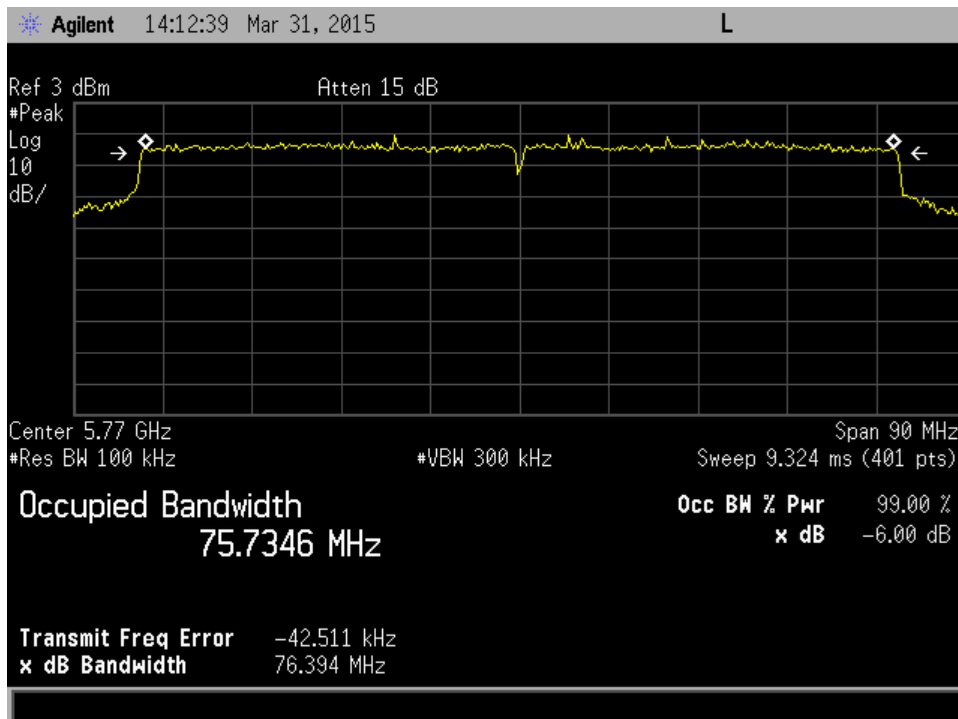
**80Mhz high ch\_J7**



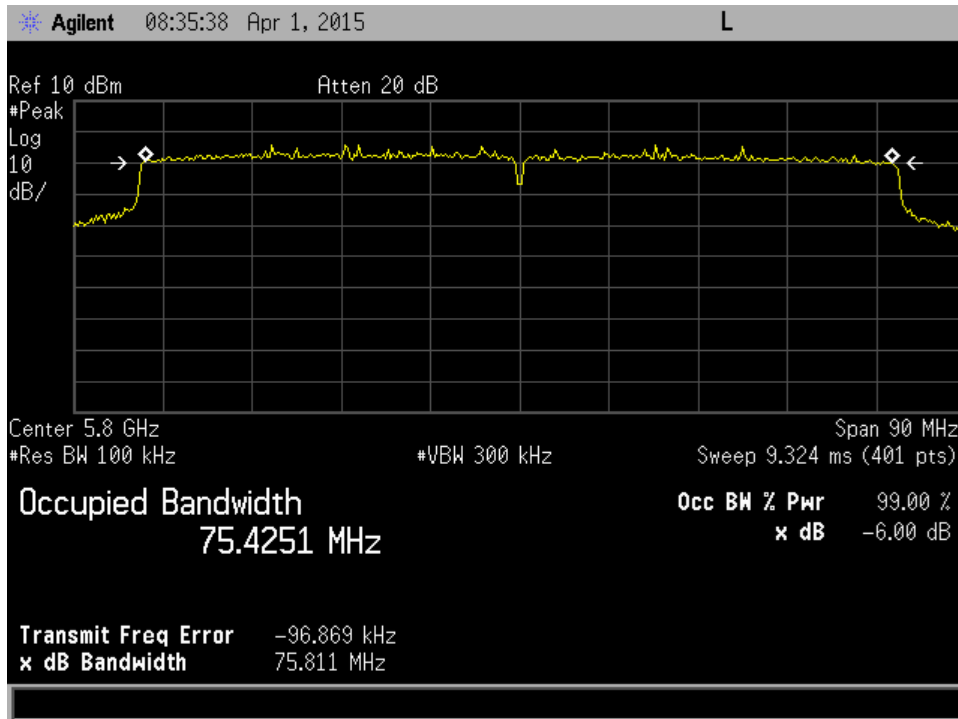
**80Mhz low ch\_J13**



80Mhz low ch\_J7



80Mhz mid ch\_J13



**80Mhz mid ch\_J7**

