



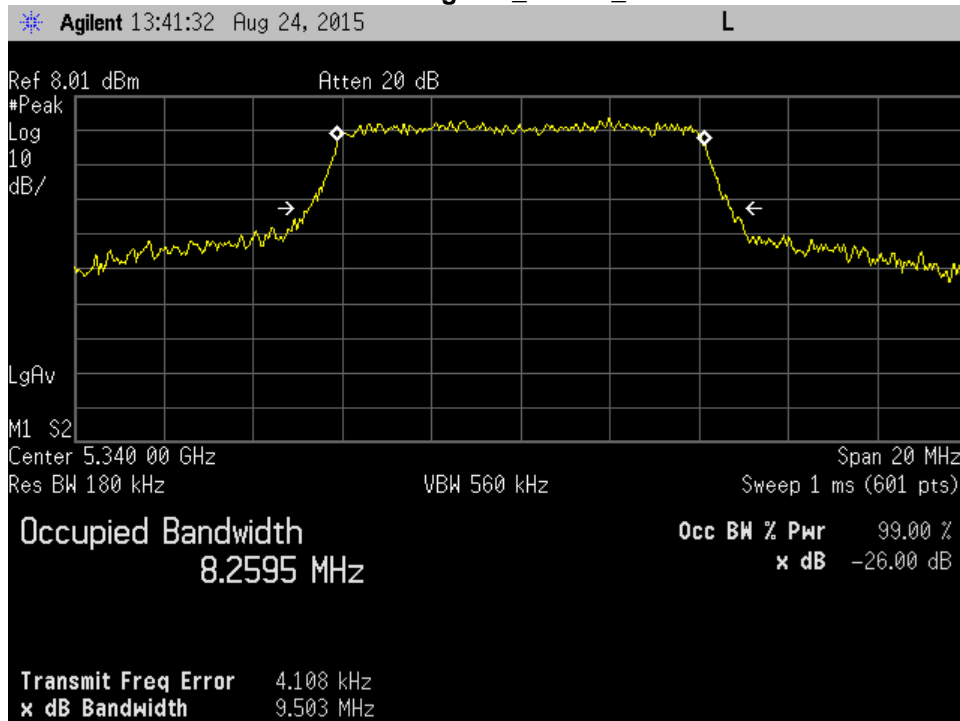
Annex D

Occupied Bandwidth

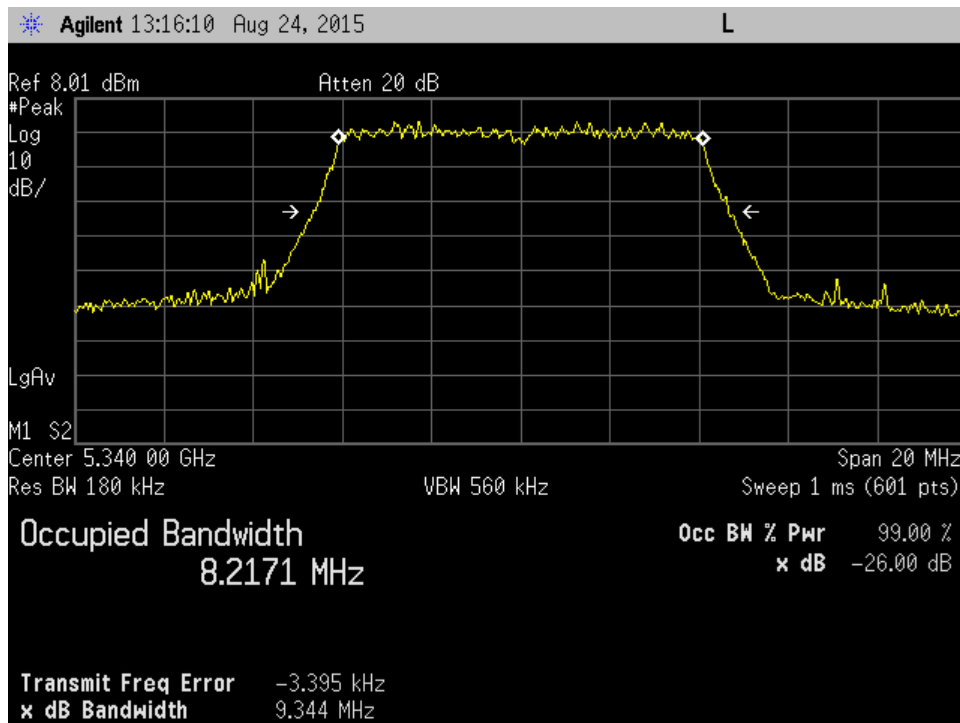
Ports J3 and J4



10 MHz high ch_OCBW_J3

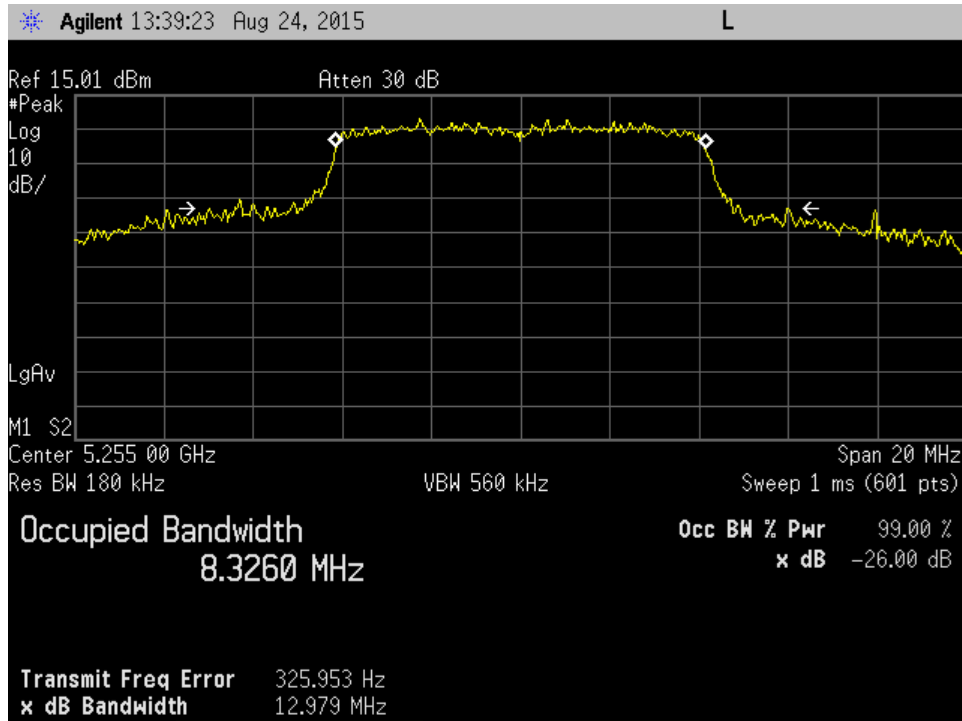


10 MHz high ch_OCBW_J4

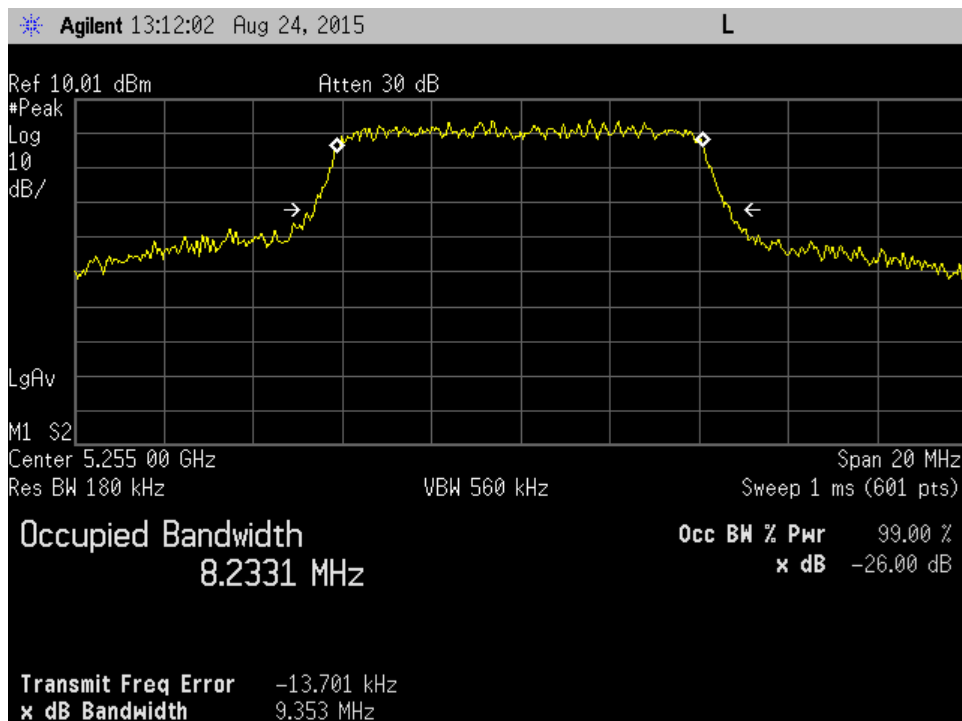




10 MHz low ch_OCBW_J3

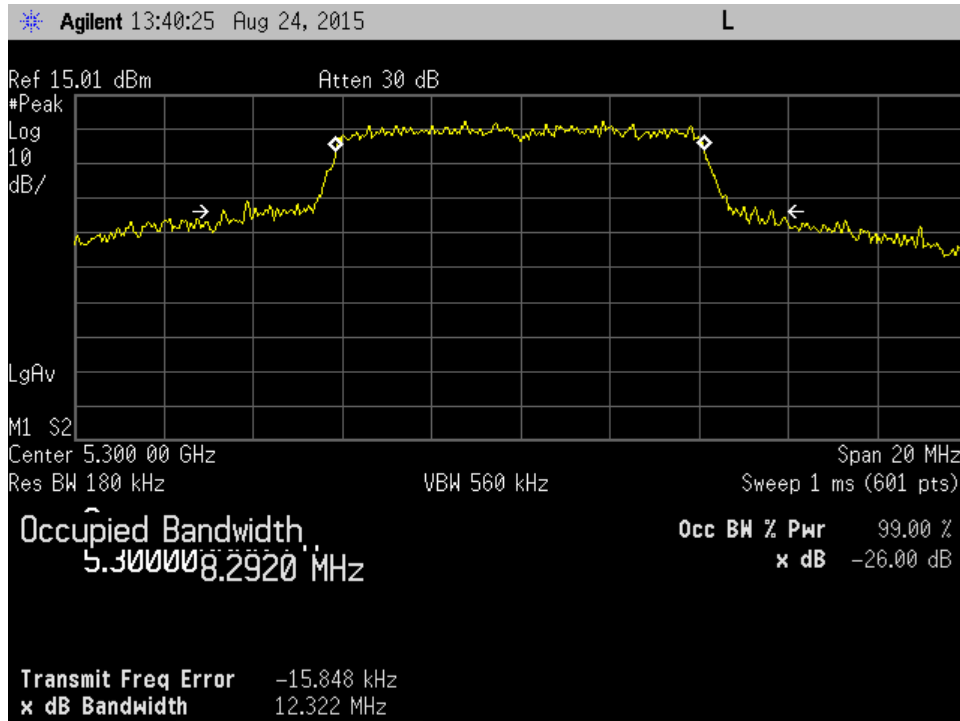


10 MHz low ch_OCBW_J4

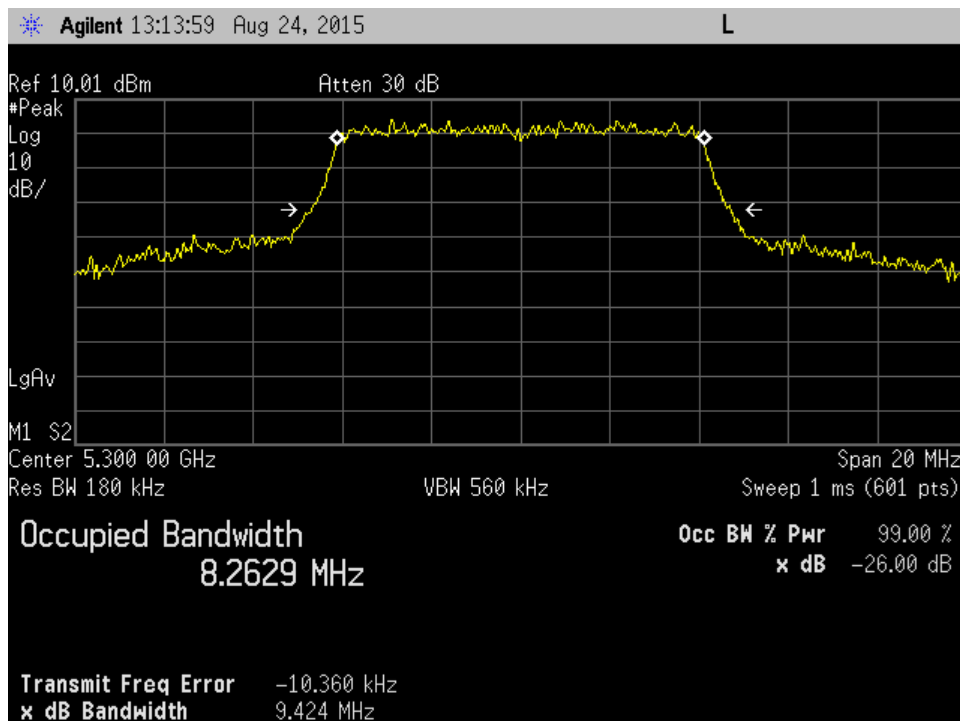




10 MHz mid ch_OCBW_J3

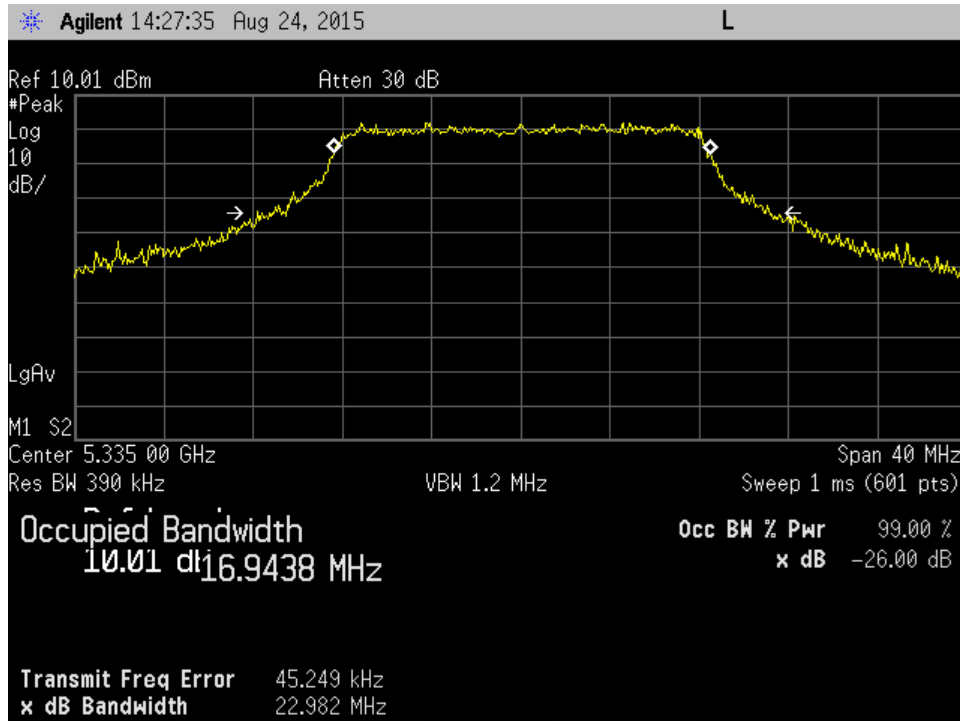


10 MHz mid ch_OCBW_J4

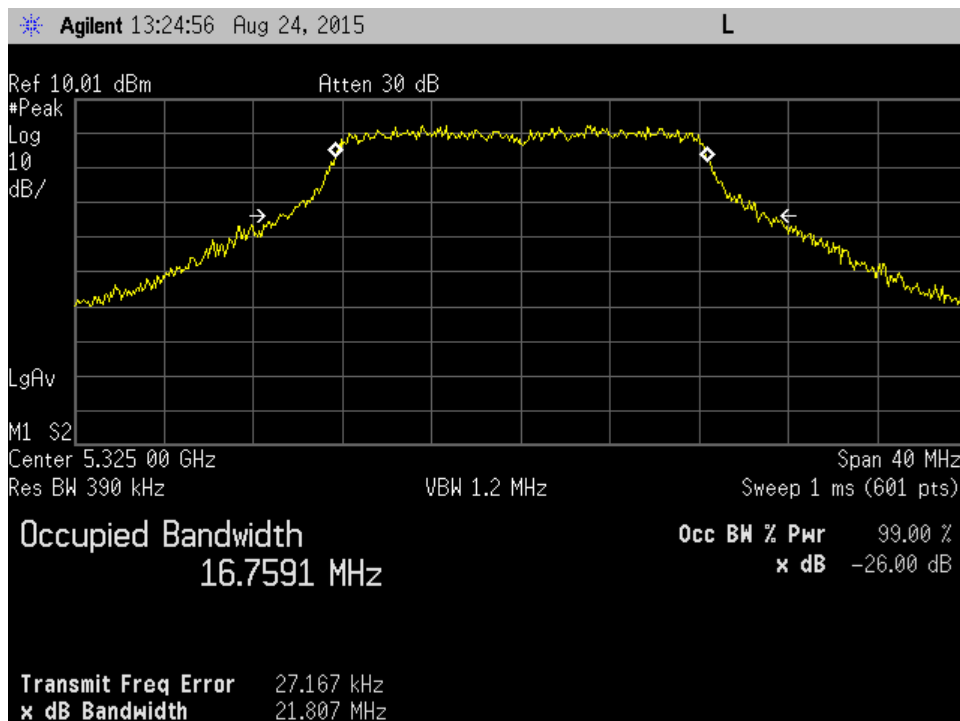




20 MHz high ch_OCBW_J3

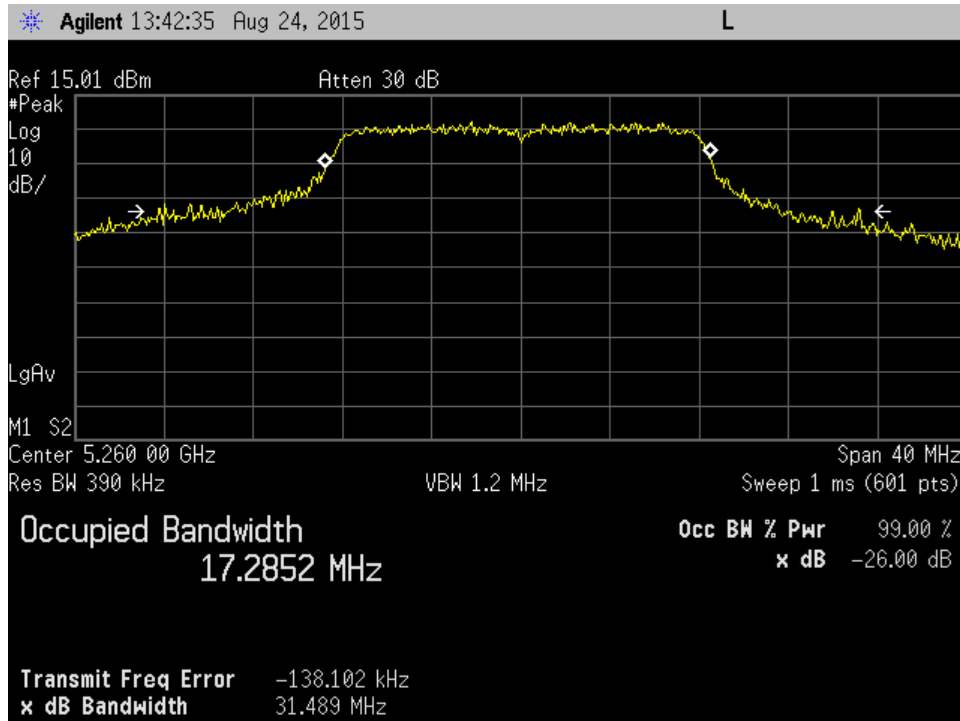


20 MHz high ch_OCBW_J4

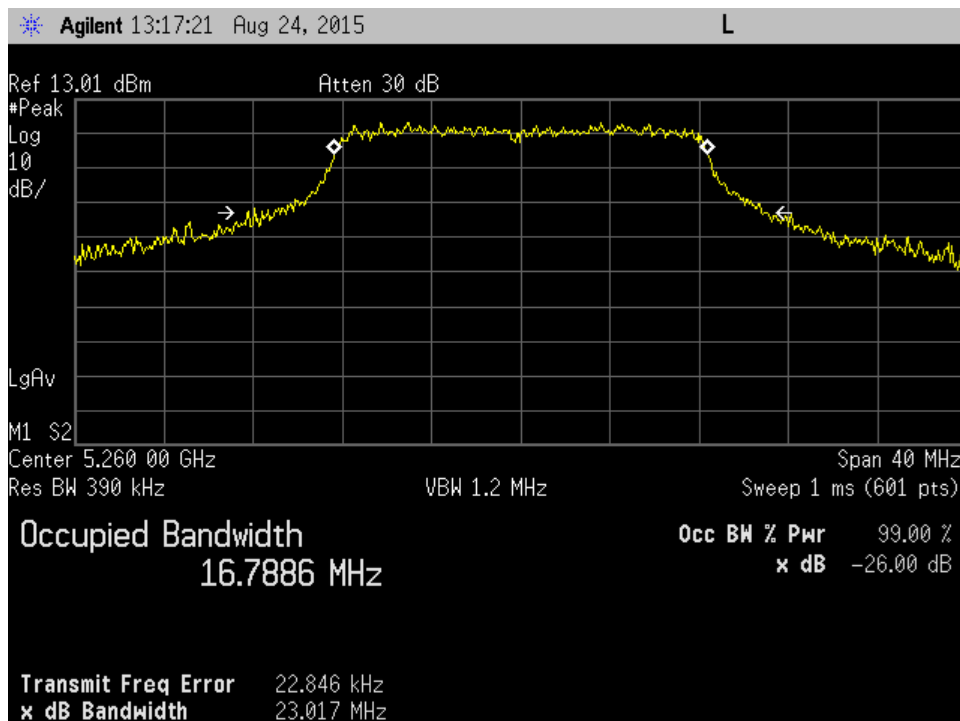




20 MHz low ch_OCBW_J3

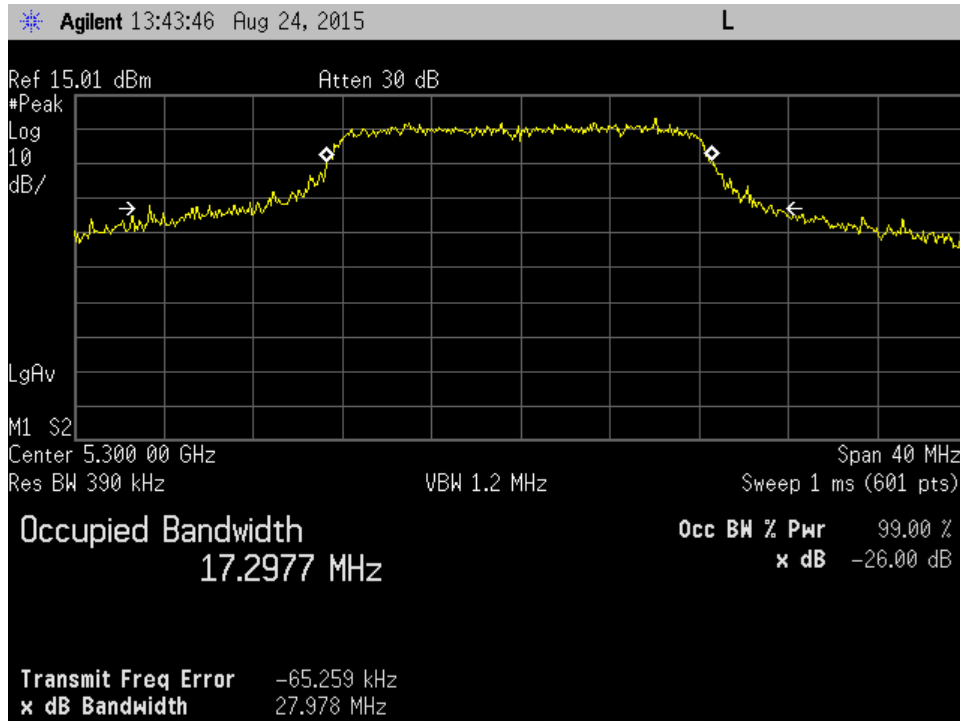


20 MHz low ch_OCBW_J4

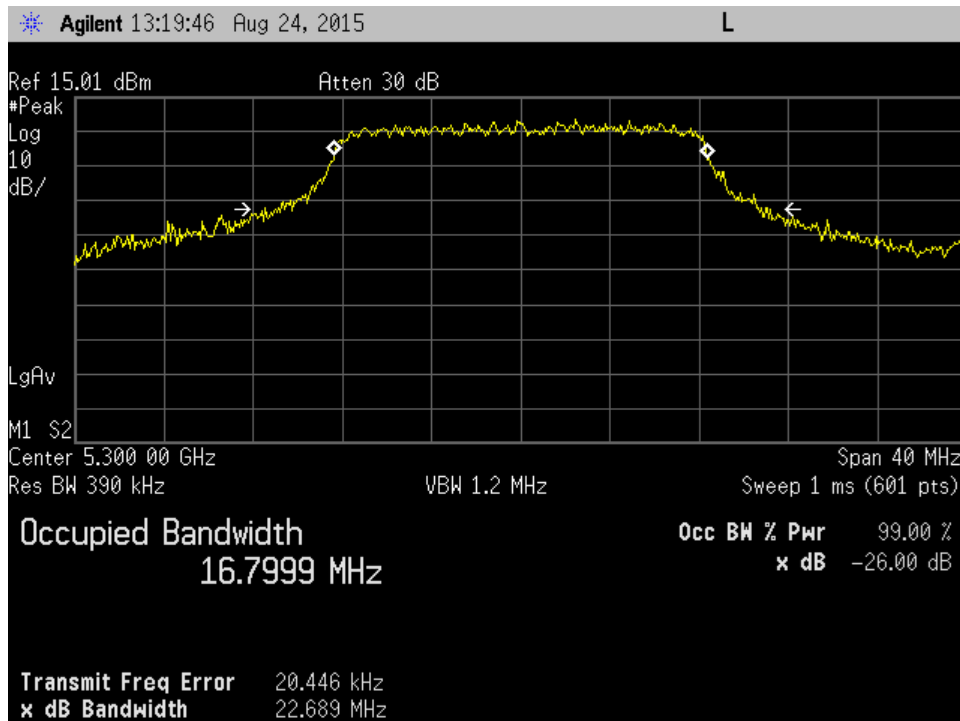




20 MHz mid ch_OCBW_J3

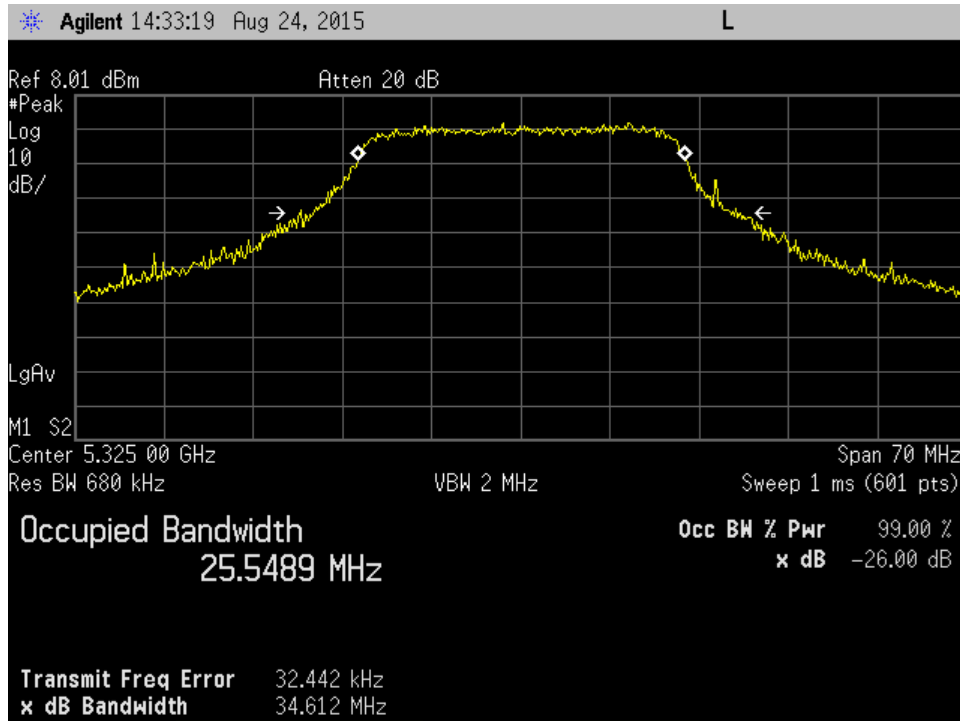


20 MHz mid ch_OCBW_J4

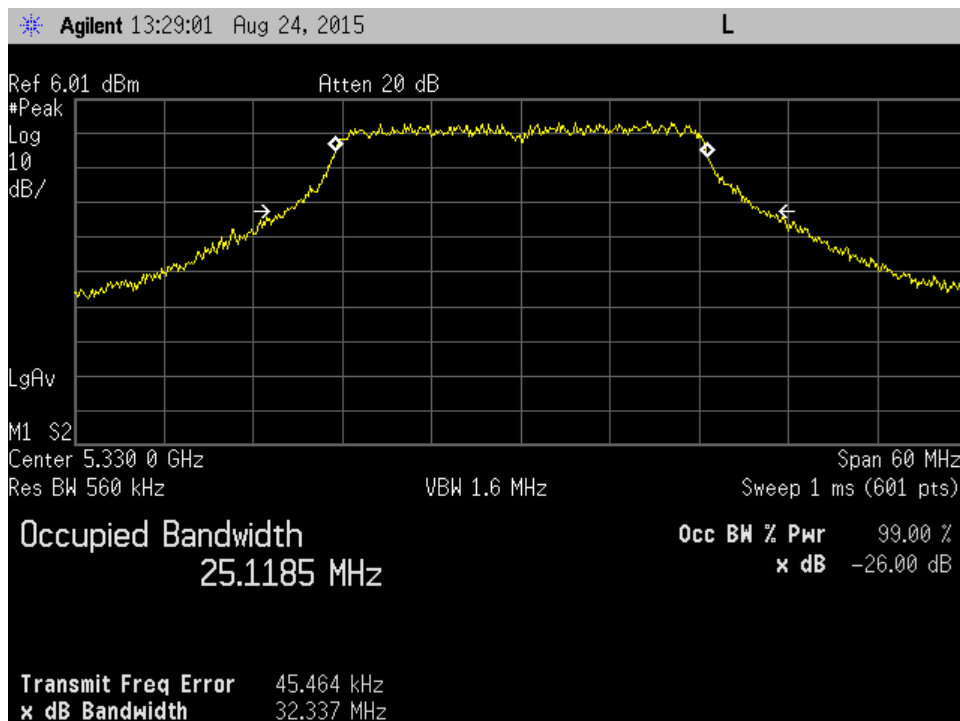




30 MHz high ch_OCBW_J3

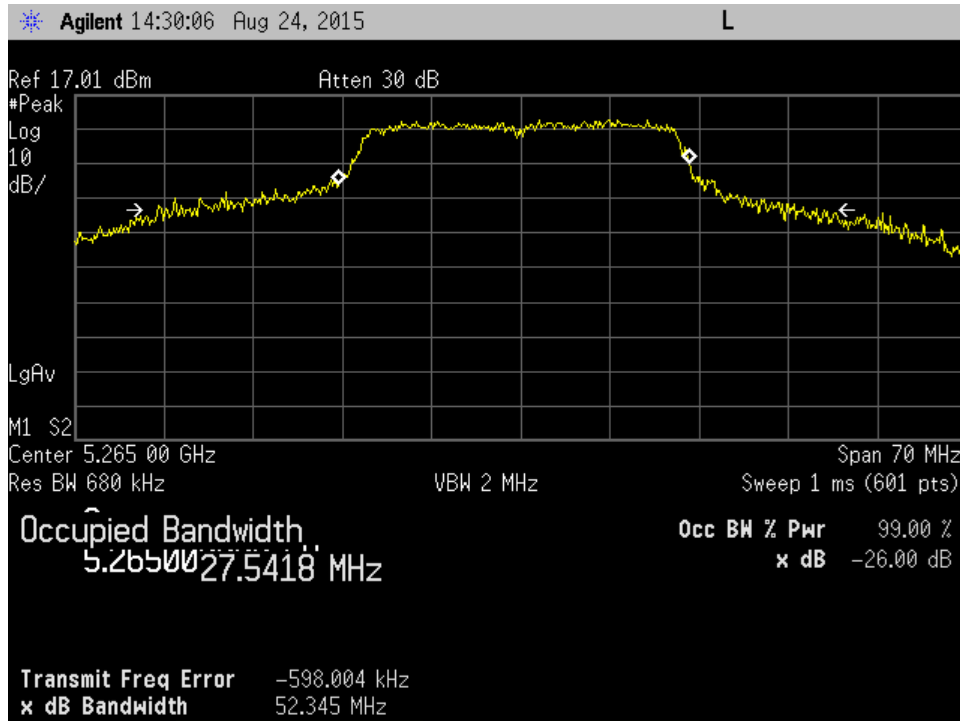


30 MHz high ch_OCBW_J4

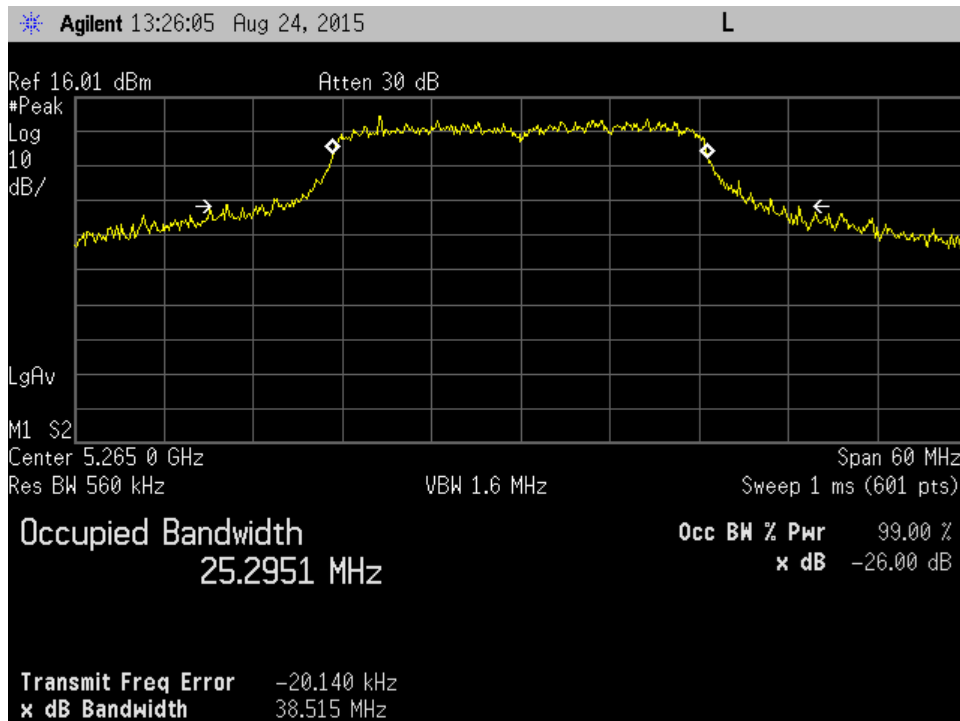




30 MHz low ch_OCBW_J3

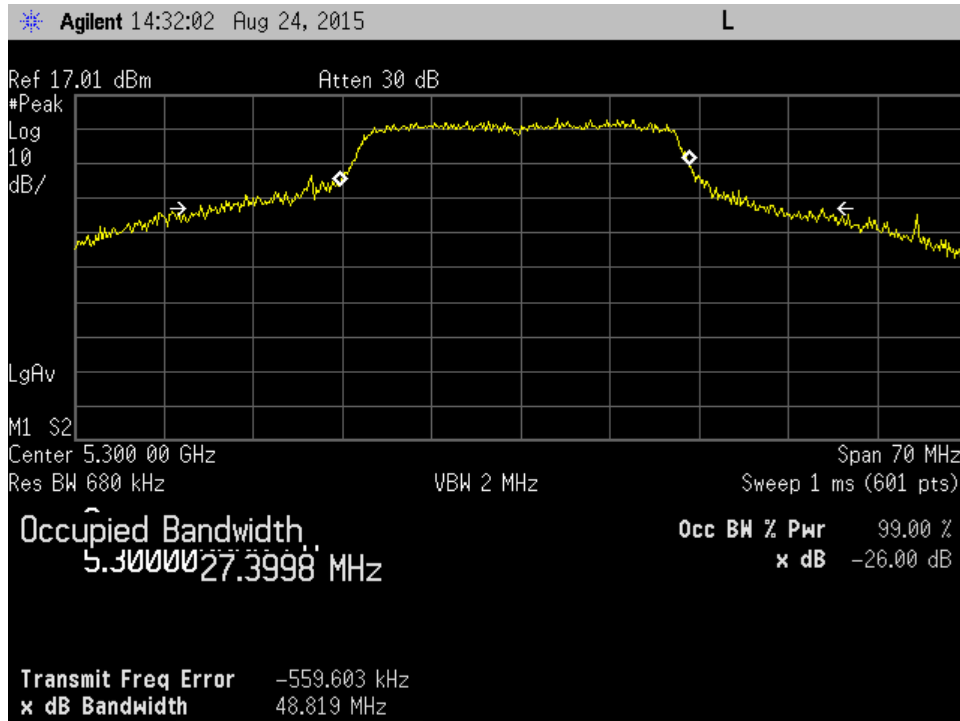


30 MHz low ch_OCBW_J4

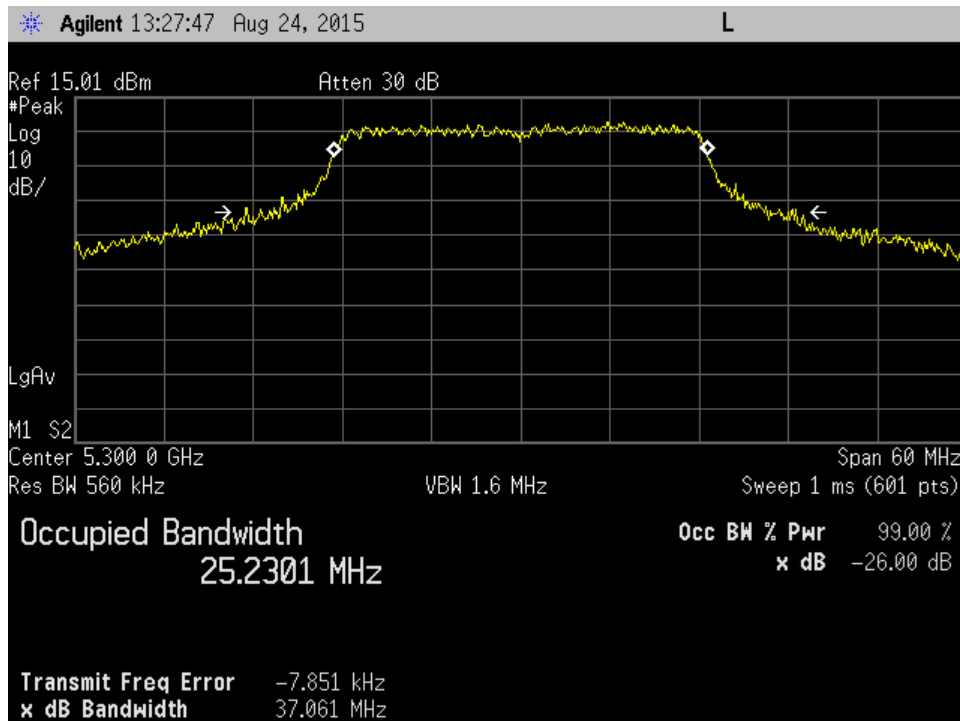




30 MHz mid ch_OCBW_J3

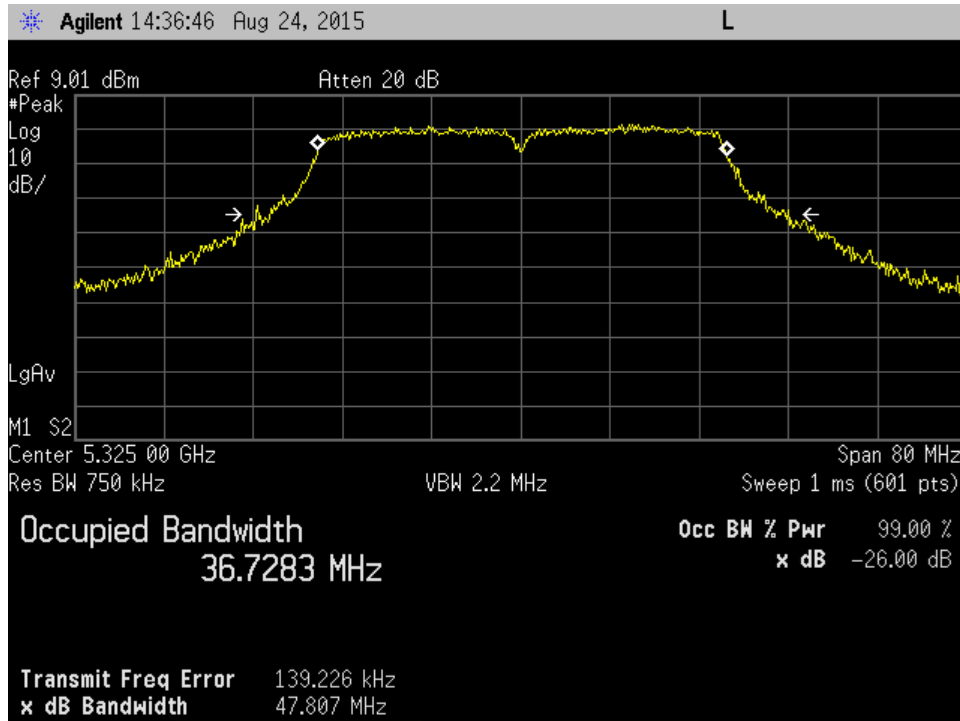


30 MHz mid ch_OCBW_J4

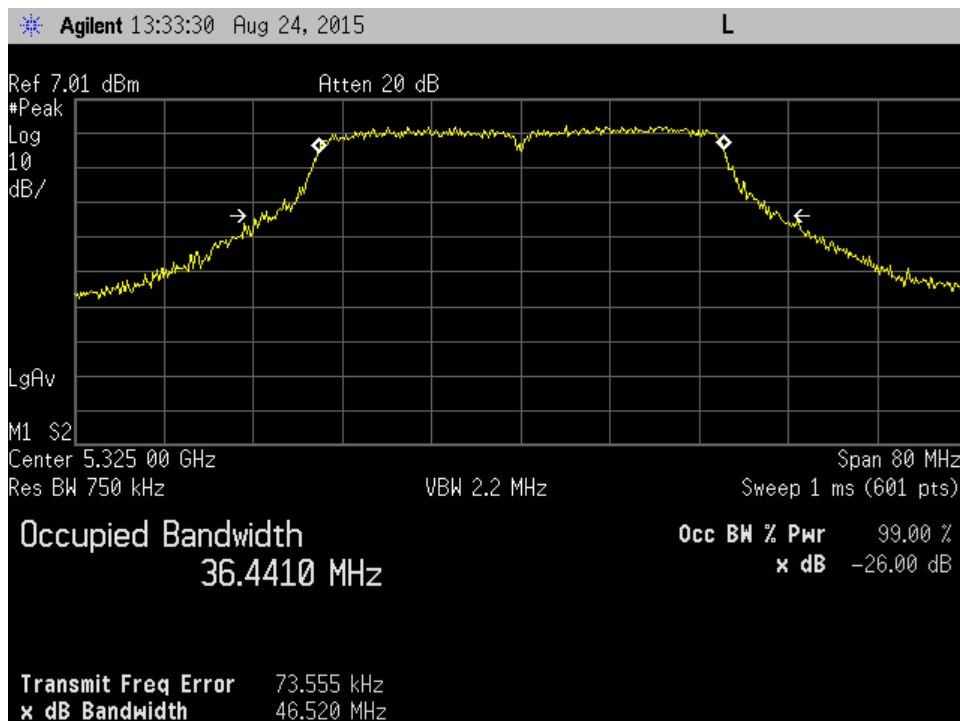




40 MHz high ch_OCBW_J3

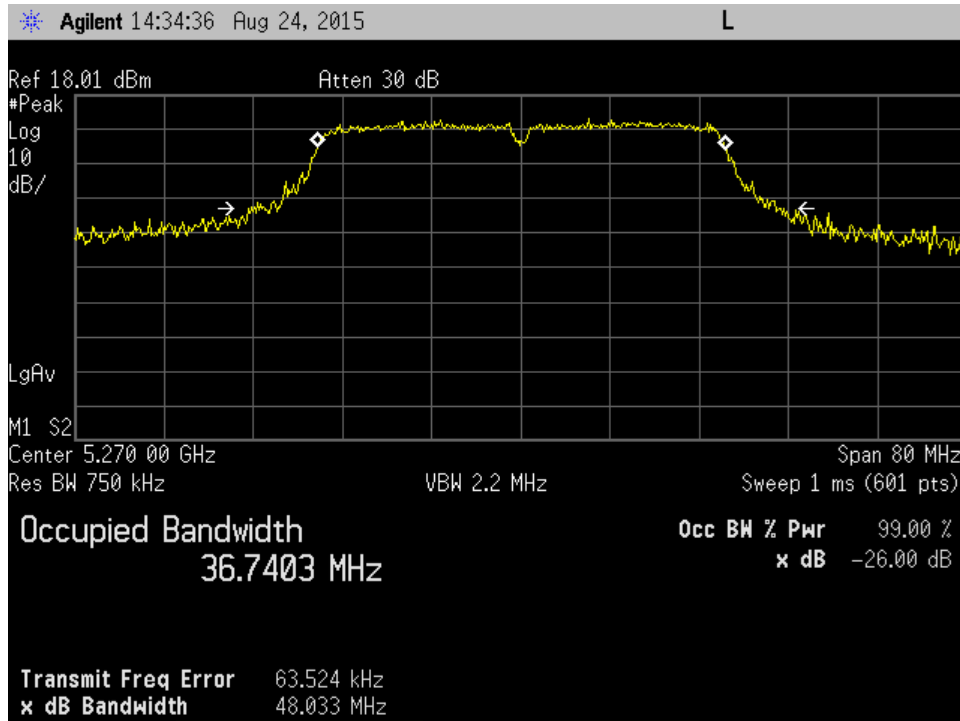


40 MHz high ch_OCBW_J4

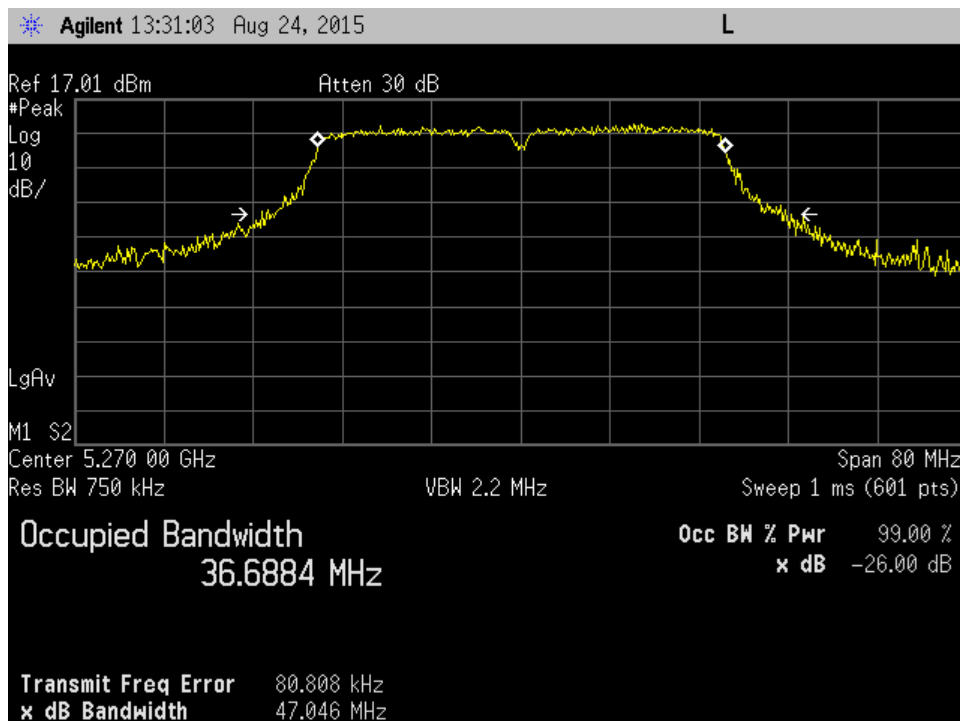




40 MHz low ch_OCBW_J3

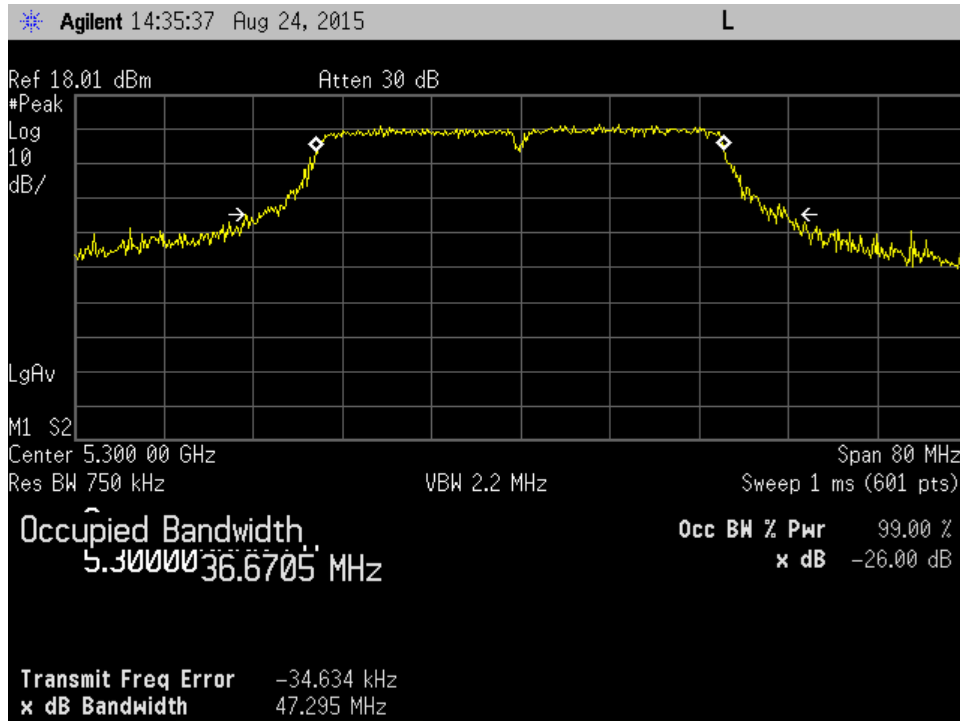


40 MHz low ch_OCBW_J4

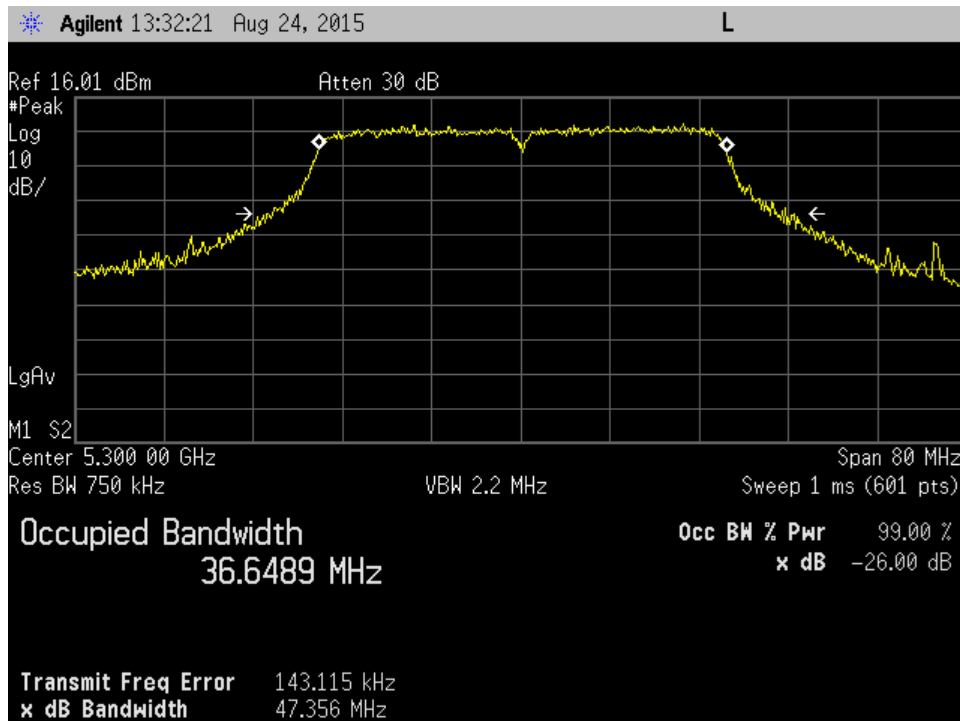




40 MHz mid ch_OCBW_J3



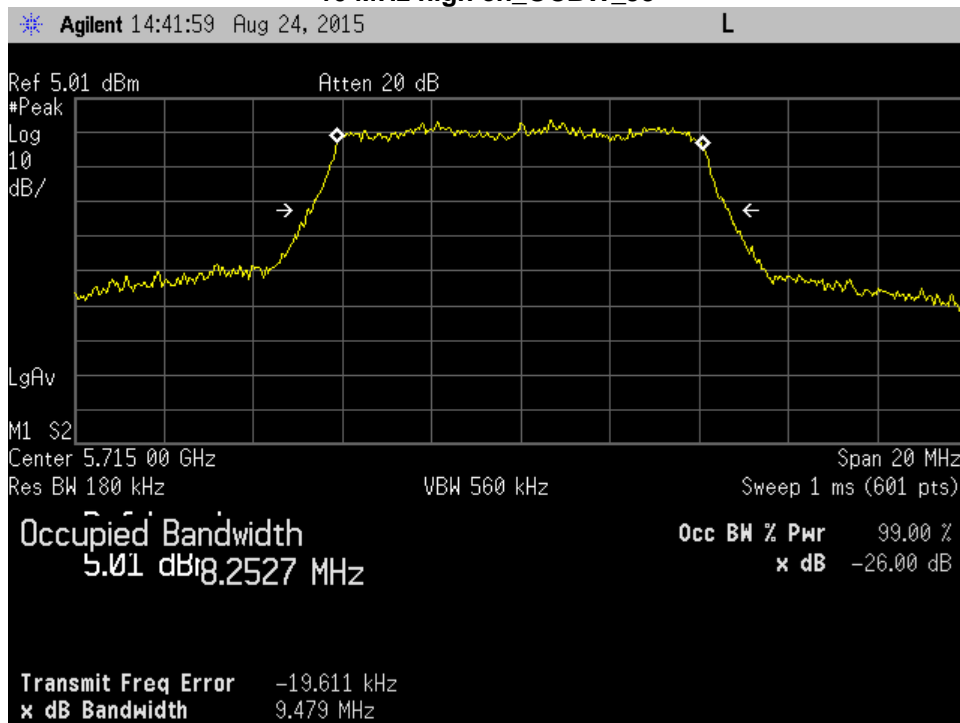
40 MHz mid ch_OCBW_J4



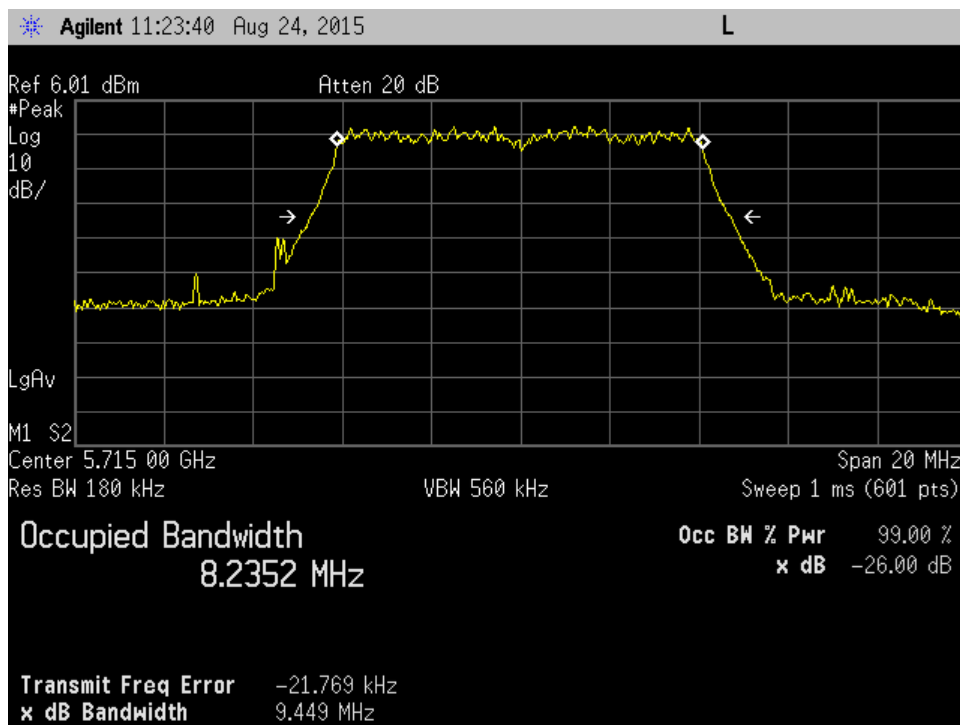


UNII-2C Data

10 MHz high ch_OCBW_J3

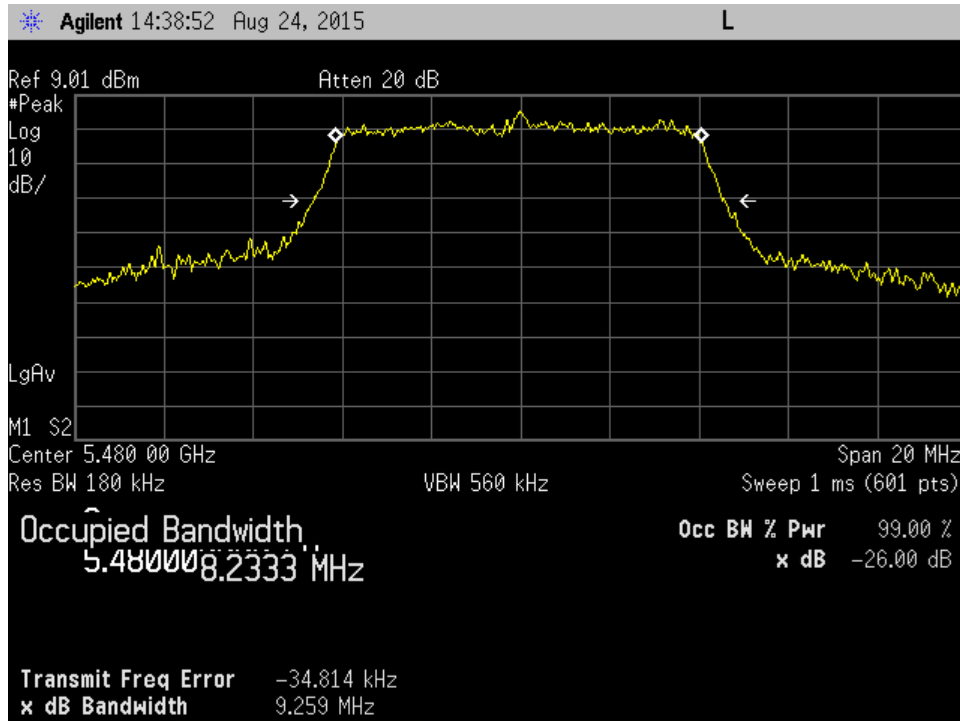


10 MHz high ch_OCBW_J4

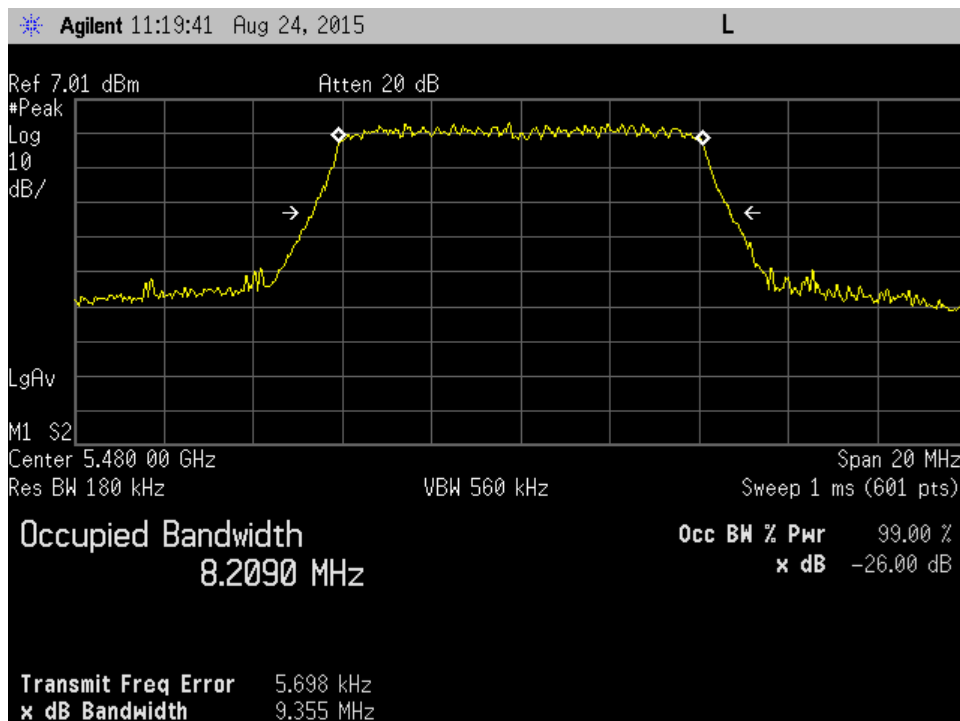




10 MHz low ch_OCBW_J3

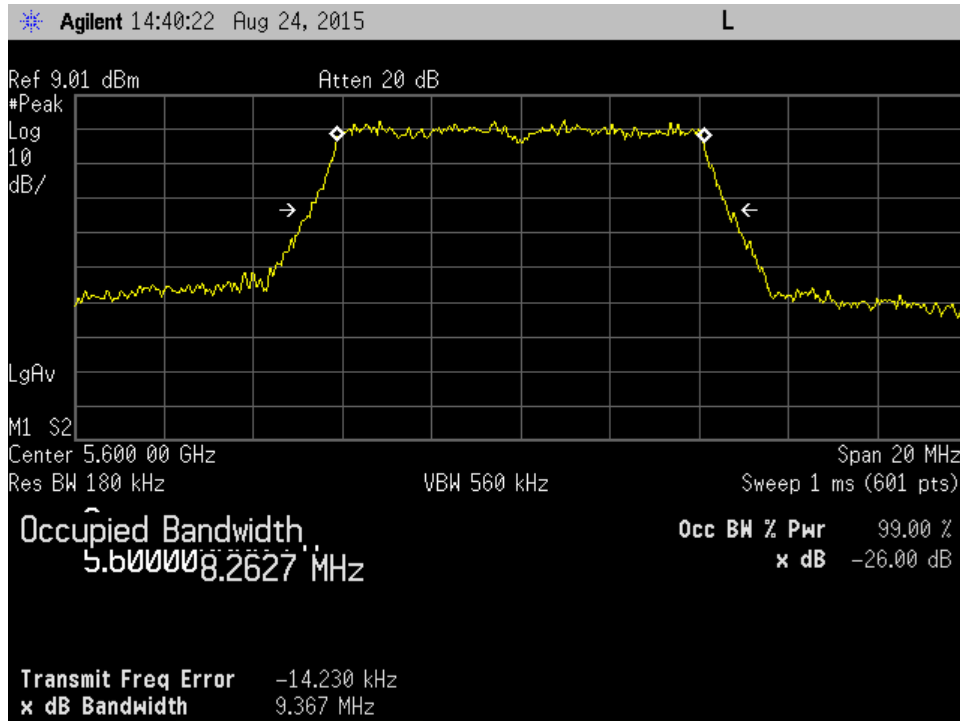


10 MHz low ch_OCBW_J4

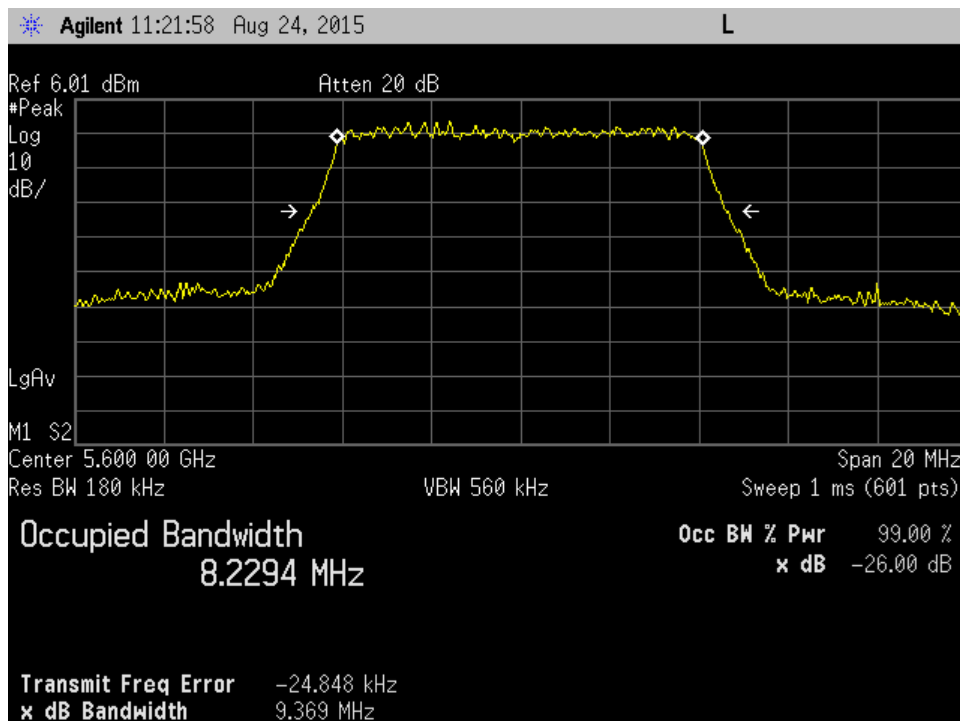




10 MHz mid ch_OCBW_J3

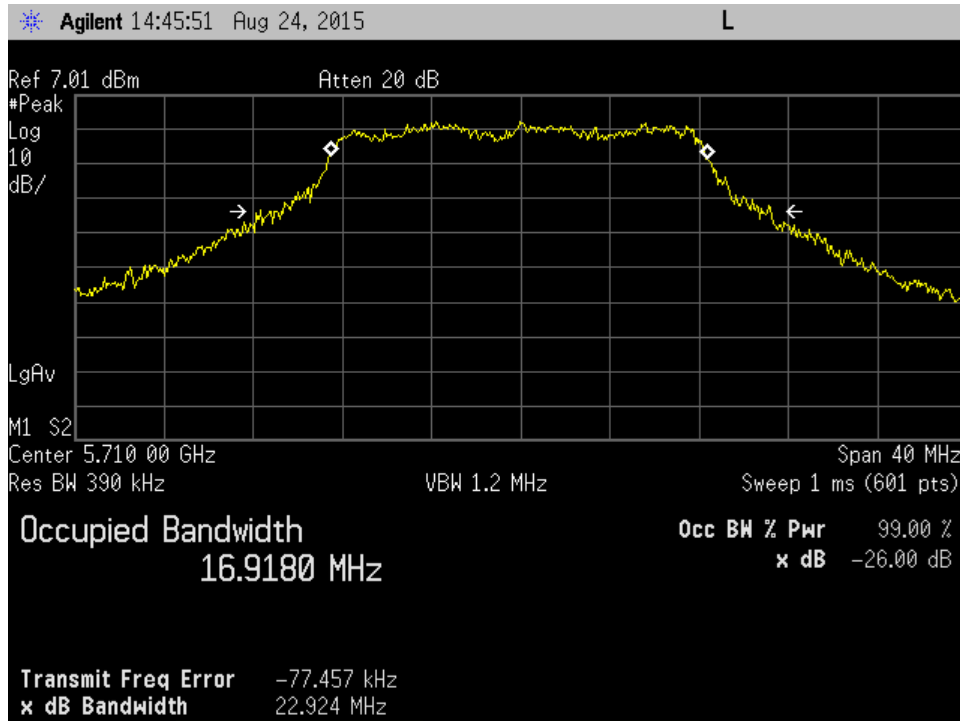


10 MHz mid ch_OCBW_J4

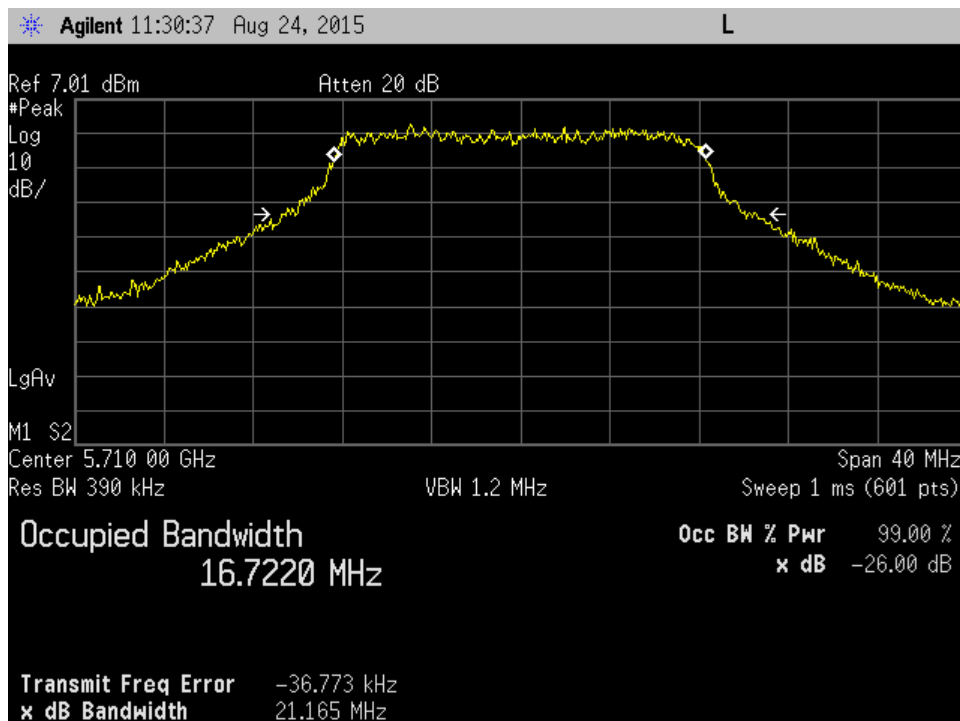




20 MHz high ch_OCBW_J3

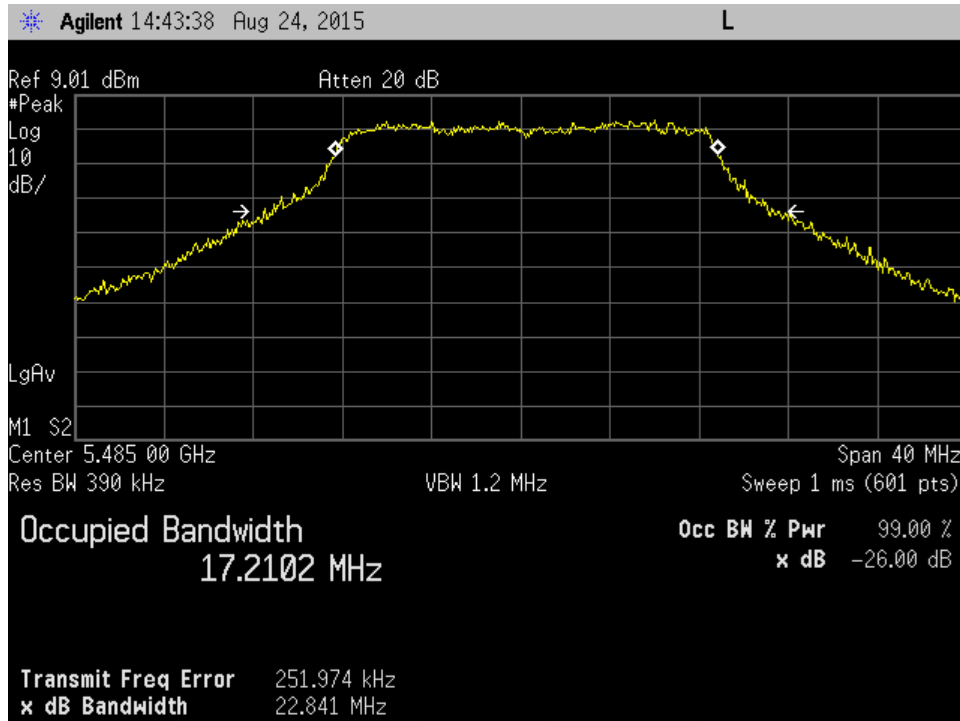


20 MHz high ch_OCBW_J4

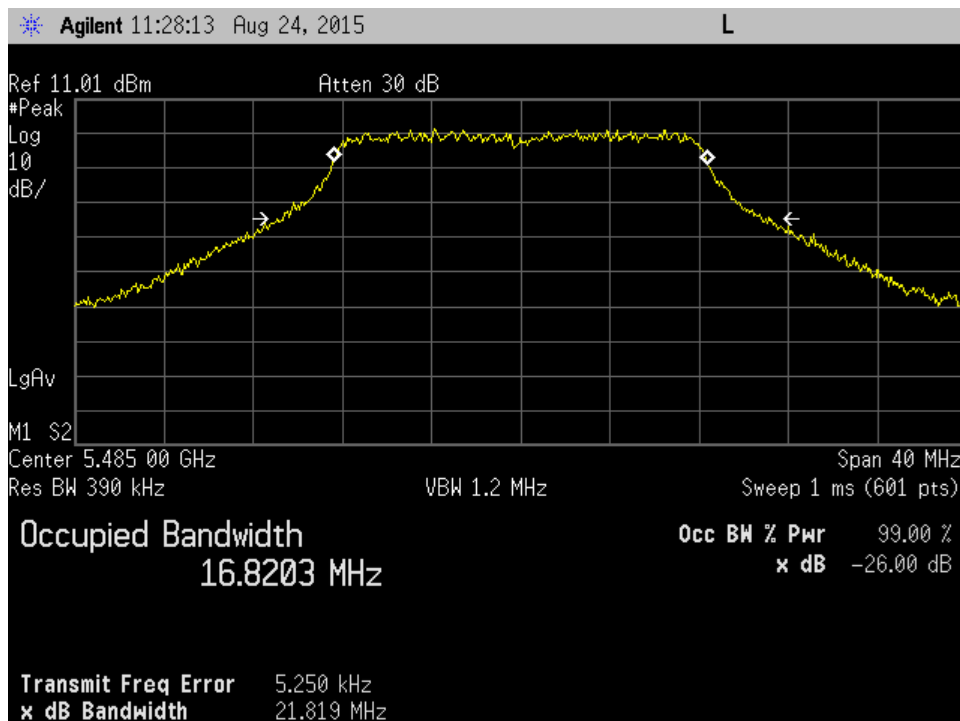




20 MHz low ch_OCBW_J3

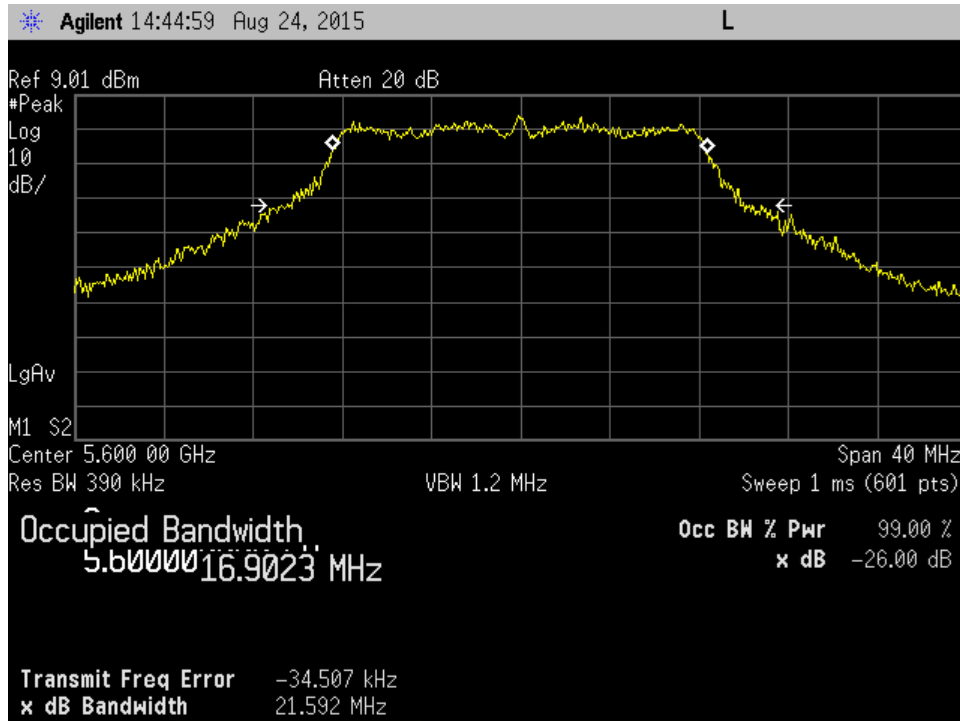


20 MHz low ch_OCBW_J4

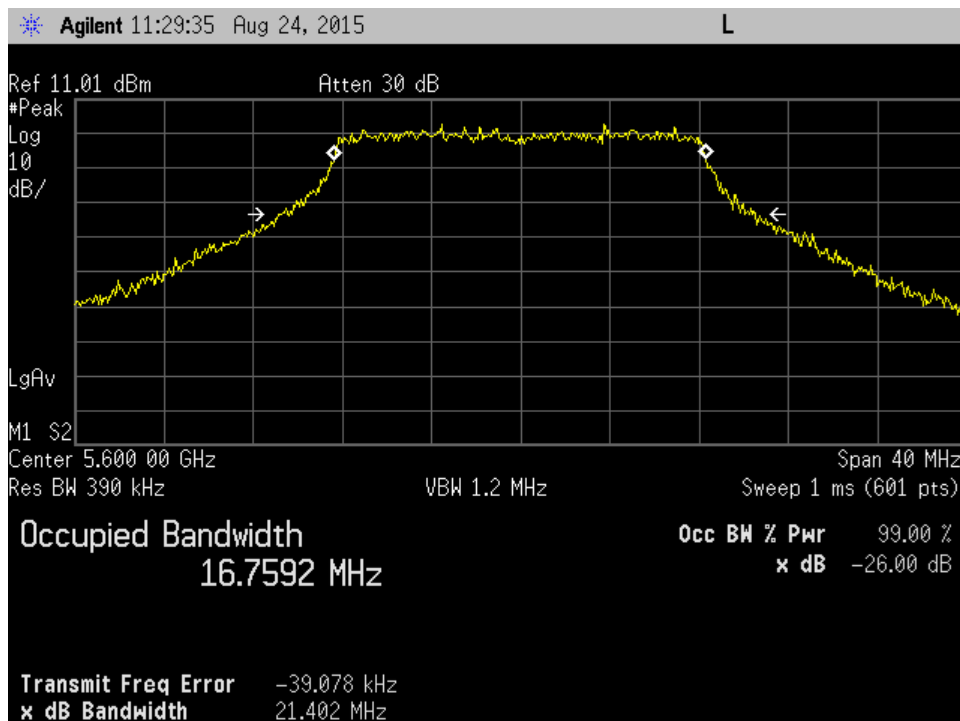




20 MHz mid ch_OCBW_J3

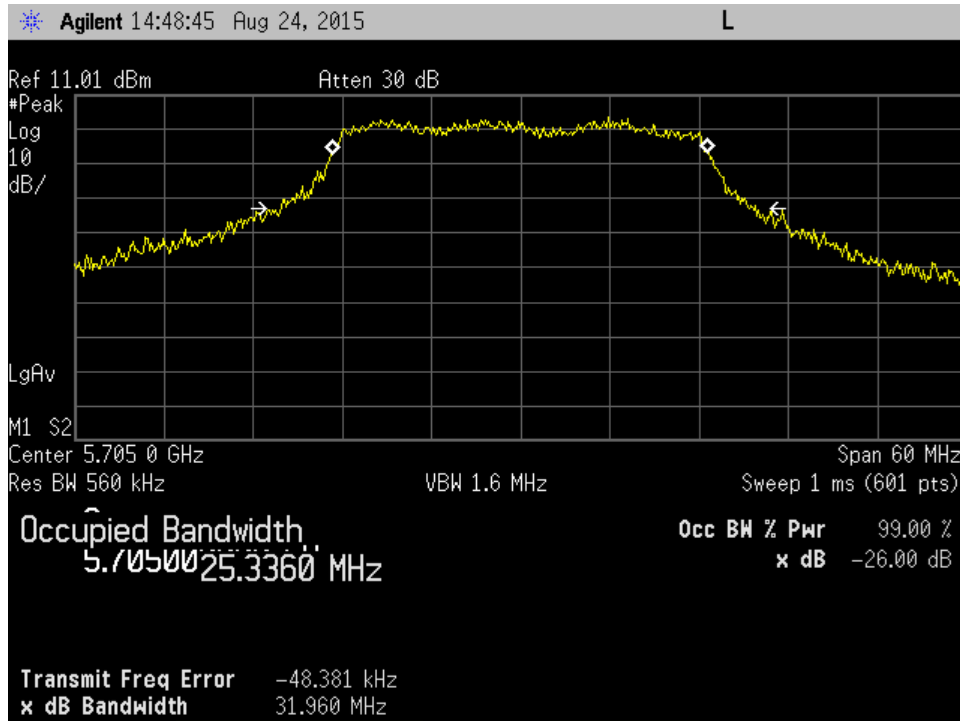


20 MHz mid ch_OCBW_J4

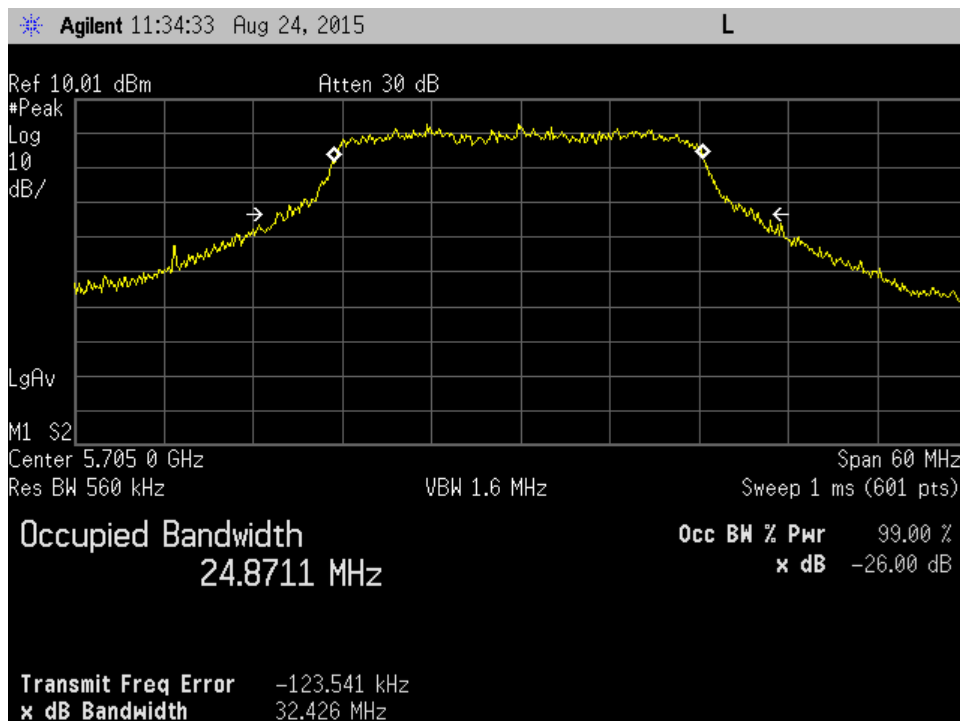




30 MHz high ch_OCBW_J3

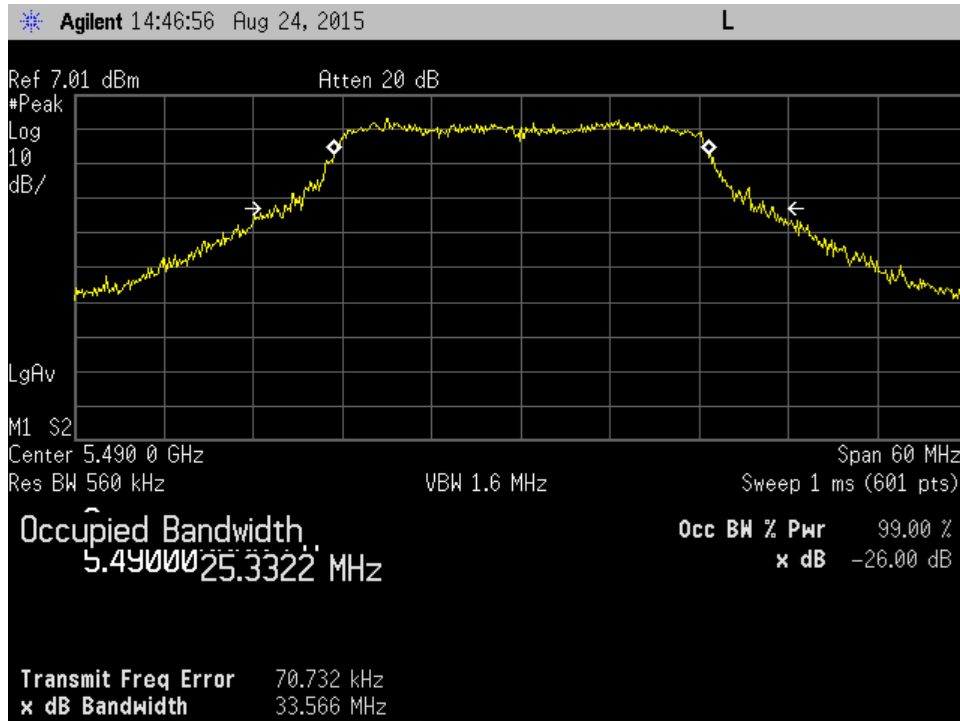


30 MHz high ch_OCBW_J4

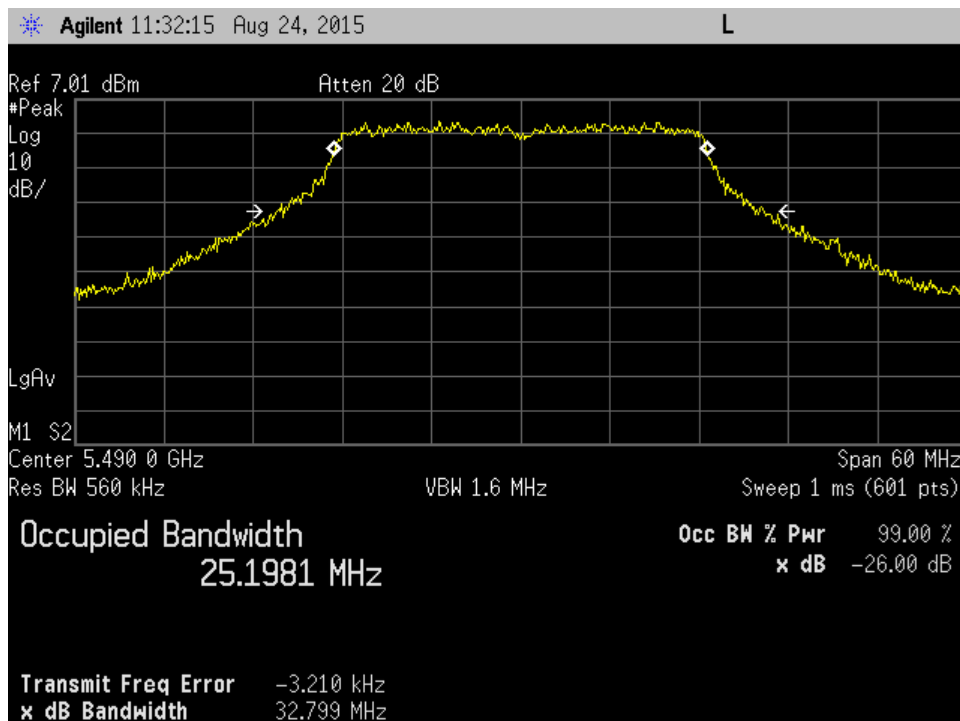




30 MHz low ch_OCBW_J3

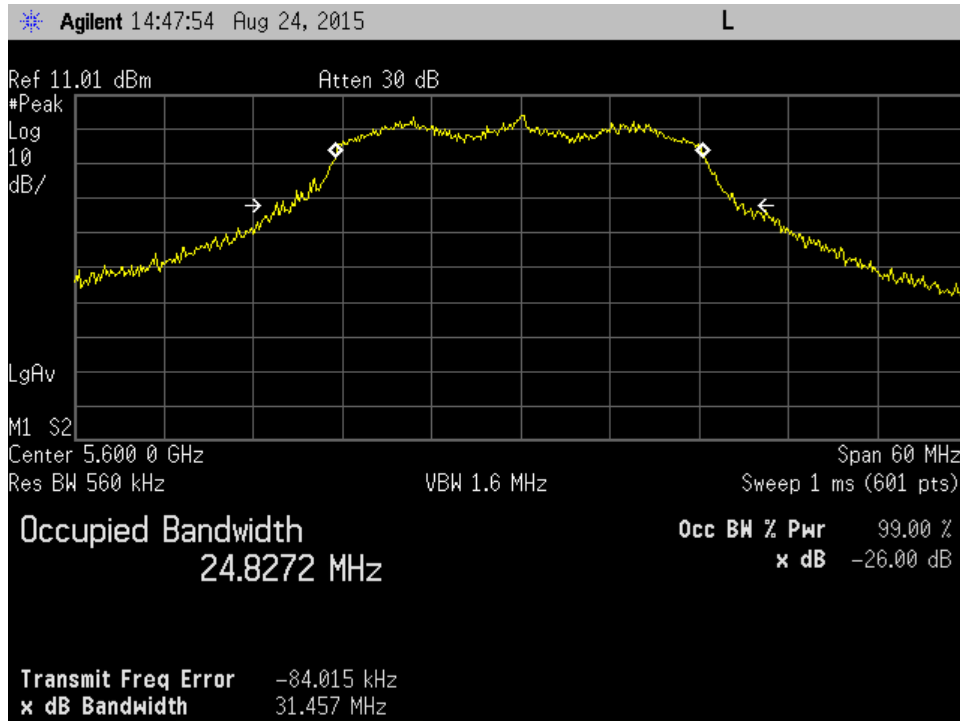


30 MHz low ch_OCBW_J4

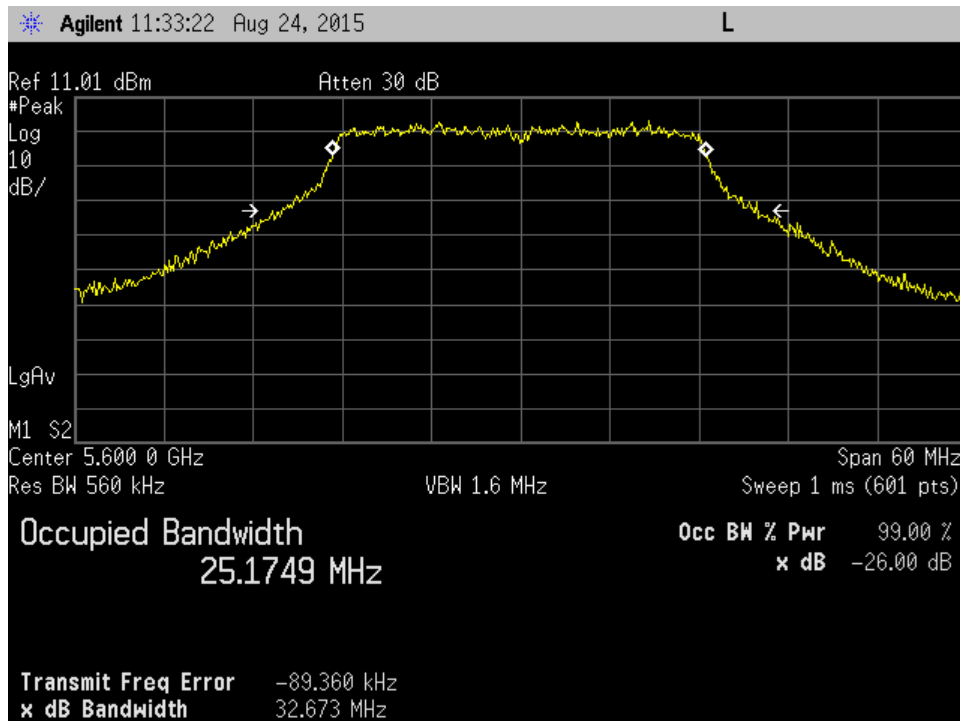




30 MHz mid ch_OCBW_J3

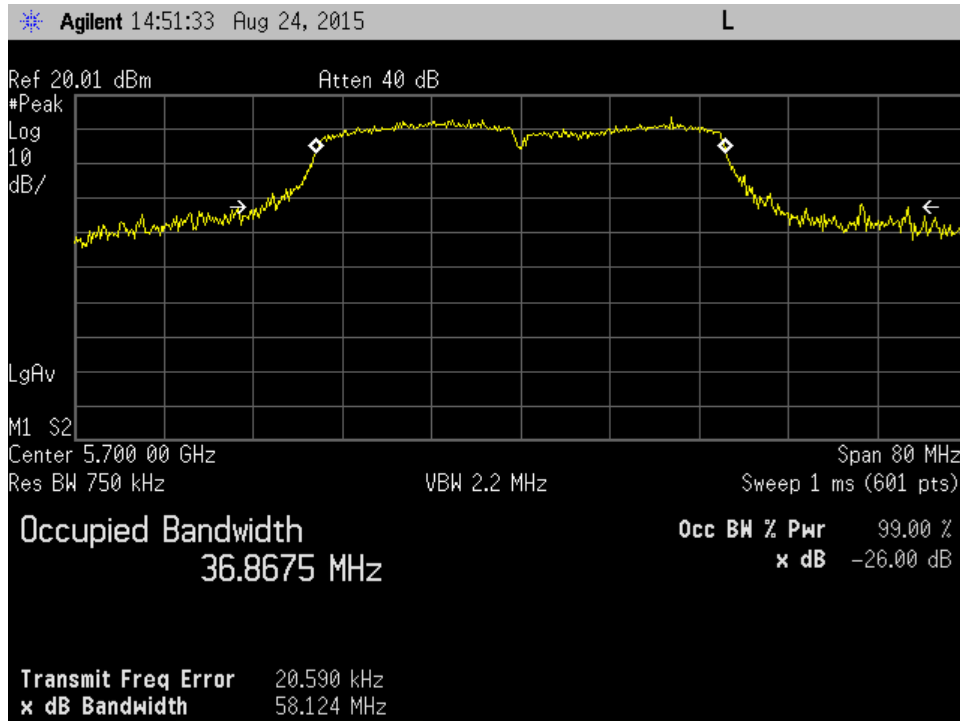


30 MHz mid ch_OCBW_J4

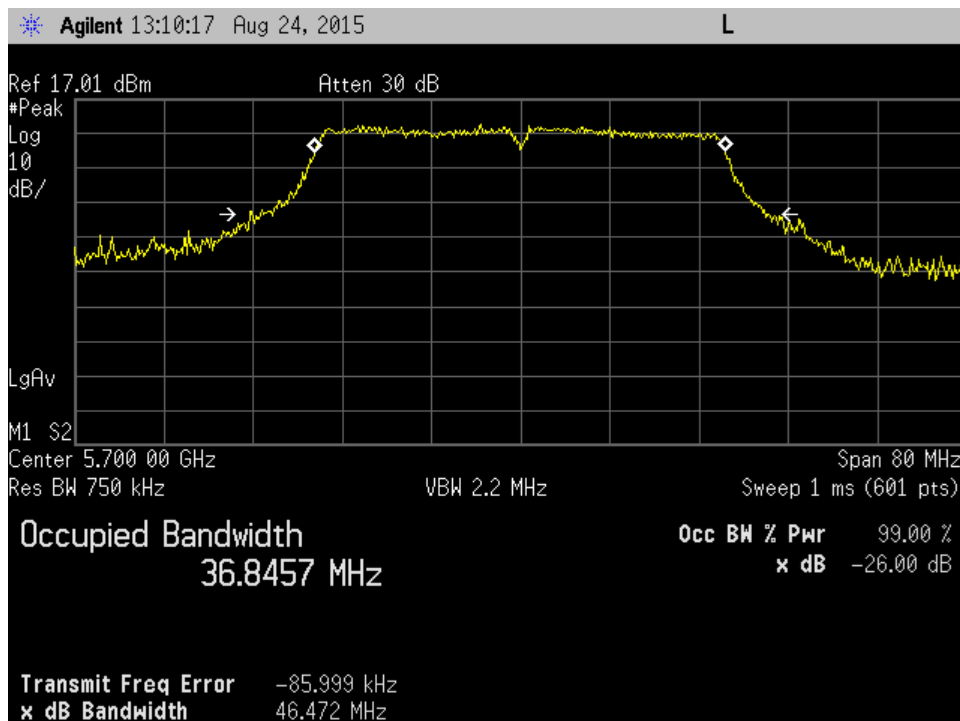




40 MHz high ch_OCBW_J3

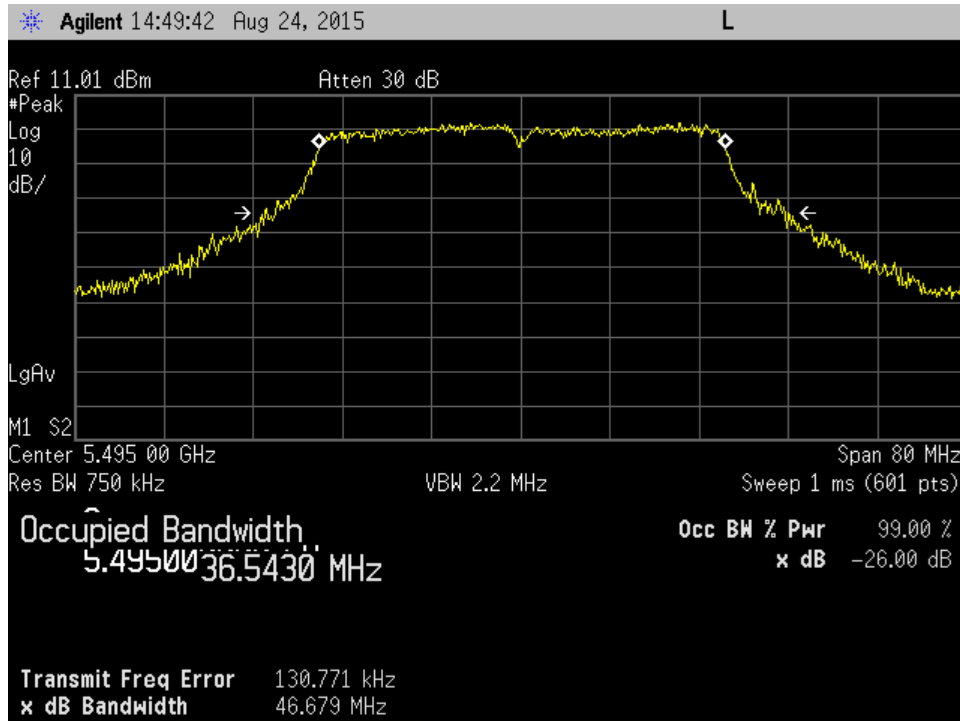


40 MHz high ch_OCBW_J4

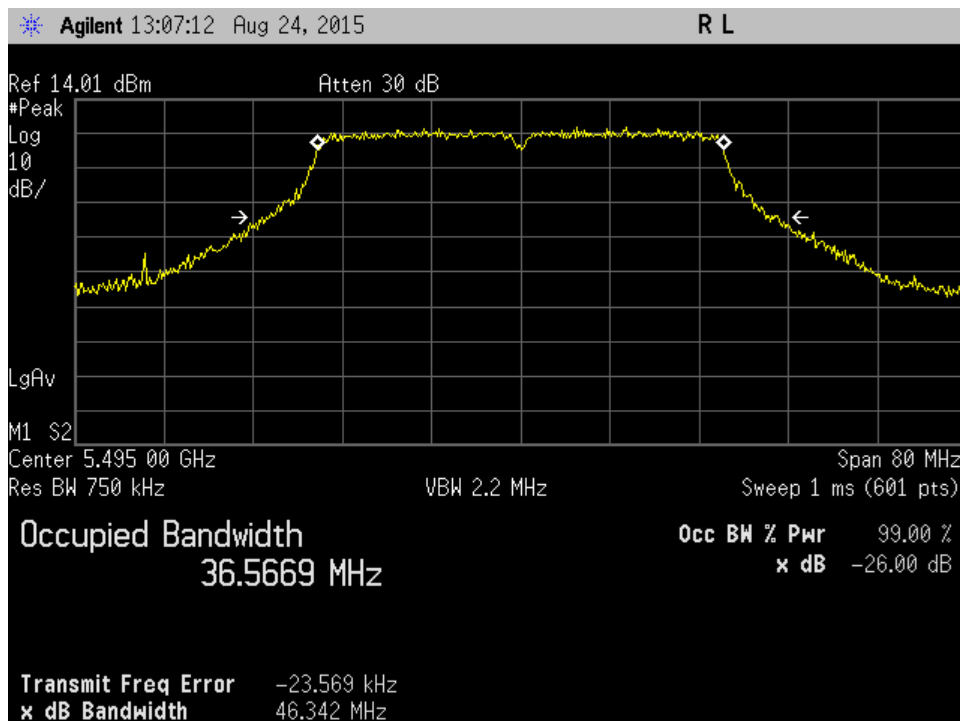




40 MHz low ch_OCBW_J3

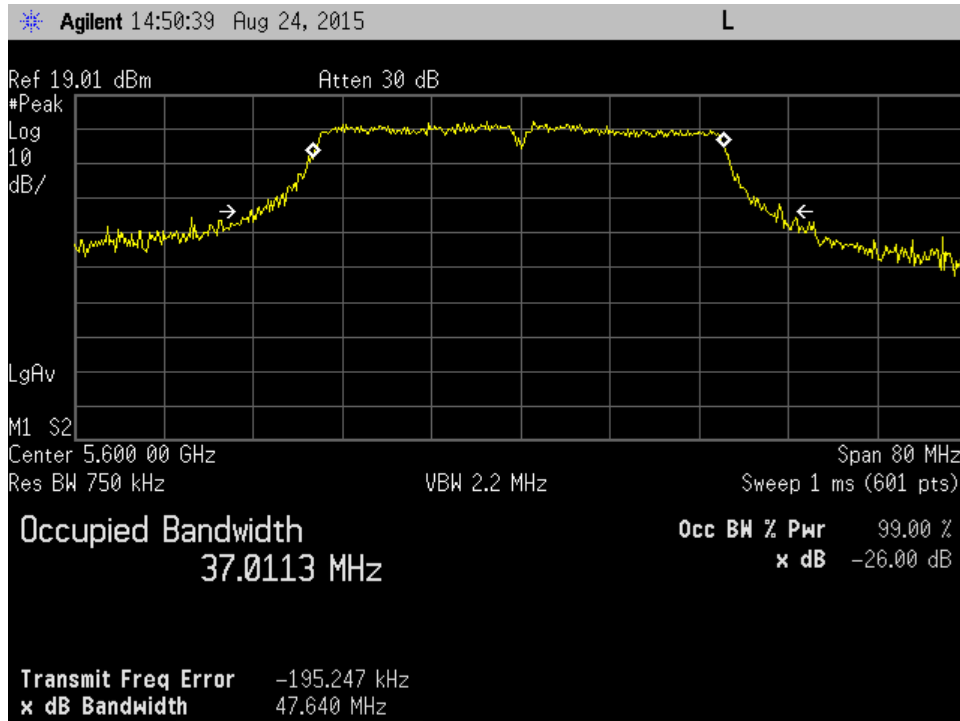


40 MHz low ch_OCBW_J4





40 MHz mid ch_OCBW_J3



40 MHz mid ch_OCBW_J4

