



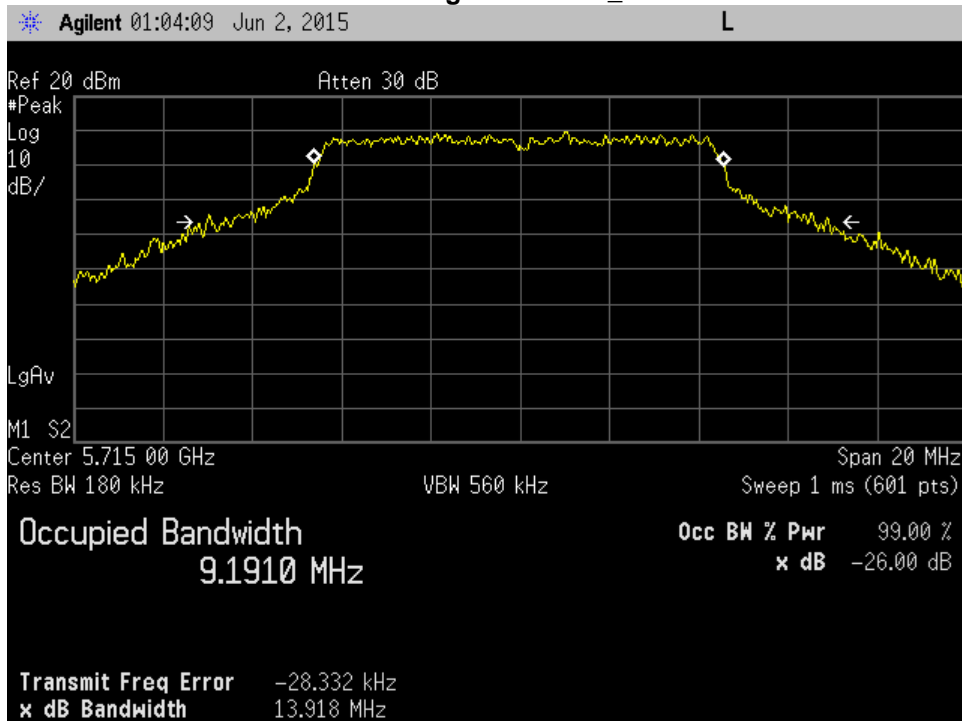
# **Annex D**

## **Occupied Bandwidth**

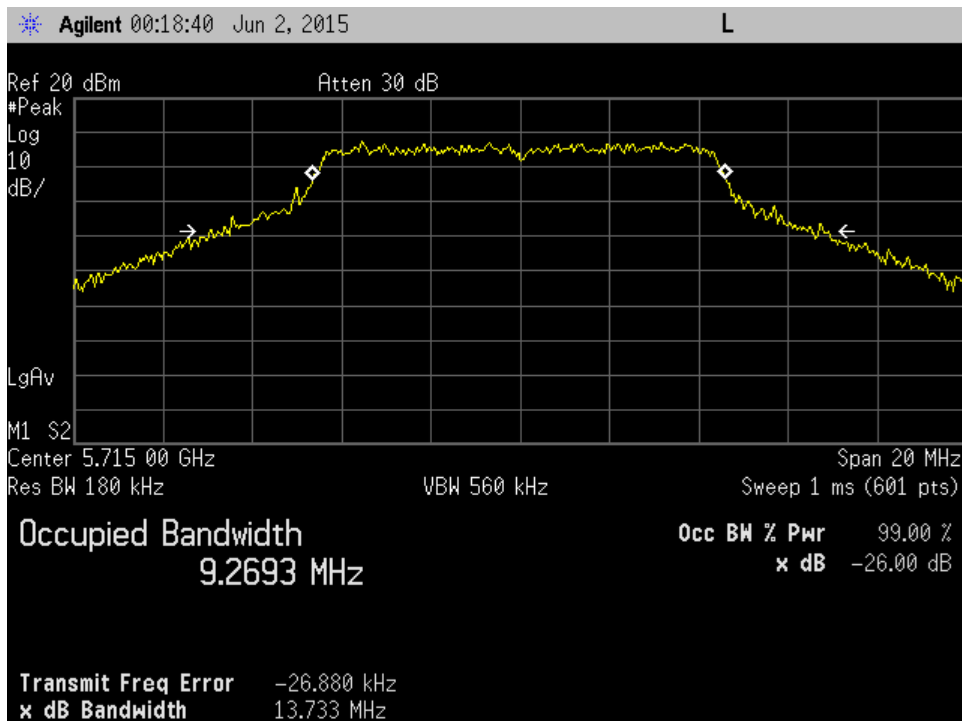
### **Ports J7 and J8**



### 10 MHz high ch OCBW\_J7



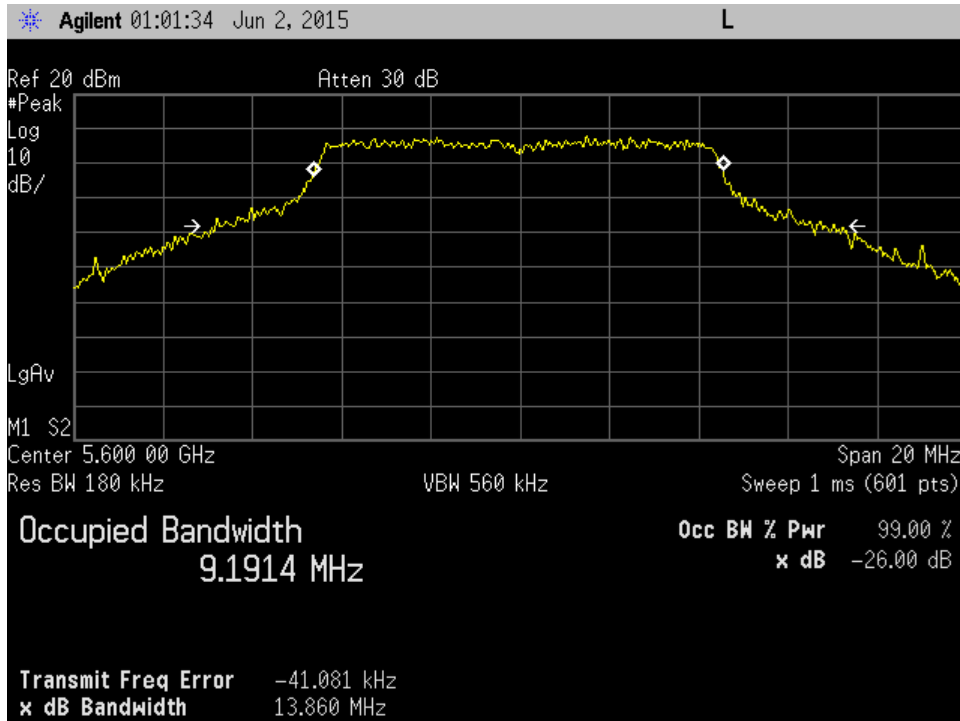
### 10 MHz high ch OCBW\_J8



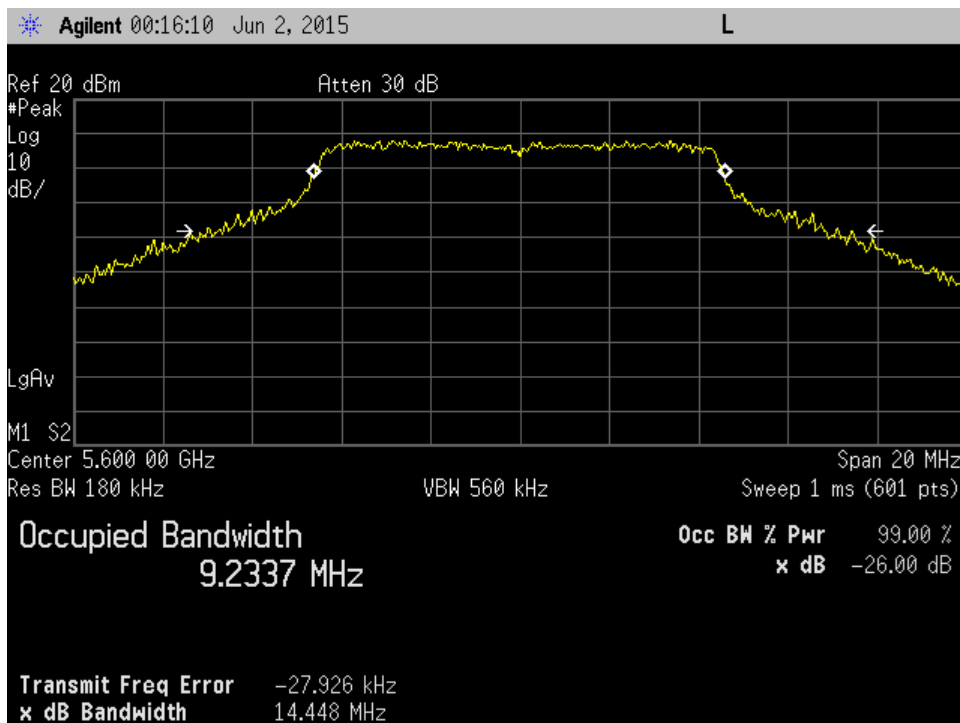




### 10 MHz mid ch OCBW\_J7

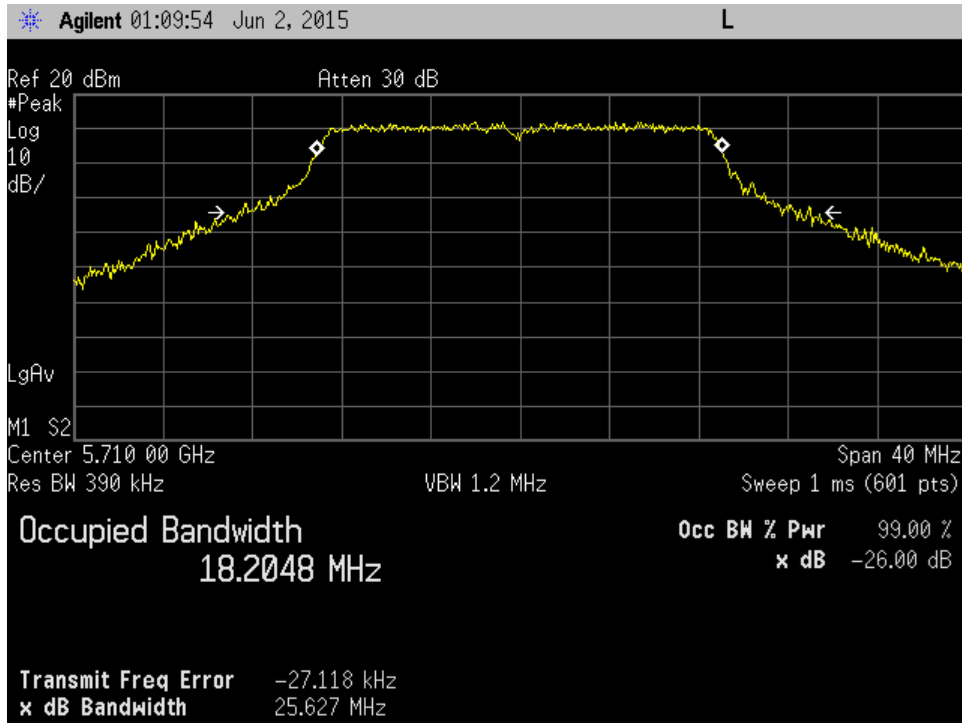


### 10 MHz mid ch OCBW\_J8

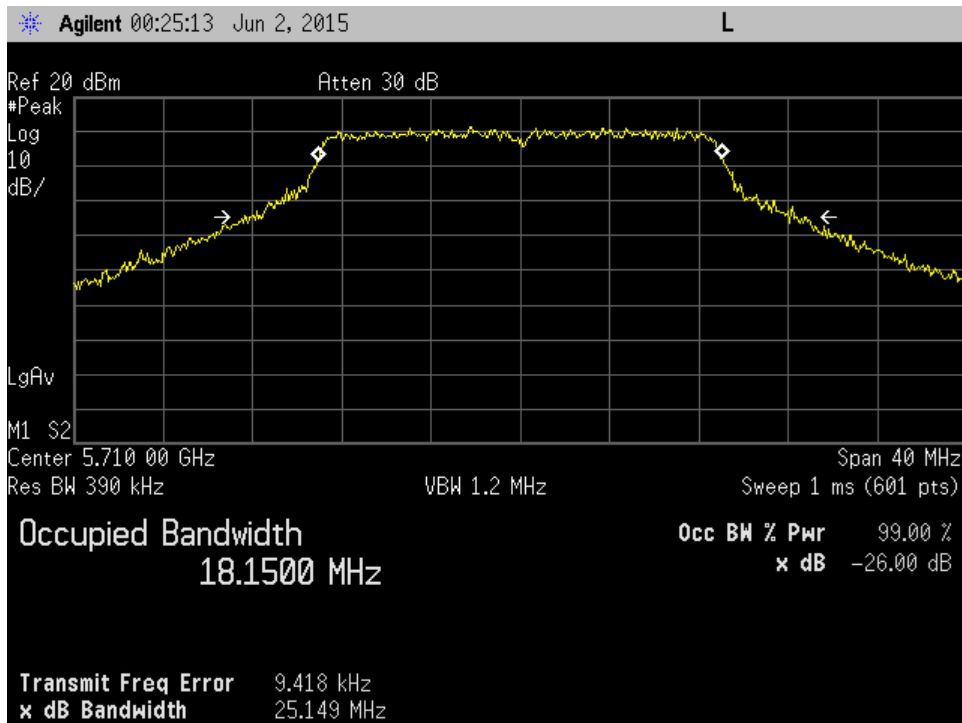




### 20 MHz high ch OCBW\_J7

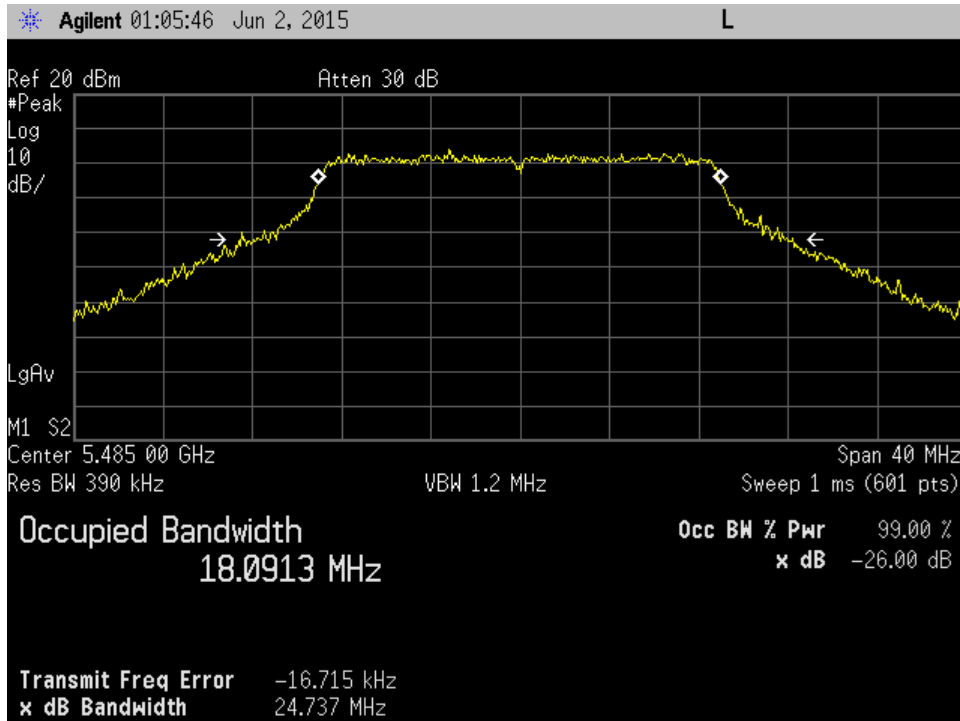


### 20 MHz high ch OCBW\_J8

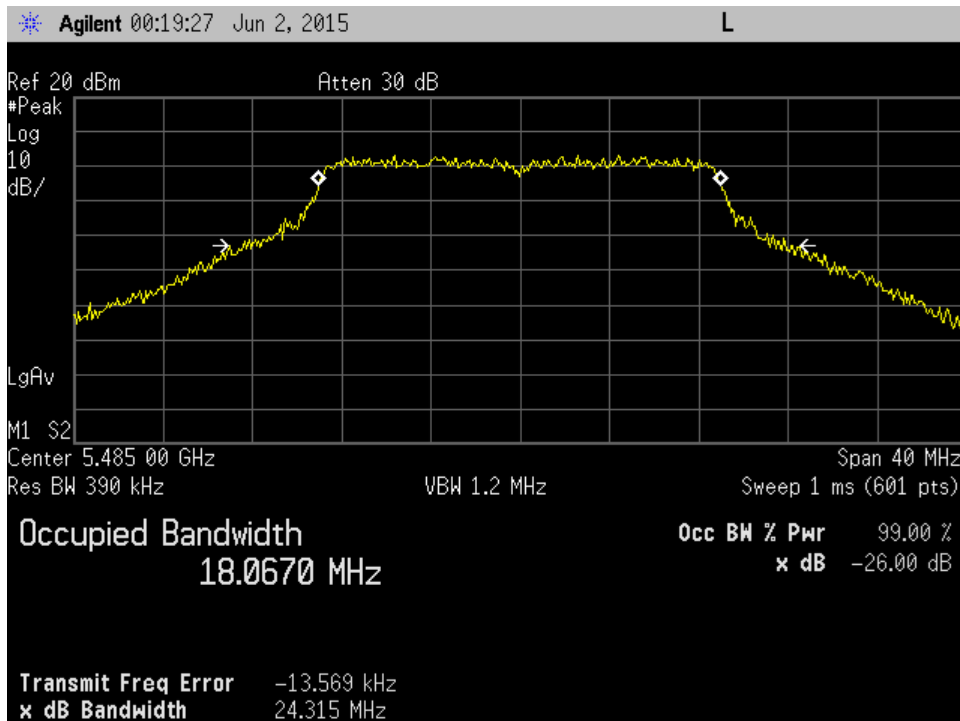




### 20 MHz low ch OCBW\_J7

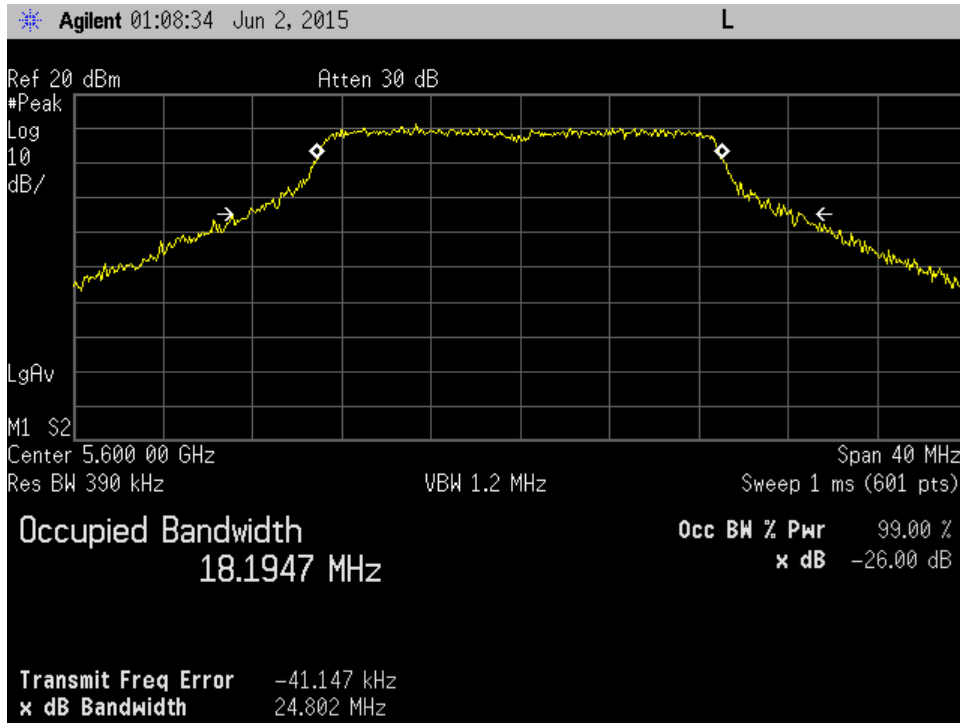


### 20 MHz low ch OCBW\_J8

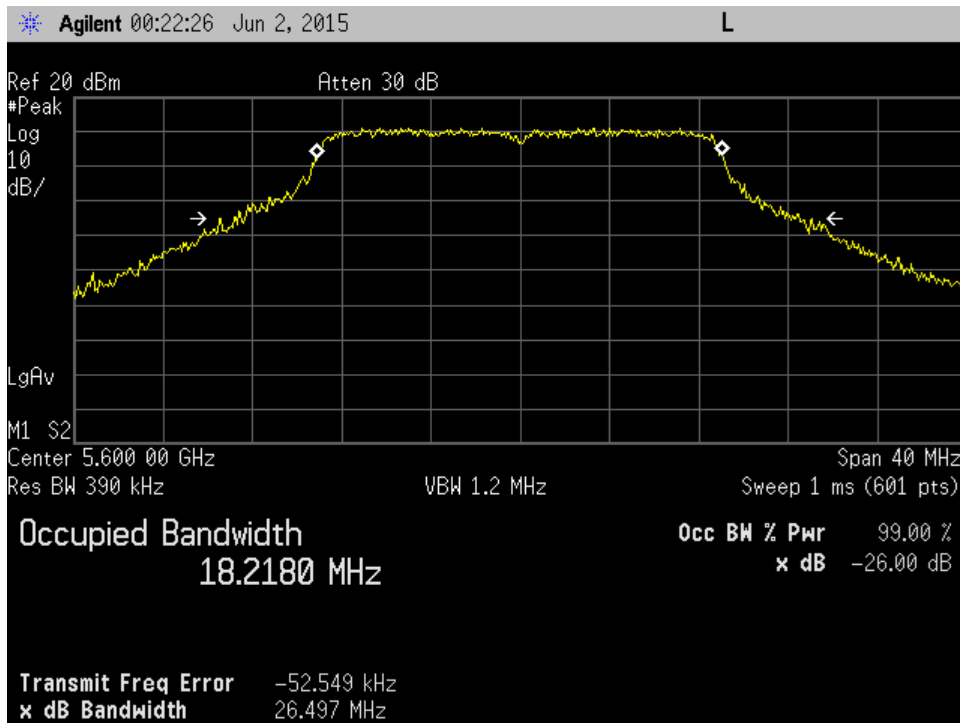




### 20 MHz mid ch OCBW\_J7

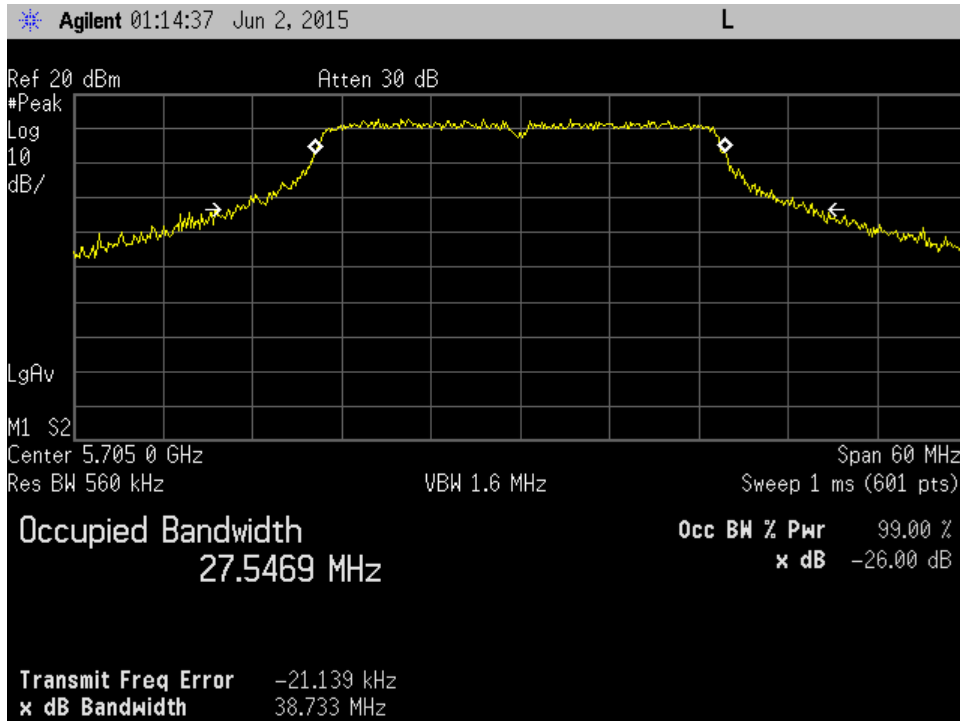


### 20 MHz mid ch OCBW\_J8

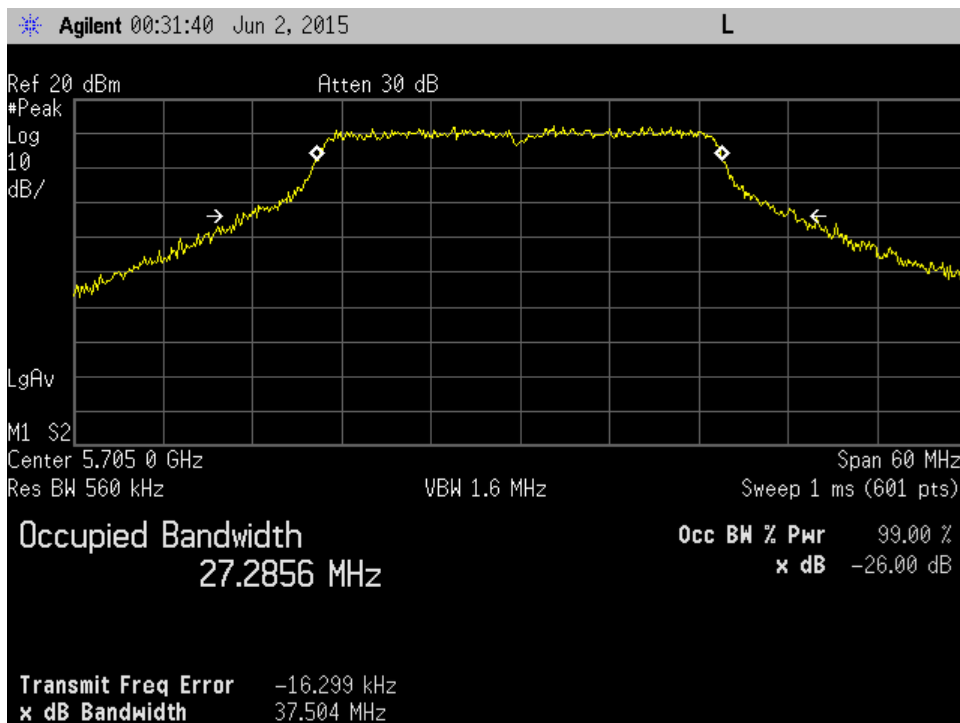




### 30 MHz high ch OCBW\_J7



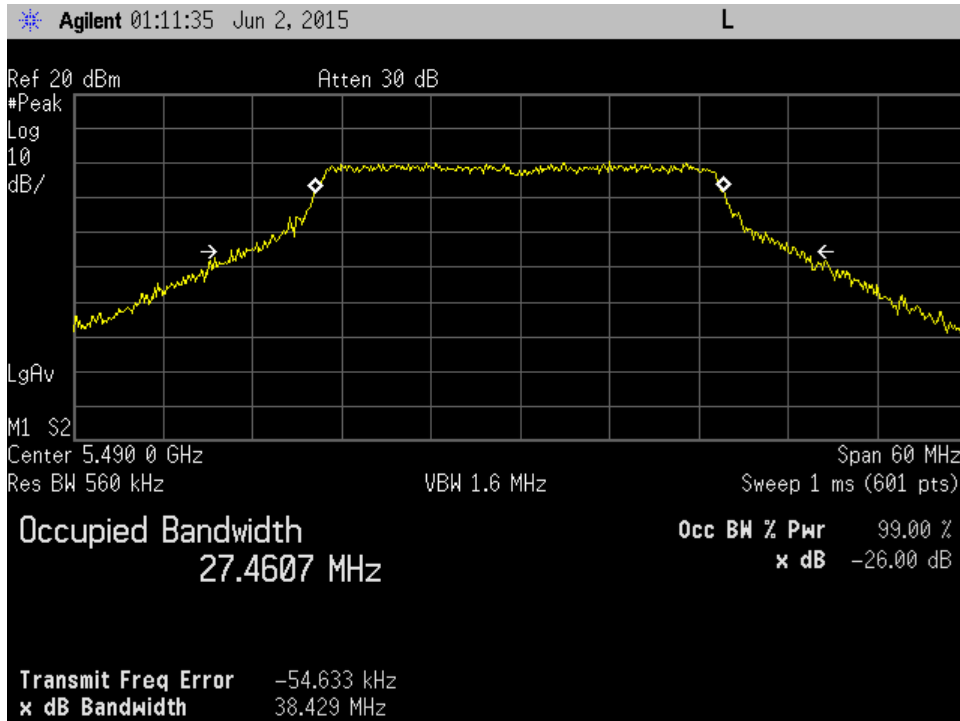
### 30 MHz high ch OCBW\_J8



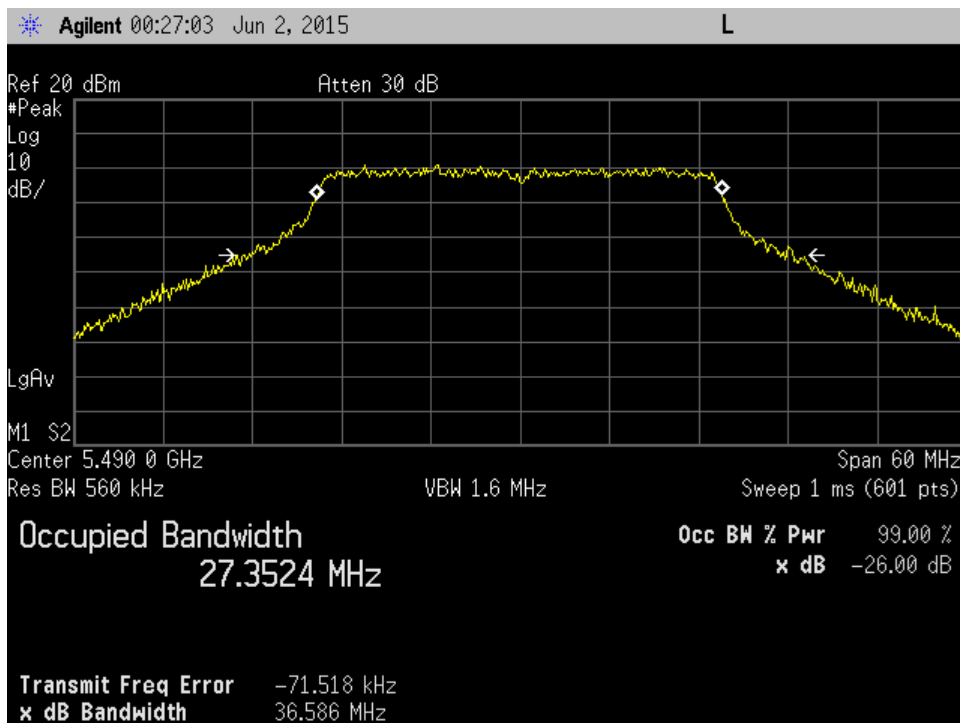




### 30 MHz low ch OCBW\_J7

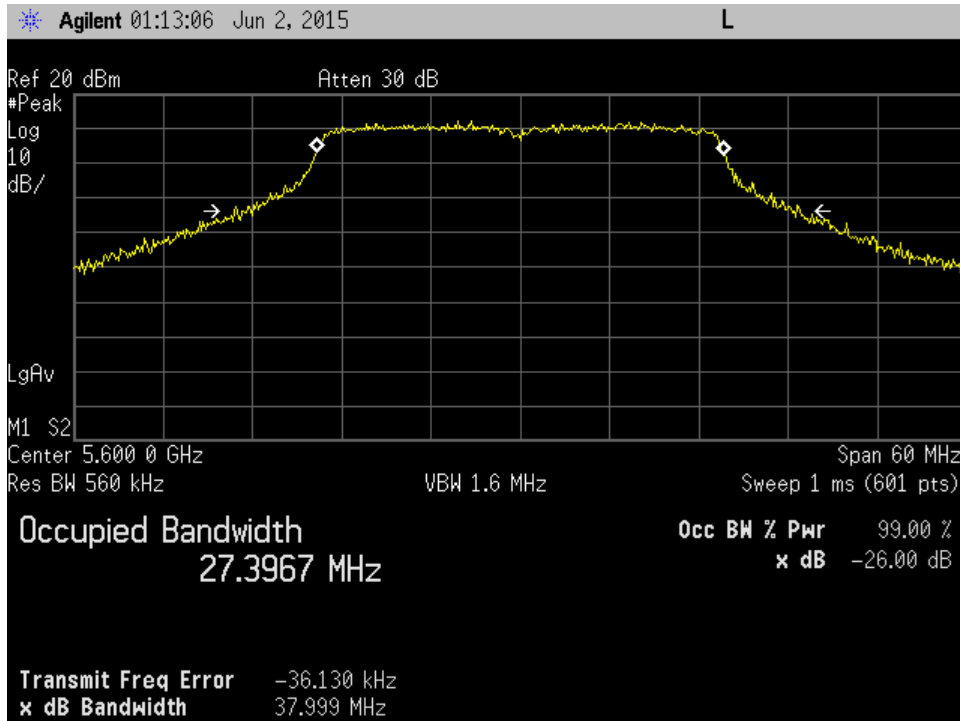


### 30 MHz low ch OCBW\_J8

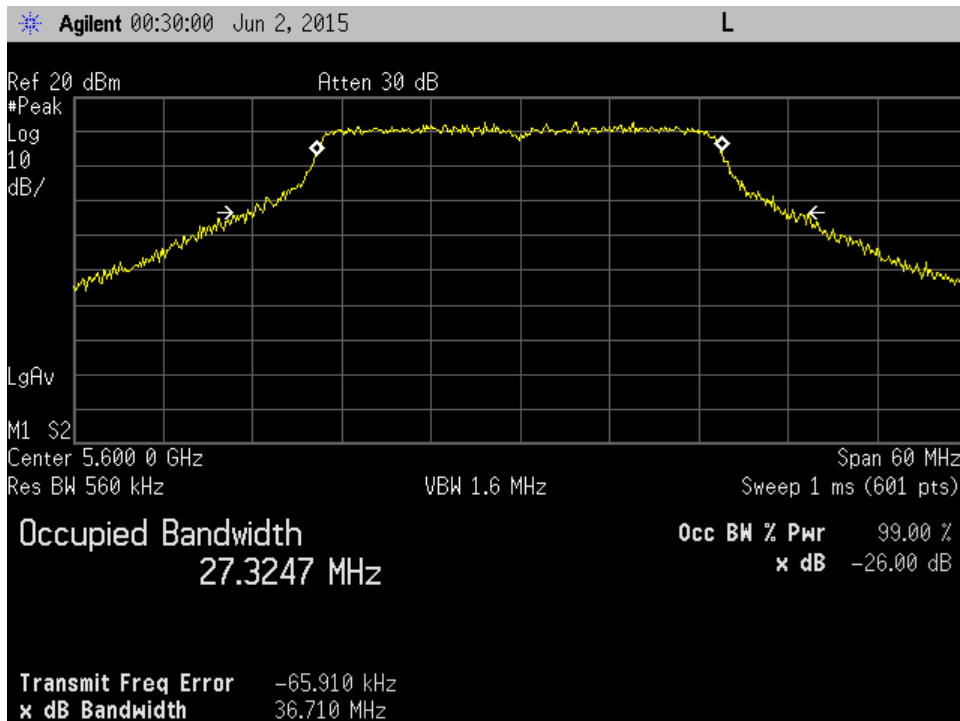




### 30 MHz mid ch OCBW\_J7

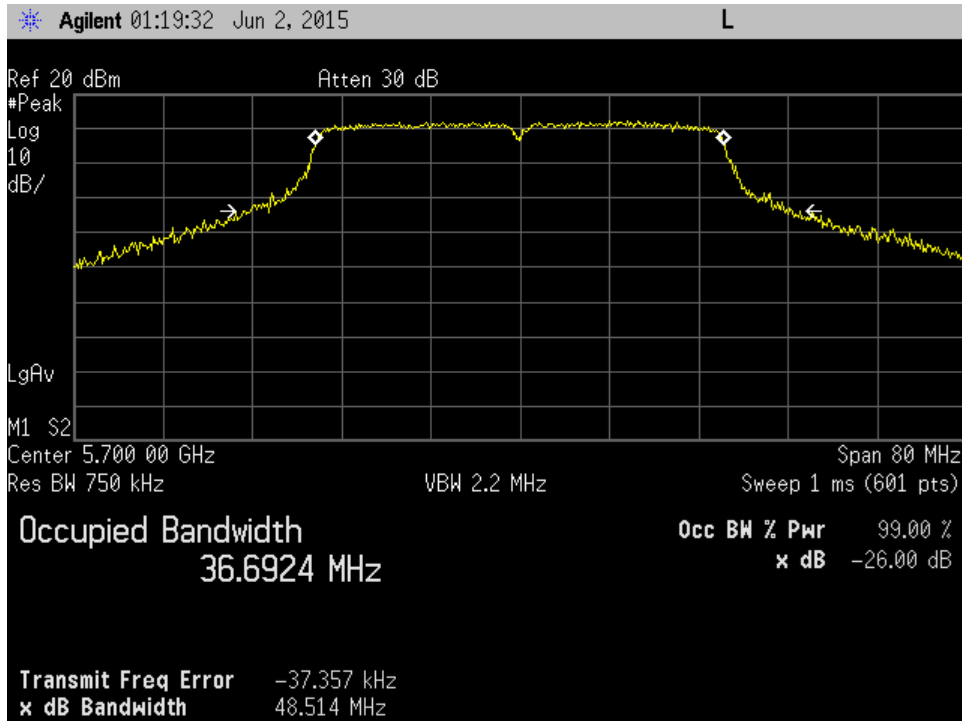


### 30 MHz mid ch OCBW\_J8

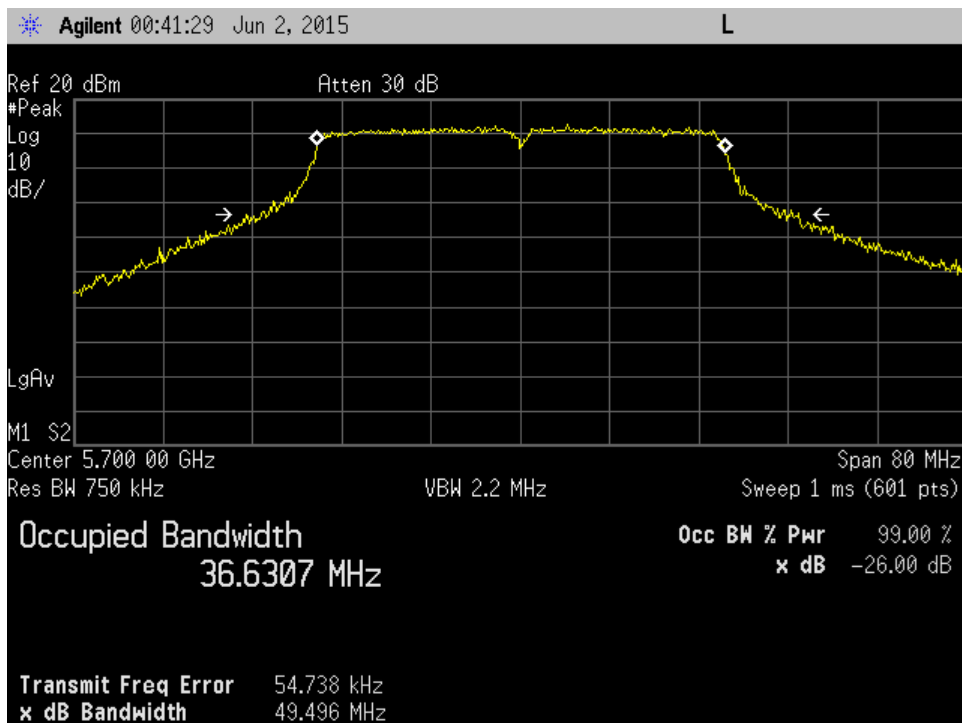




### 40 MHz high ch OCBW\_J7

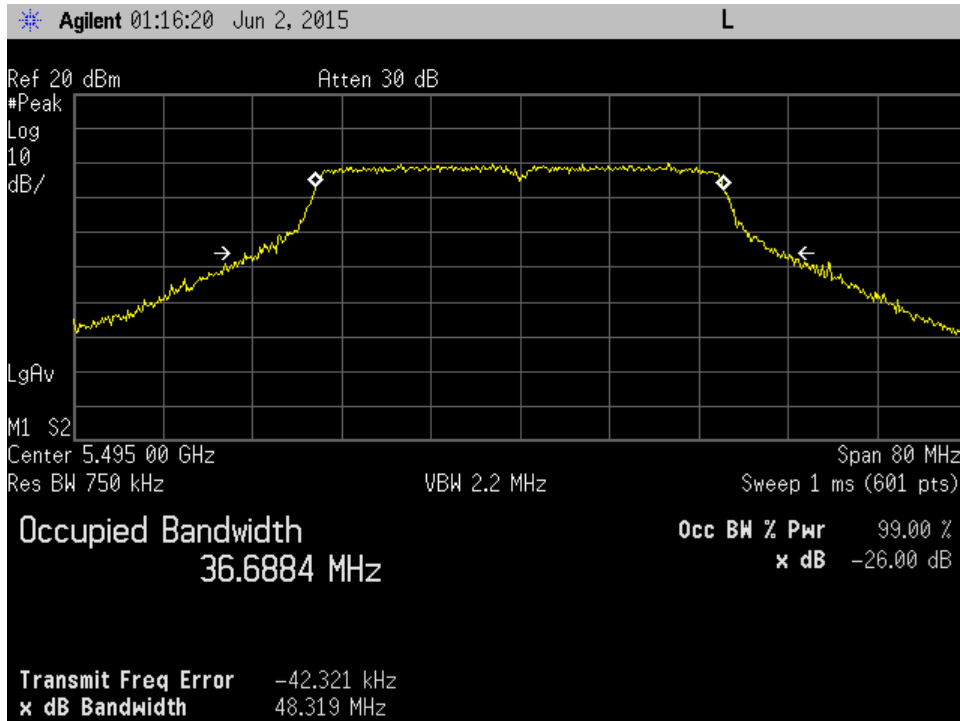


### 40 MHz high ch OCBW\_J8

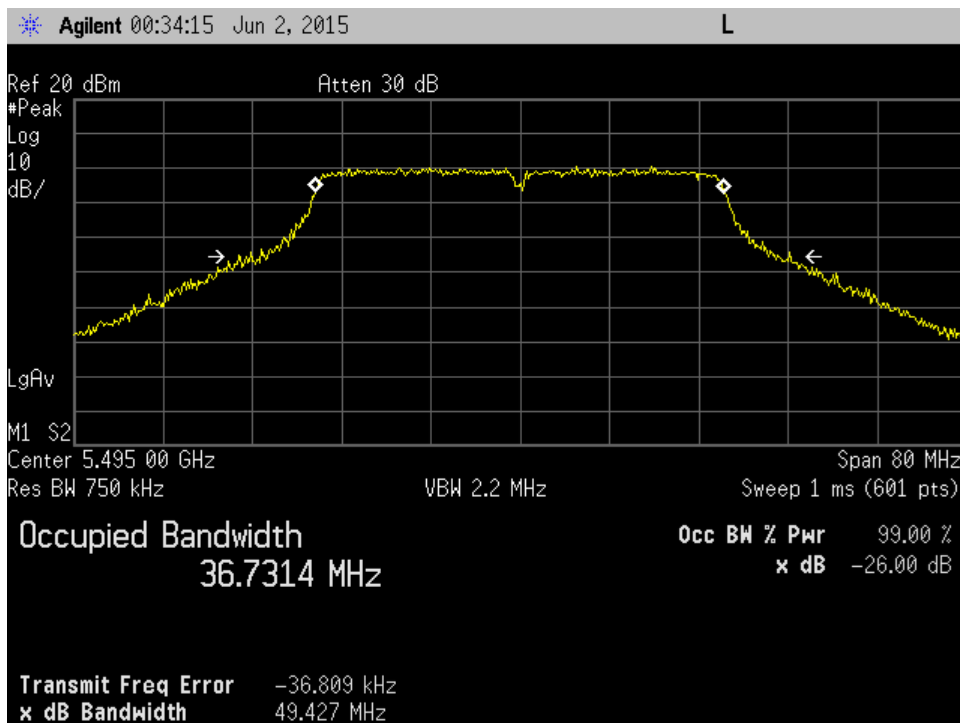




### 40 MHz low ch OCBW\_J7

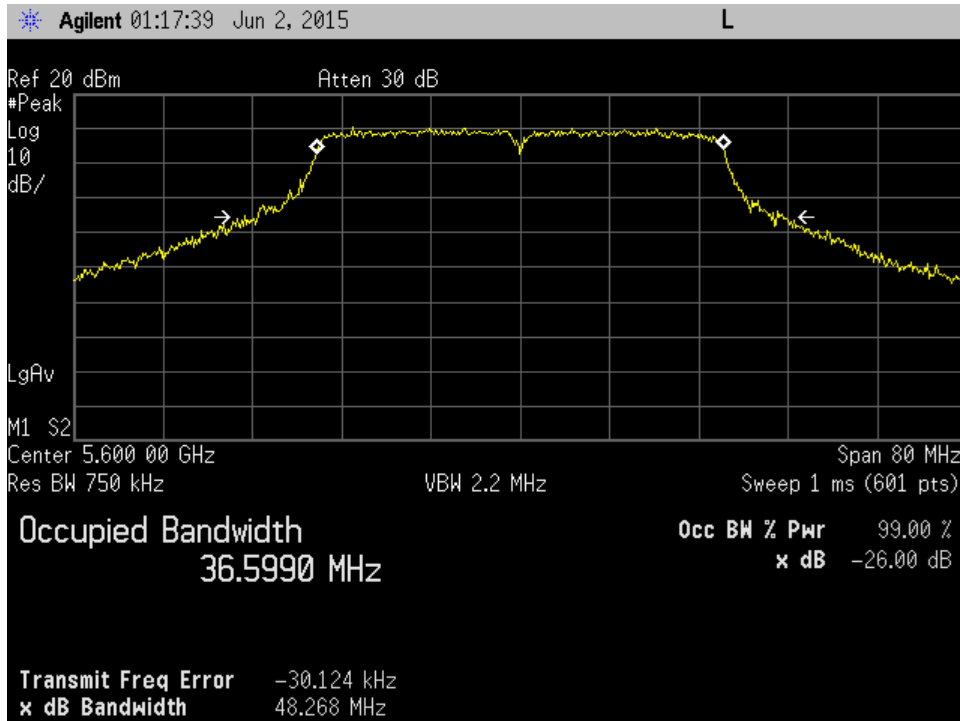


### 40 MHz low ch OCBW\_J8

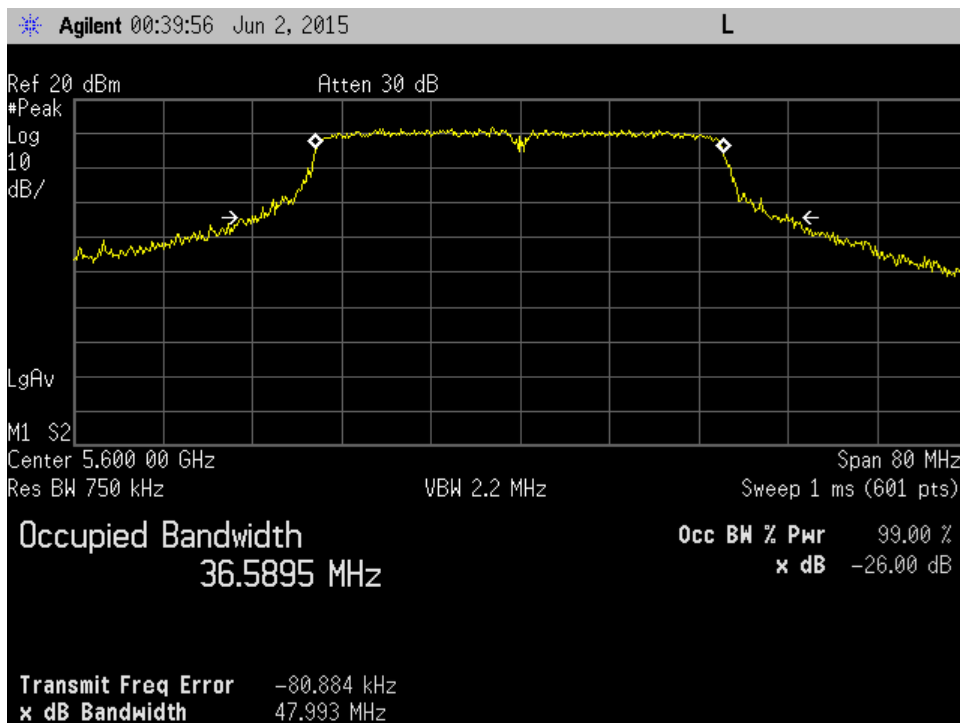




### 40 MHz mid ch OCBW\_J7

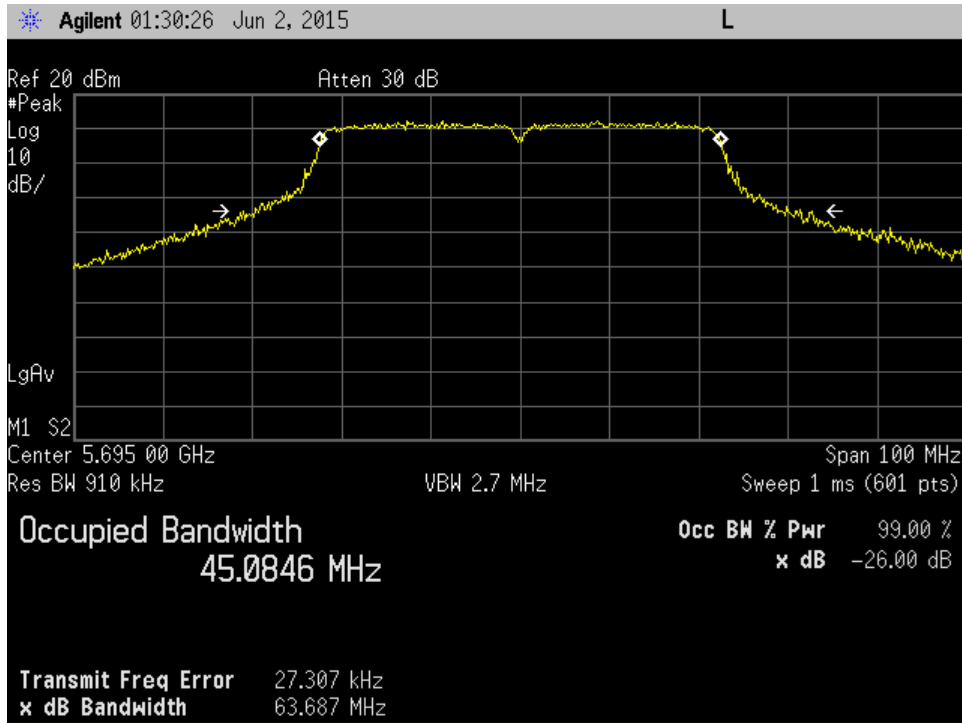


### 40 MHz mid ch OCBW\_J8

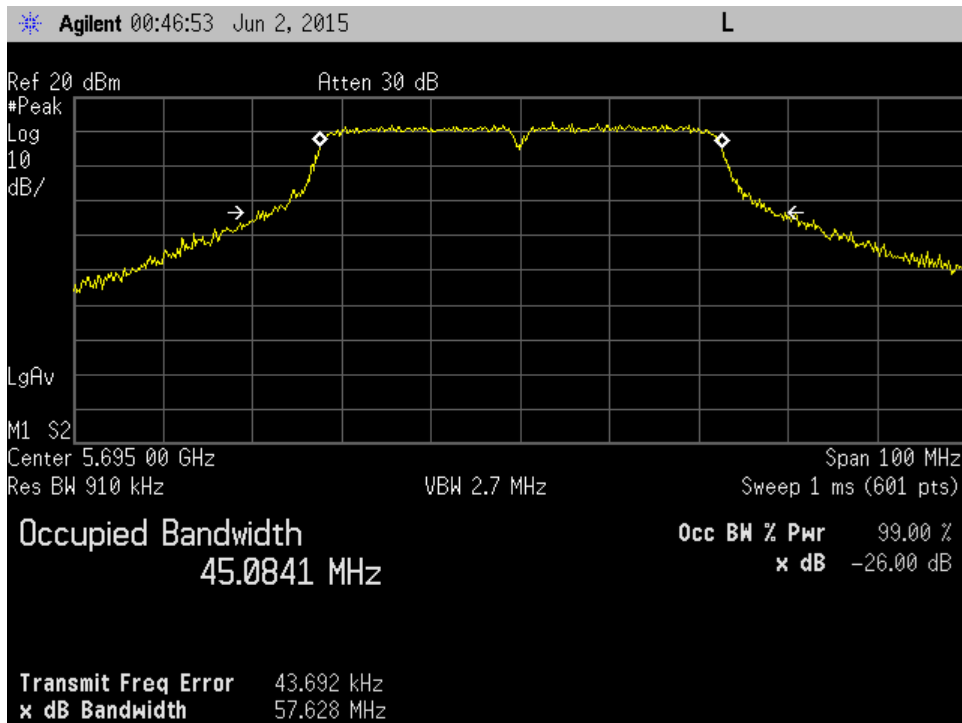




### 50 MHz high ch OCBW\_J7

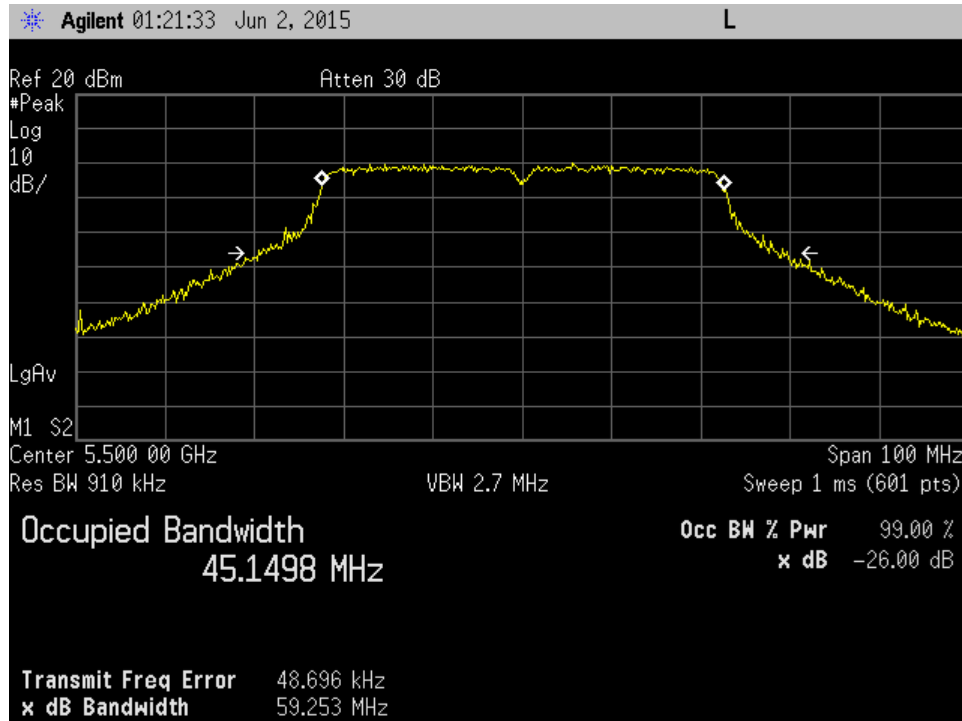


### 50 MHz high ch OCBW\_J8

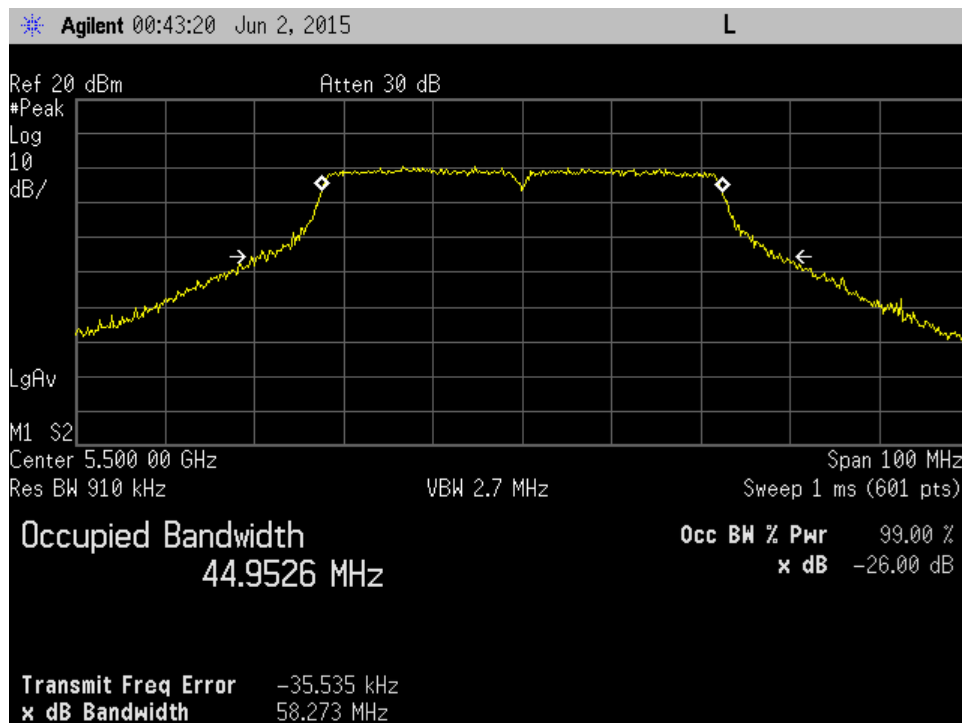




### 50 MHz low ch OCBW\_J7

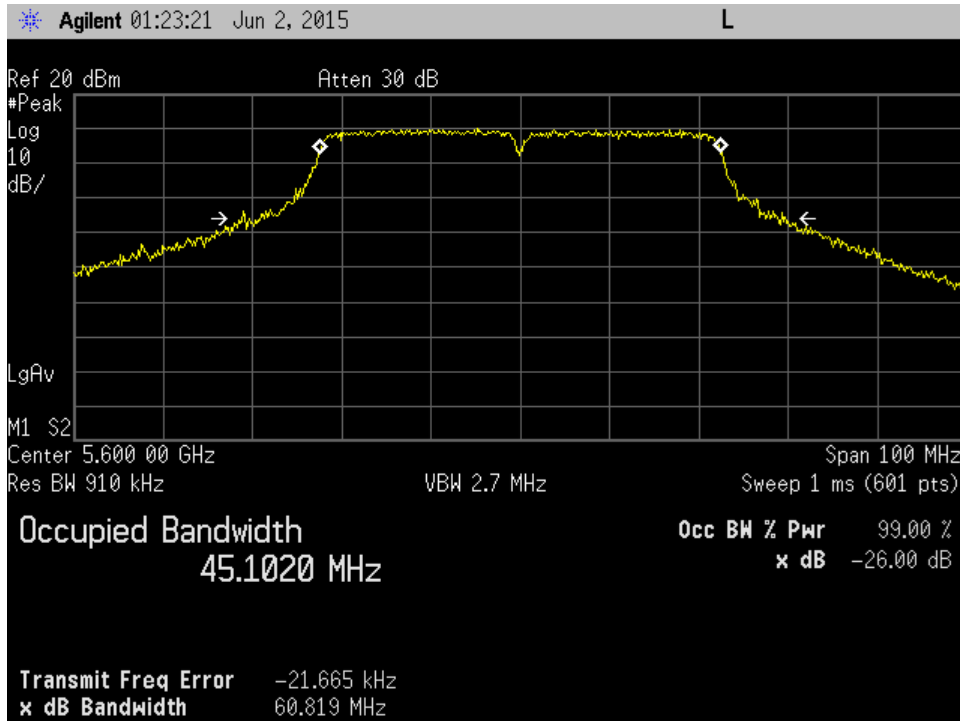


### 50 MHz low ch OCBW\_J8

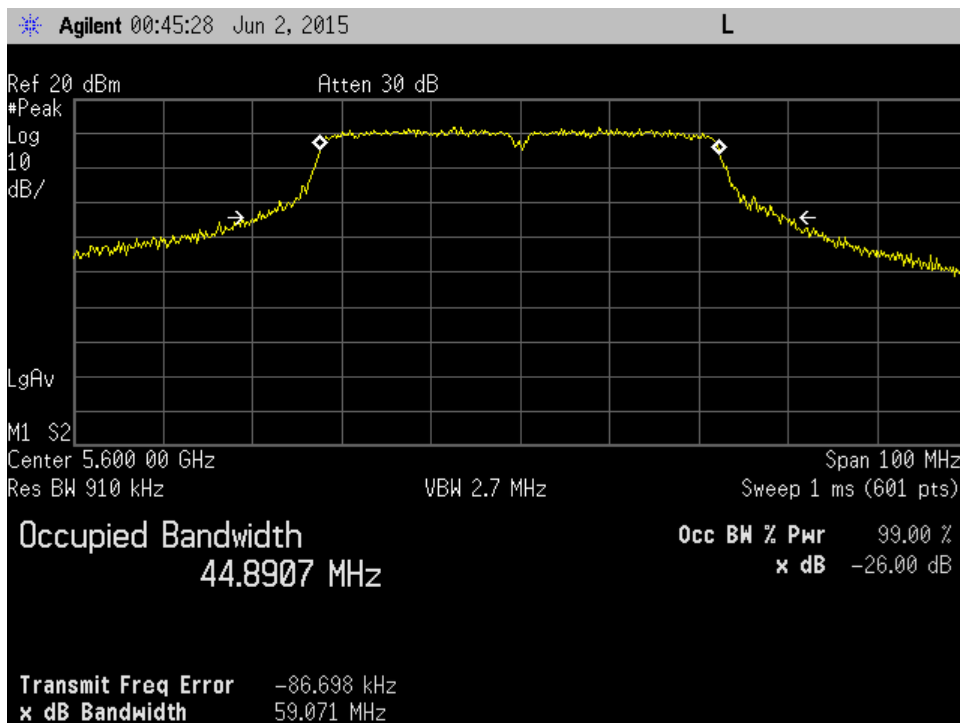




### 50 MHz mid ch OCBW\_J7



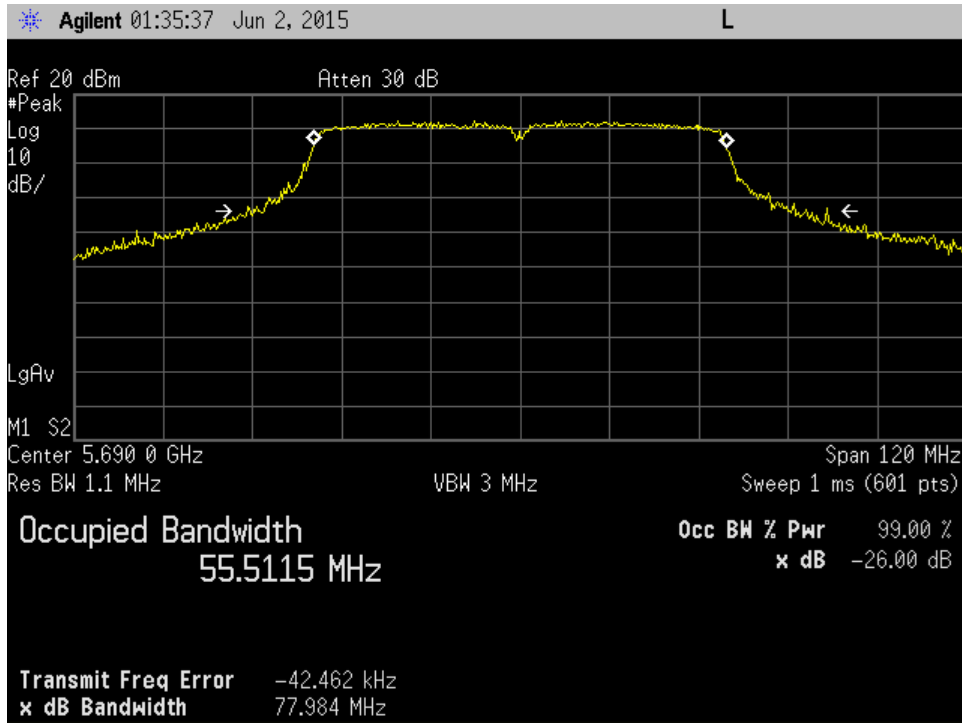
### 50 MHz mid ch OCBW\_J8



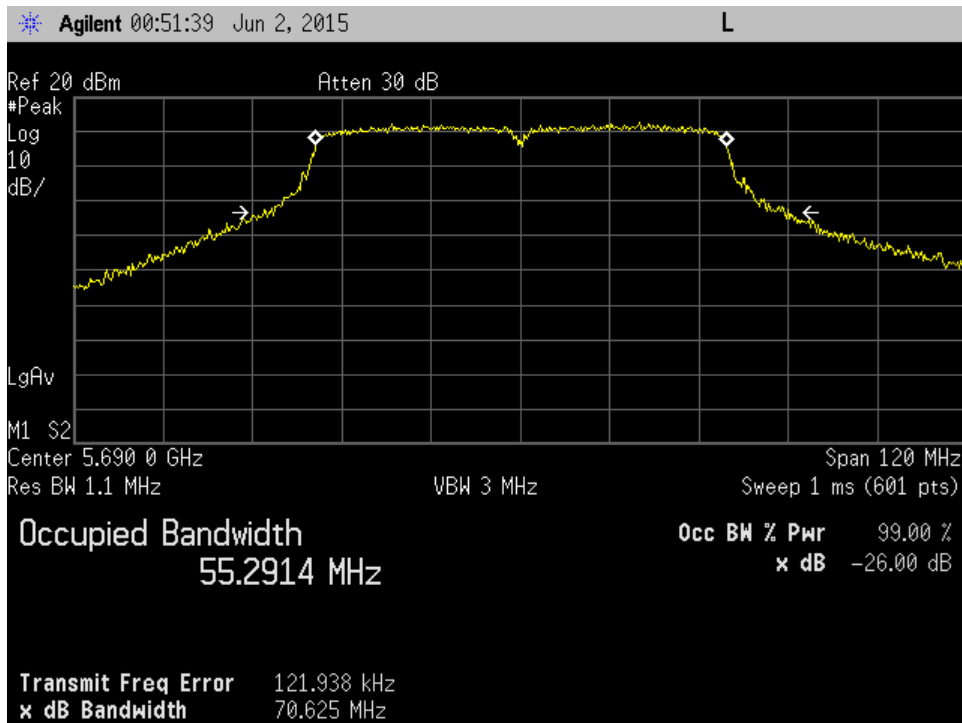




**60 MHz high ch OCBW\_J7**

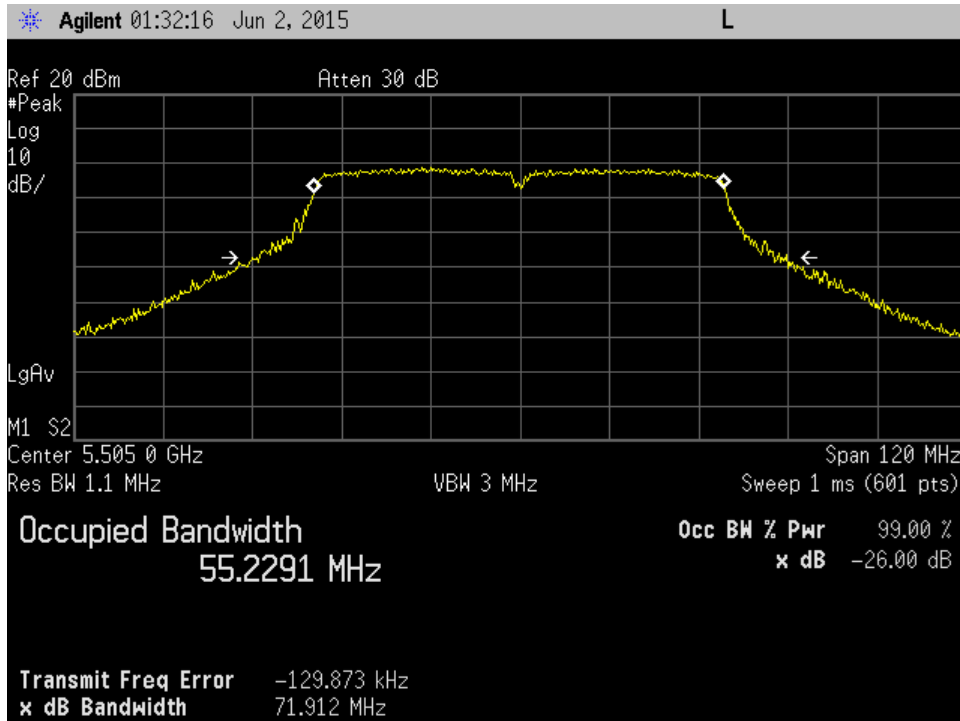


**60 MHz high ch OCBW\_J8**

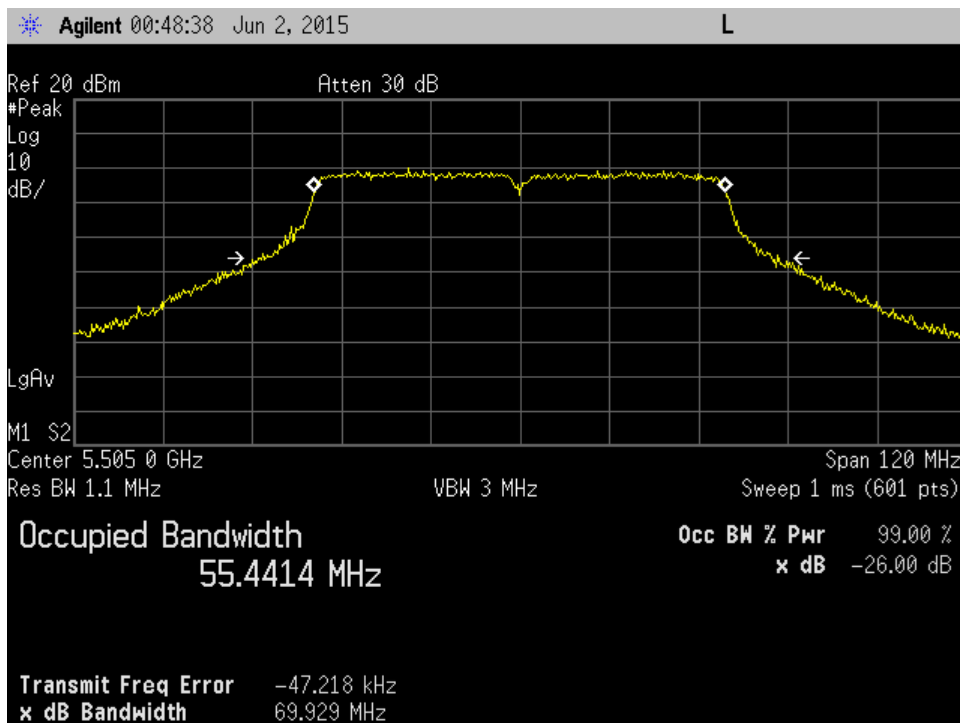




### 60 MHz low ch OCBW\_J7

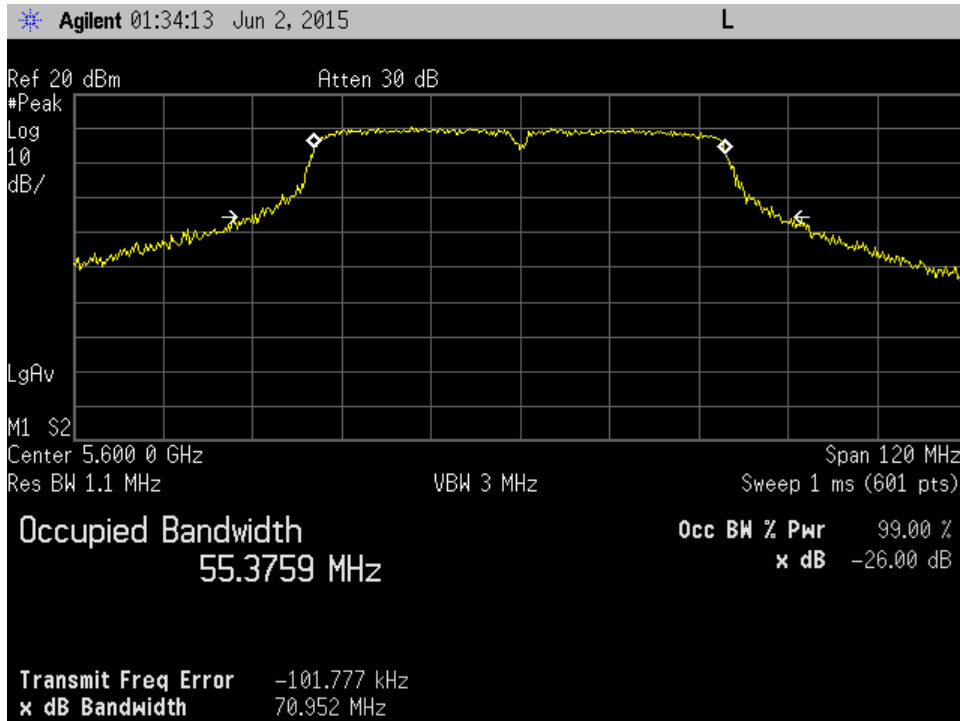


### 60 MHz low ch OCBW\_J8

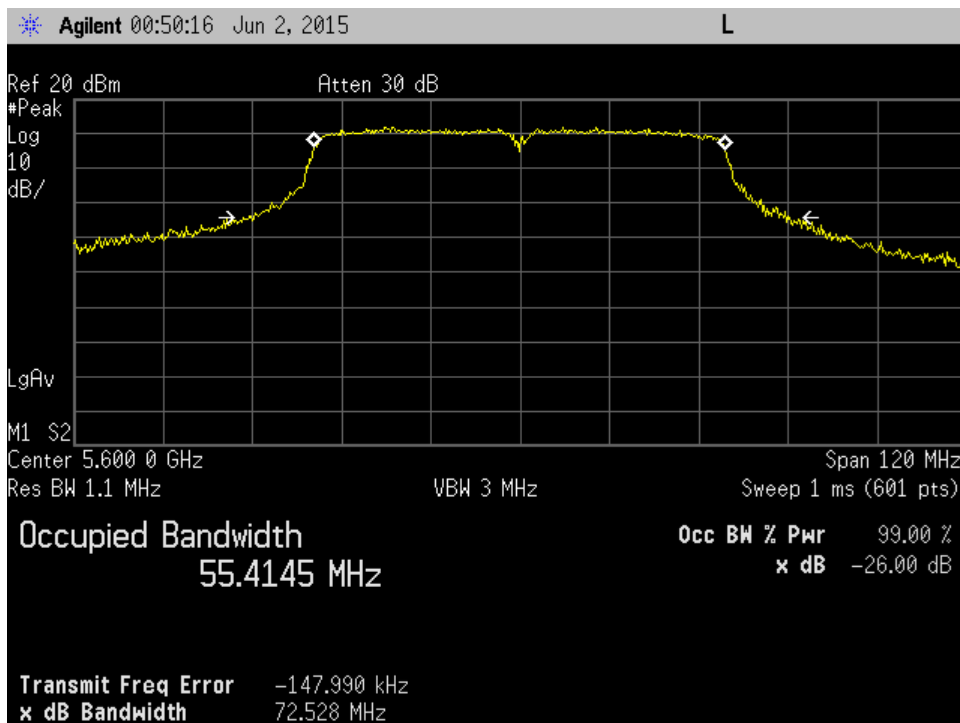




### 60 MHz mid ch OCBW\_J7

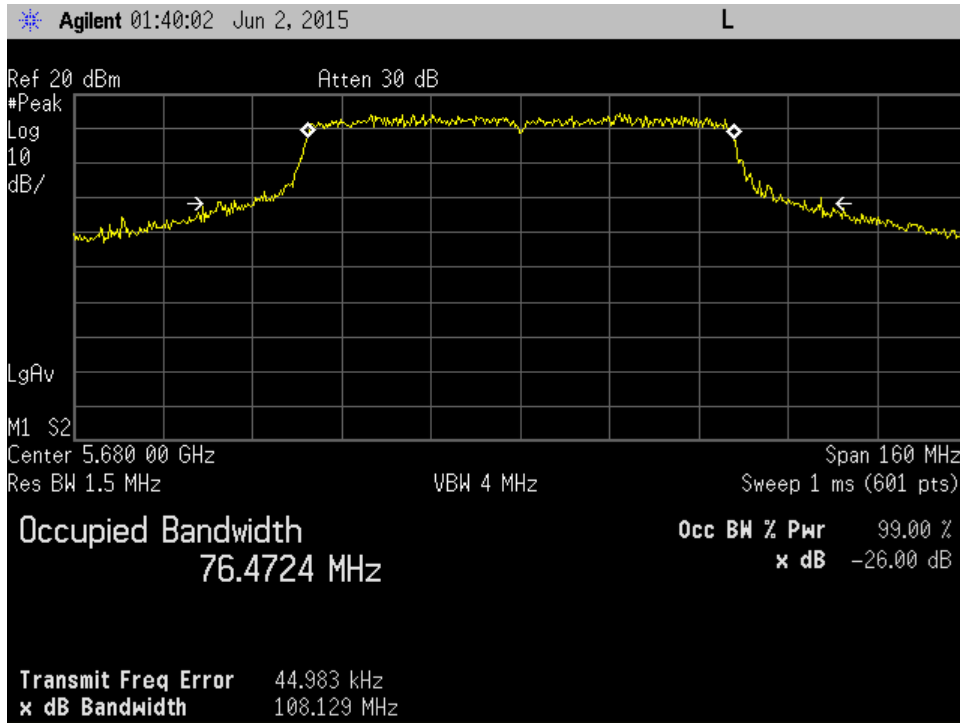


### 60 MHz mid ch OCBW\_J8

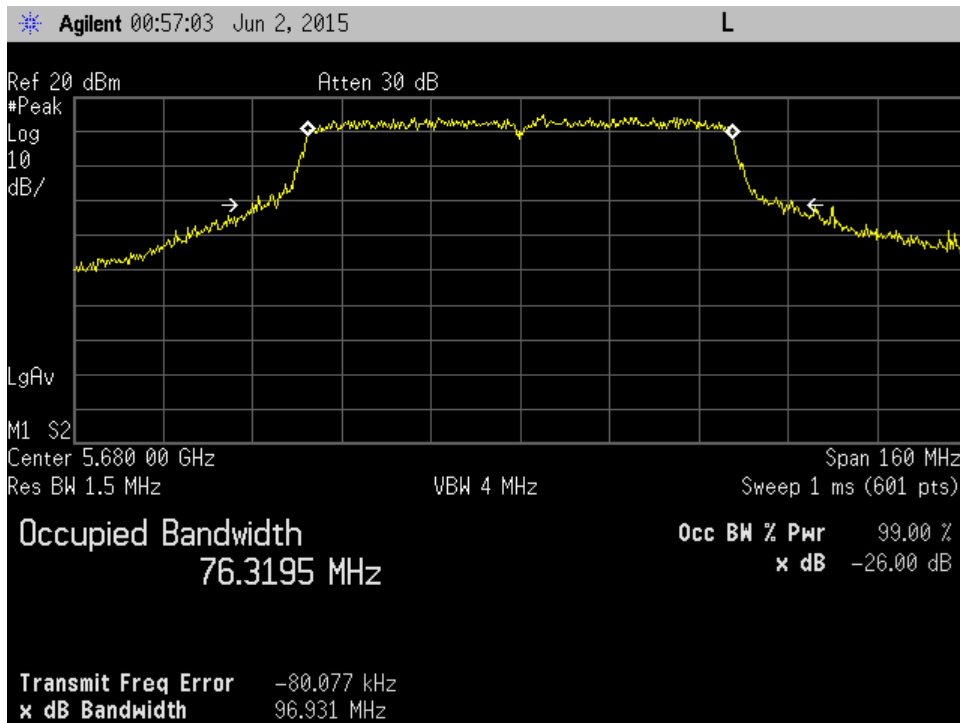




### 80 MHz high ch OCBW\_J7

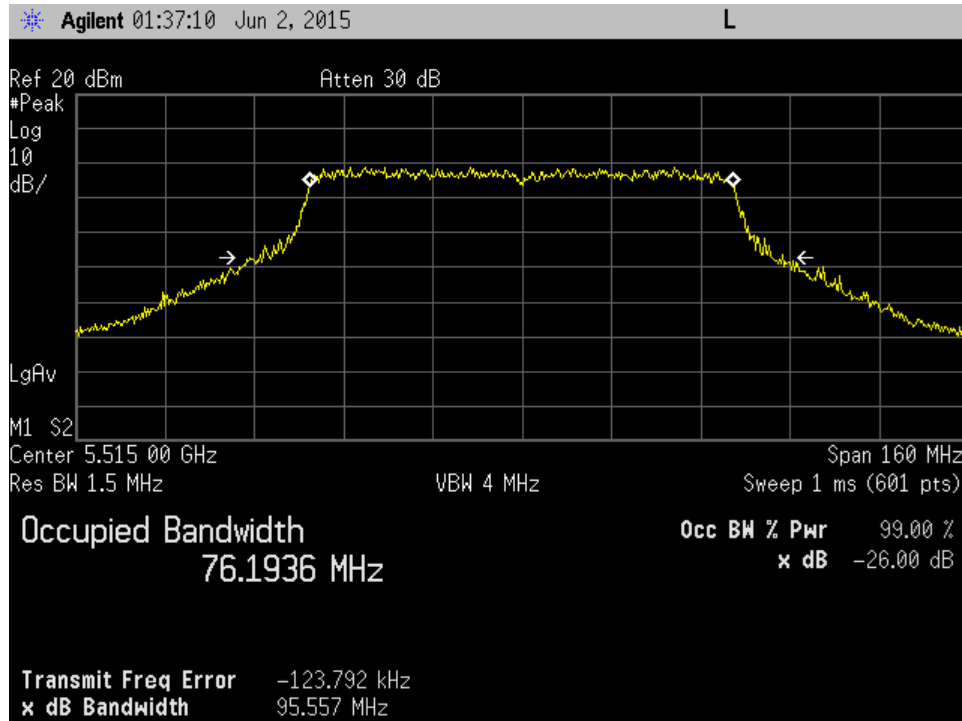


### 80 MHz high ch OCBW\_J8

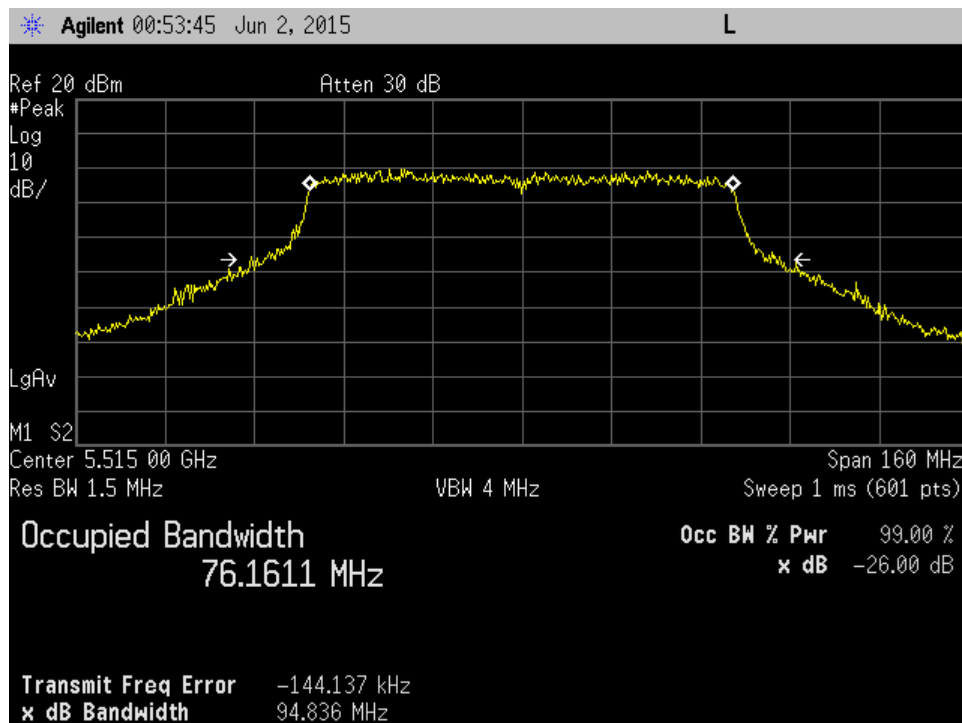




### 80 MHz low ch OCBW\_J7

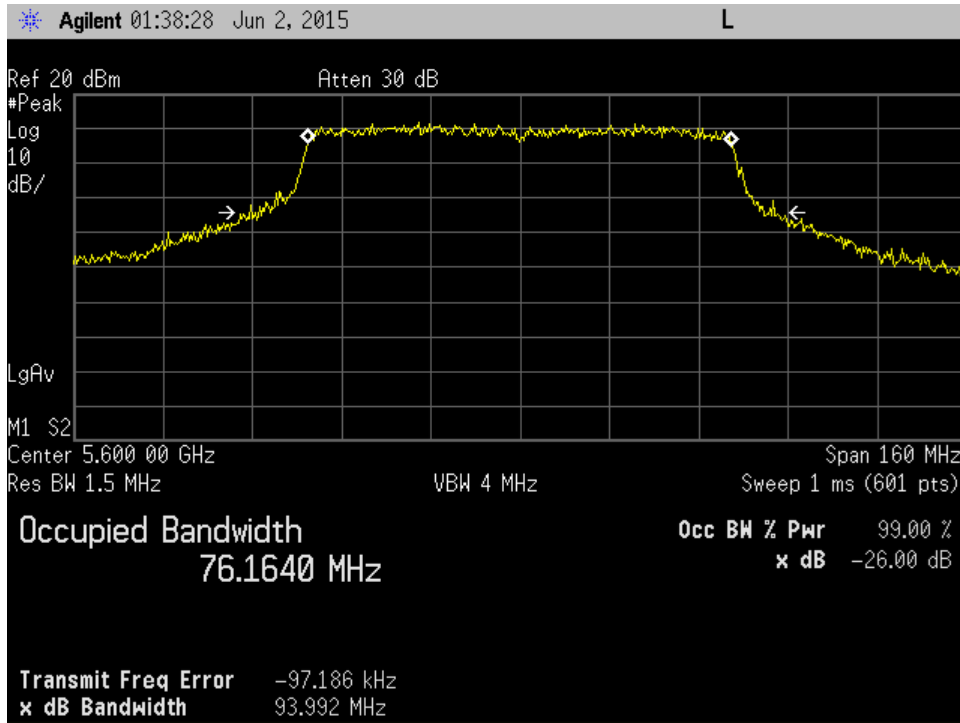


### 80 MHz low ch OCBW\_J8





**80 MHz mid ch OCBW\_J7**



**80 MHz mid ch OCBW\_J8**

