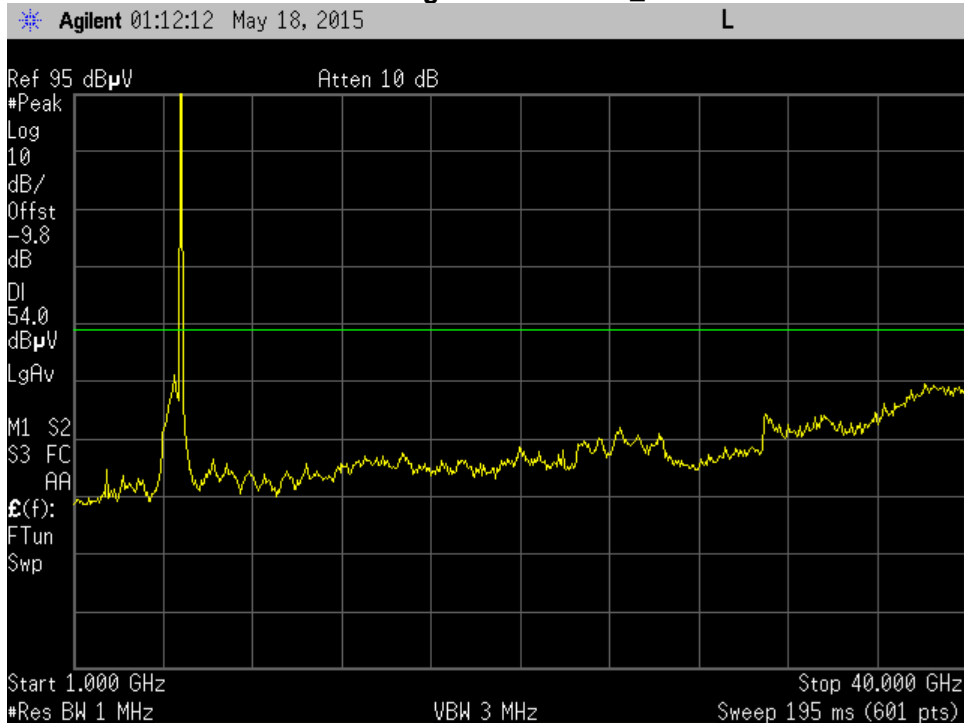




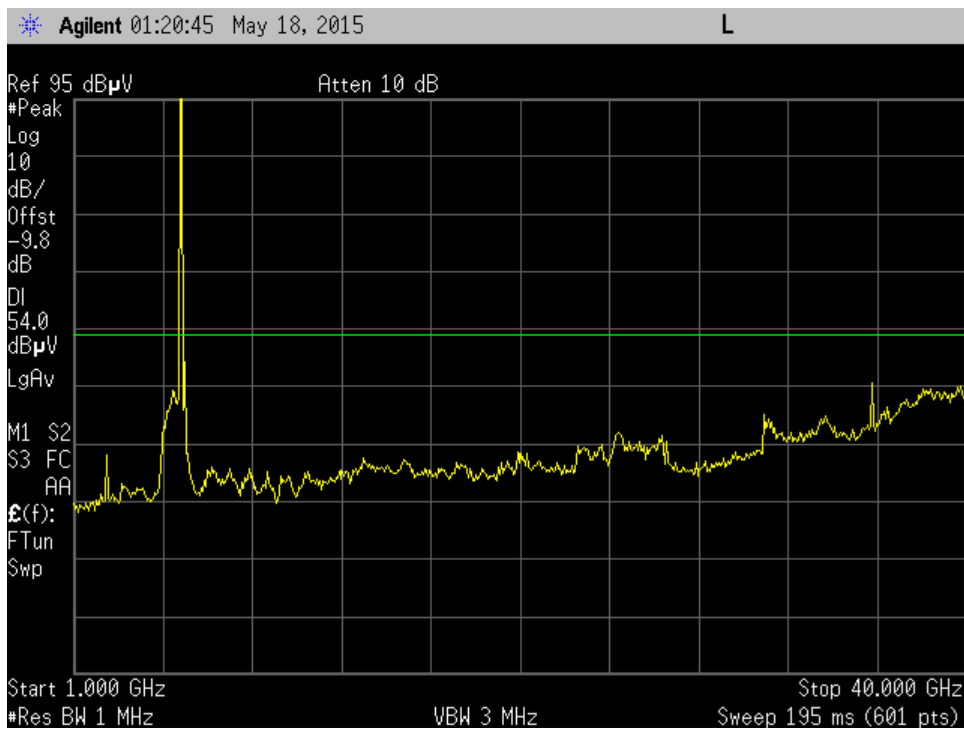
Annex B
Undesireable Emissions Conducted
Port J7 and J8



10 MHz high ch 1 - 40 GHz_J7

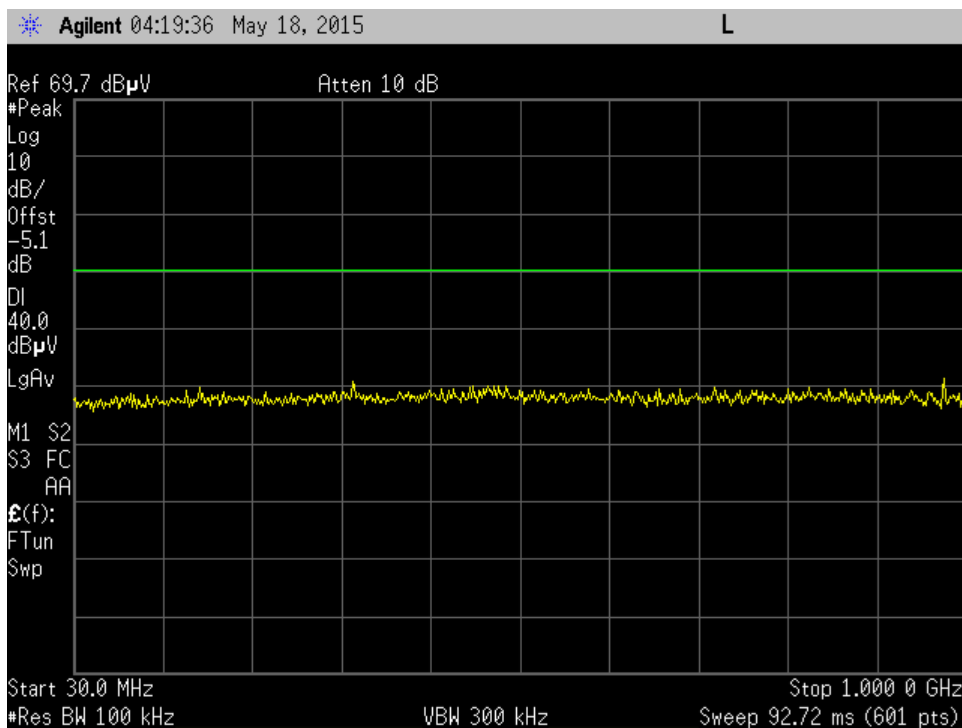


10 MHz high ch 1 - 40 GHz_J8

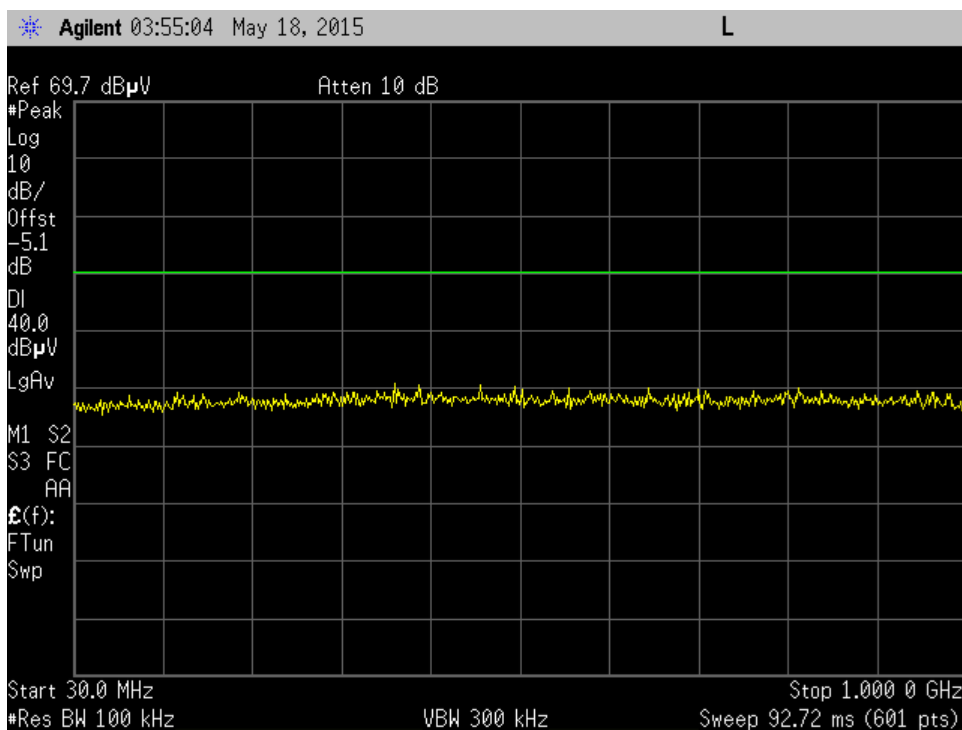




10 MHz high ch 30 - 1000 MHz_J7

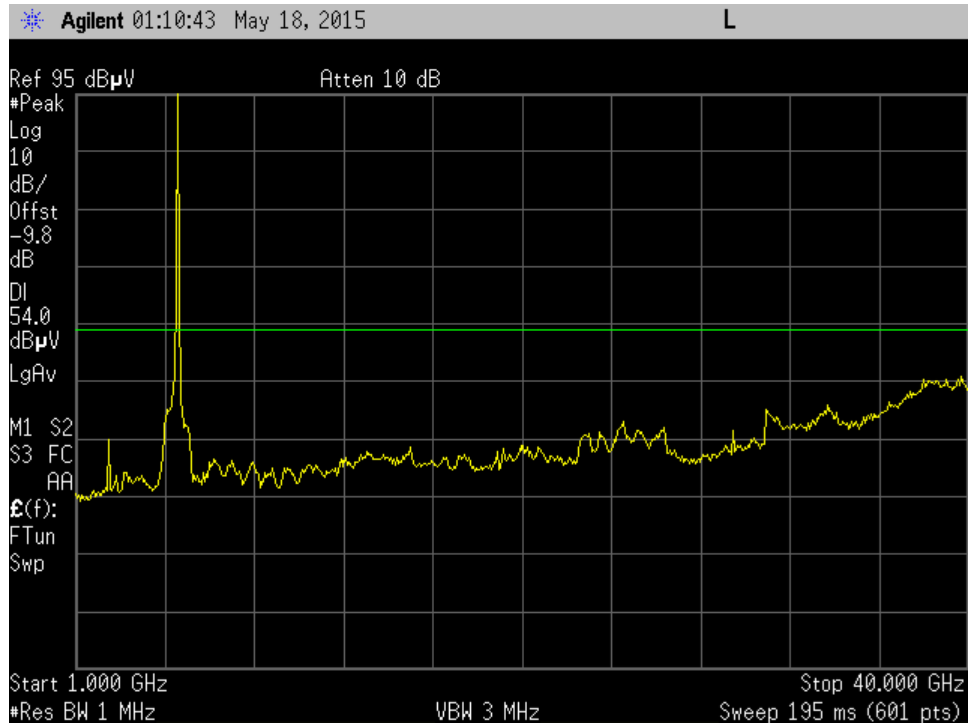


10 MHz high ch 30 - 1000 MHz_J8

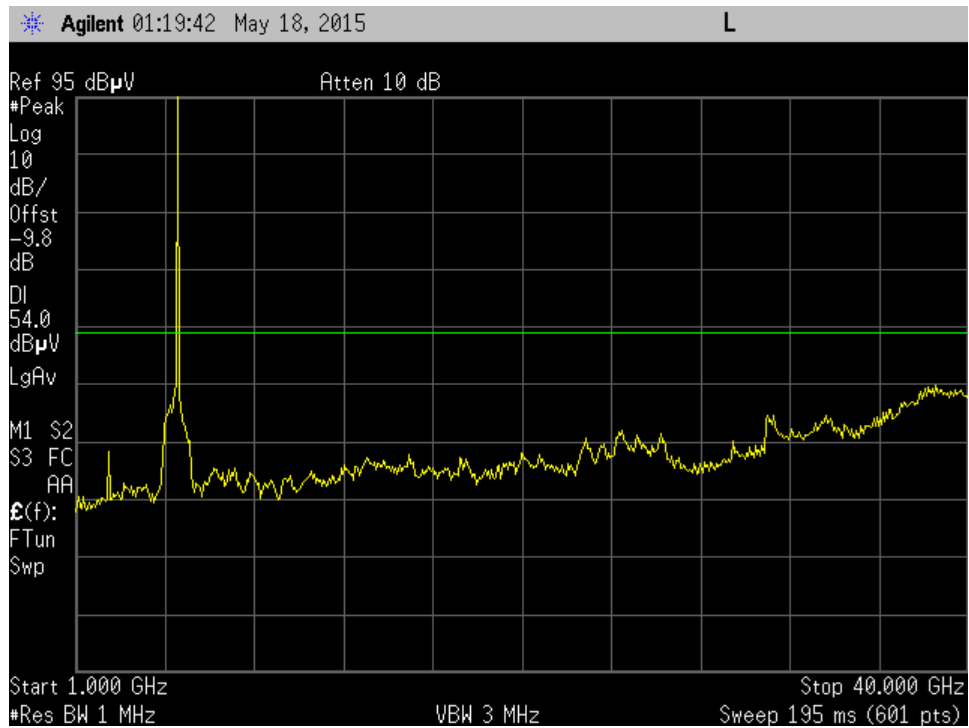




10 MHz low ch 1 - 40 GHz_J7

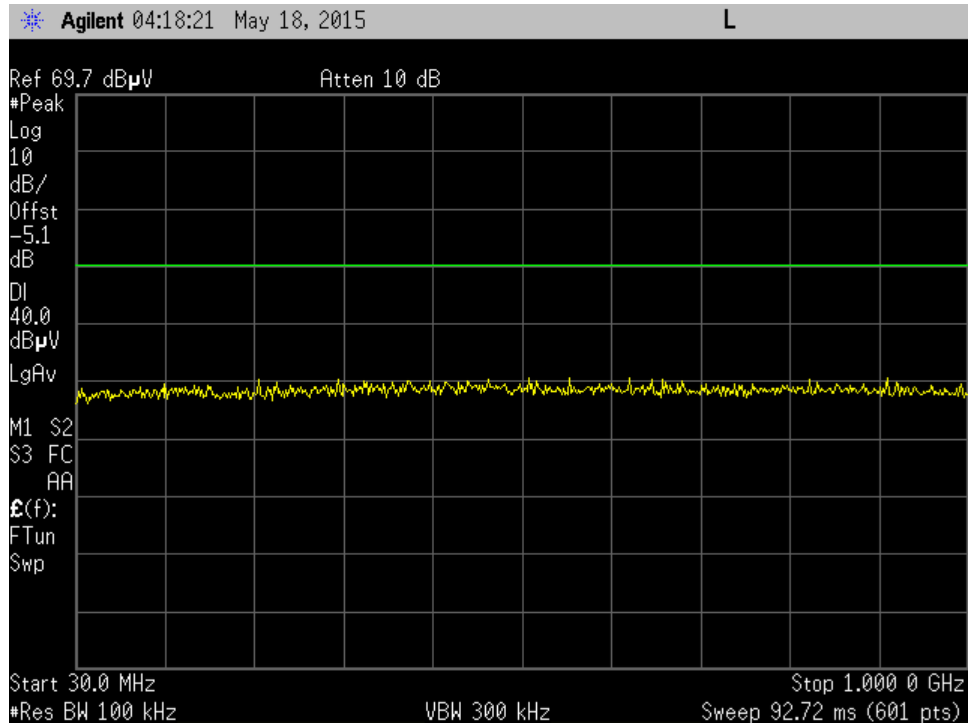


10 MHz low ch 1 - 40 GHz_J8

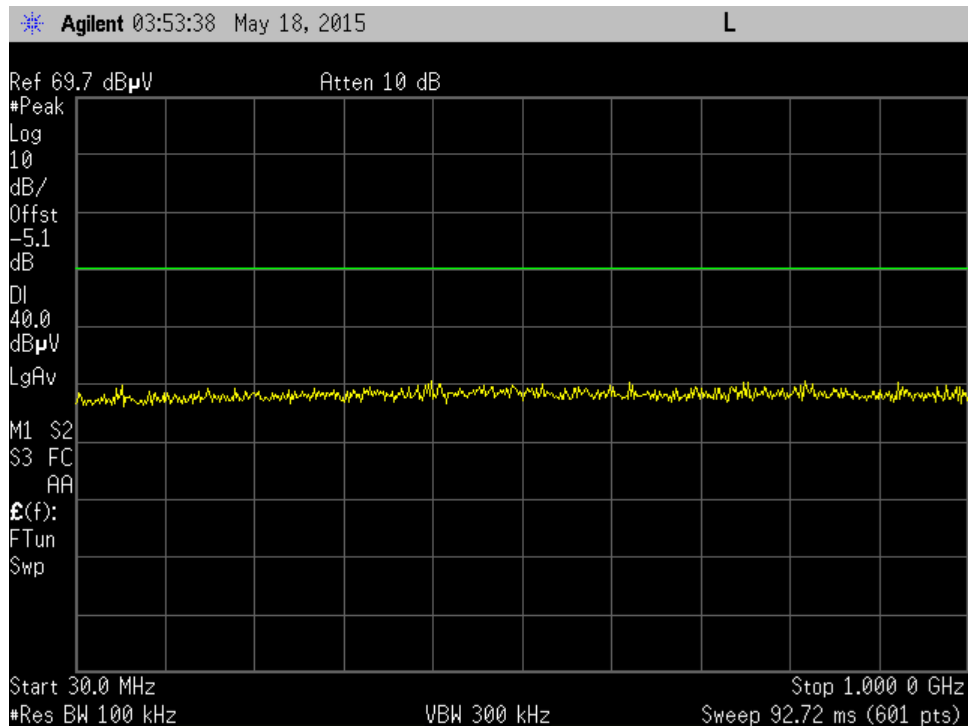




10 MHz low ch 30 - 1000 MHz_J7

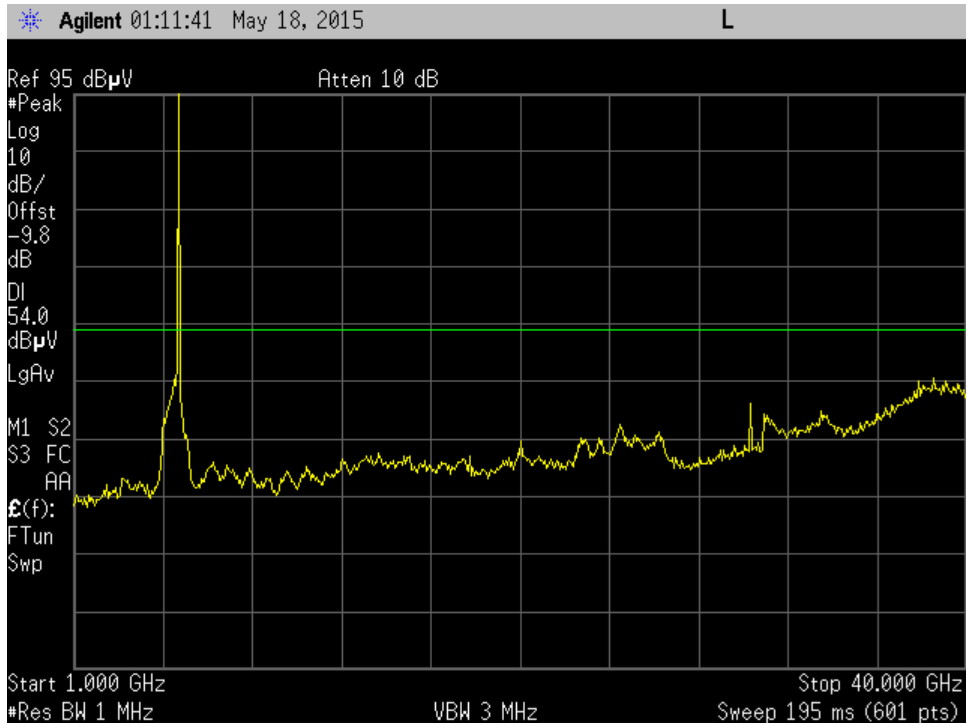


10 MHz low ch 30 - 1000 MHz_J8

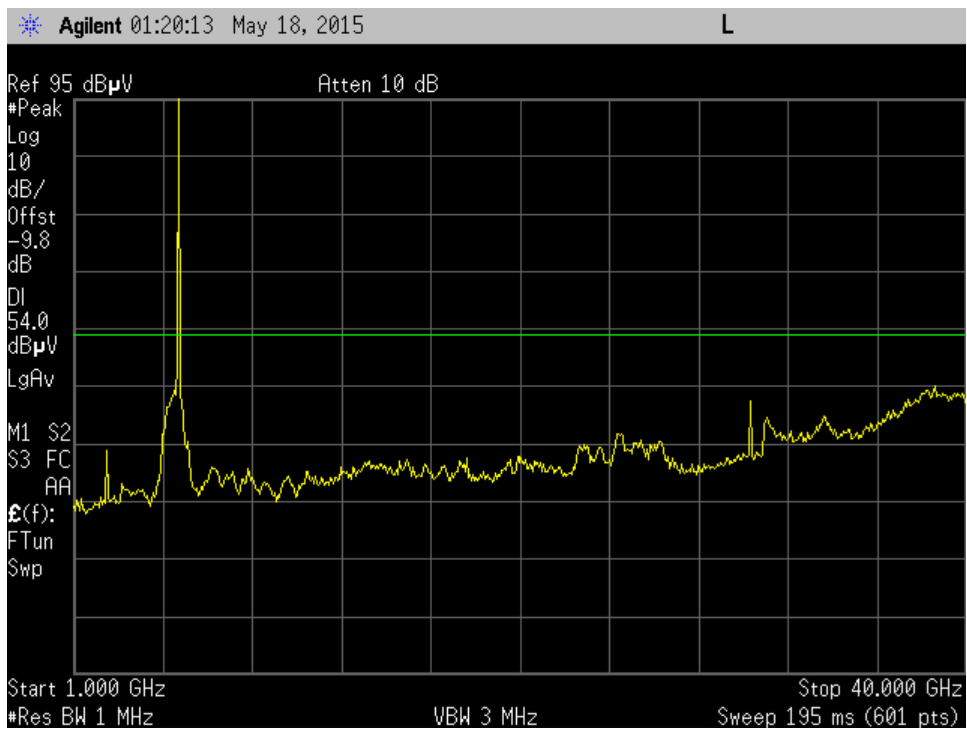




10 MHz mid ch 1 - 40 GHz_J7

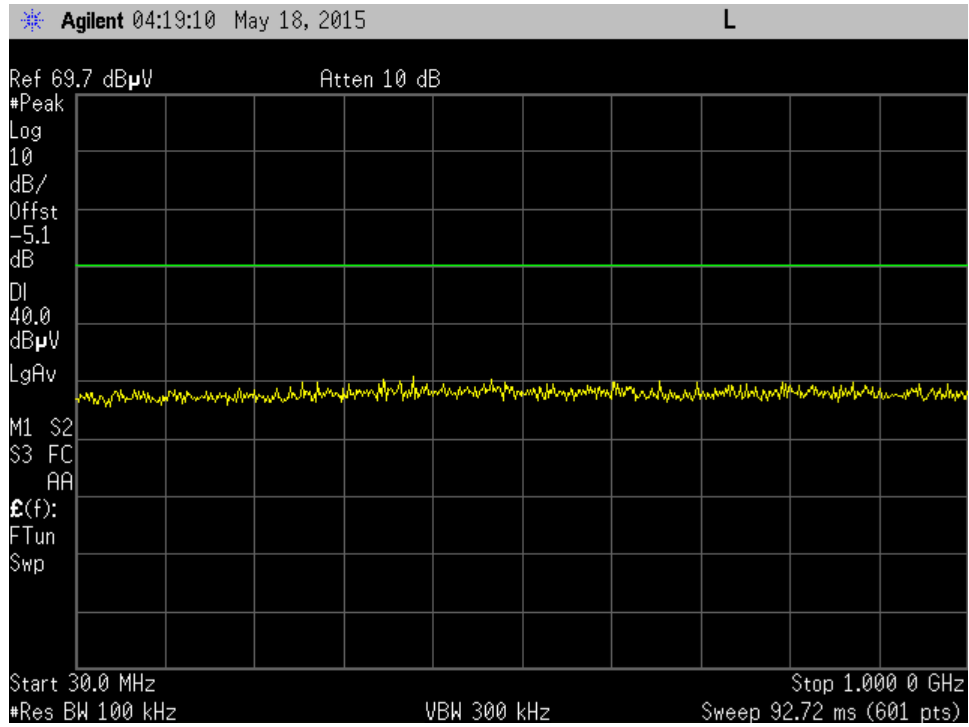


10 MHz mid ch 1 - 40 GHz_J8

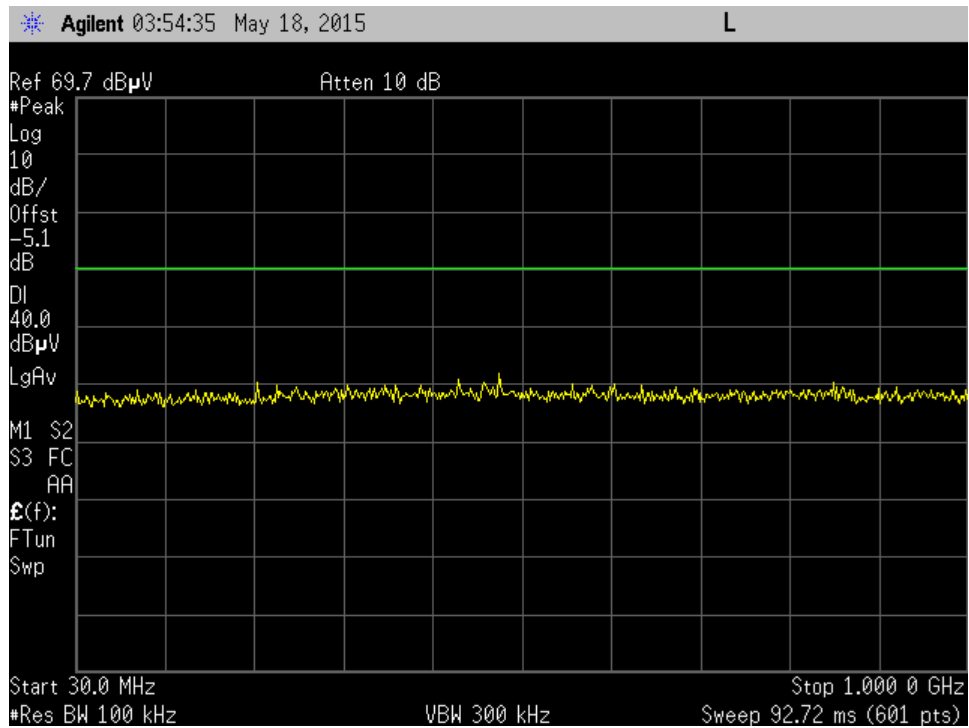




10 MHz mid ch 30 - 1000 MHz_J7

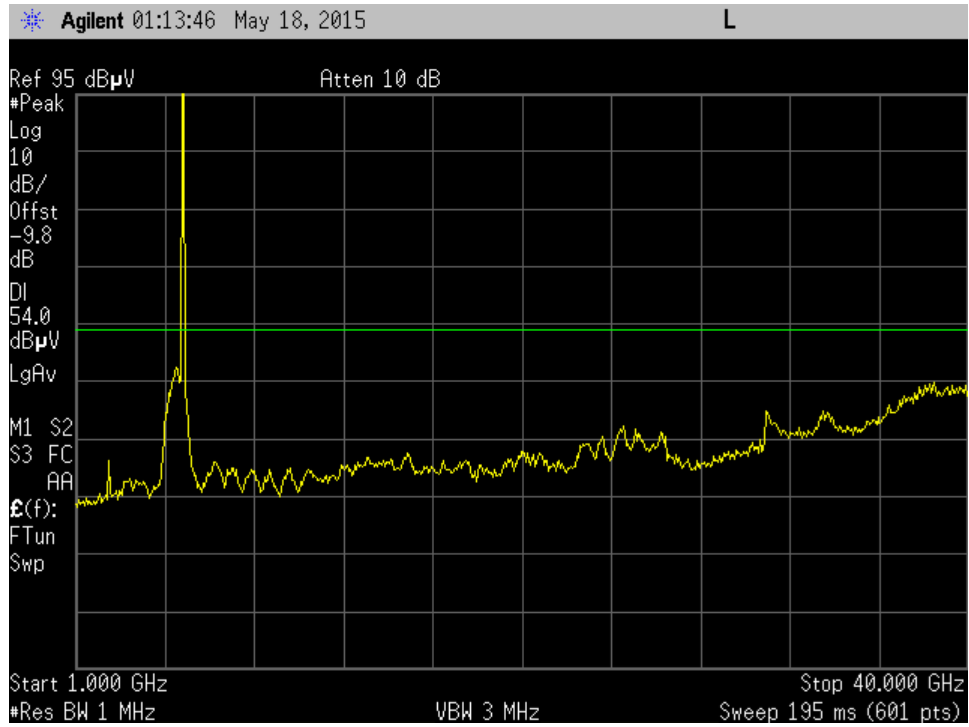


10 MHz mid ch 30 - 1000 MHz_J8

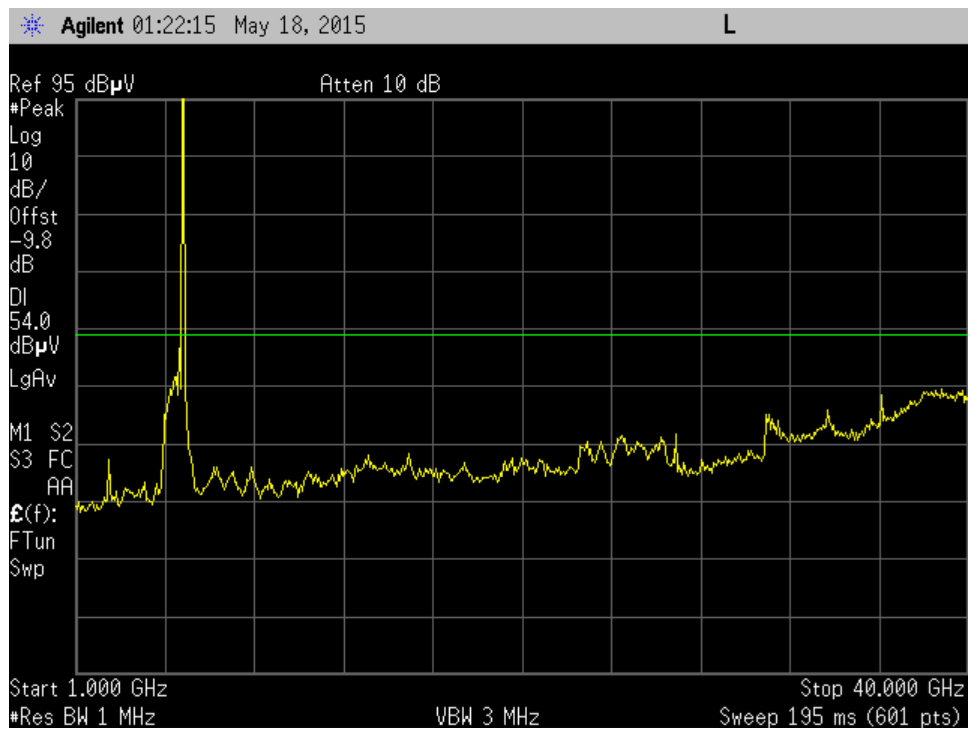




20 MHz high ch 1 - 40 GHz_J7

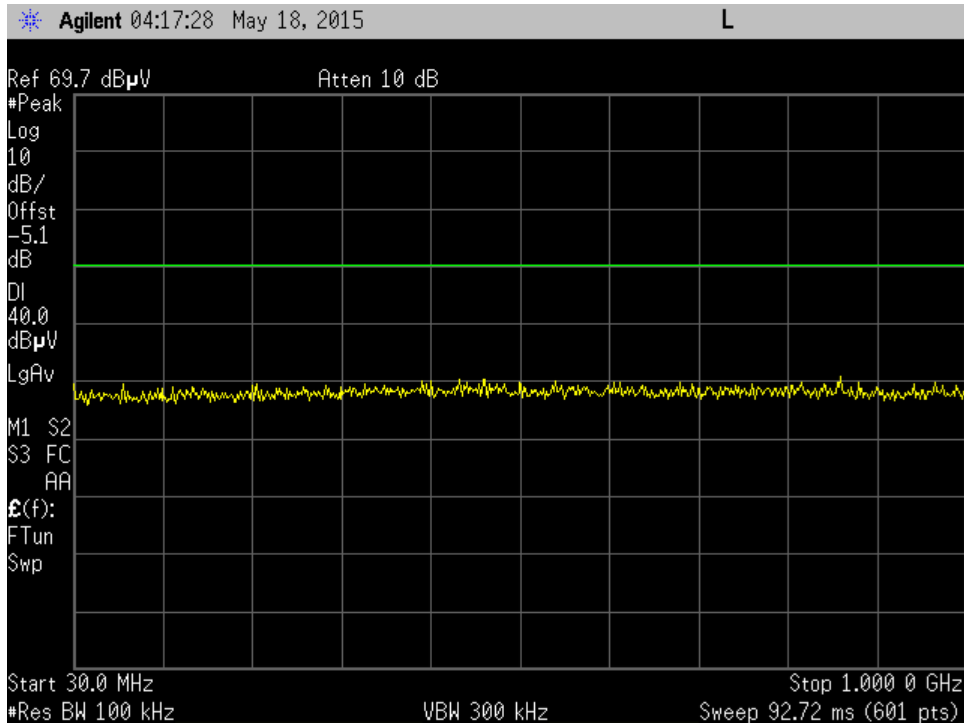


20 MHz high ch 1 - 40 GHz_J8

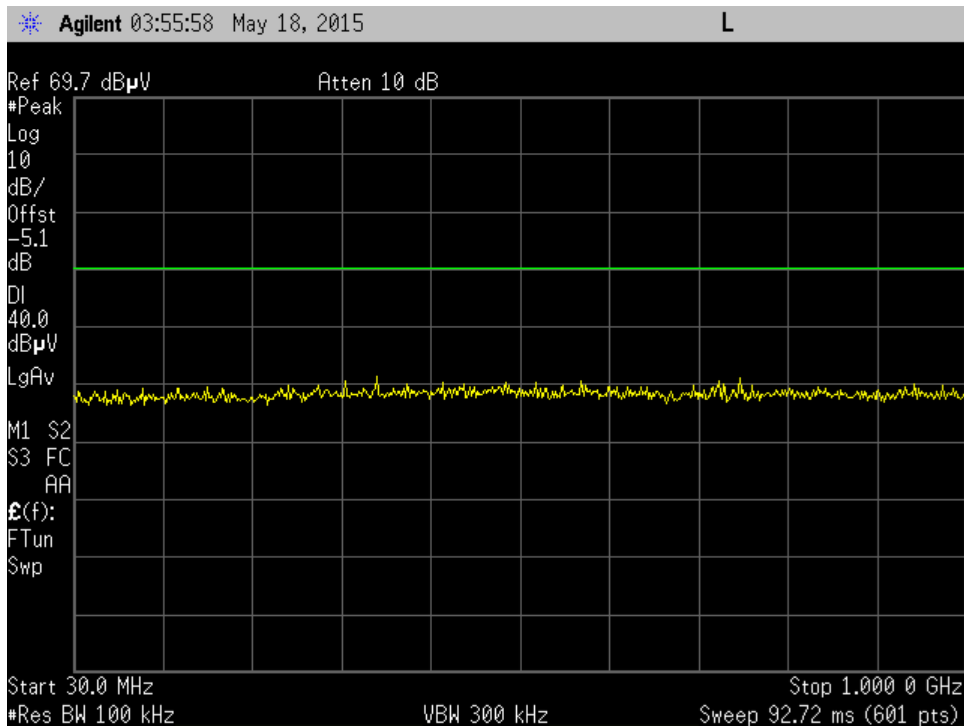




20 MHz high ch 30 - 1000 MHz_J7

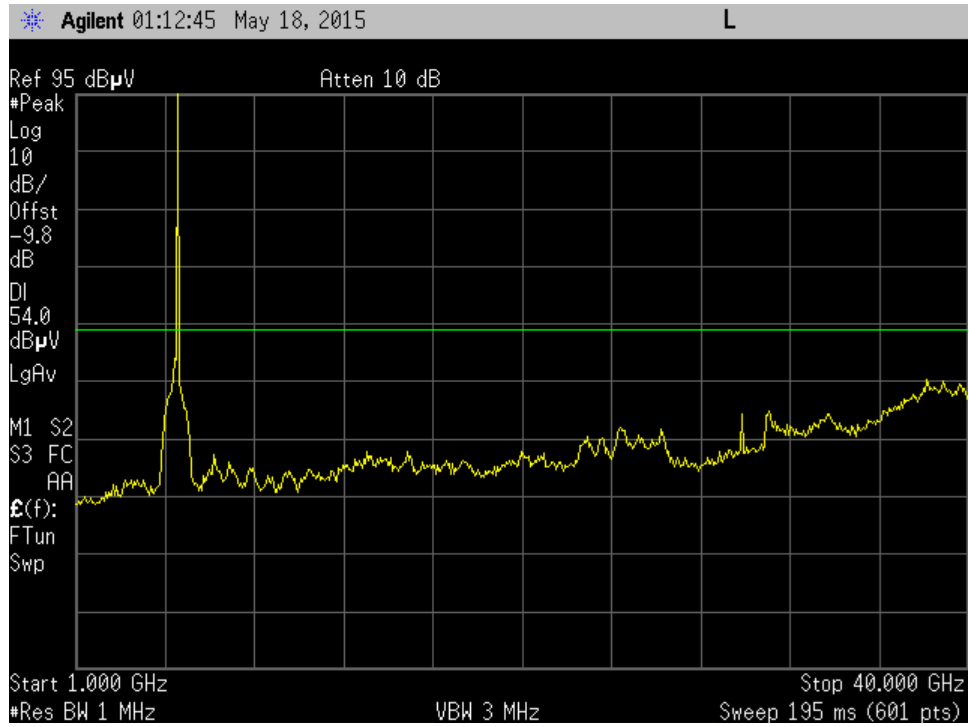


20 MHz high ch 30 - 1000 MHz_J8

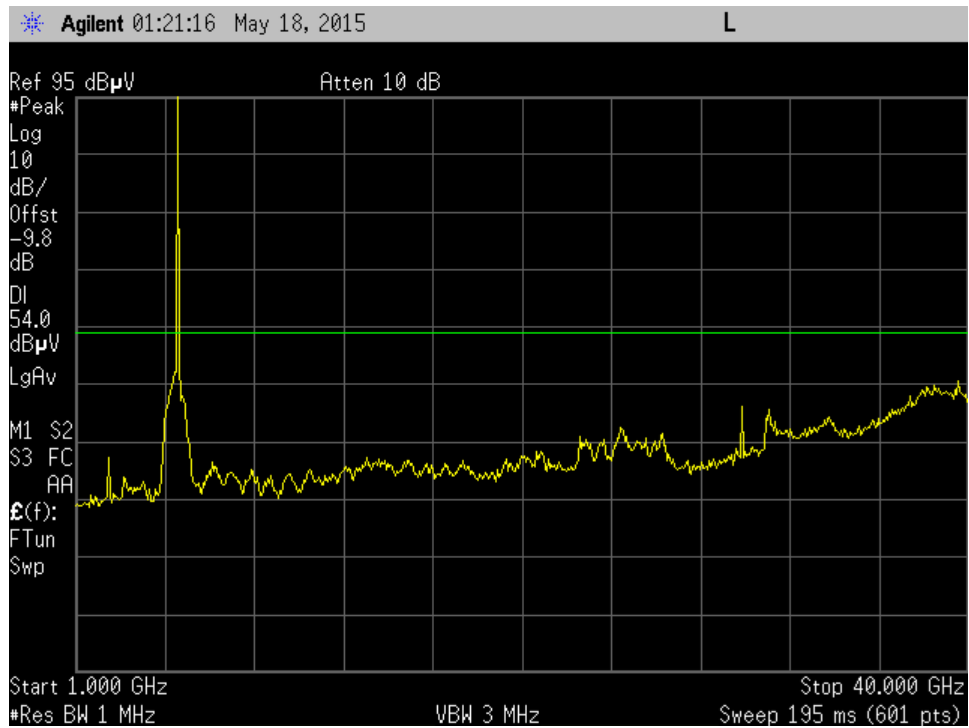




20 MHz low ch 1 - 40 GHz_J7

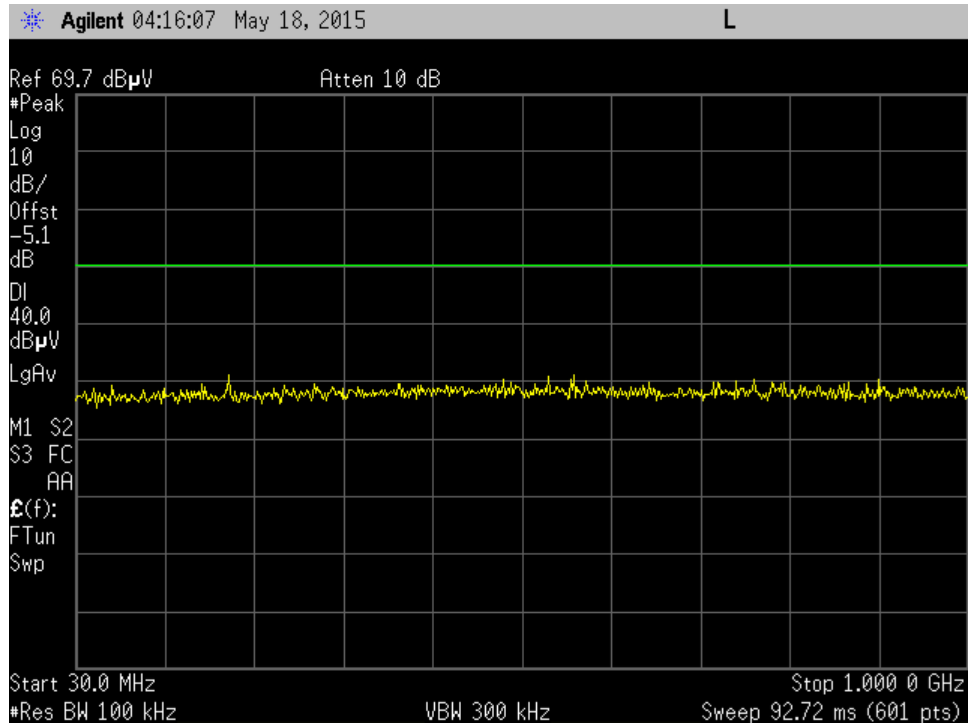


20 MHz low ch 1 - 40 GHz_J8

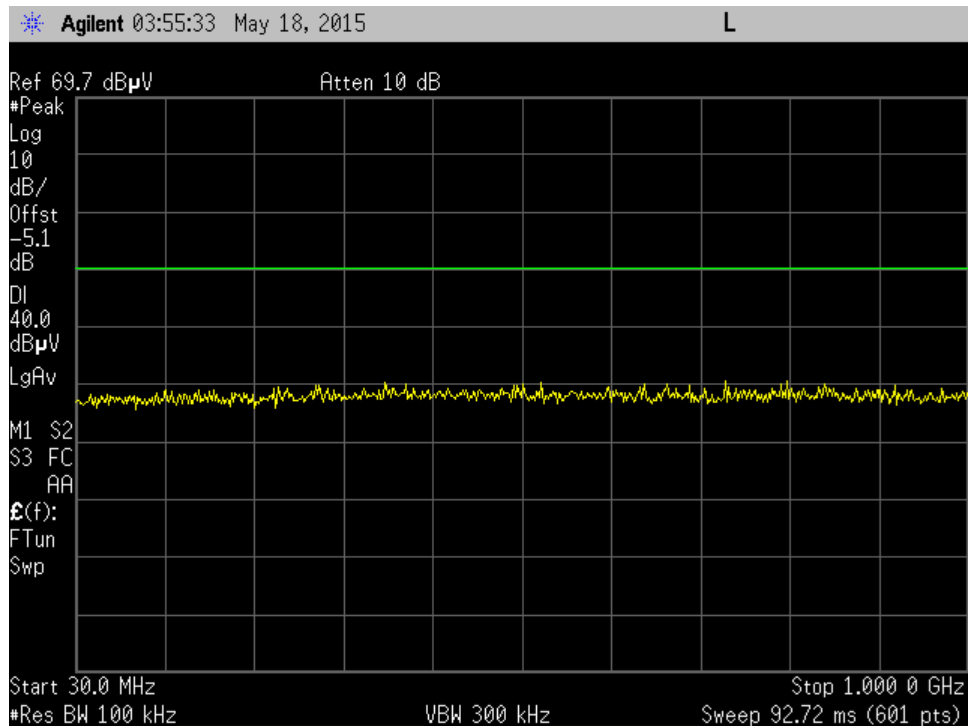




20 MHz low ch 30 - 1000 MHz_J7

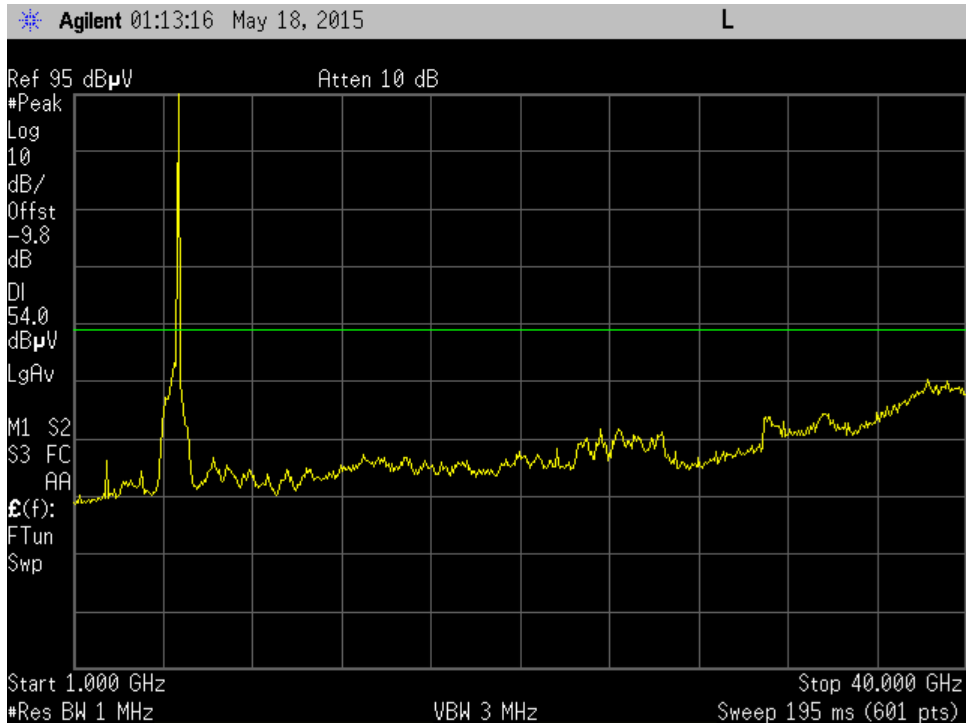


20 MHz low ch 30 - 1000 MHz_J8

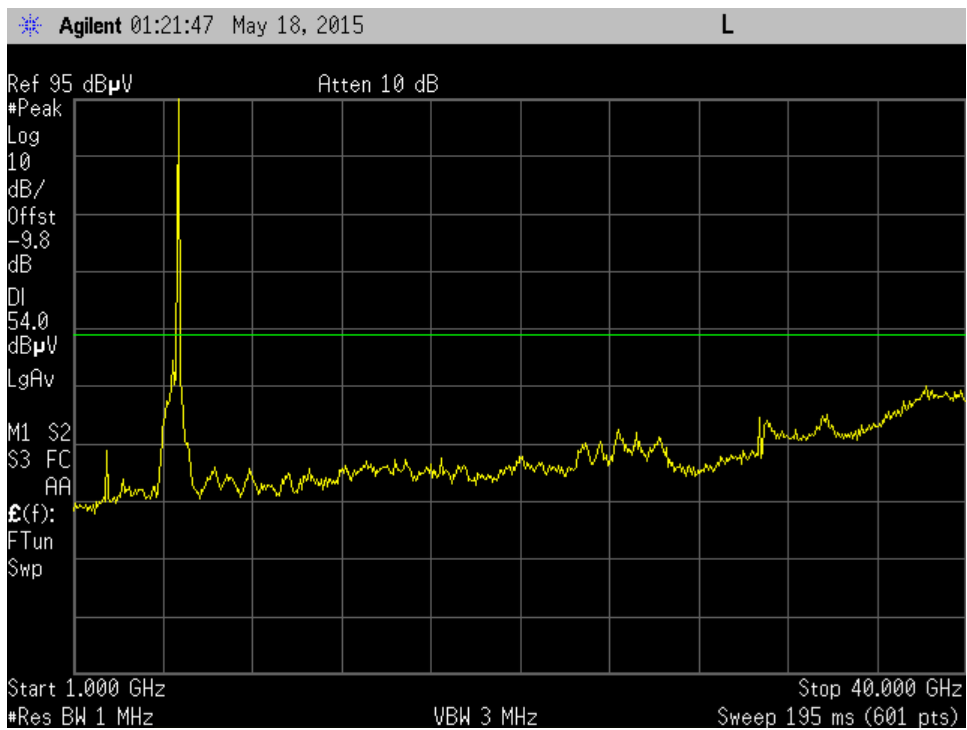




20 MHz mid ch 1 - 40 GHz_J7

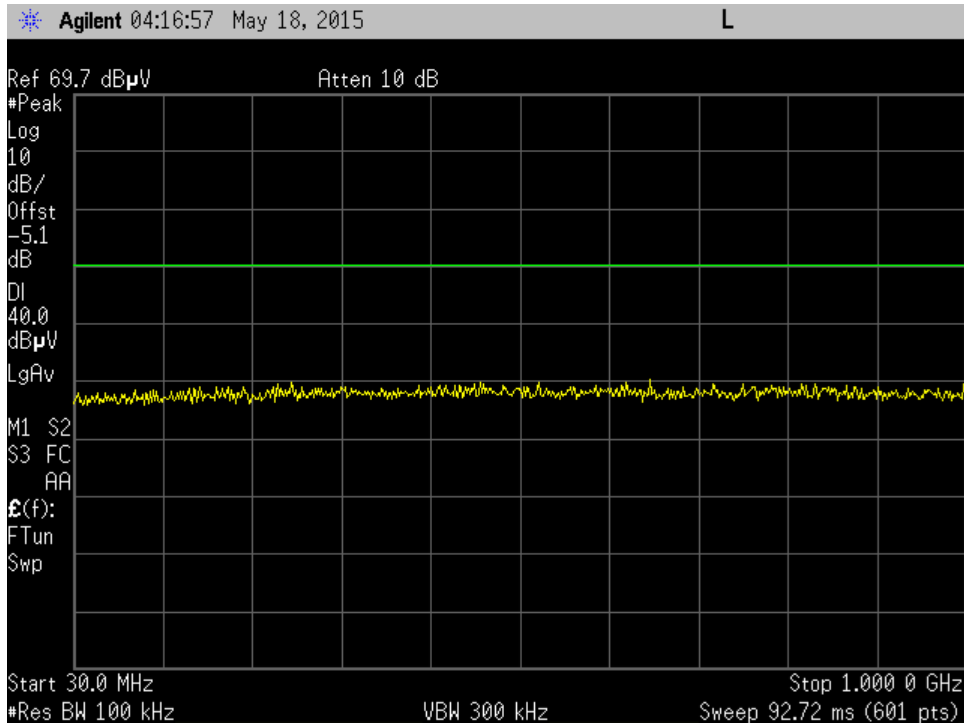


20 MHz mid ch 1 - 40 GHz_J8





20 MHz mid ch 30 - 1000 MHz_J7

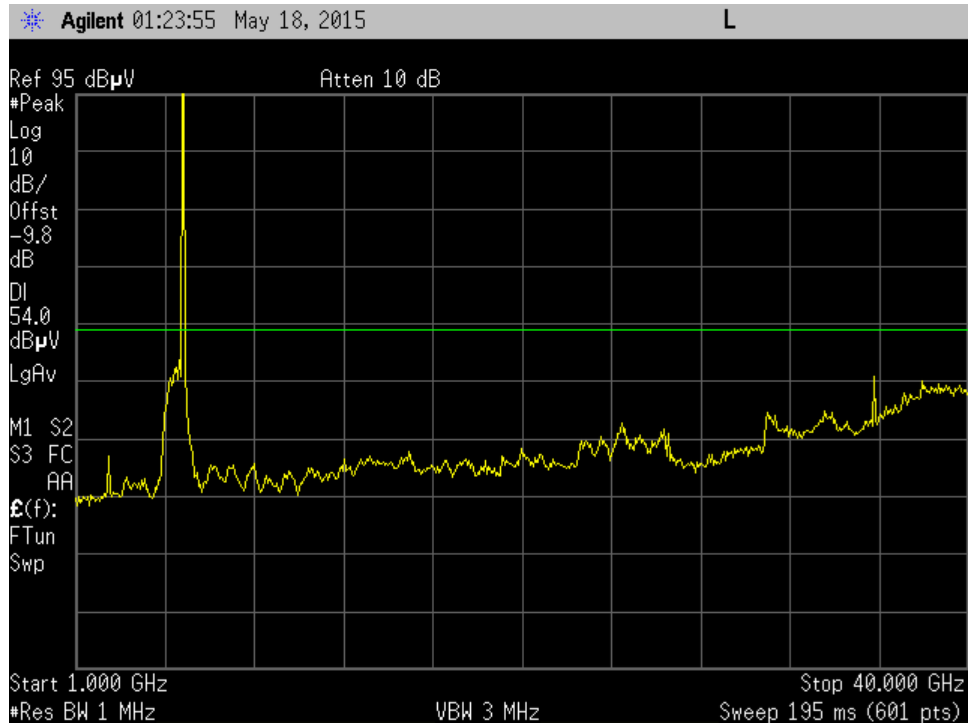


30 MHz high ch 1 - 40 GHz_J7

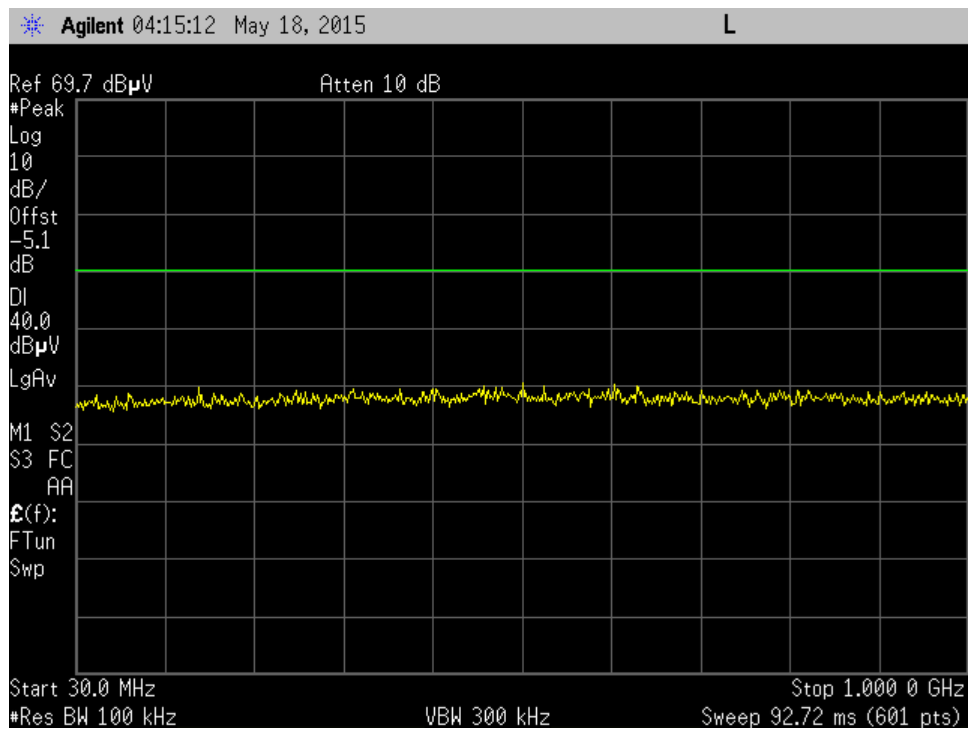




30 MHz high ch 1 - 40 GHz_J8

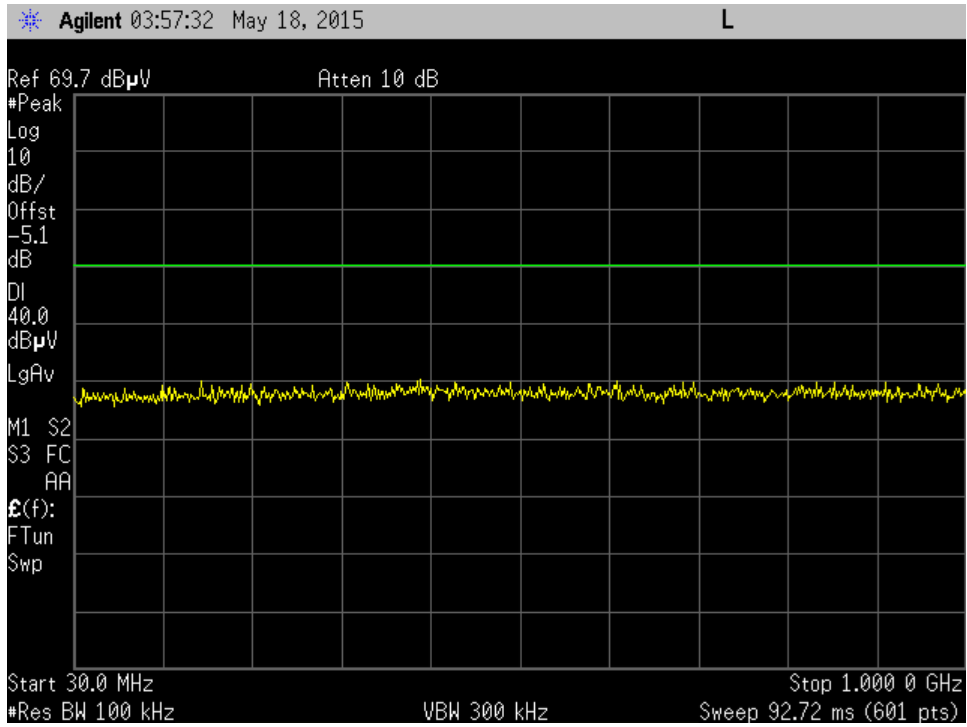


30 MHz high ch 30 - 1000 MHz_J7

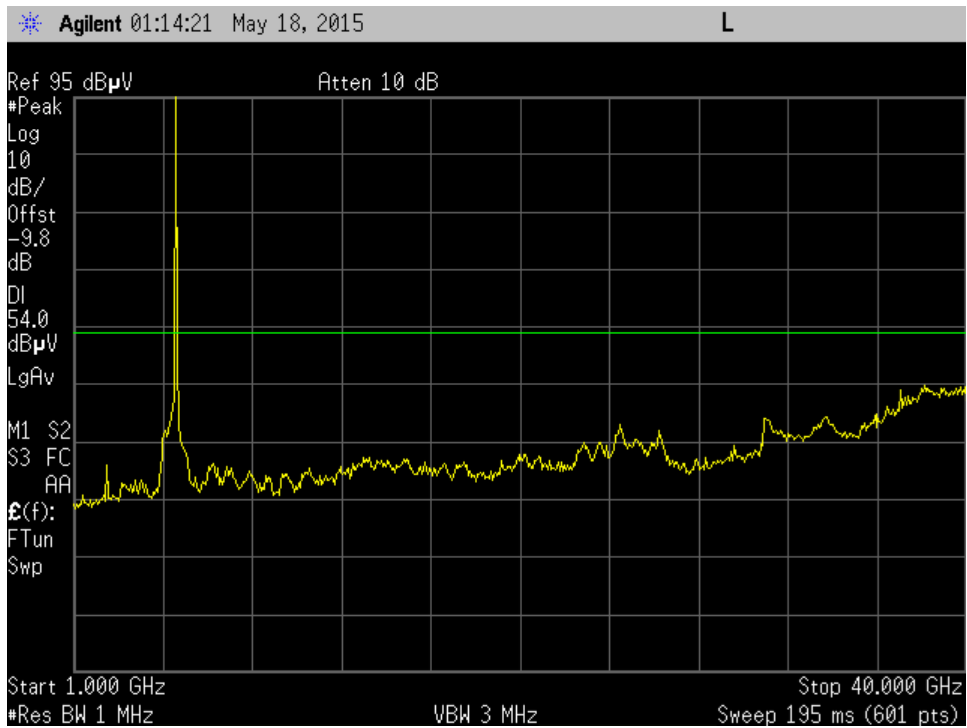




30 MHz high ch 30 - 1000 MHz_J8

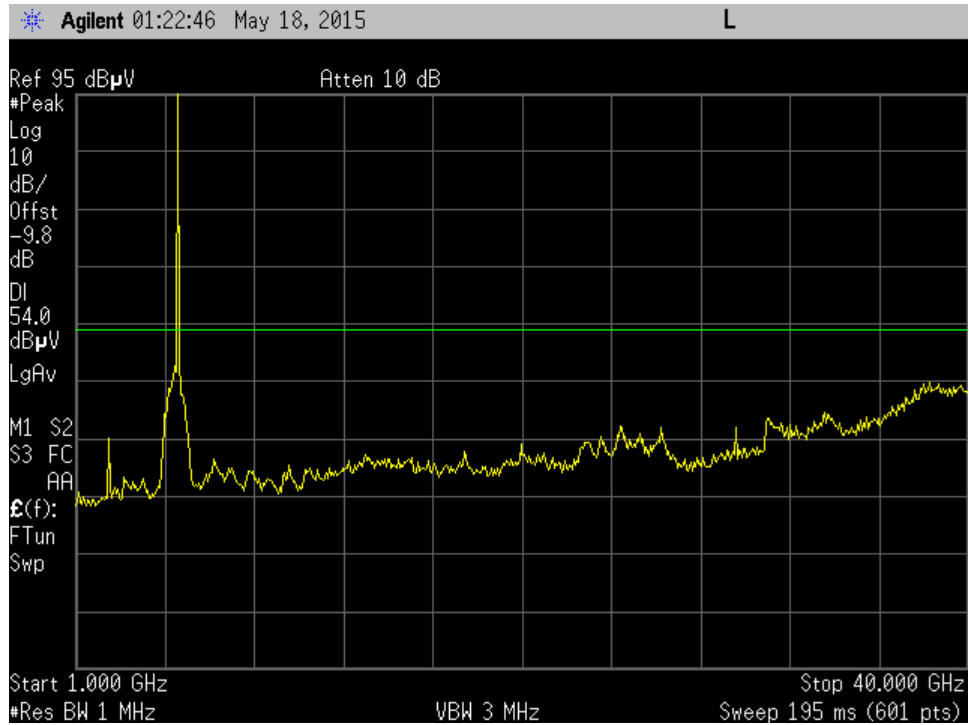


30 MHz low ch 1 - 40 GHz_J7

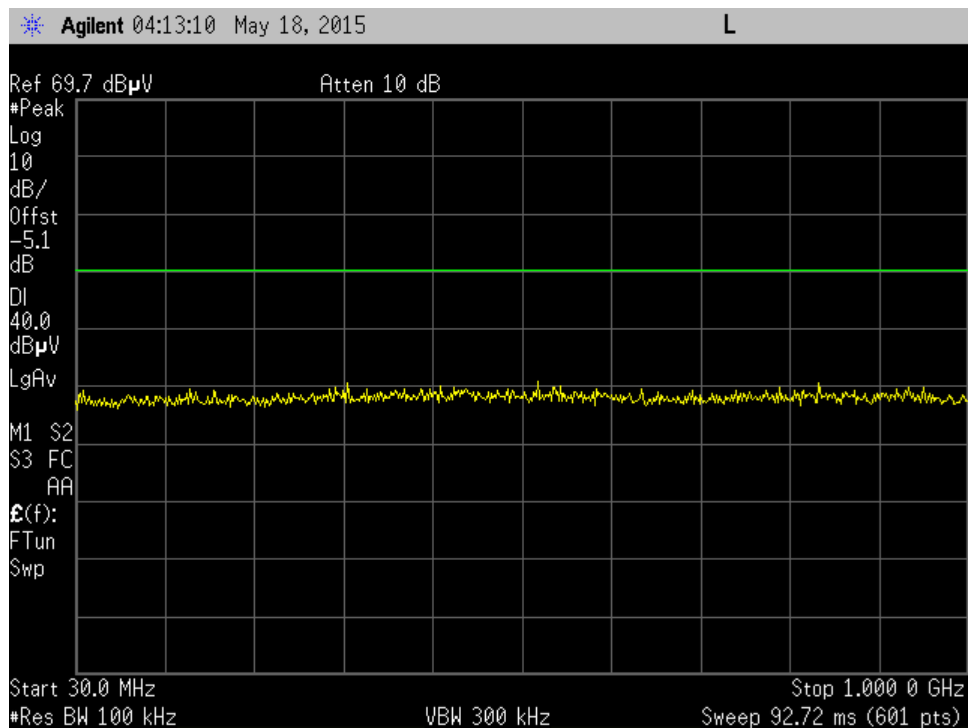




30 MHz low ch 1 - 40 GHz_J8

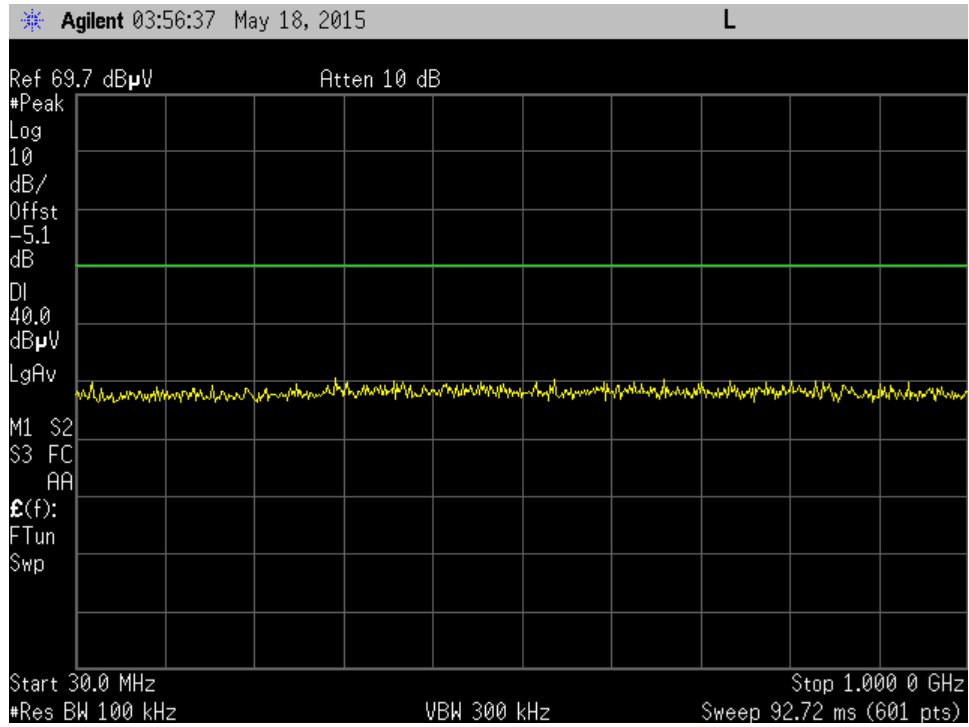


30 MHz low ch 30 - 1000 MHz_J7

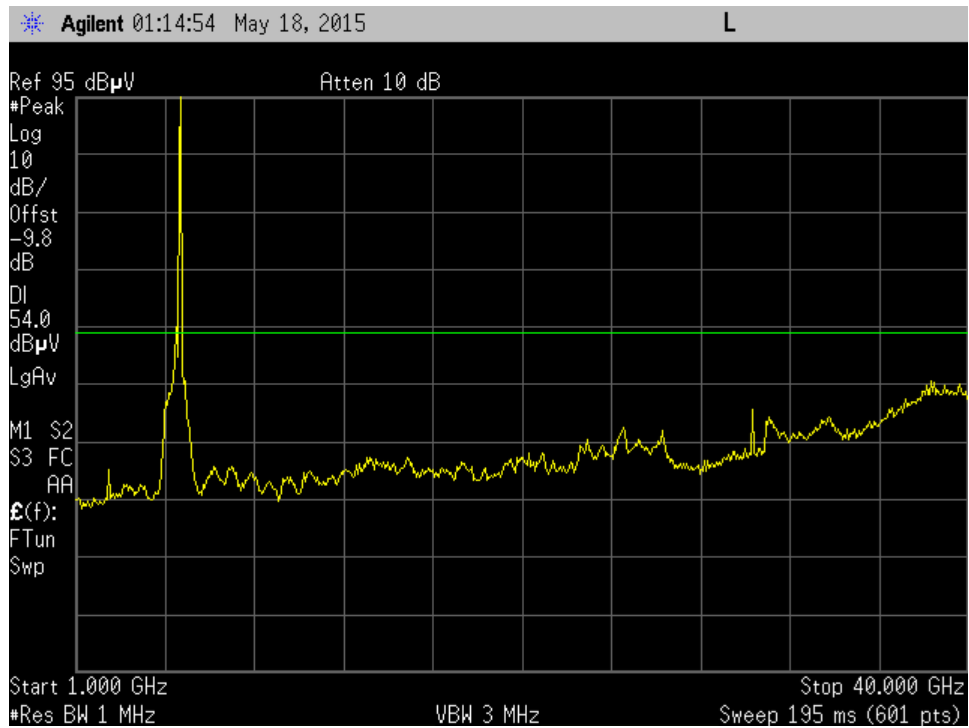




30 MHz low ch 30 - 1000 MHz_J8

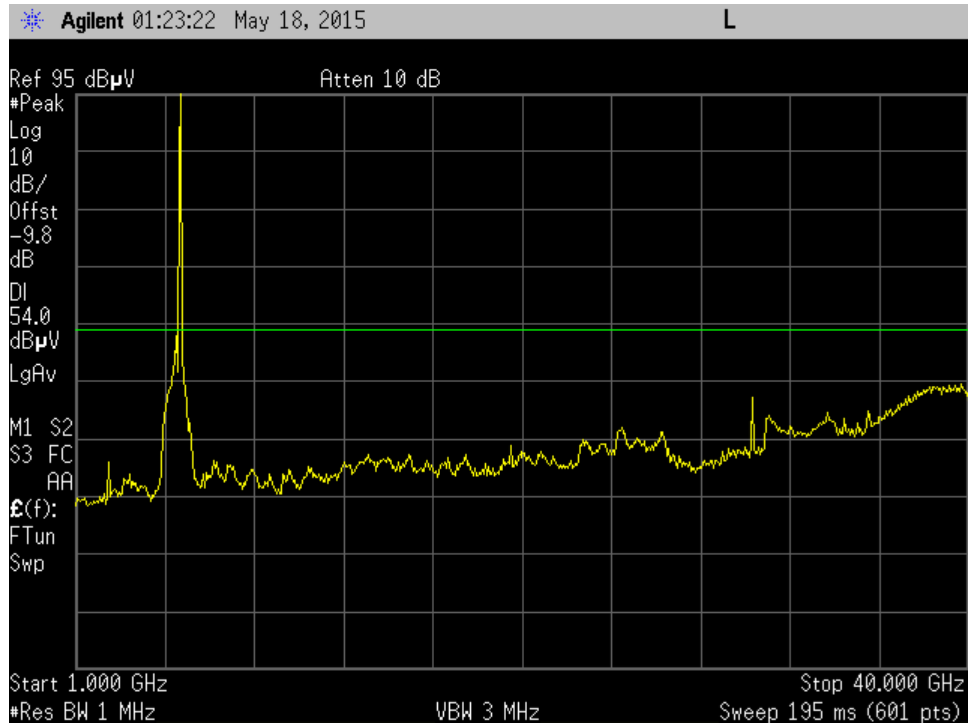


30 MHz mid ch 1 - 40 GHz_J7

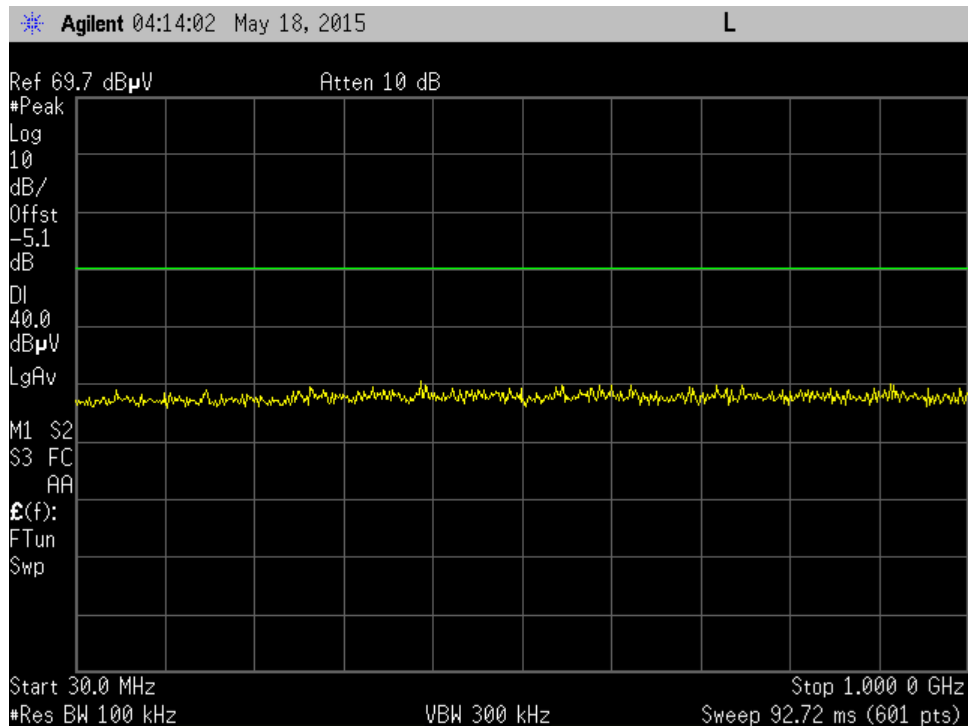




30 MHz mid ch 1 - 40 GHz_J8

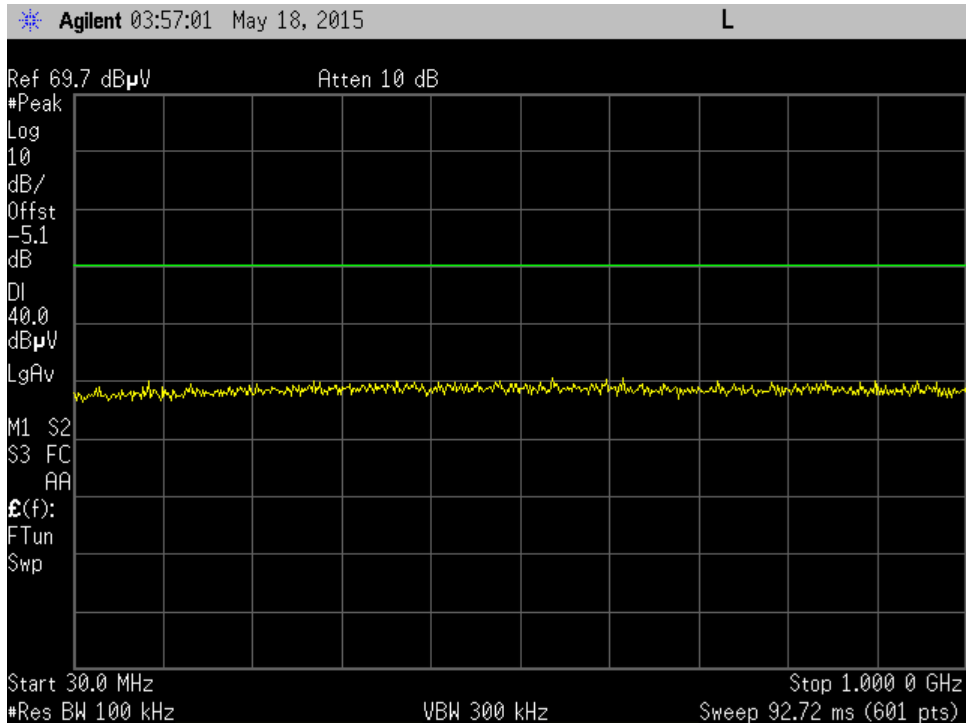


30 MHz mid ch 30 - 1000 MHz_J7

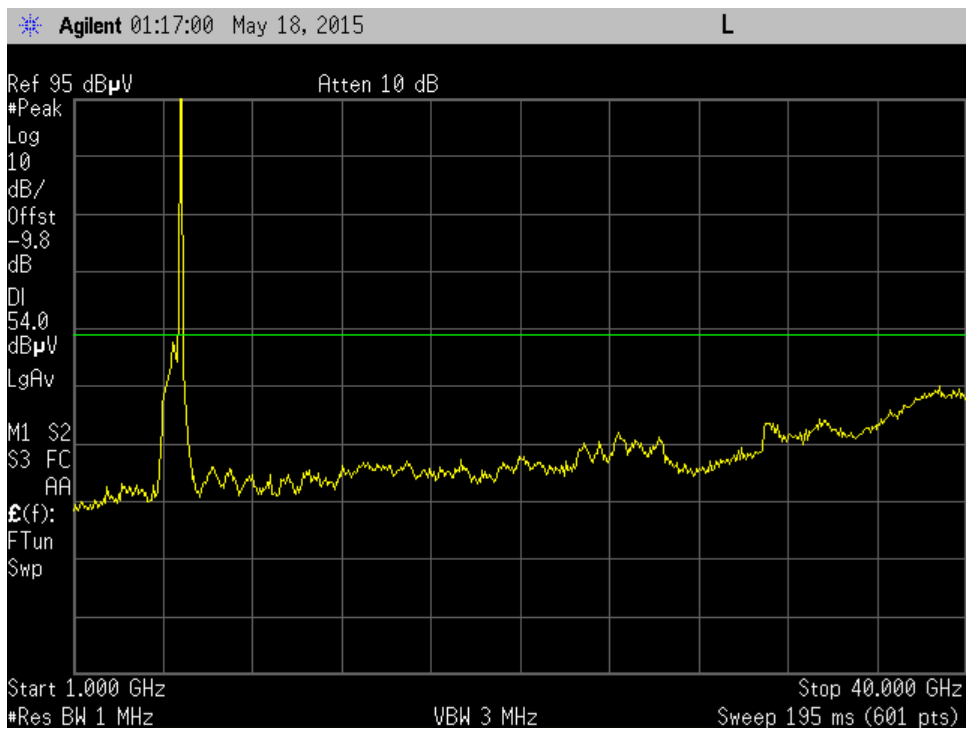




30 MHz mid ch 30 - 1000 MHz_J8

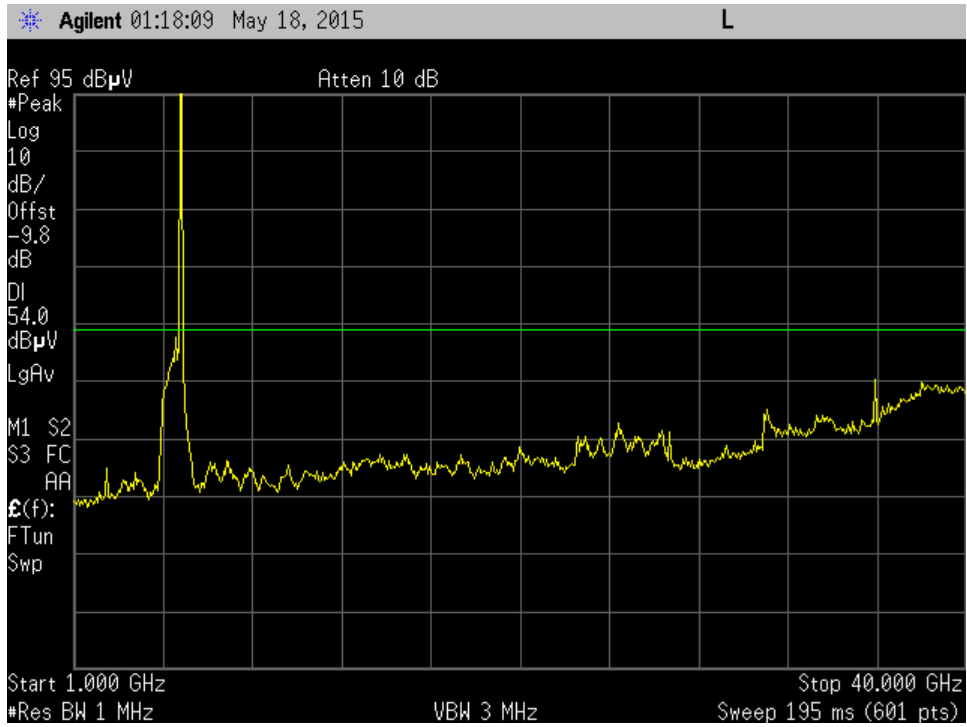


40 MHz high ch 1 - 40 GHz_J7

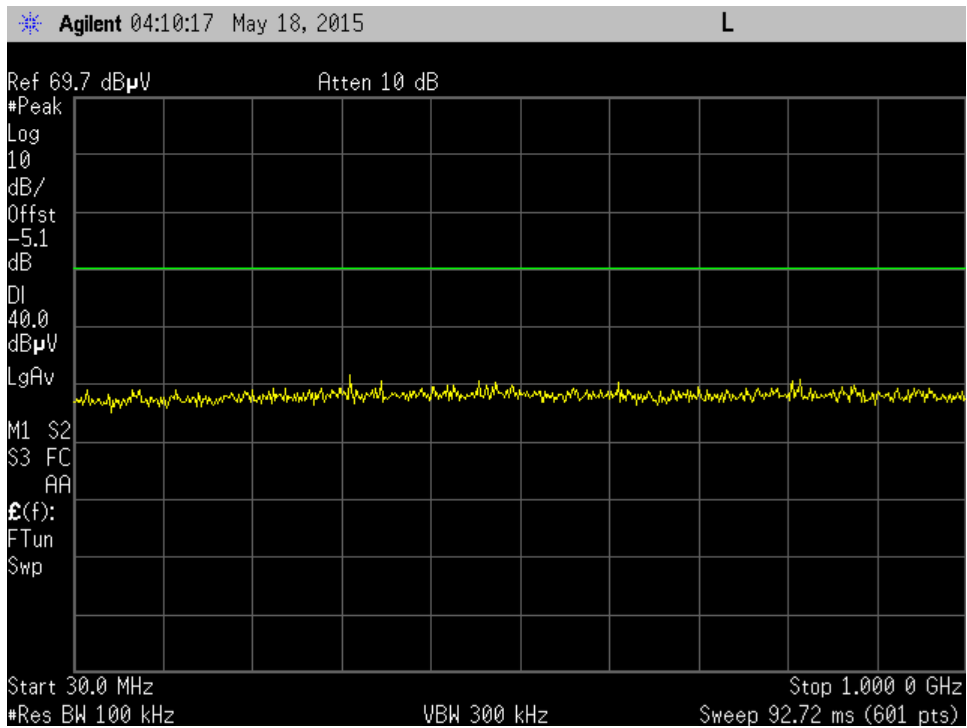




40 MHz high ch 1 - 40 GHz_J8

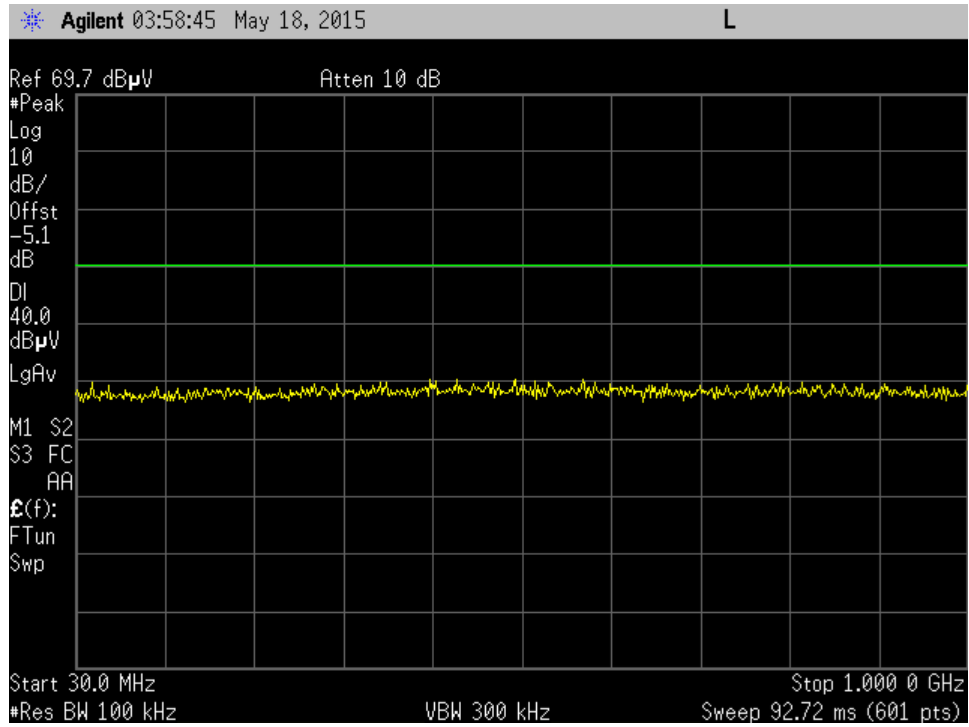


40 MHz high ch 30 - 1000 MHz_J7

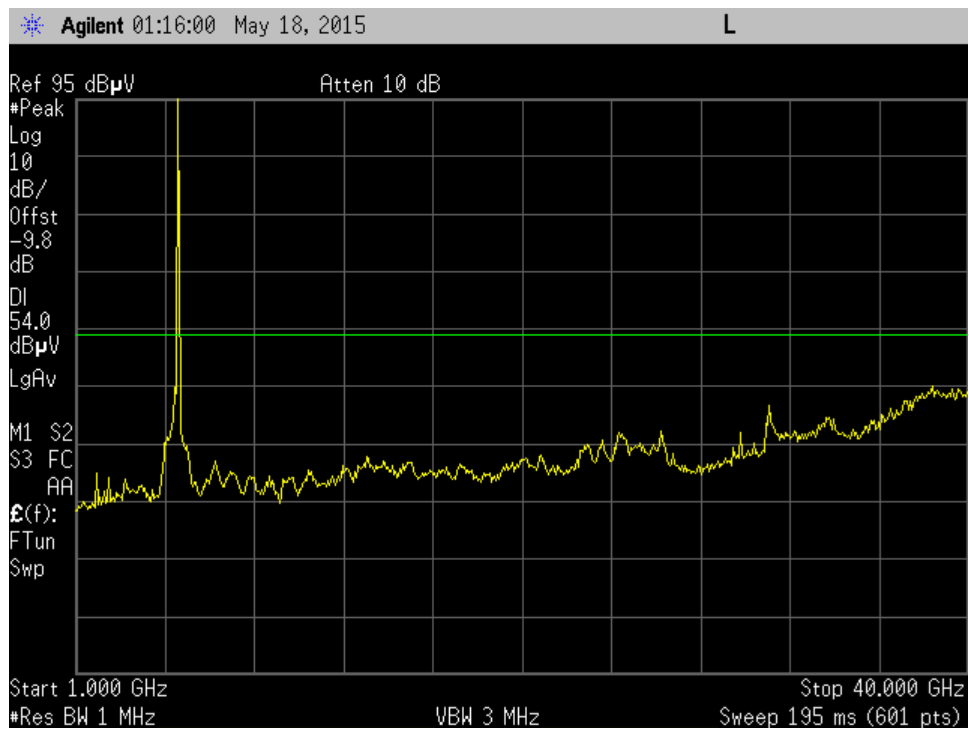




40 MHz high ch 30 - 1000 MHz_J8

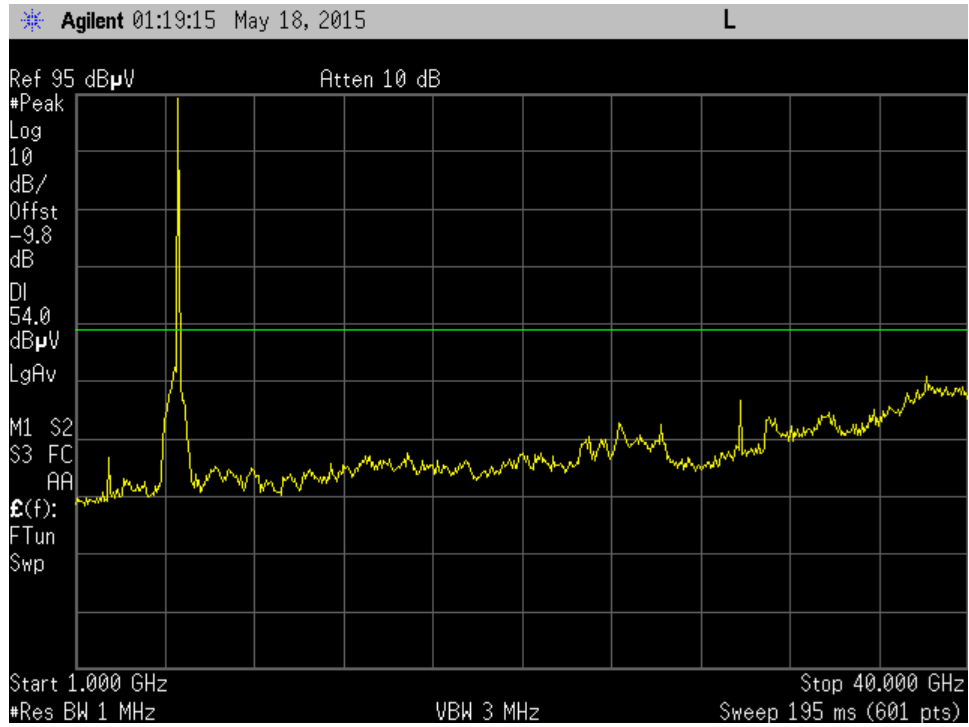


40 MHz low ch 1 - 40 GHz_J7

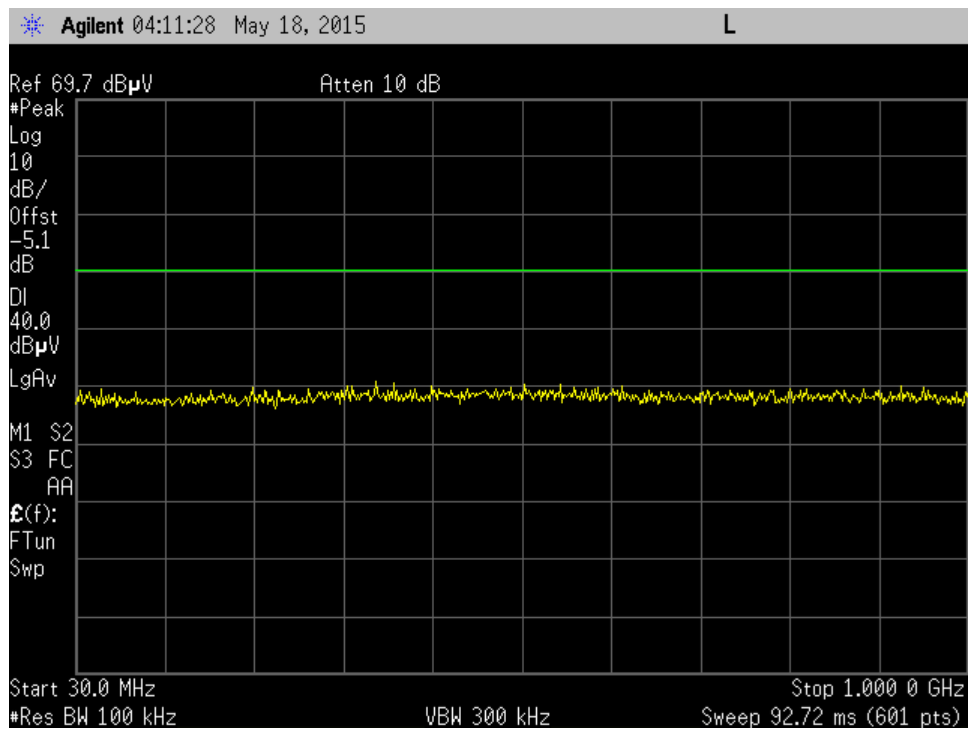




40 MHz low ch 1 - 40 GHz_J8

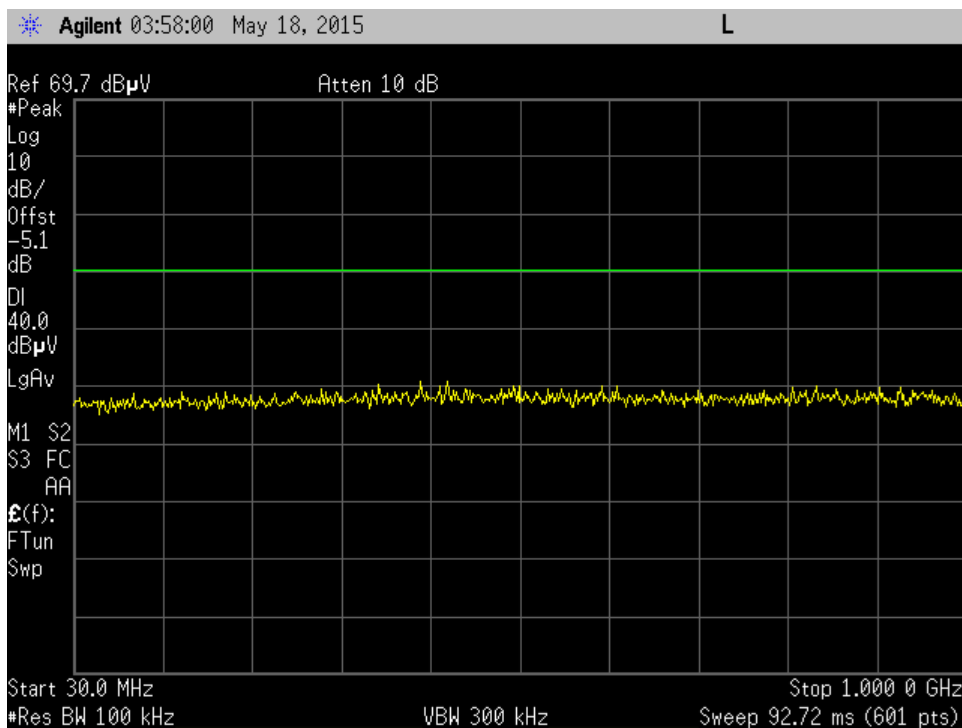


40 MHz low ch 30 - 1000 MHz_J7

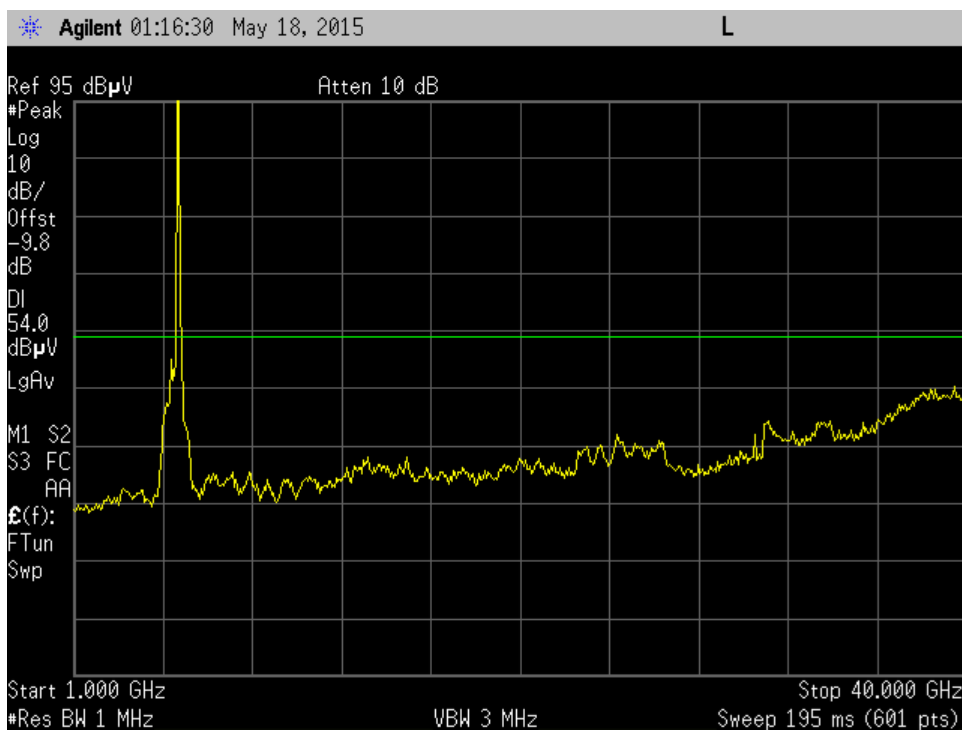




40 MHz low ch 30 - 1000 MHz_J8

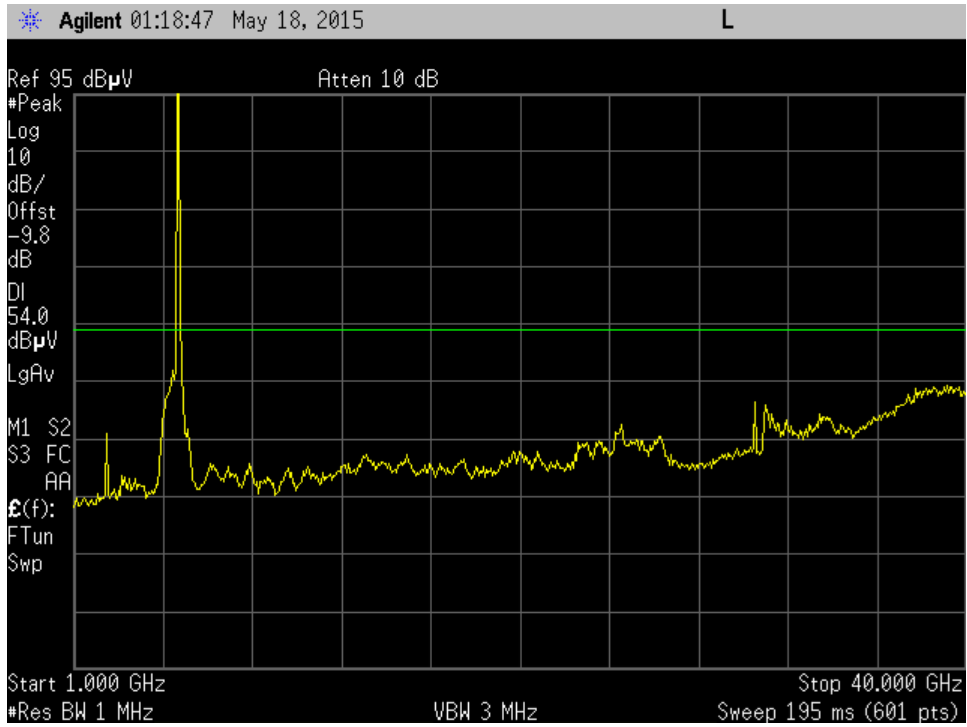


40 MHz mid ch 1 - 40 GHz_J7

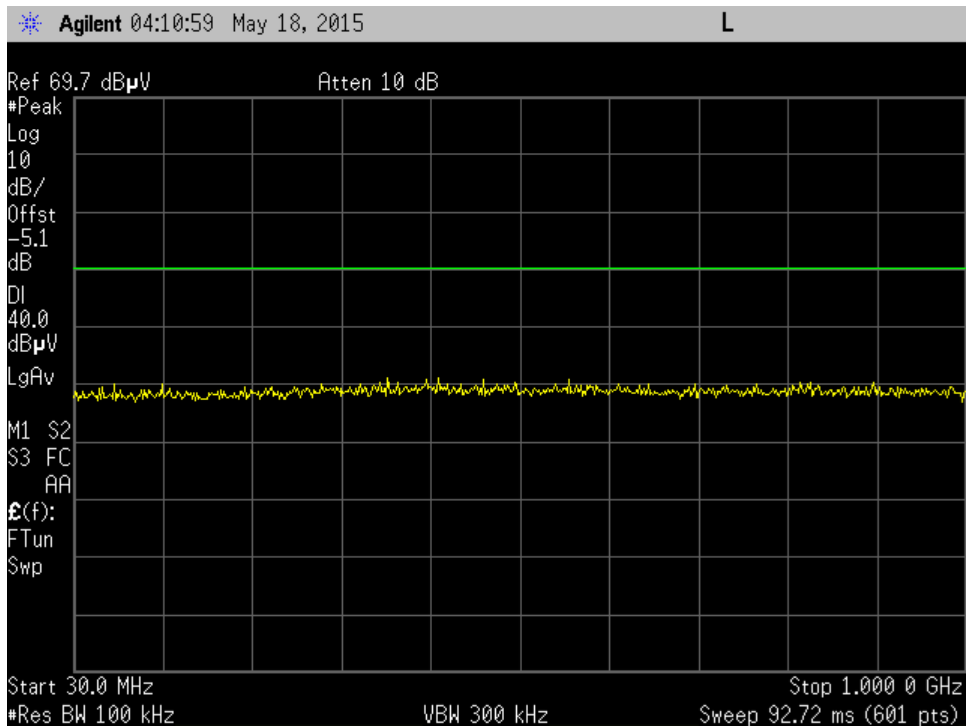




40 MHz mid ch 1 - 40 GHz_J8

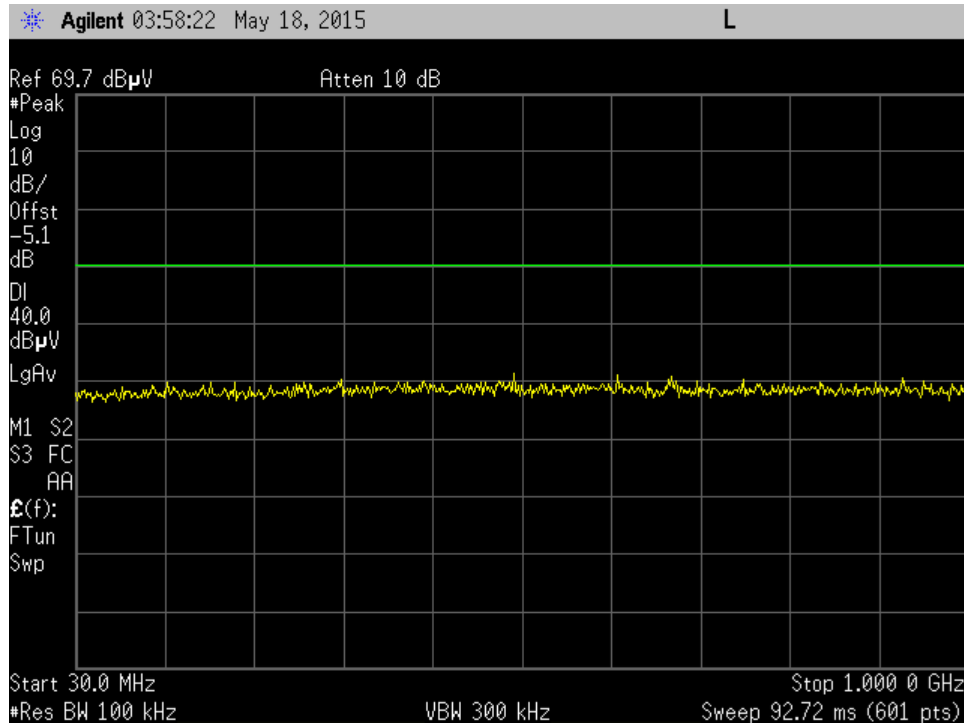


40 MHz mid ch 30 - 1000 MHz_J7





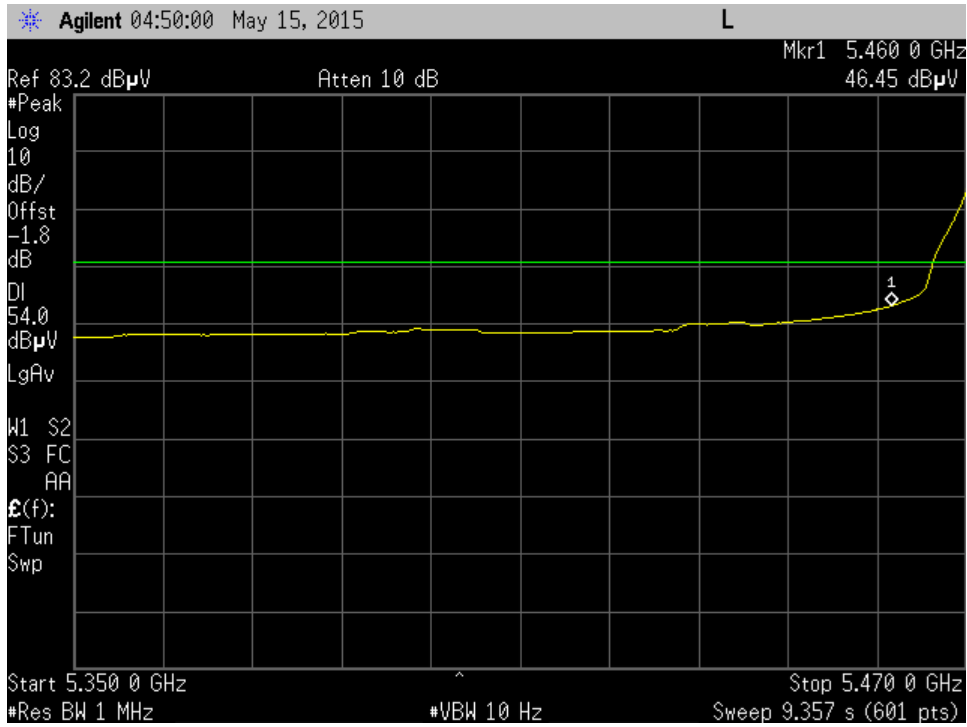
40 MHz mid ch 30 - 1000 MHz_J8



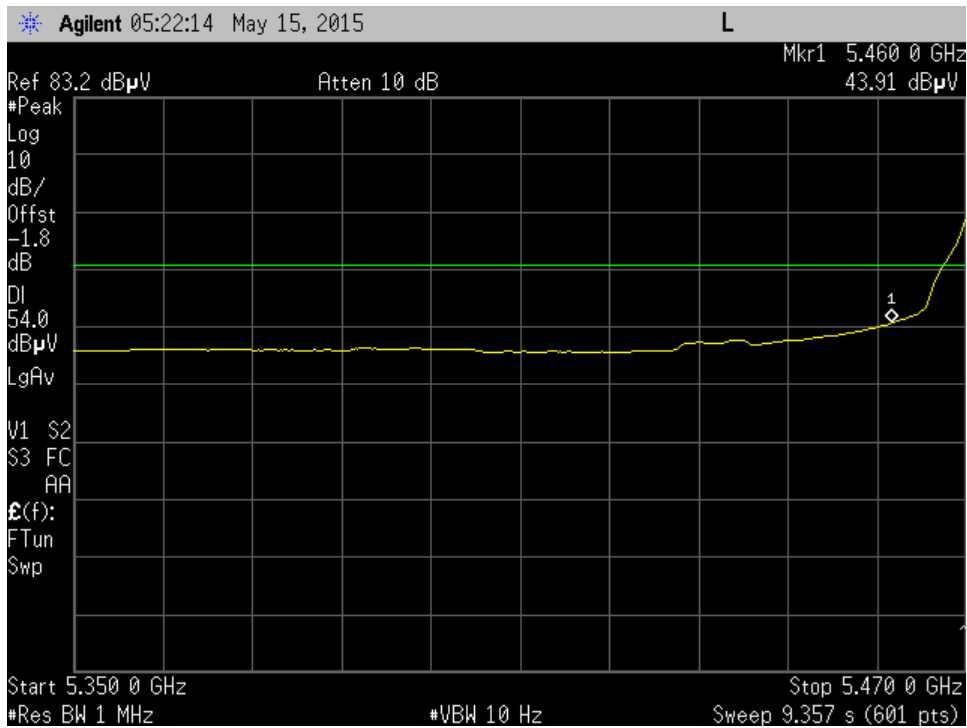
				Band Edge - Peak Data_intergrated across 1 MHz at 5470 MHz				Ant. Gain = 10dBi
Bandwidth	Test Frequency	Data Rate	TP	J7 Measured Level	J8 Measured Level	Limit	J7 Margin	J8 Margin
MHz	MHz			dBm	dBm	dBm/MHz	dB	dB
10	5480	vt0	13	-27.7	-29.6	-27	-0.7	-2.6
20	5485	vt0	9	-27.2	-28.7	-27	-0.2	-1.7
20	5490	vt0	17	-28.8	-29.4	-27	-1.8	-2.4
20	5495	vt0	17	-38.6	-31.4	-27	-11.6	-4.4
30	5490	vt0	5	-27.8	-28.9	-27	-0.8	-1.9
30	5495	vt0	14	-27.3	-30.1	-27	-0.3	-3.1
30	5500	vt0	20	-28.3	-28.1	-27	-1.3	-1.1
40	5495	vf0	5	-28.3	-28.9	-27	-1.3	-1.9
40	5500	vf0	12	-27.9	-29.1	-27	-0.9	-2.1
40	5505	vf0	15	-30.4	-31.9	-27	-3.4	-4.9
40	5510	vf0	18	-29	-32.9	-27	-2	-5.9



10 MHz Low side_J7

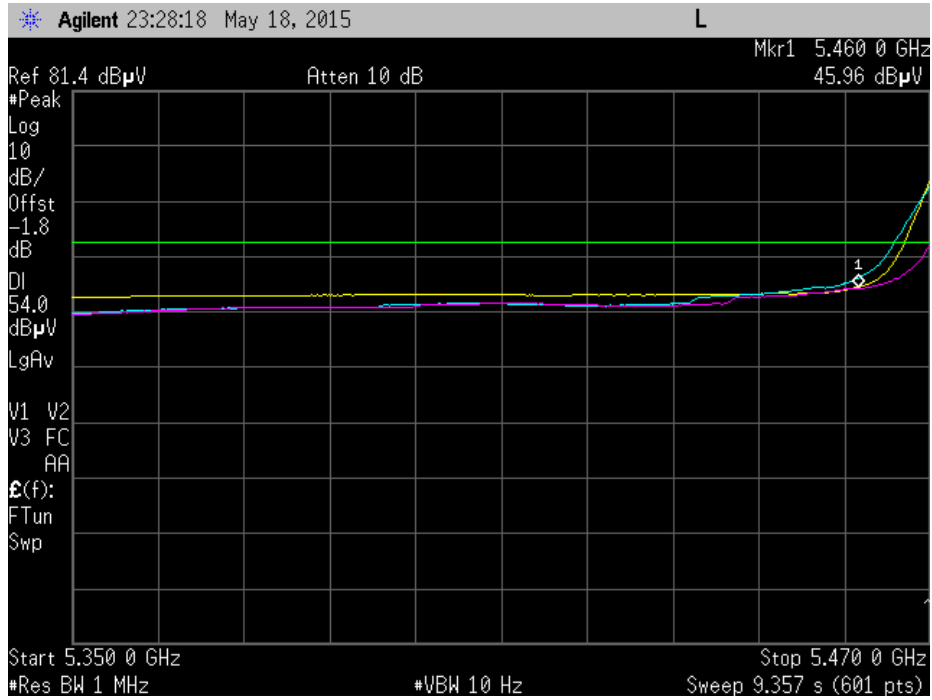


10 MHz Low side_J8

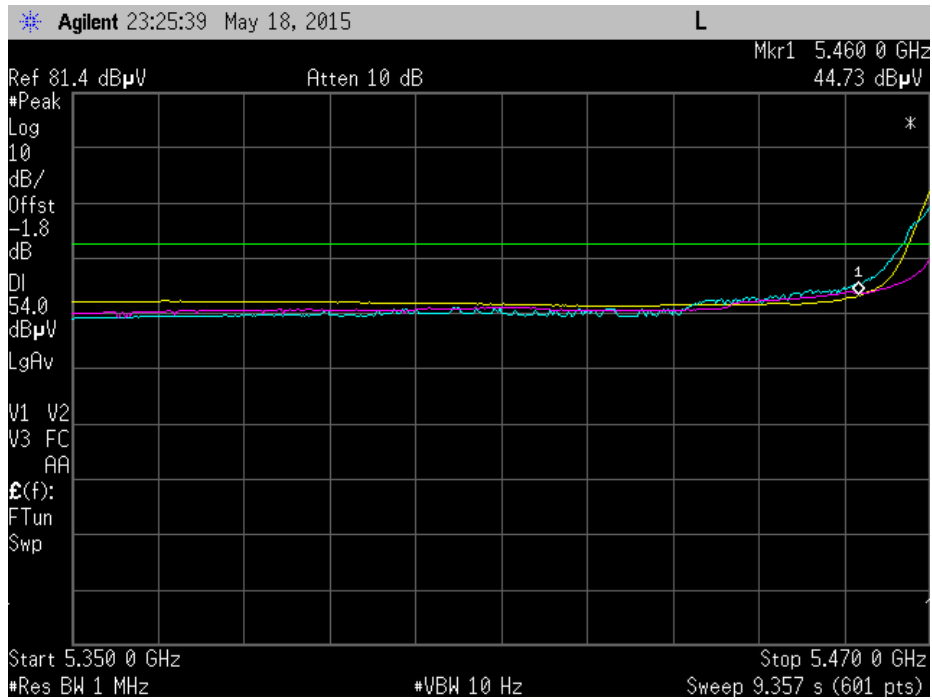




20 MHz Low side_J7

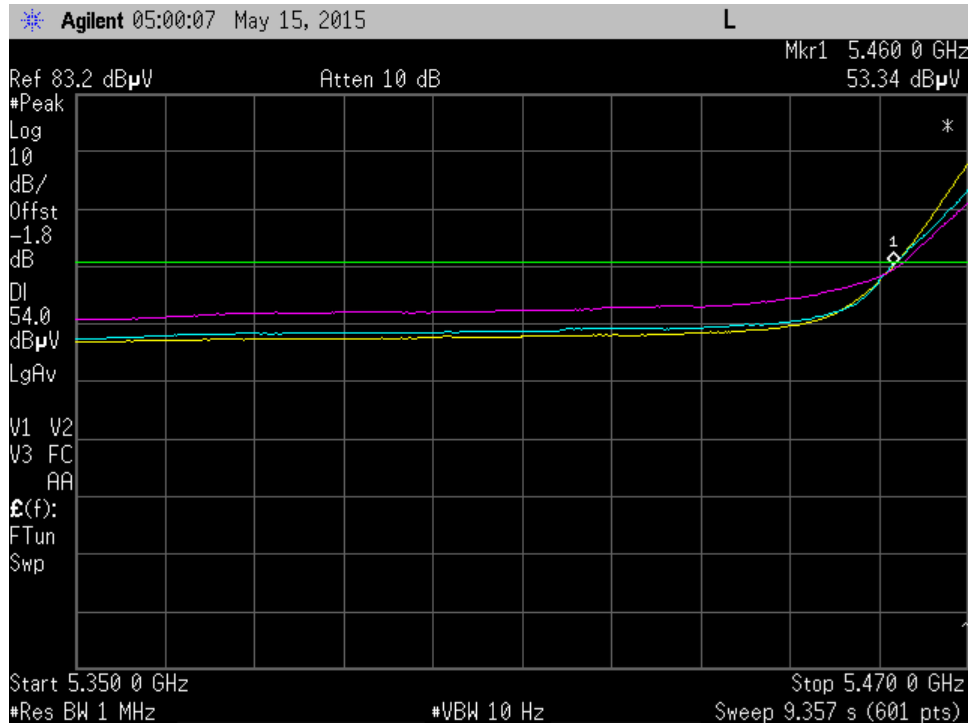


20 MHz Low side_J8

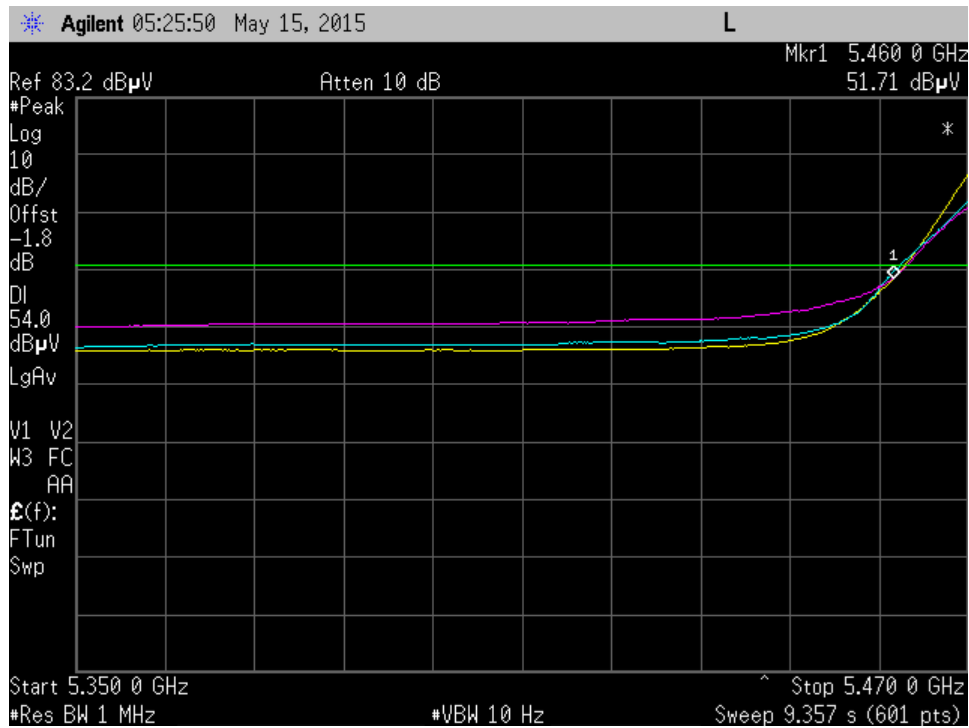




30 MHz Low side_J7

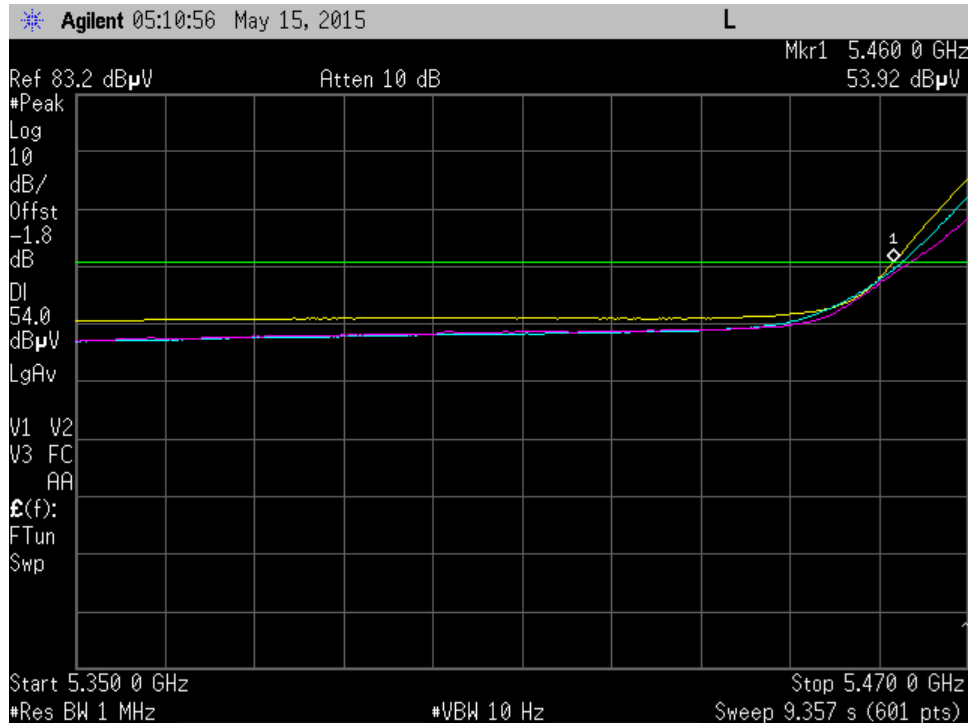


30 MHz Low side_J8





40 MHz low side_J7



40 MHz Low side_J8

