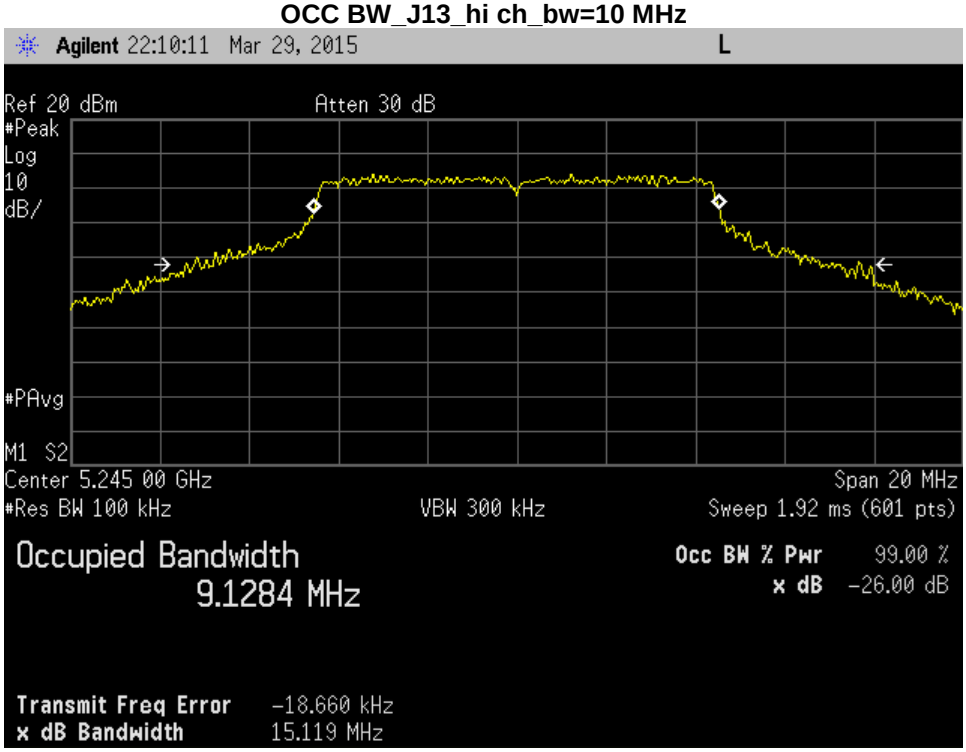
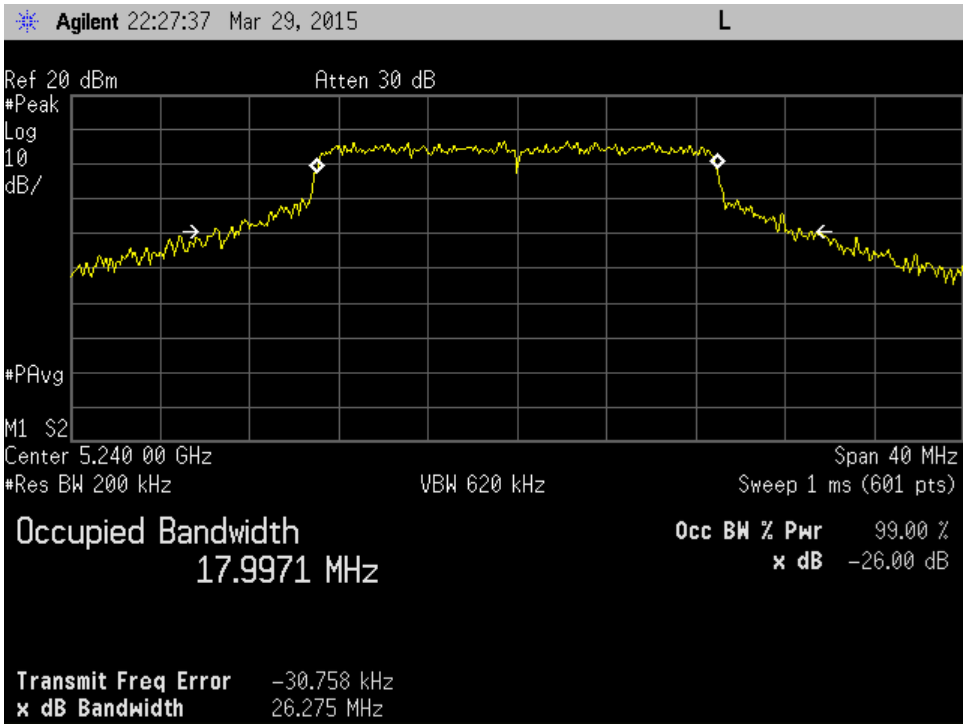
Annex D
Occupied Bandwidth
Ports J7 and J13

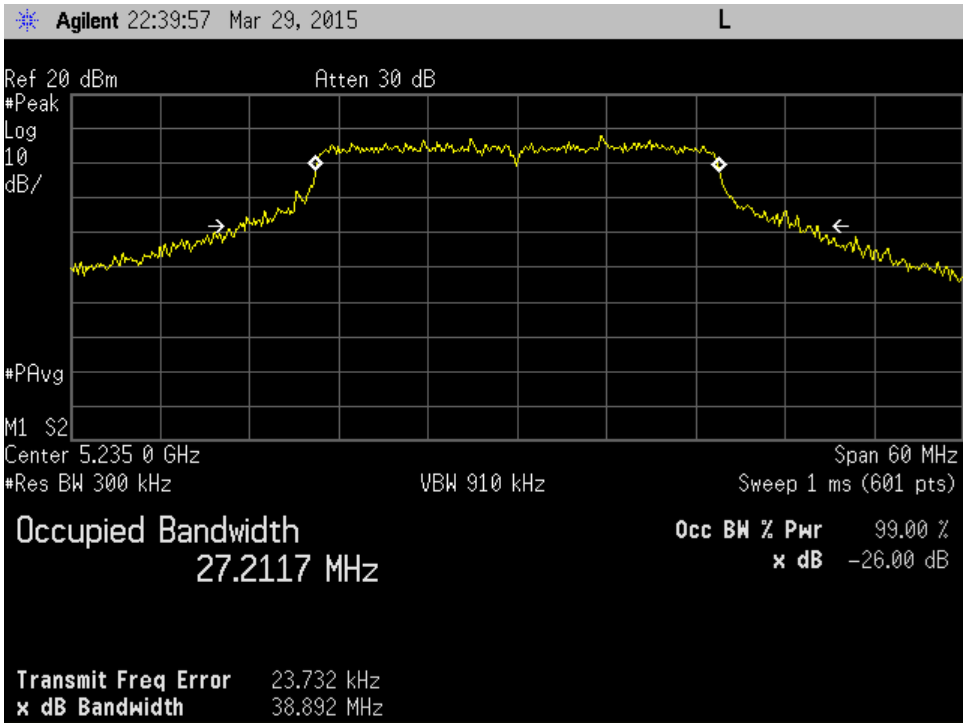
OCC BW_J13_hi ch_bw=10 MHz



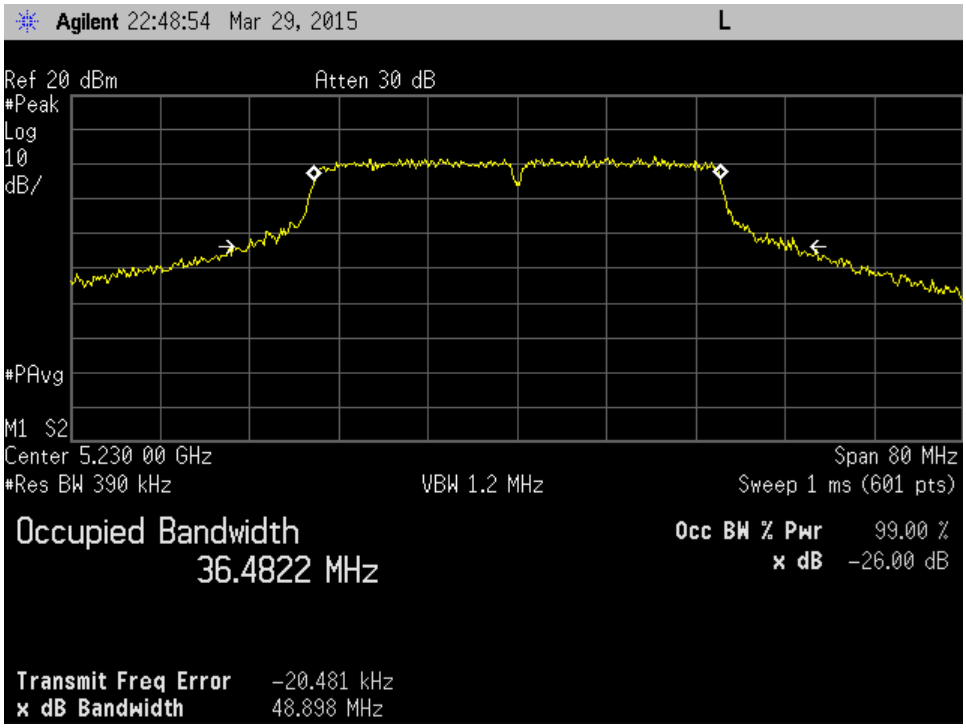
OCC BW_J13_hi ch_bw=20 MHz



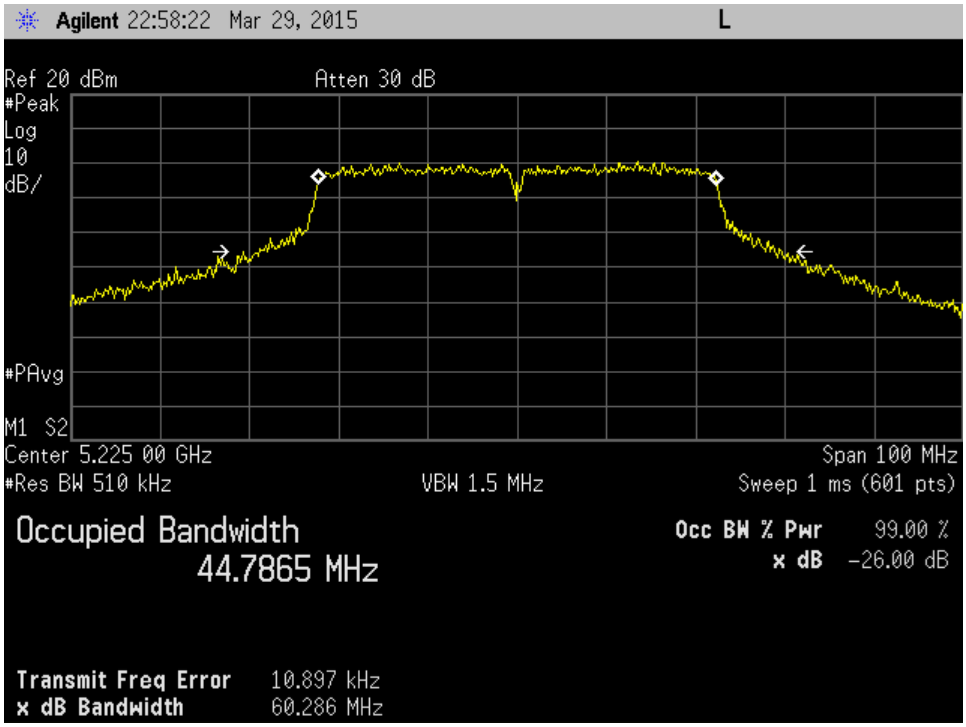
OCC BW_J13_hi ch_bw=30 MHz



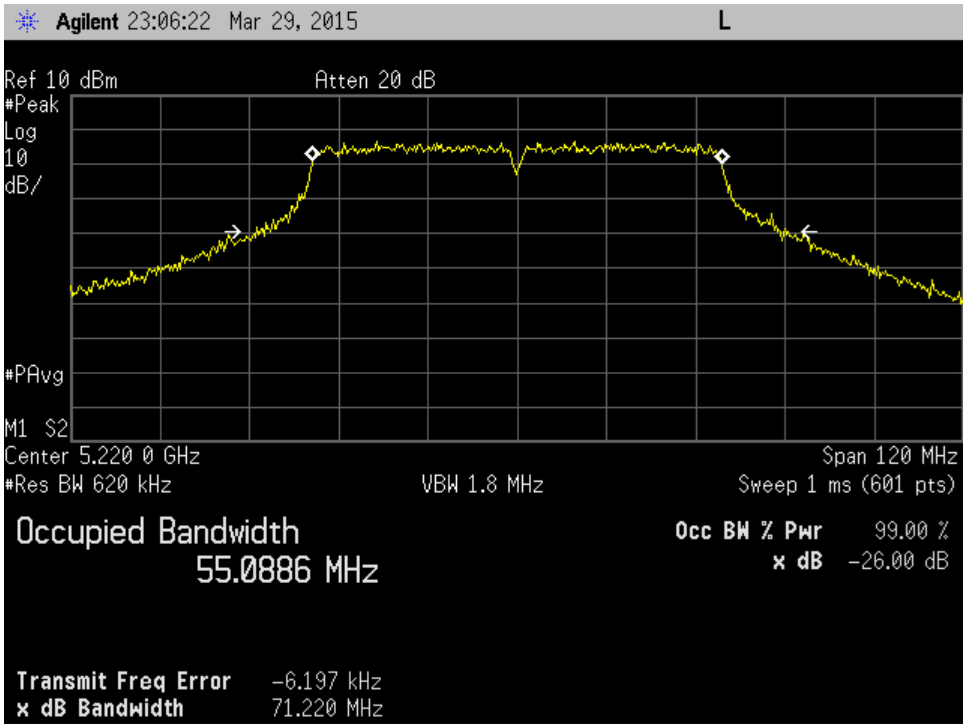
OCC BW_J13_hi ch_bw=40 MHz



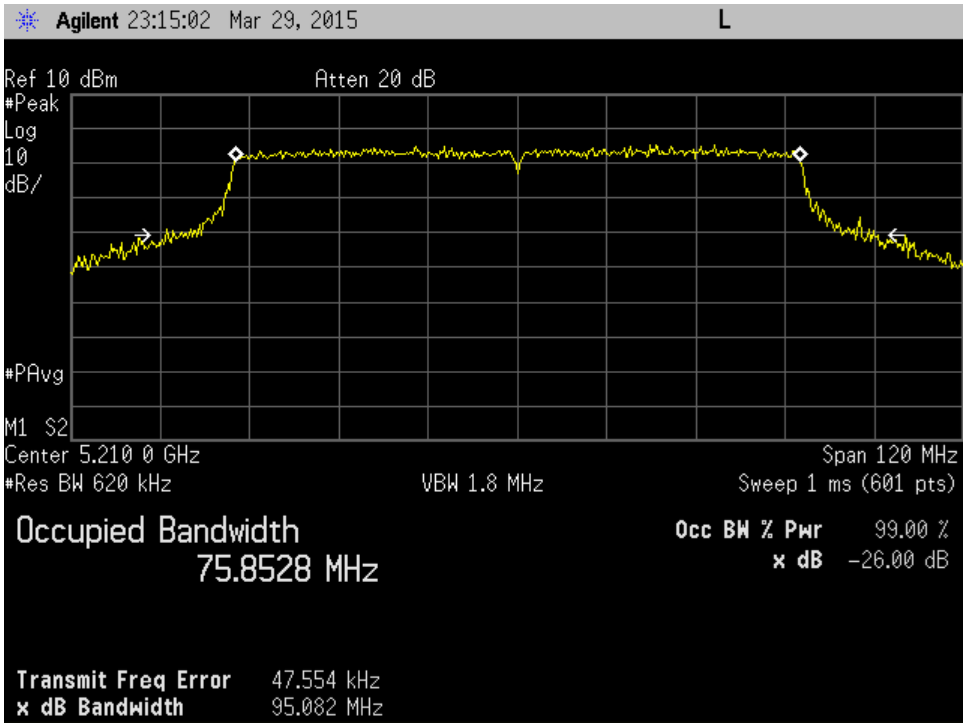
OCC BW_J13_hi ch_bw=50 MHz



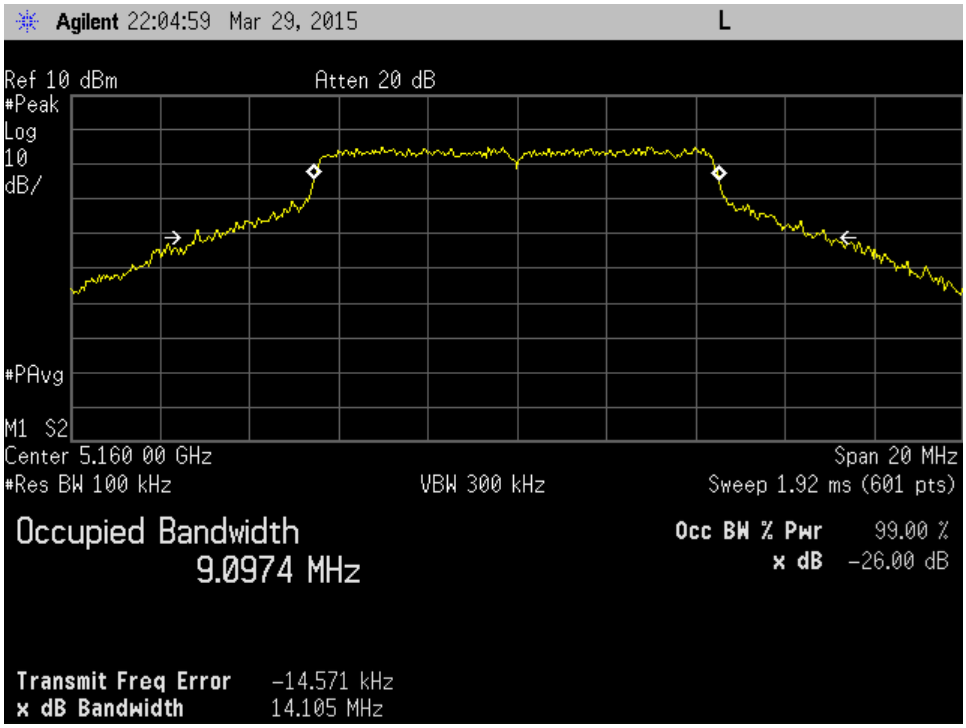
OCC BW_J13_hi ch_bw=60 MHz



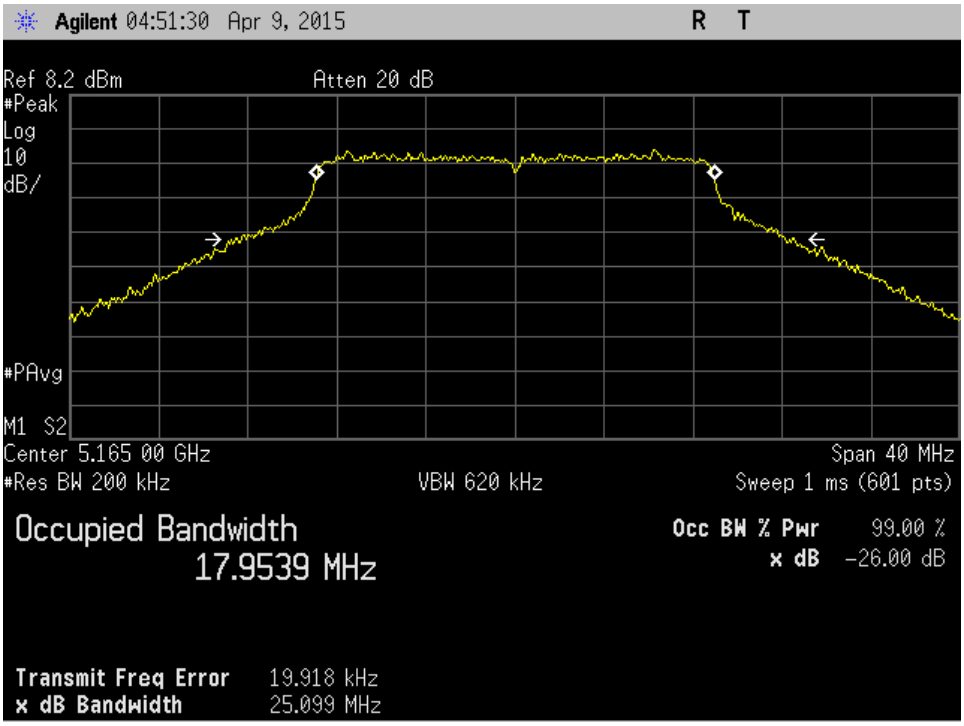
OCC BW_J13_hi ch_bw=80 MHz



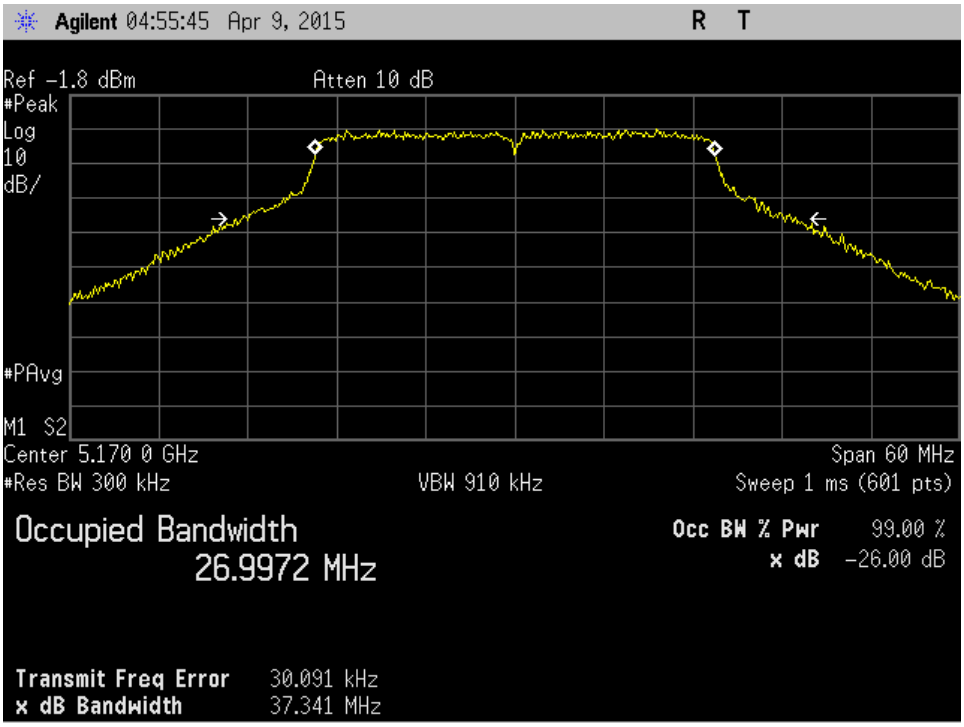
OCC BW_J13_low ch_bw=10 MHz



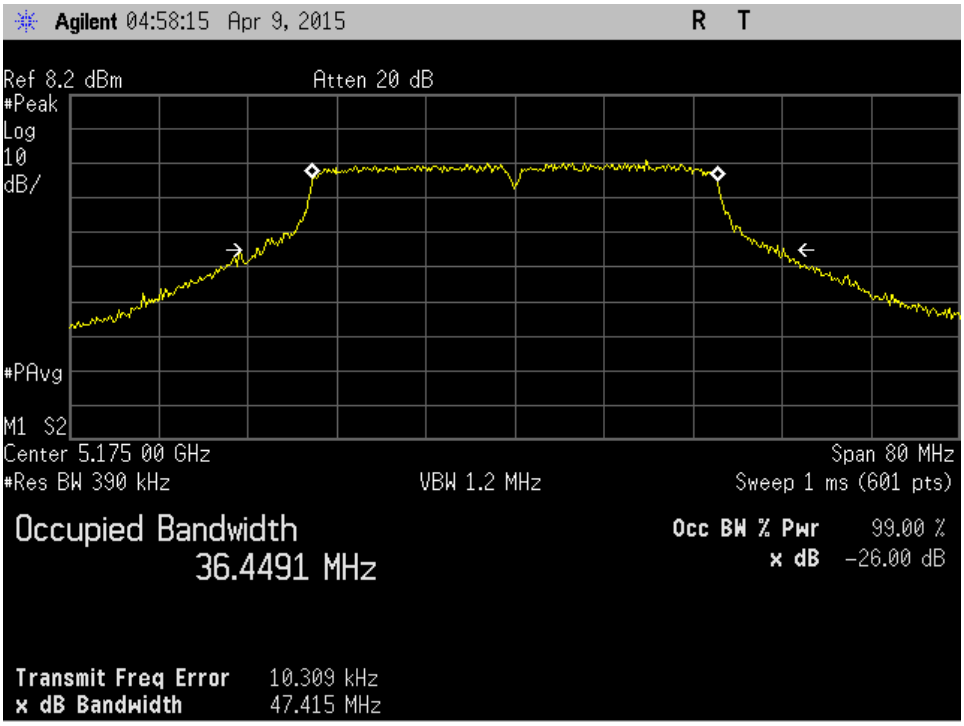
OCC BW_J13_low ch_bw=20 MHz



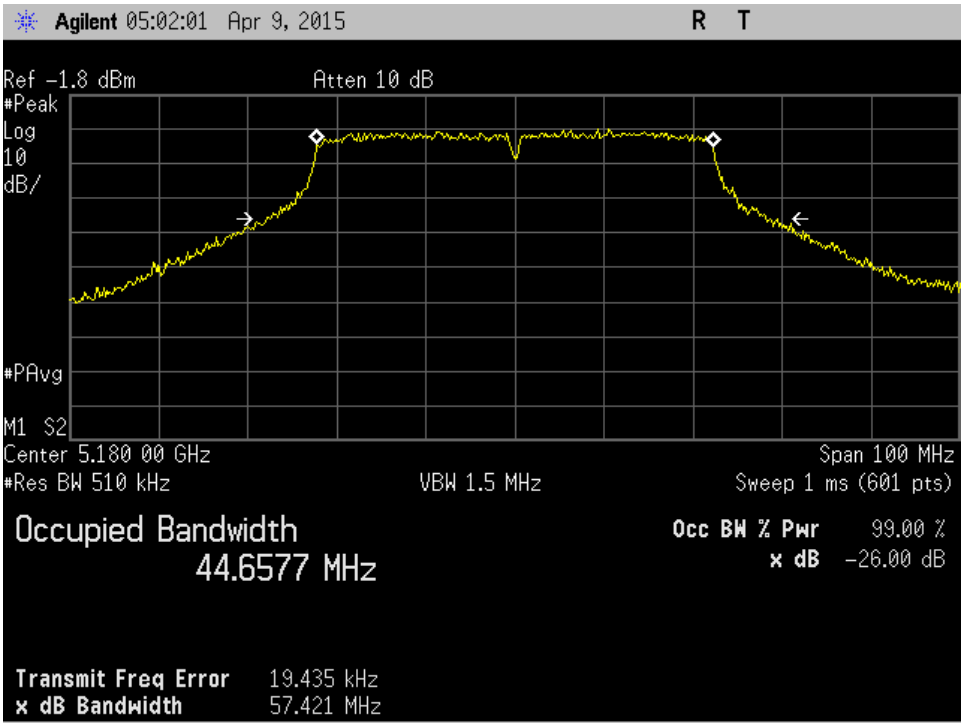
OCC BW_J13_low ch_bw=30 MHz



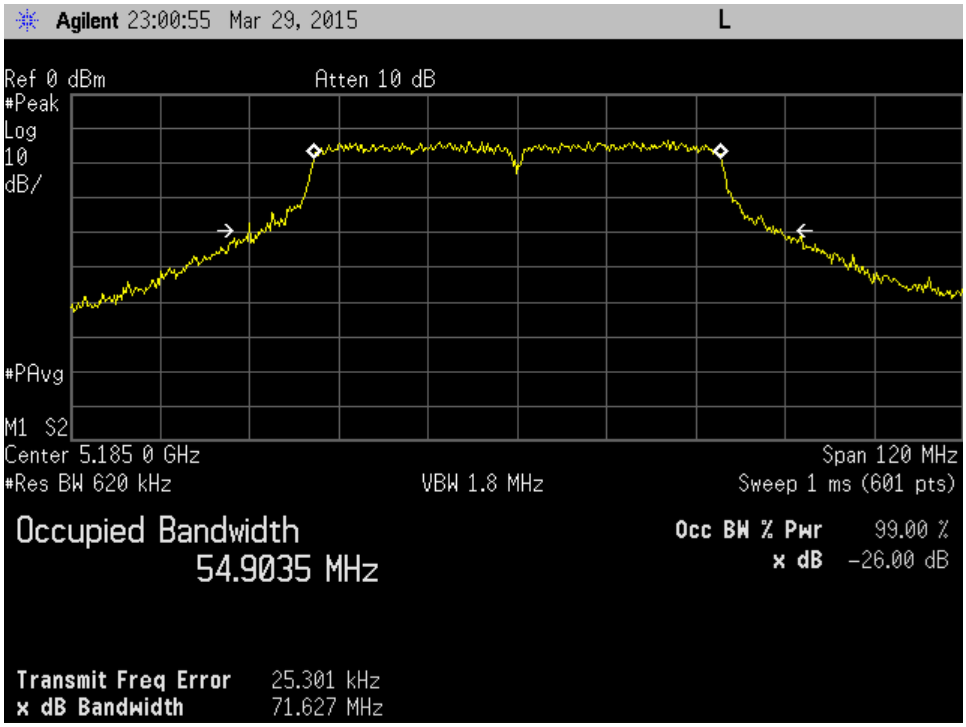
OCC BW_J13_low ch_bw=40 MHz



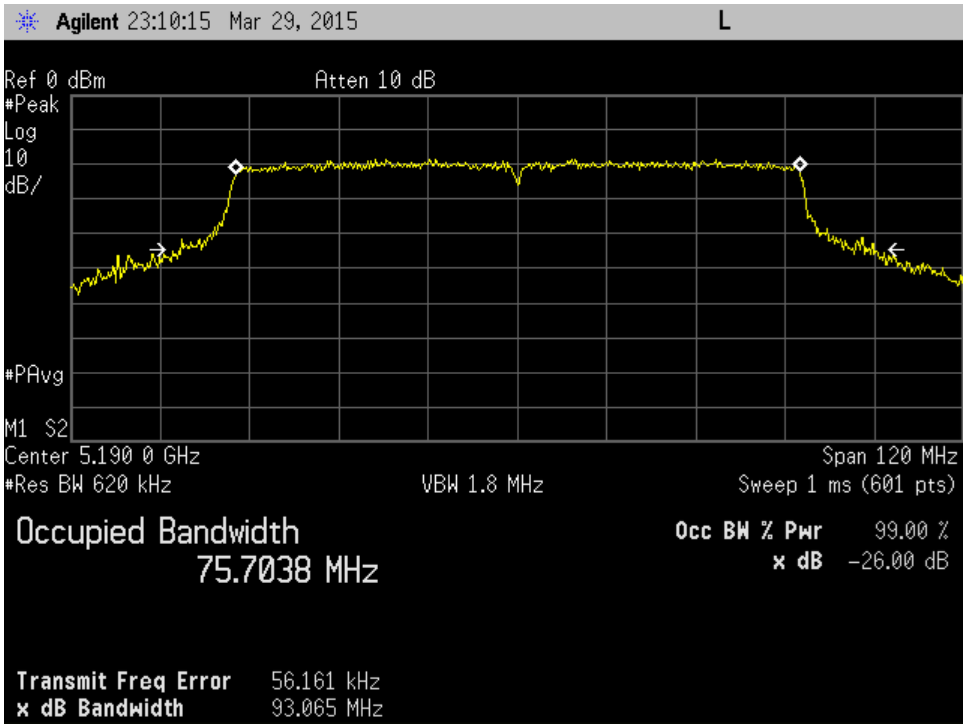
OCC BW_J13_low ch_bw=50 MHz



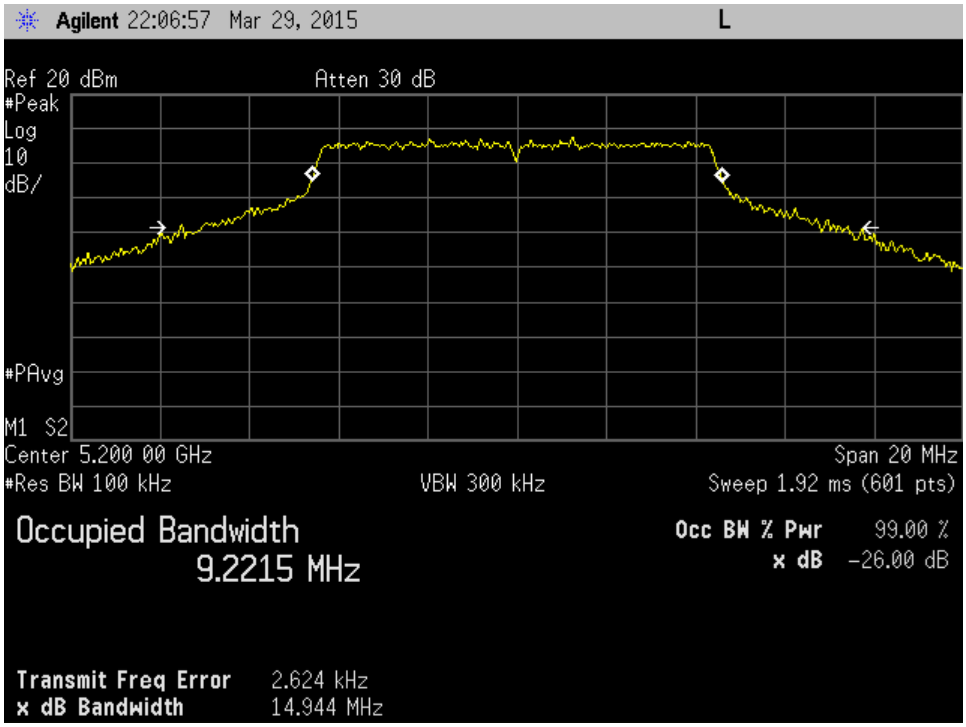
OCC BW_J13_low ch_bw=60 MHz



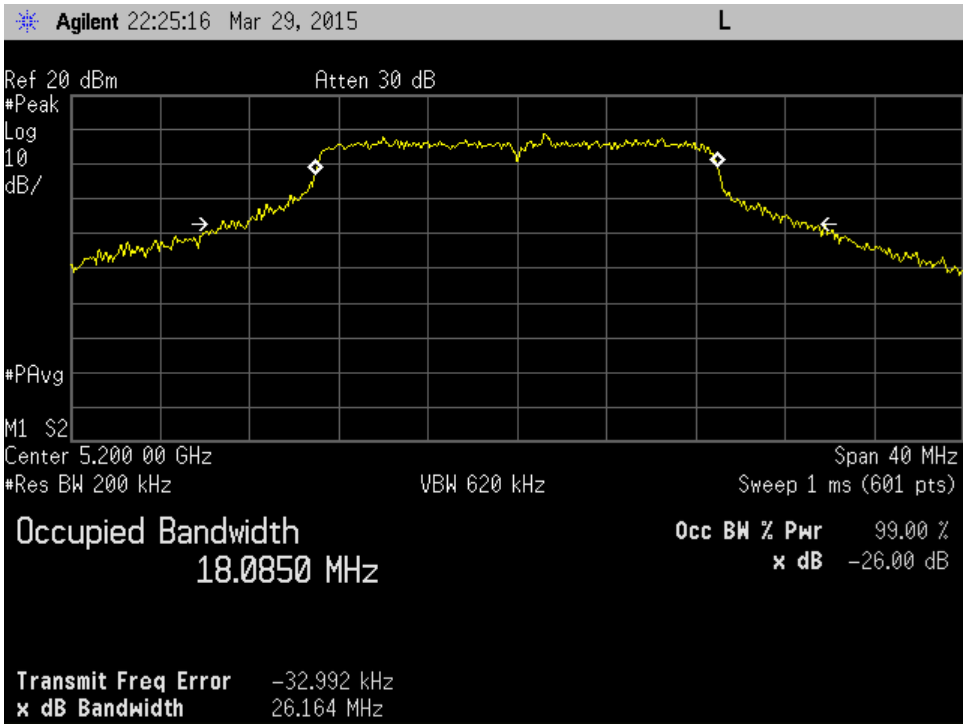
OCC BW_J13_low ch_bw=80 MHz



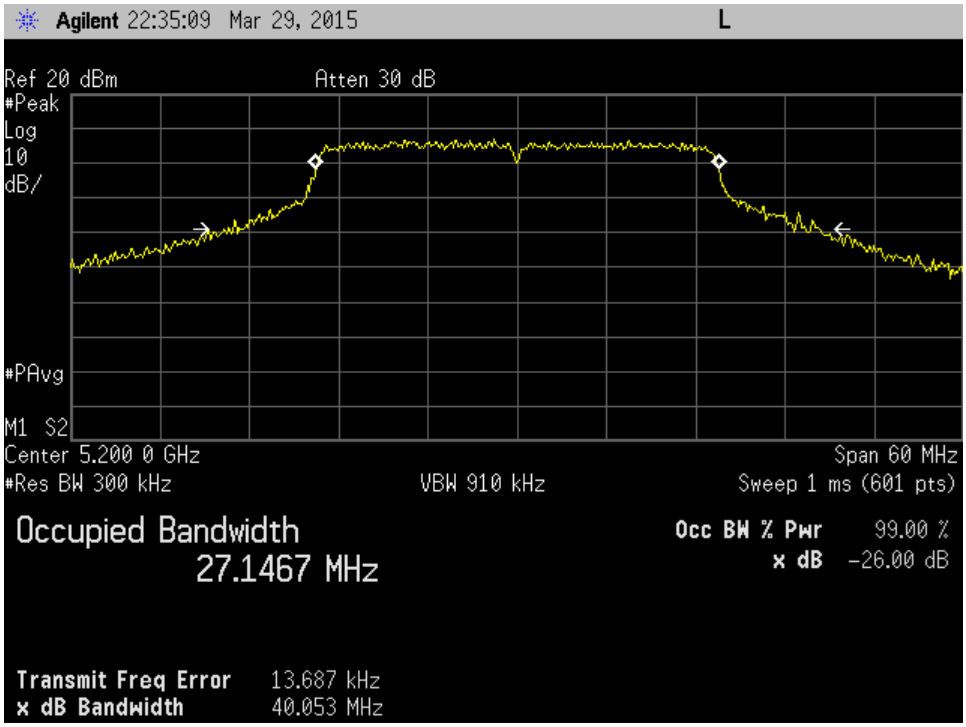
OCC BW_J13_mid ch_bw=10 MHz



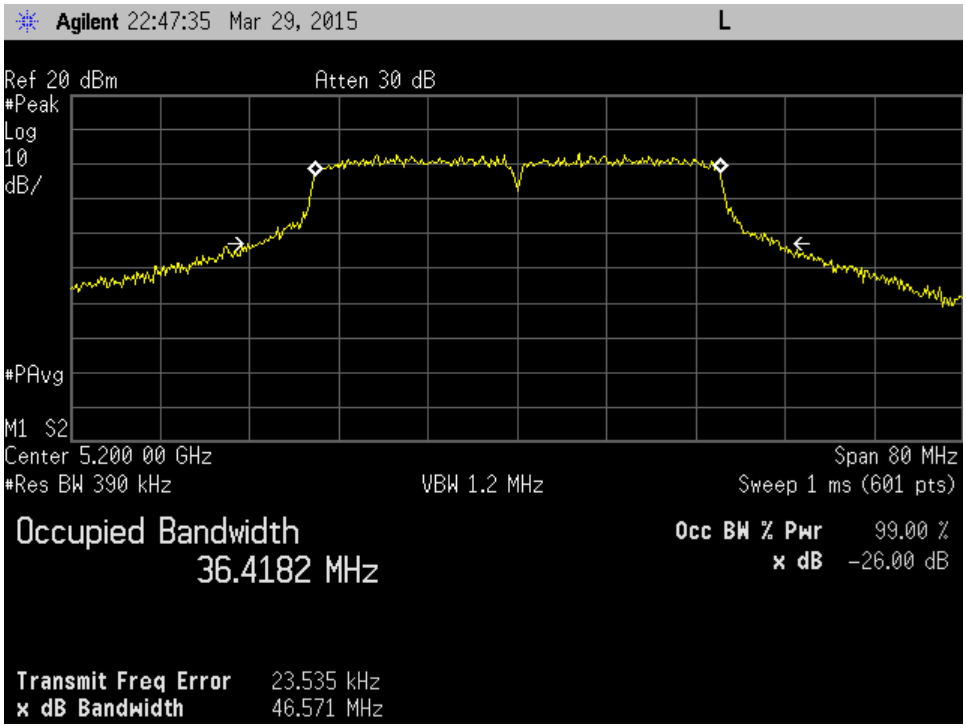
OCC BW_J13_mid ch_bw=20 MHz



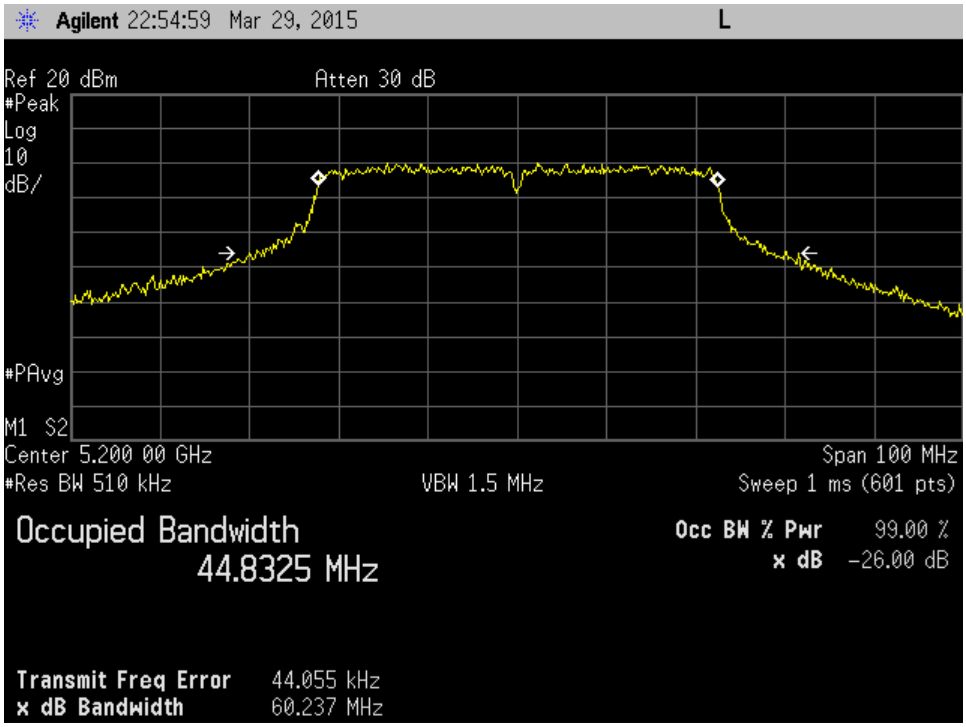
OCC BW_J13_mid ch_bw=30 MHz



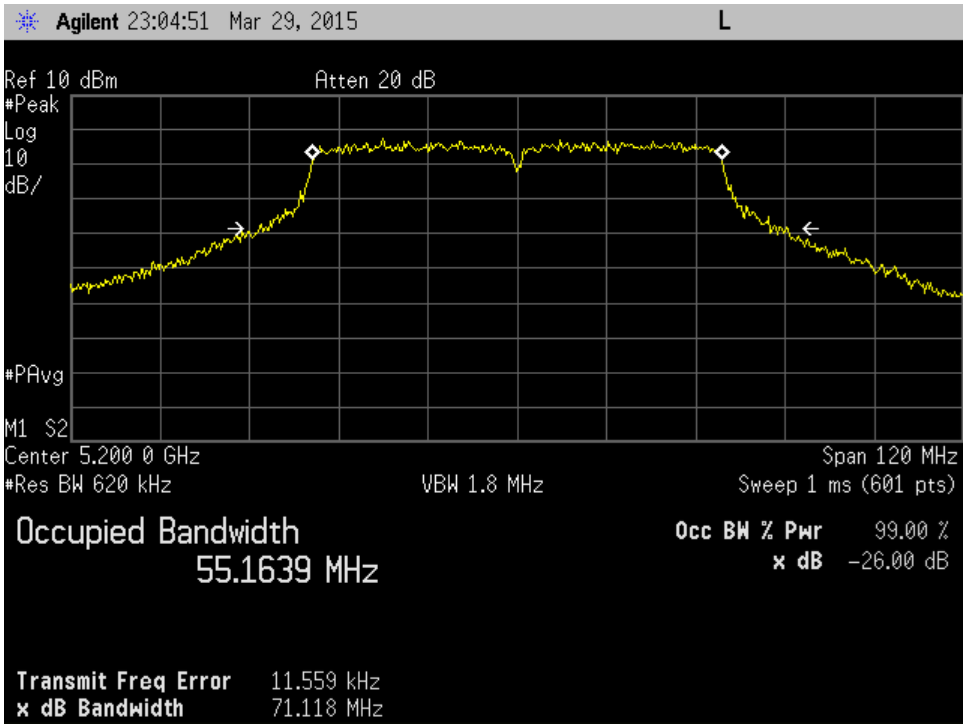
OCC BW_J13_mid ch_bw=40 MHz



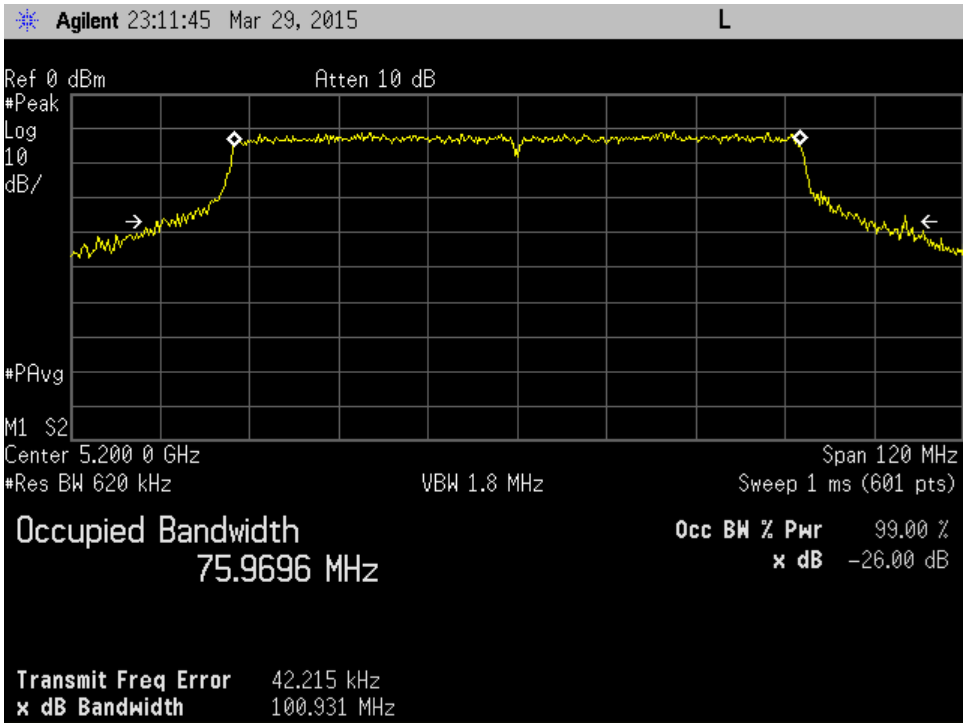
OCC BW_J13_mid ch_bw=50 MHz



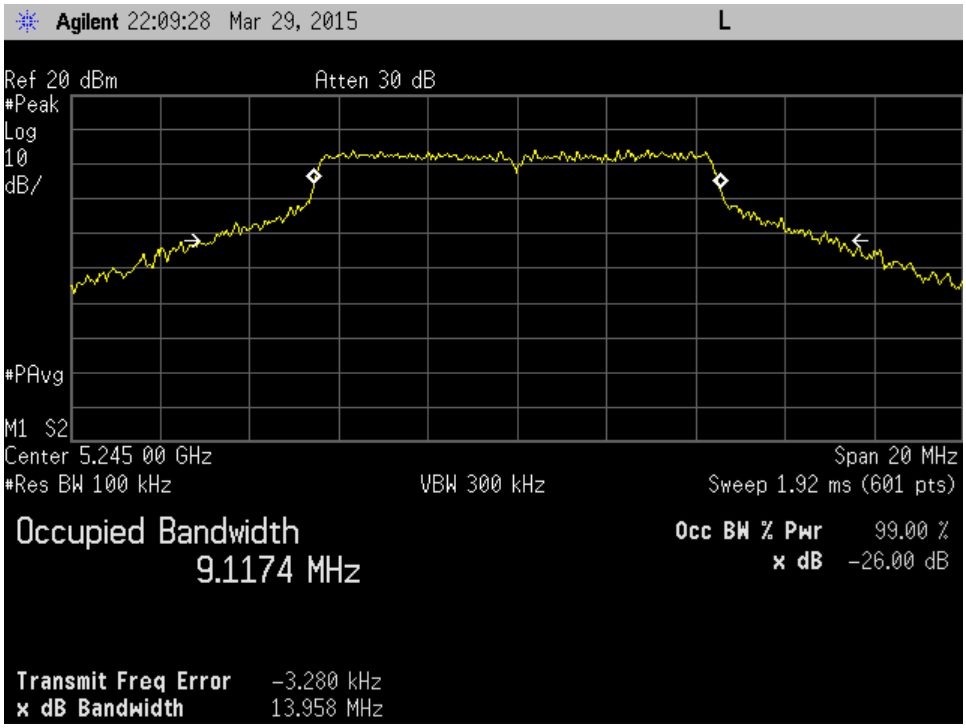
OCC BW_J13_mid ch_bw=60 MHz



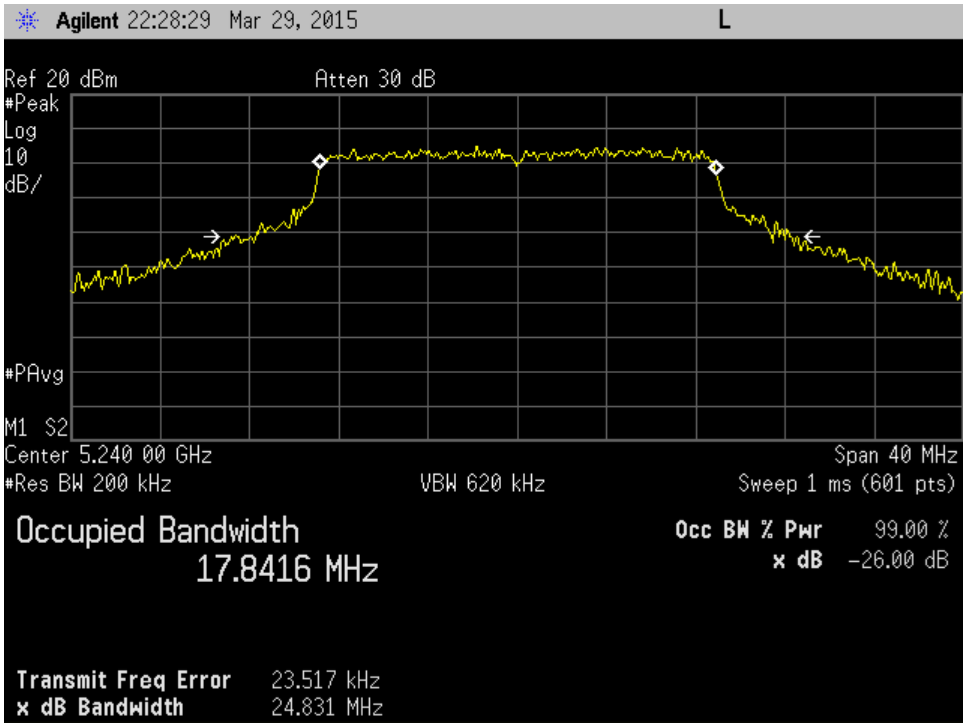
OCC BW_J13_mid ch_bw=80 MHz



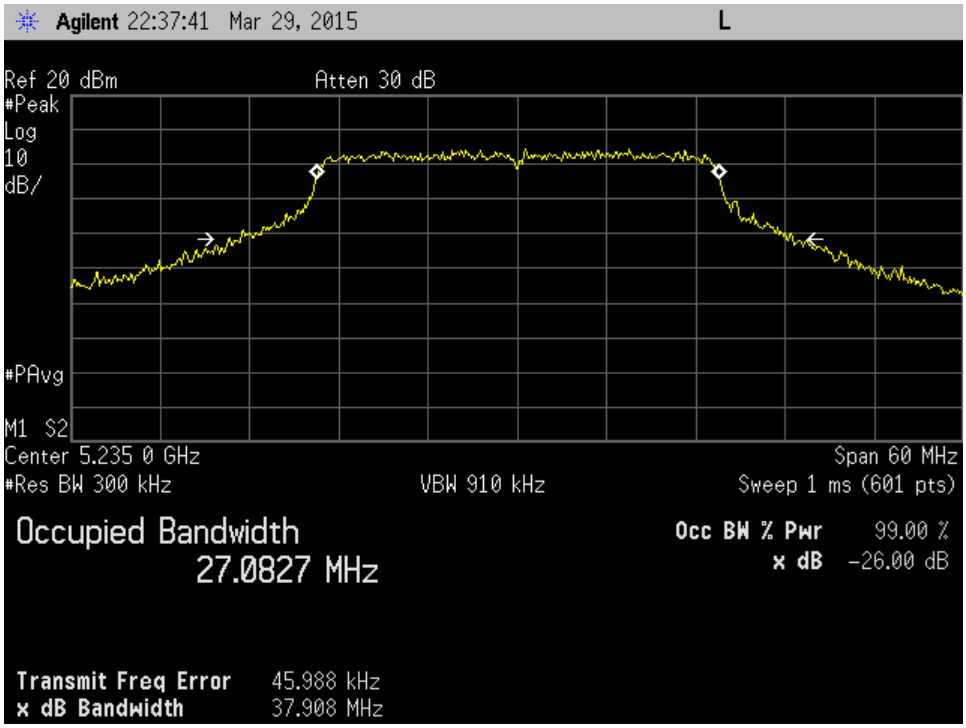
OCC BW_J7_hi ch_bw=10 MHz



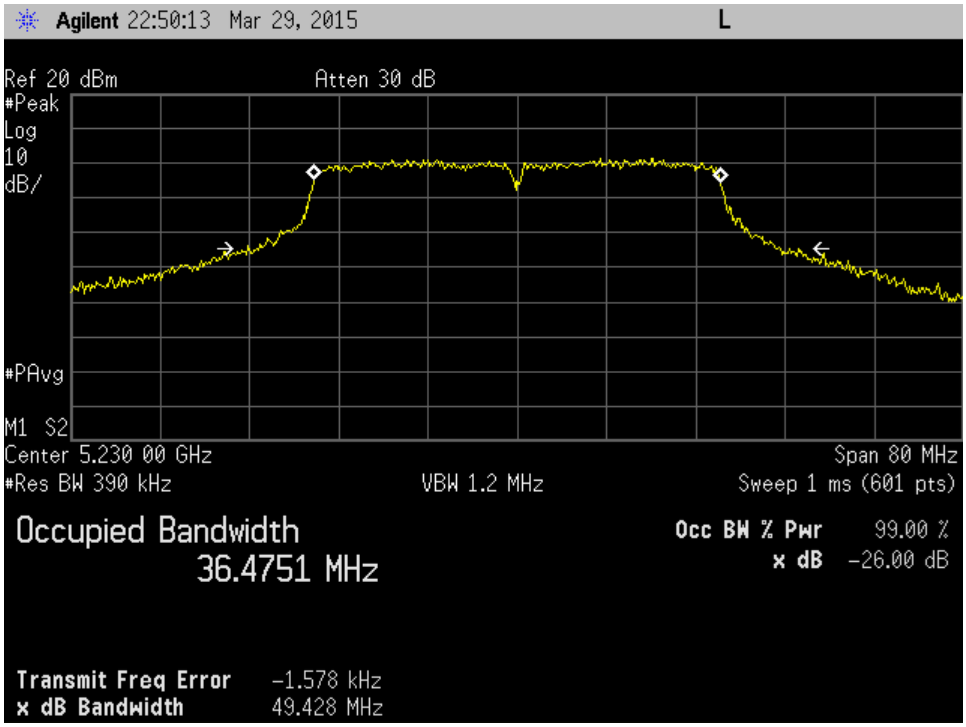
OCC BW_J7_hi ch_bw=20 MHz



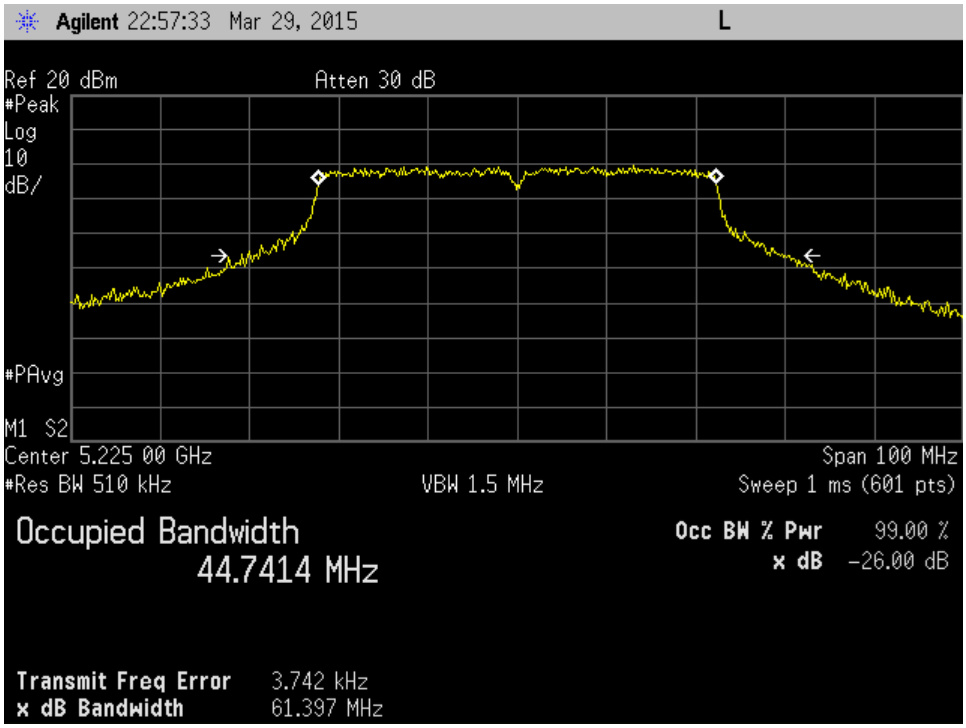
OCC BW_J7_hi ch_bw=30 MHz



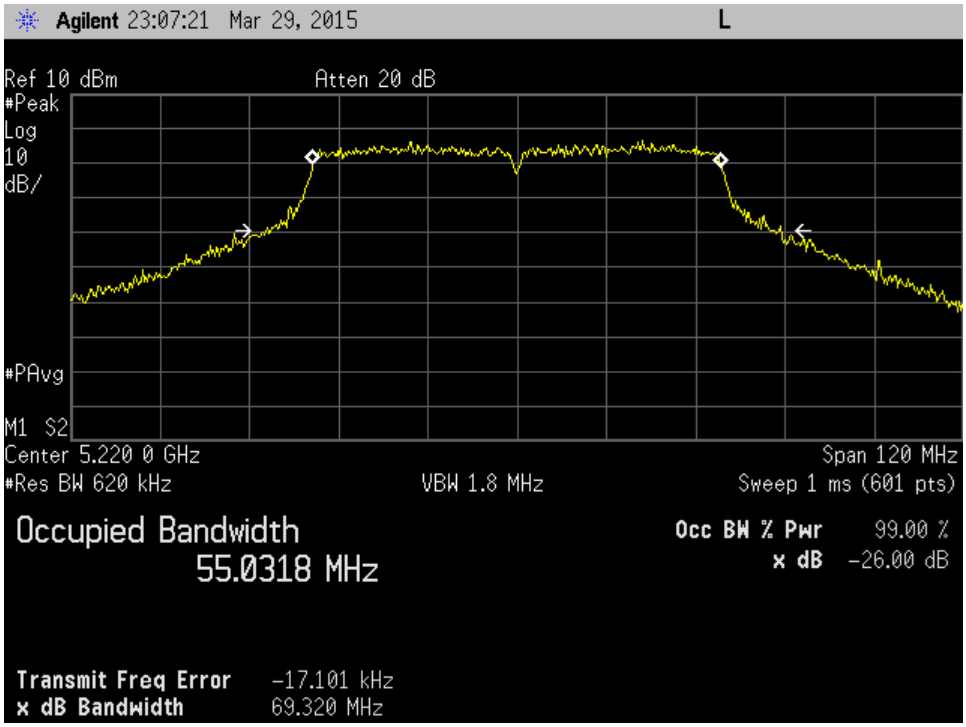
OCC BW_J7_hi ch_bw=40 MHz



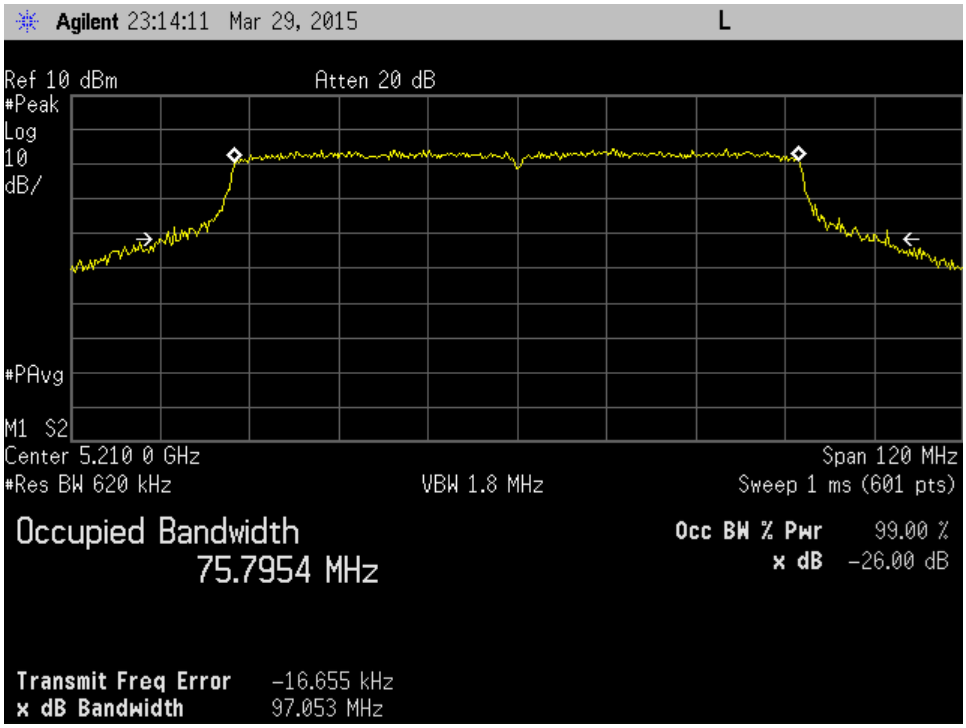
OCC BW_J7_hi ch_bw=50 MHz



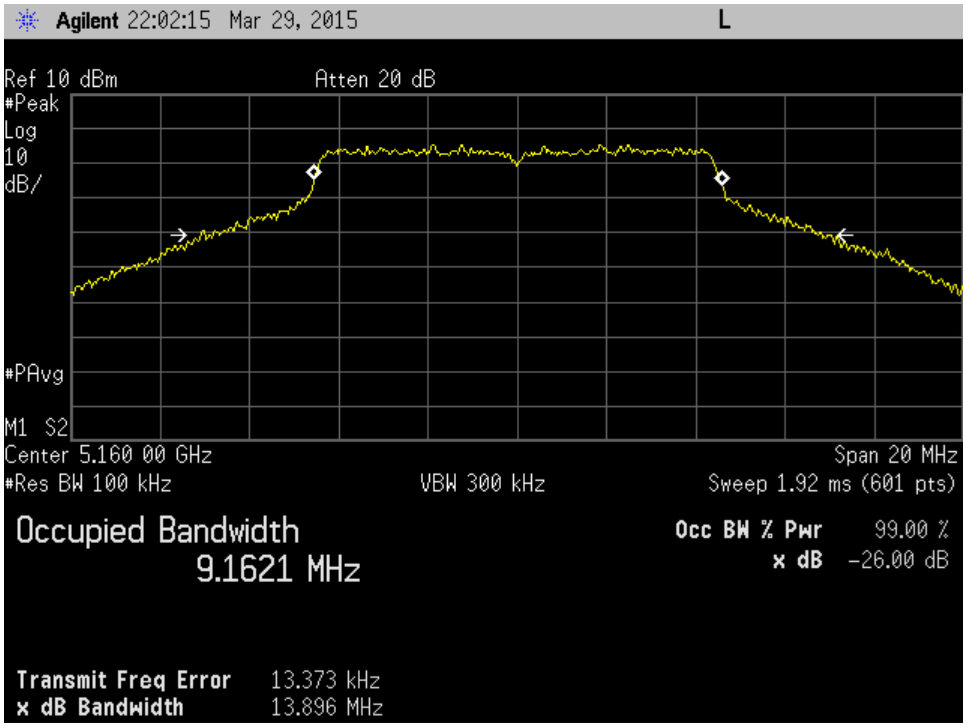
OCC BW_J7_hi ch_bw=60 MHz



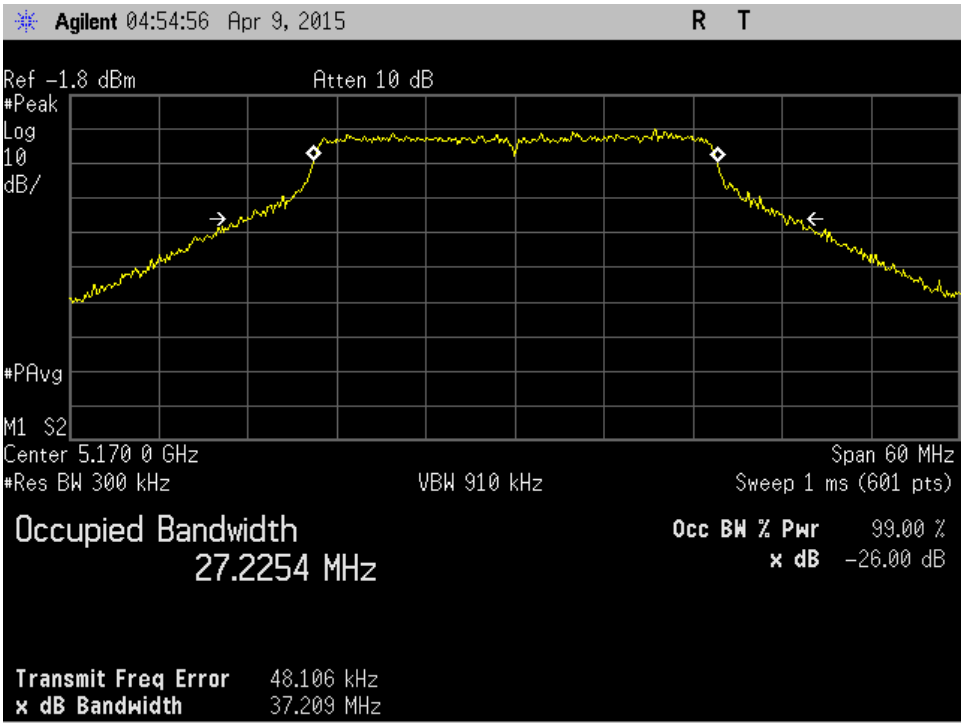
OCC BW_J7_hi ch_bw=80 MHz



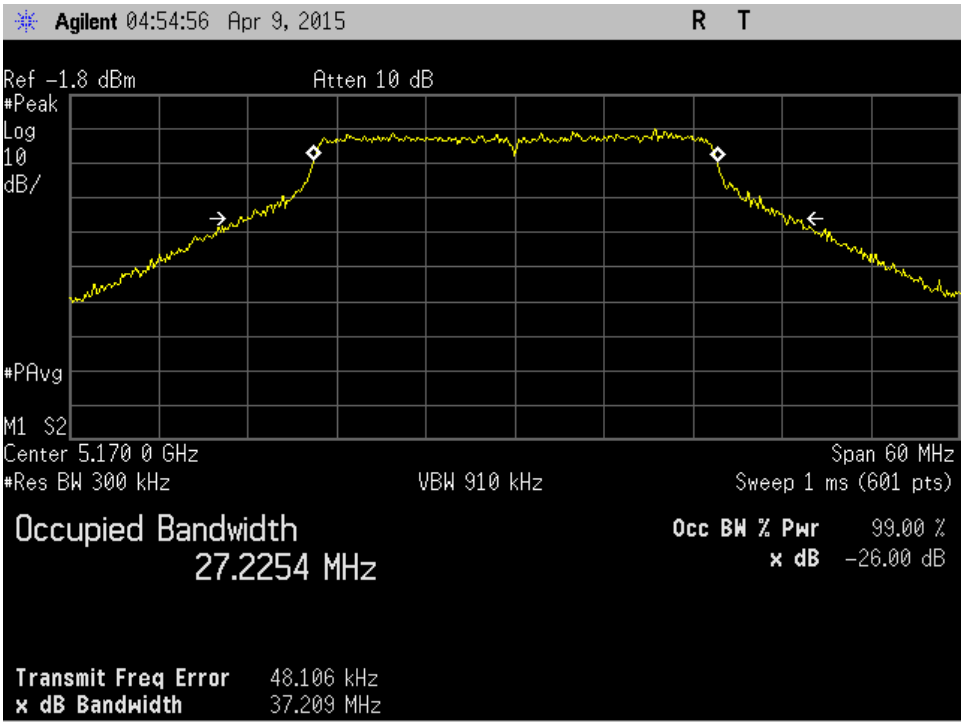
OCC BW_J7_low ch_bw=10 MHz



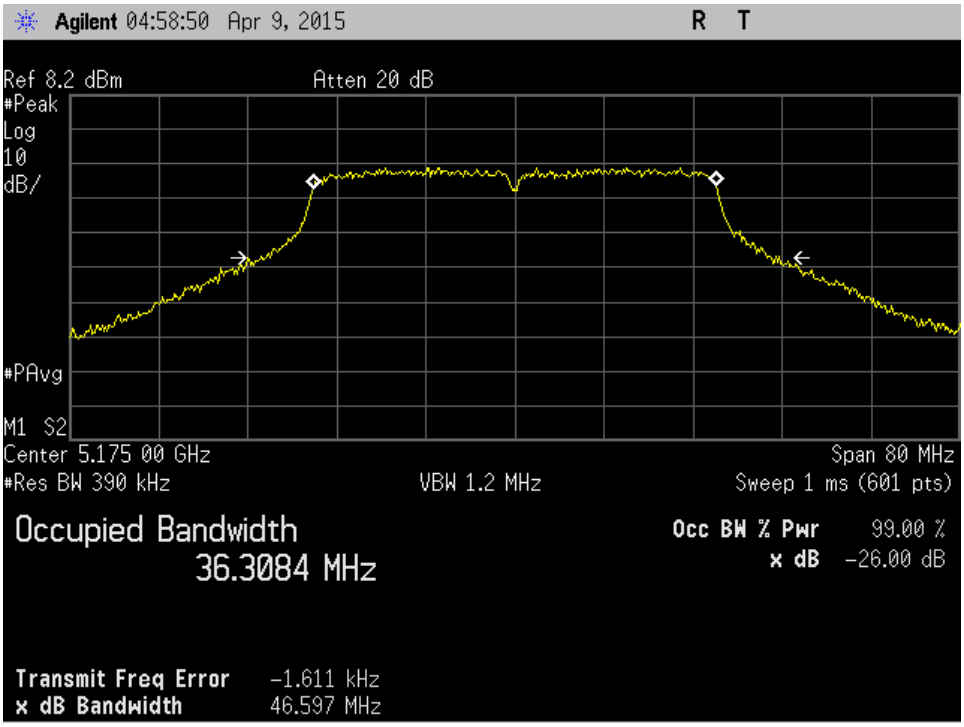
OCC BW_J7_low ch_bw=20 MHz



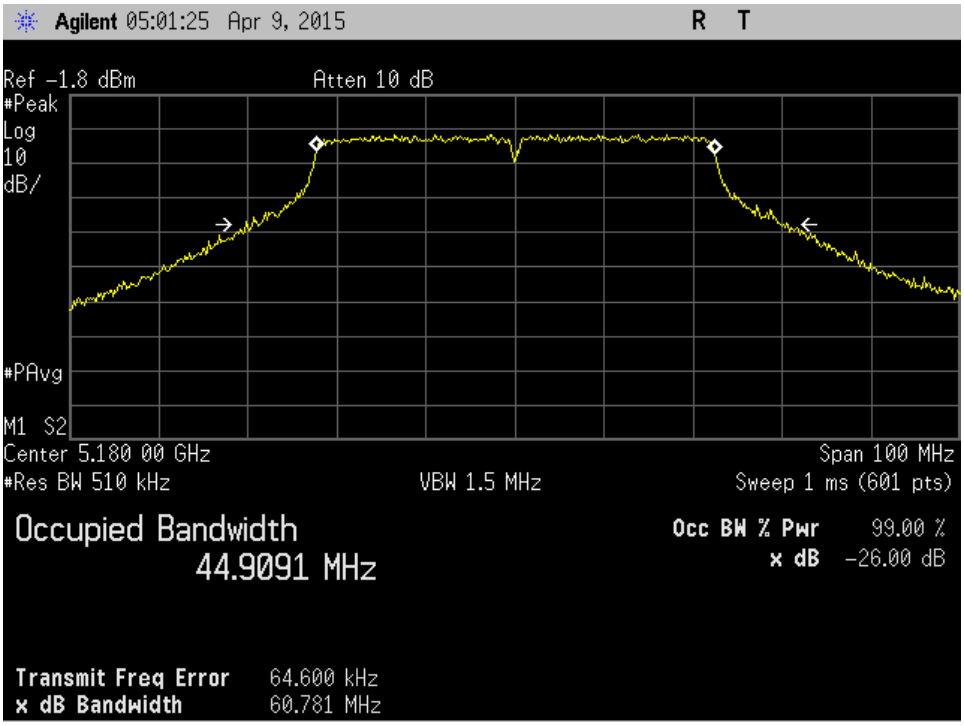
OCC BW_J7_low ch_bw=30 MHz



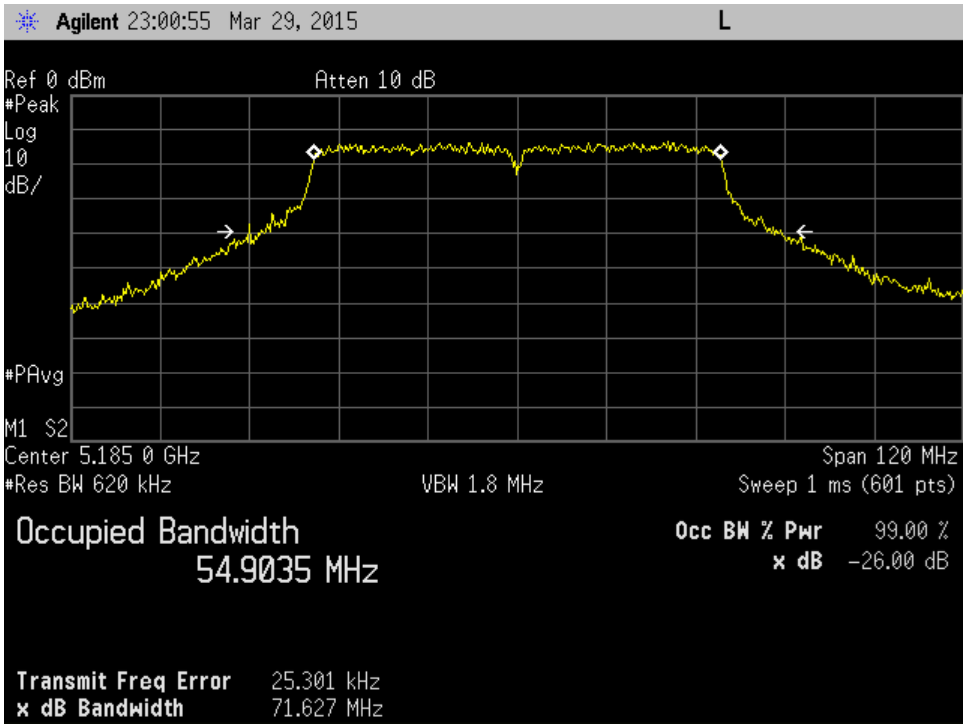
OCC BW_J7_low ch_bw=40 MHz



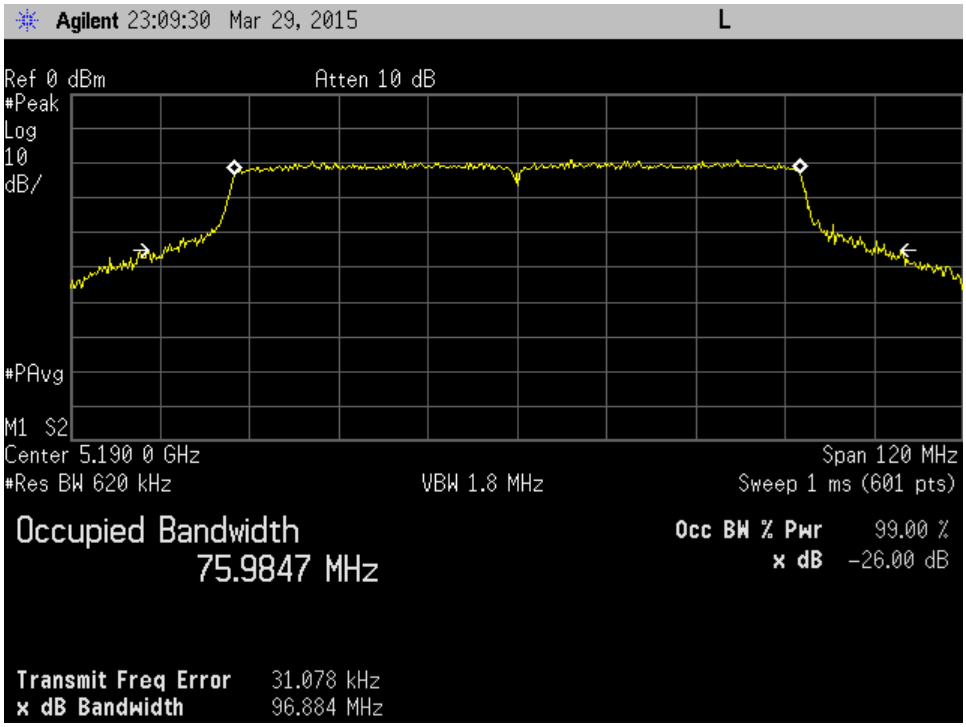
OCC BW_J7_low ch_bw=50 MHz



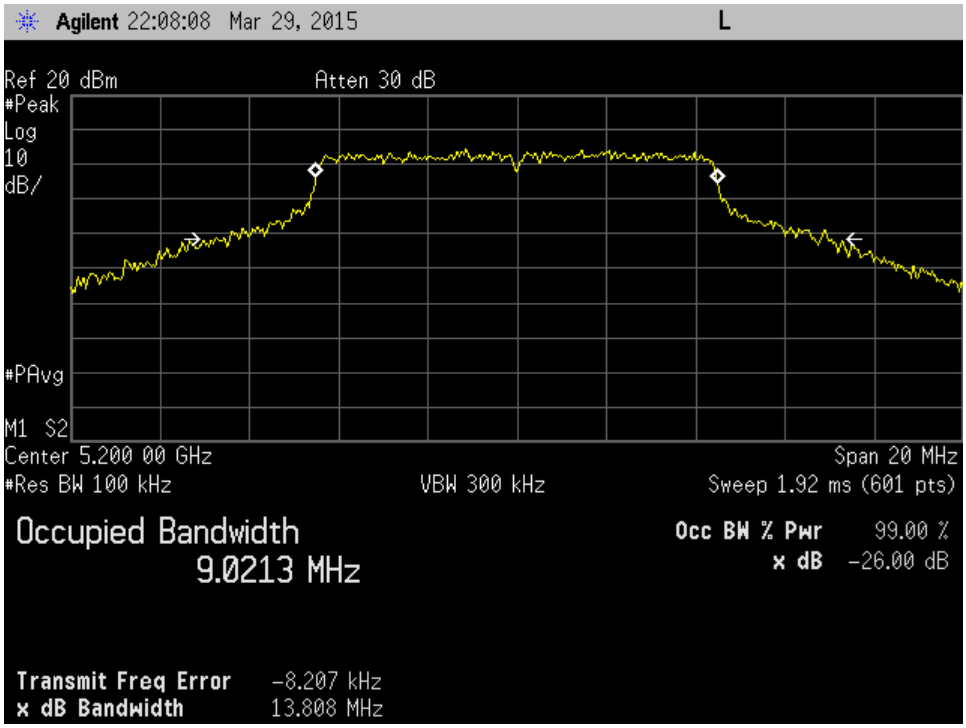
OCC BW_J7_low ch_bw=60 MHz



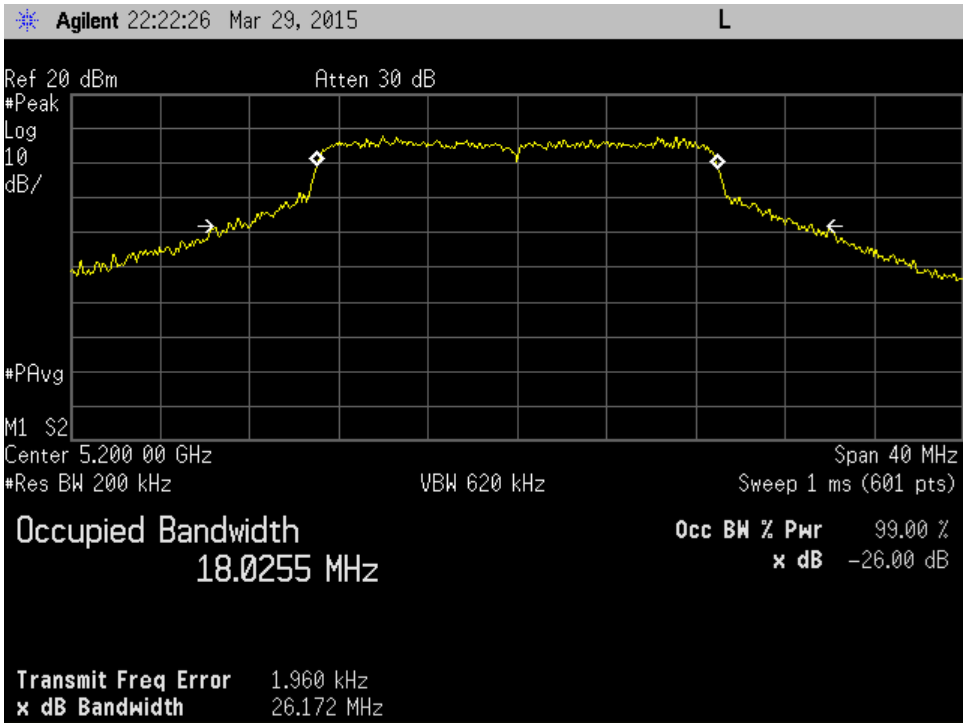
OCC BW_J7_low ch_bw=80 MHz



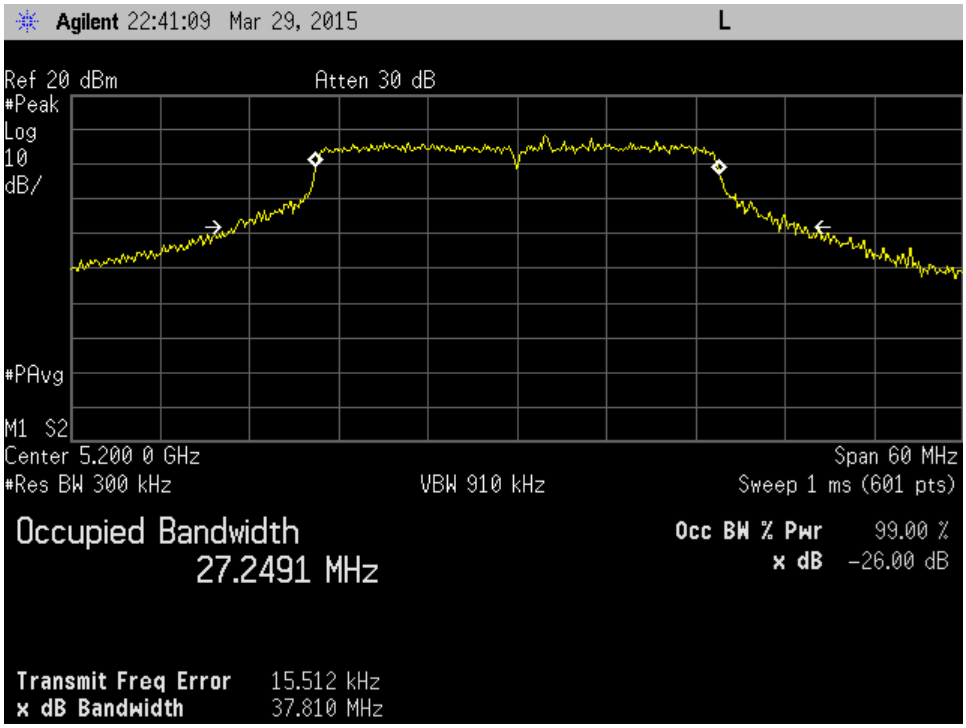
OCC BW_J7_mid ch_bw=10 MHz



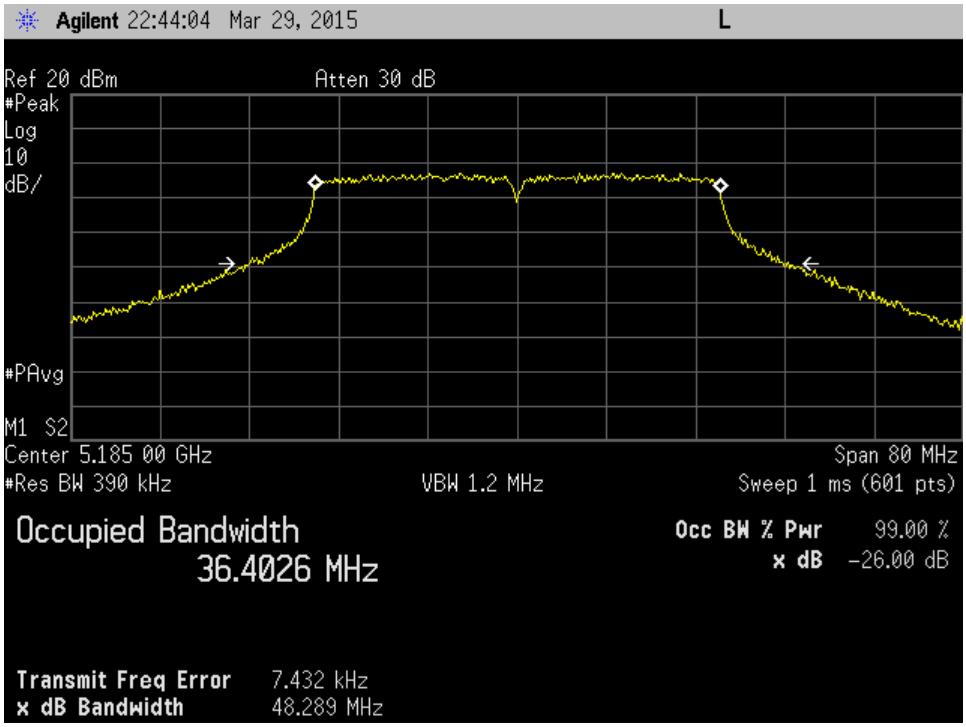
OCC BW_J7_mid ch_bw=20 MHz



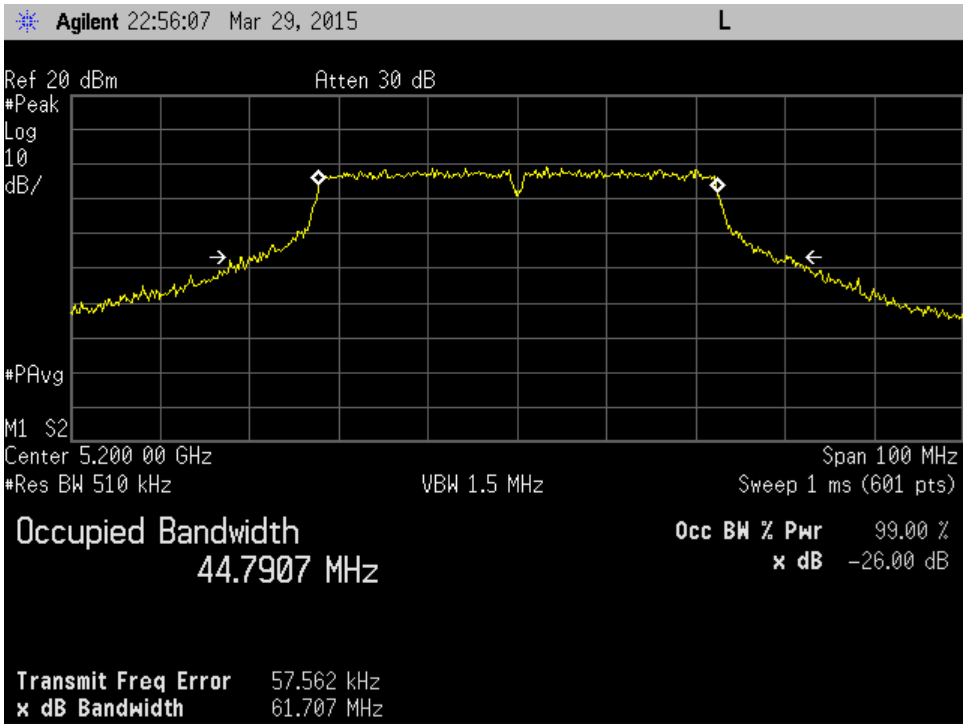
OCC BW_J7_mid ch_bw=30 MHz



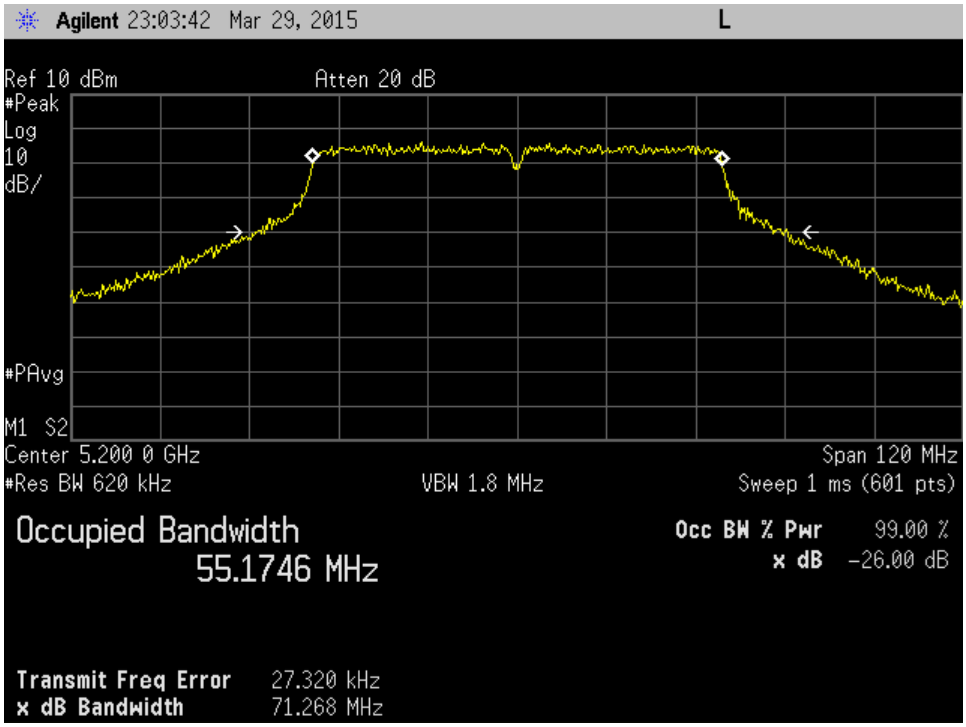
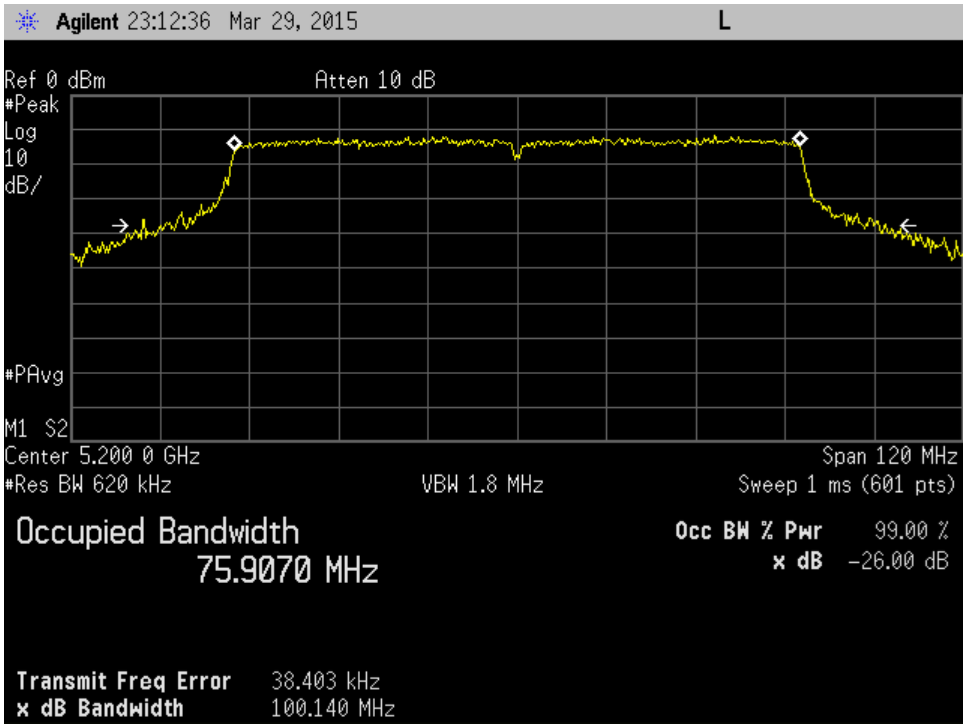
OCC BW_J7_mid ch_bw=40 MHz



OCC BW_J7_mid ch_bw=50 MHz



OCC BW_J7_mid ch_bw=60 MHz



OCC BW_J7_mid ch_bw=80 MHz