

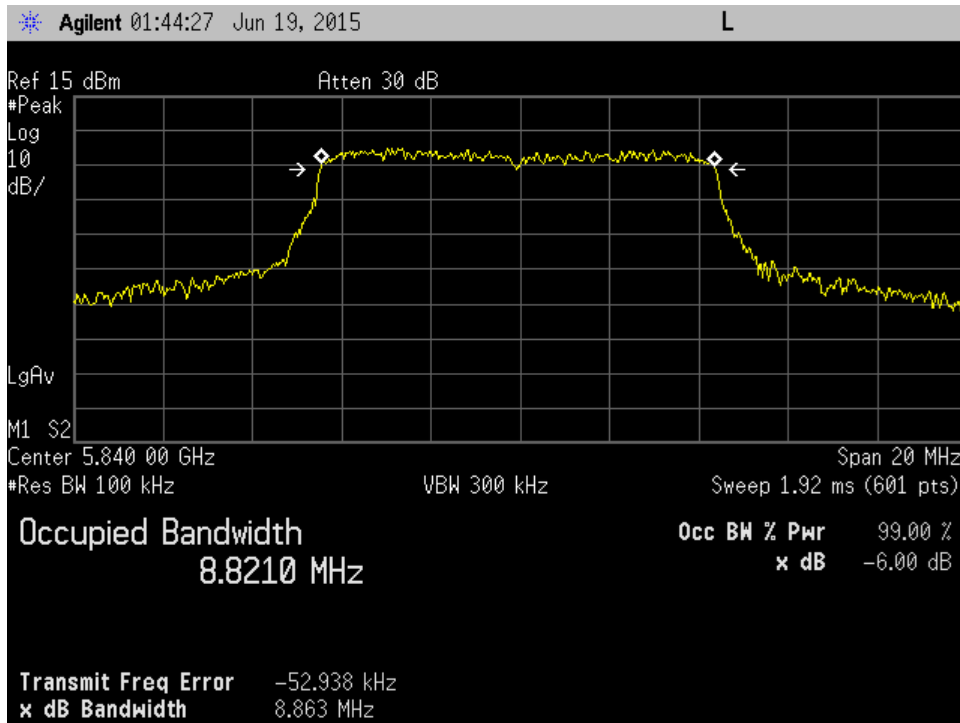


Annex C

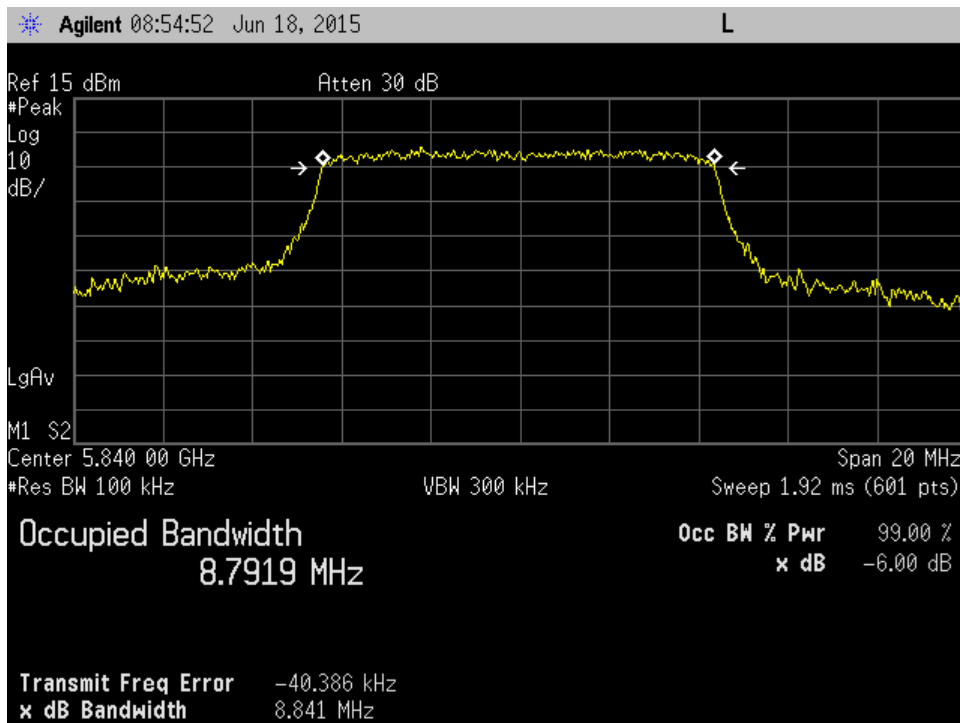
Occupied Bandwidth



10MHz high ch_J1

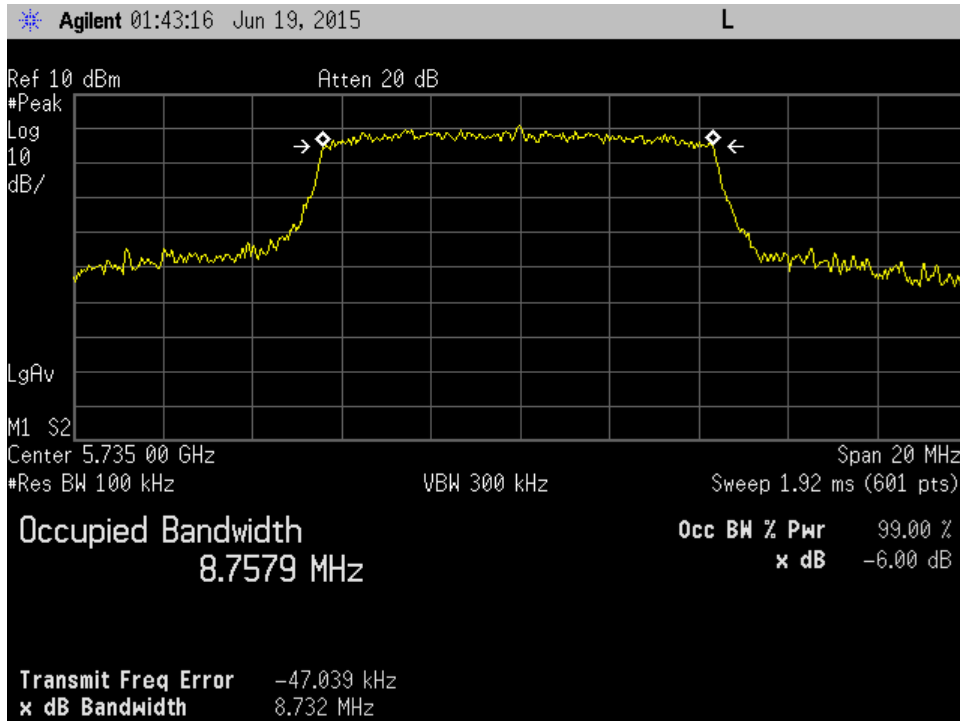


10MHz high ch_J3

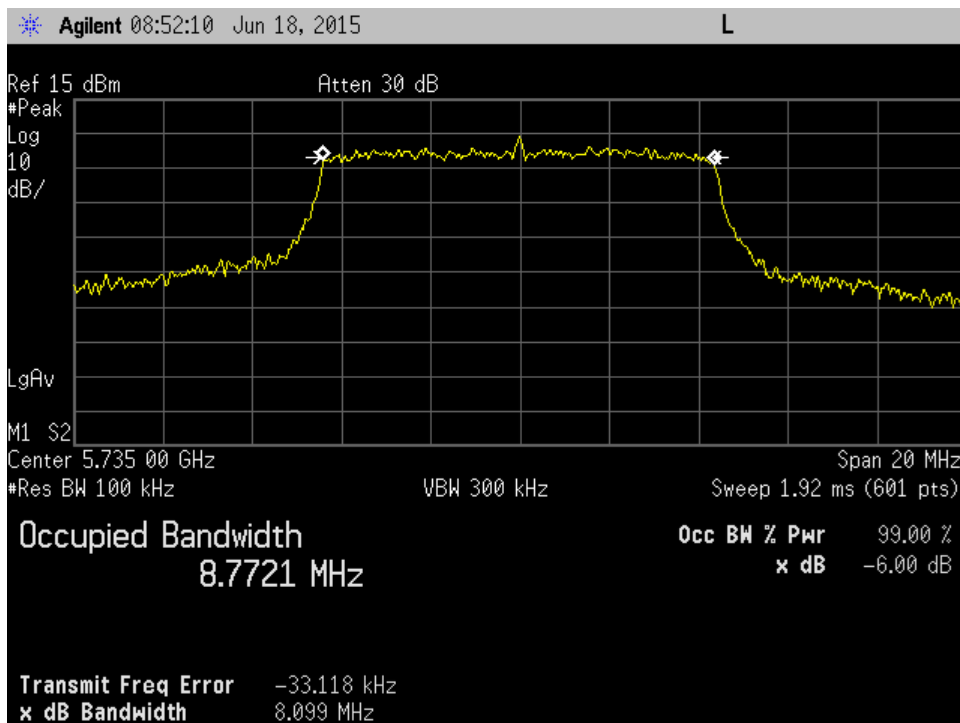




10MHz low ch_J1

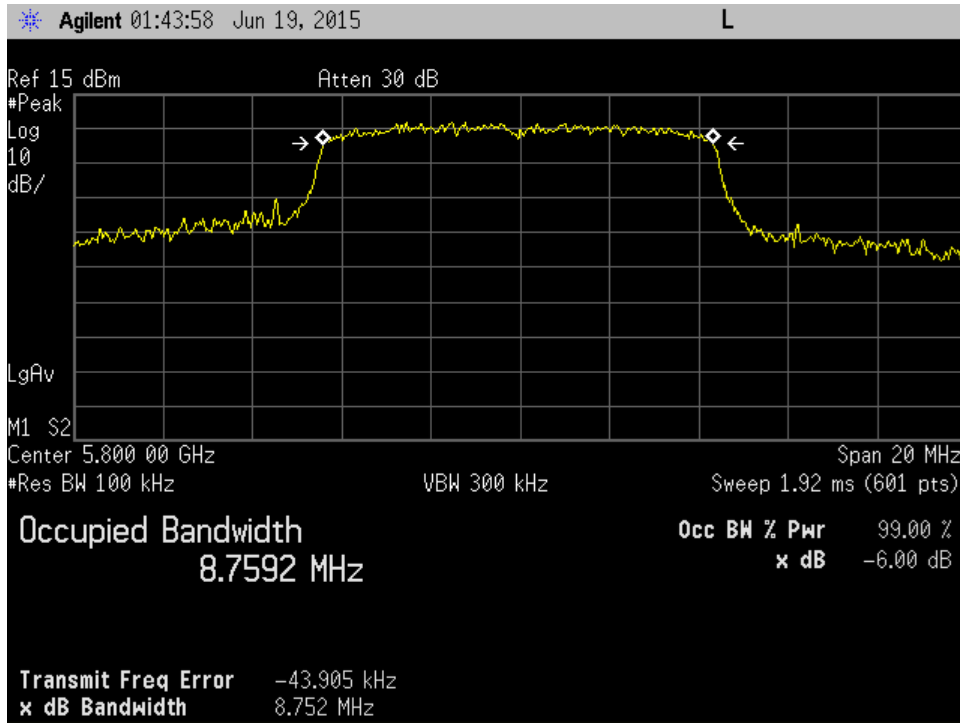


10MHz low ch_J3

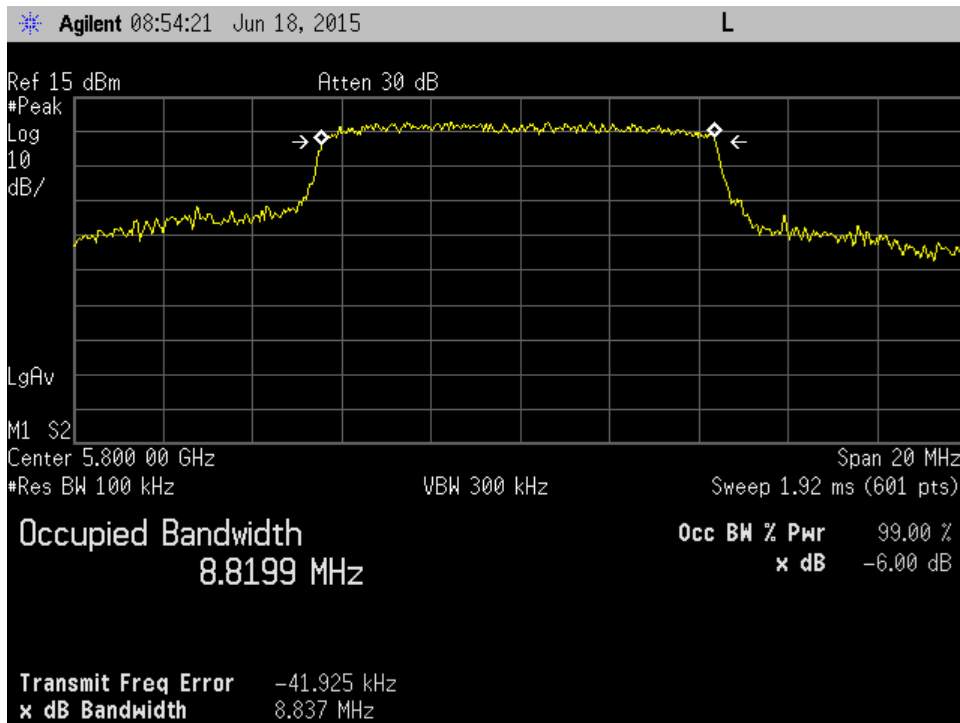




10MHz mid ch_J1

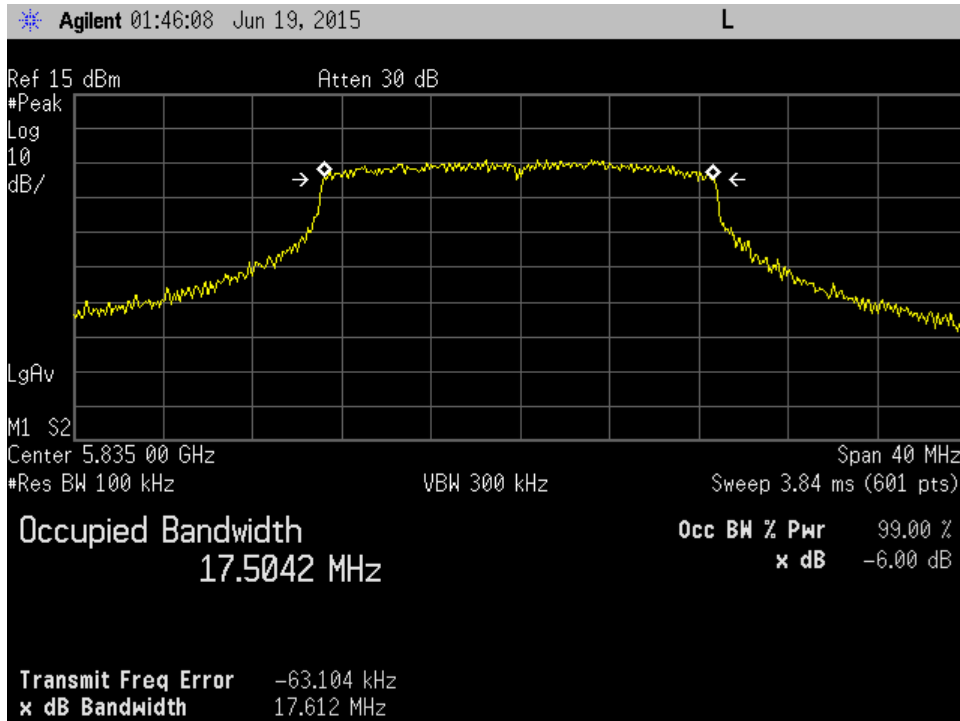


10MHz mid ch_J3

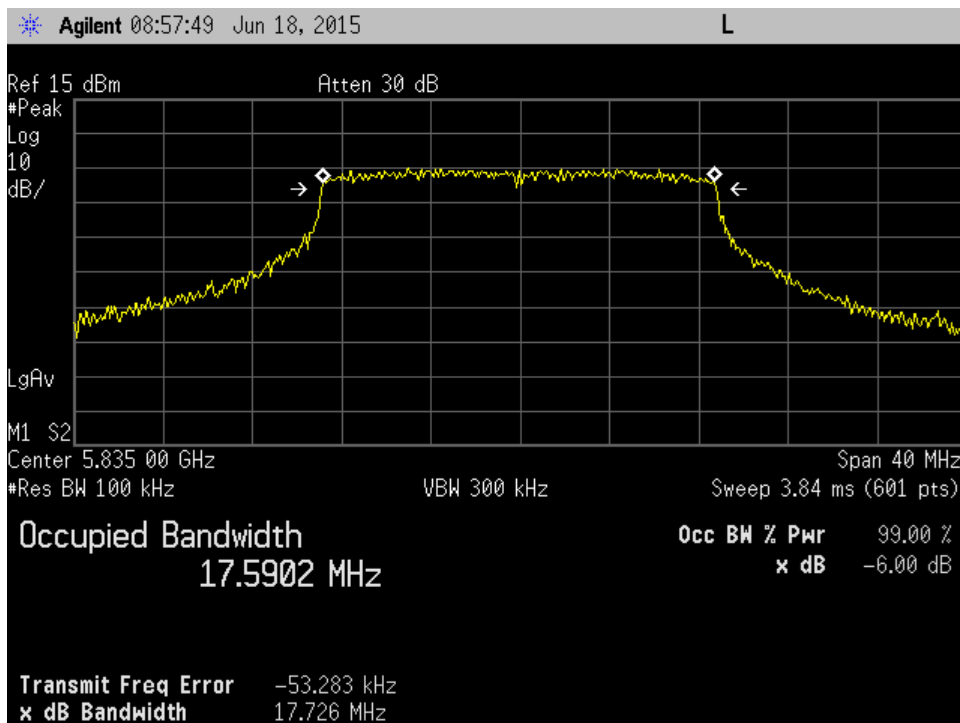




20MHz high ch_J1

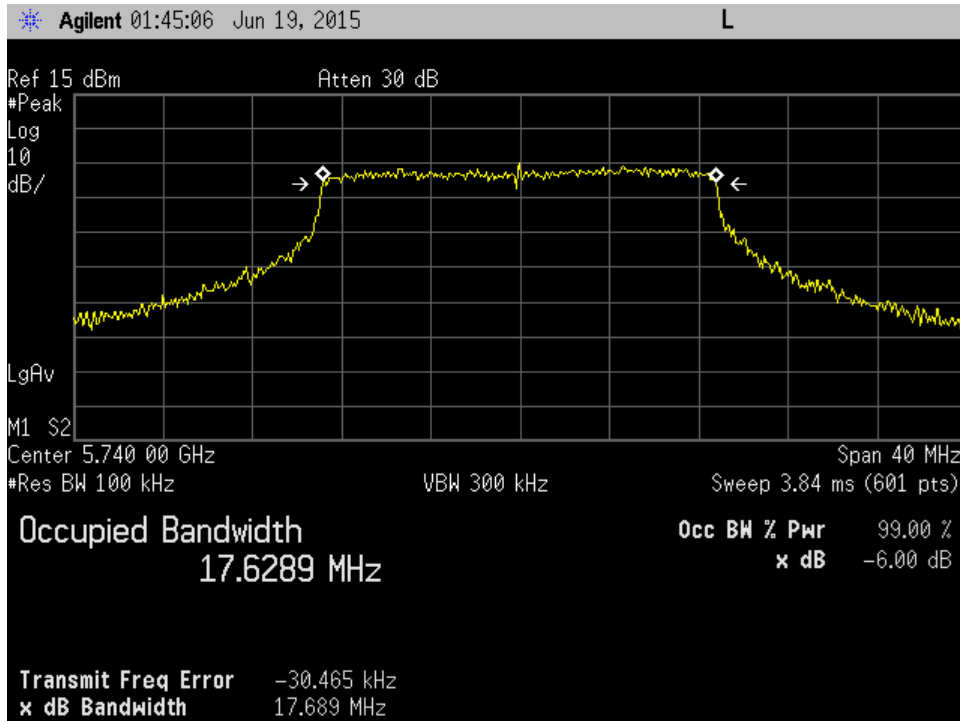


20MHz high ch_J3

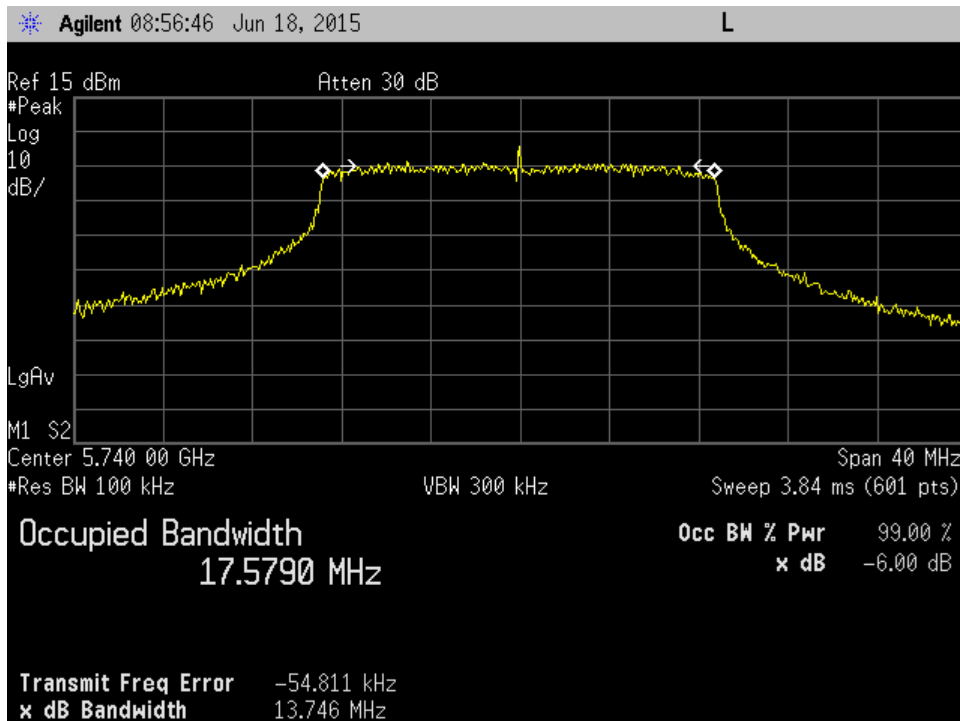




20MHz low ch_J1

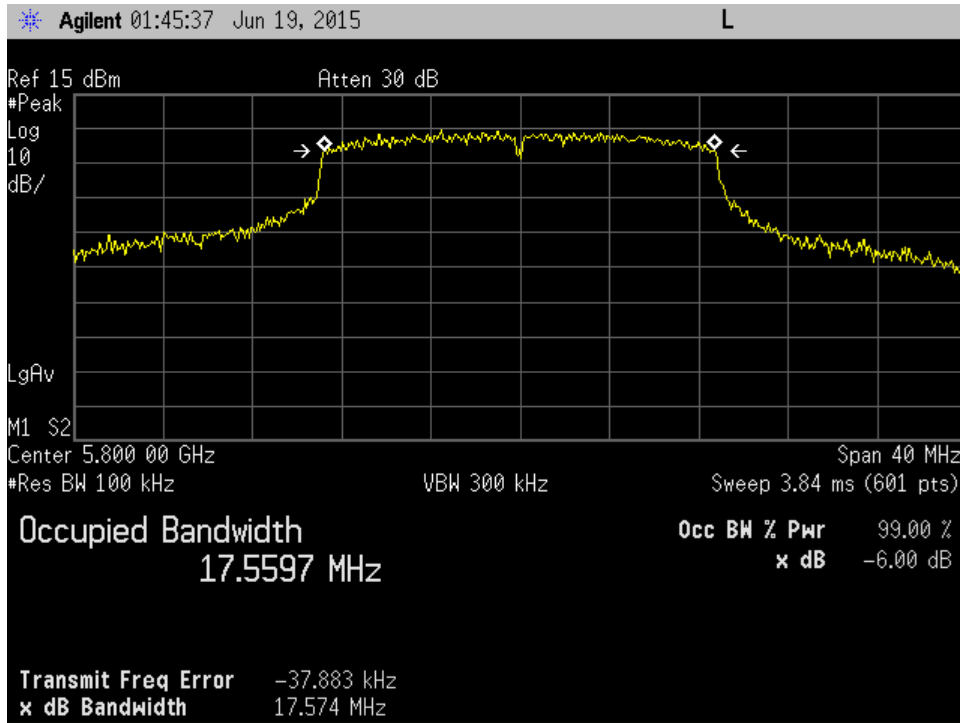


20MHz low ch_J3

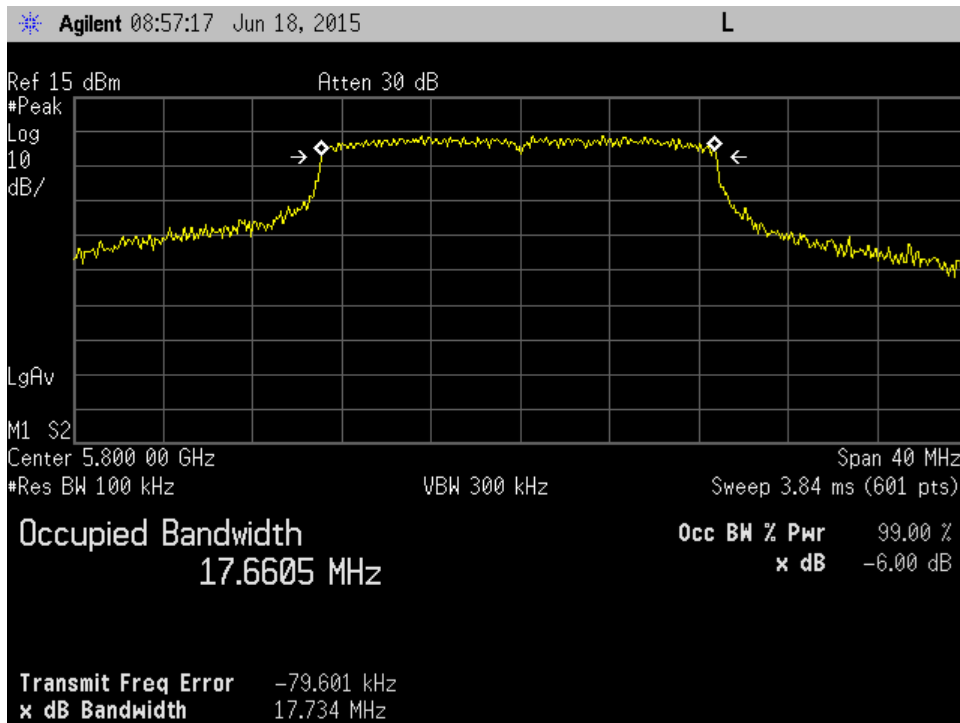




20MHz mid ch_J1

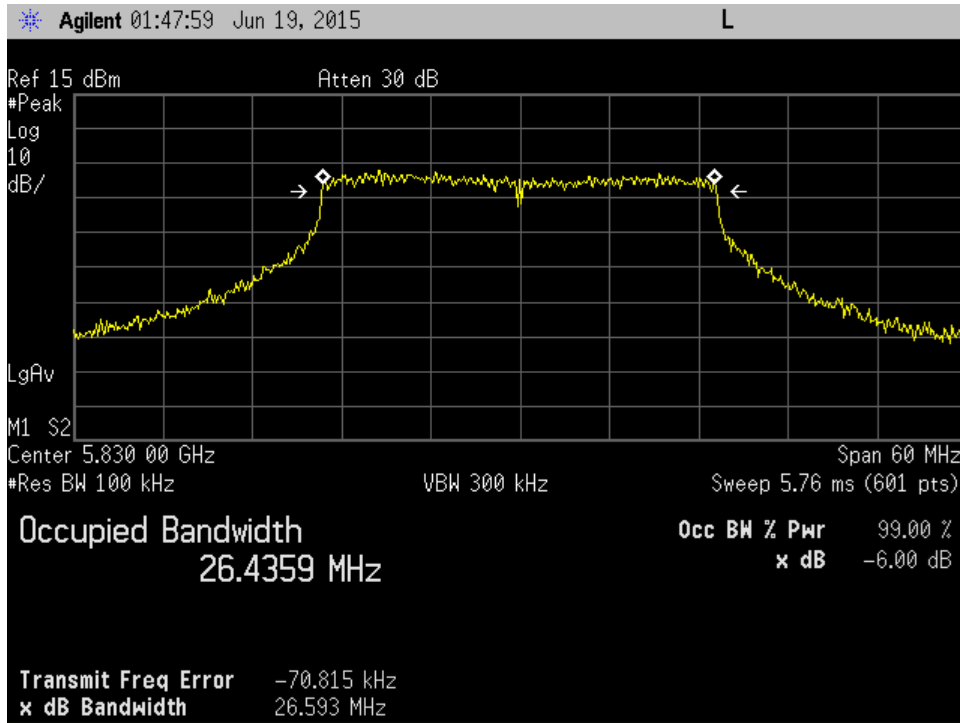


20MHz mid ch_J3

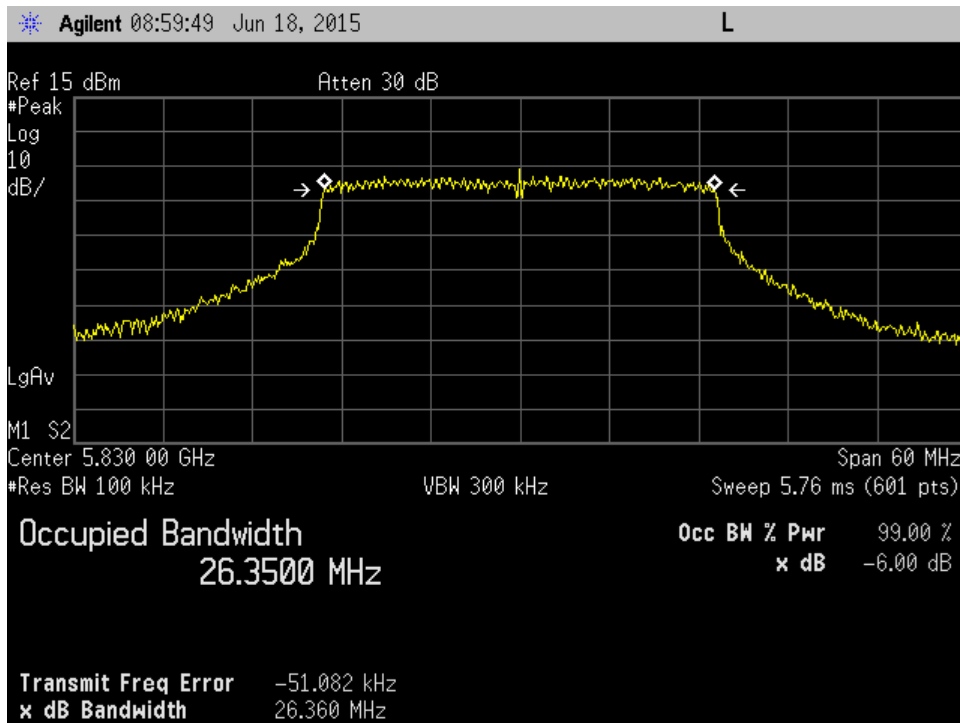




30MHz high ch_J1

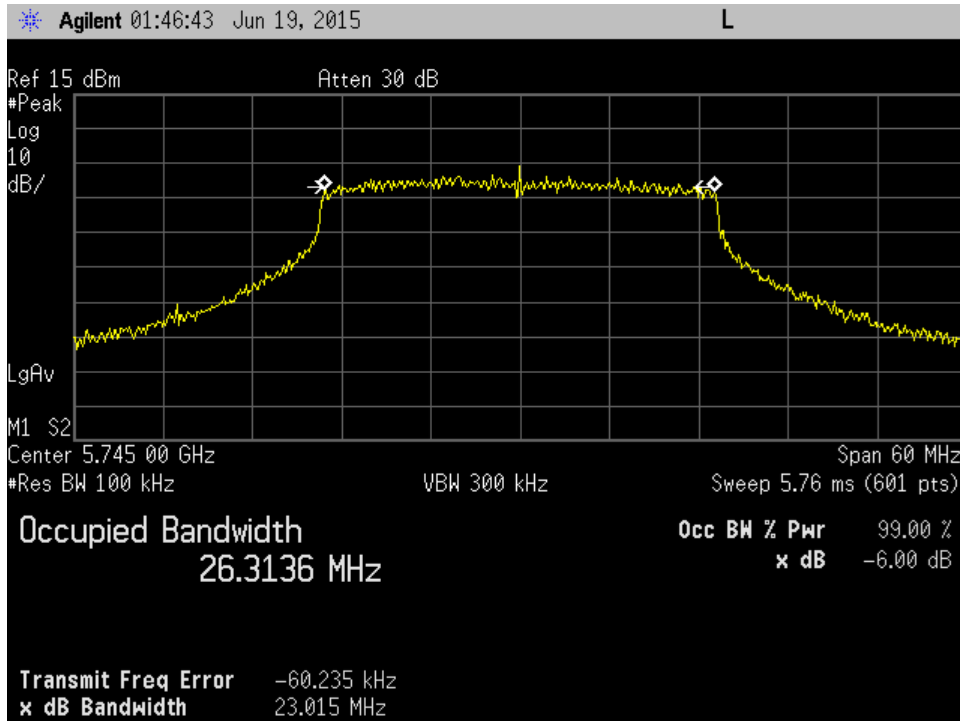


30MHz high ch_J3

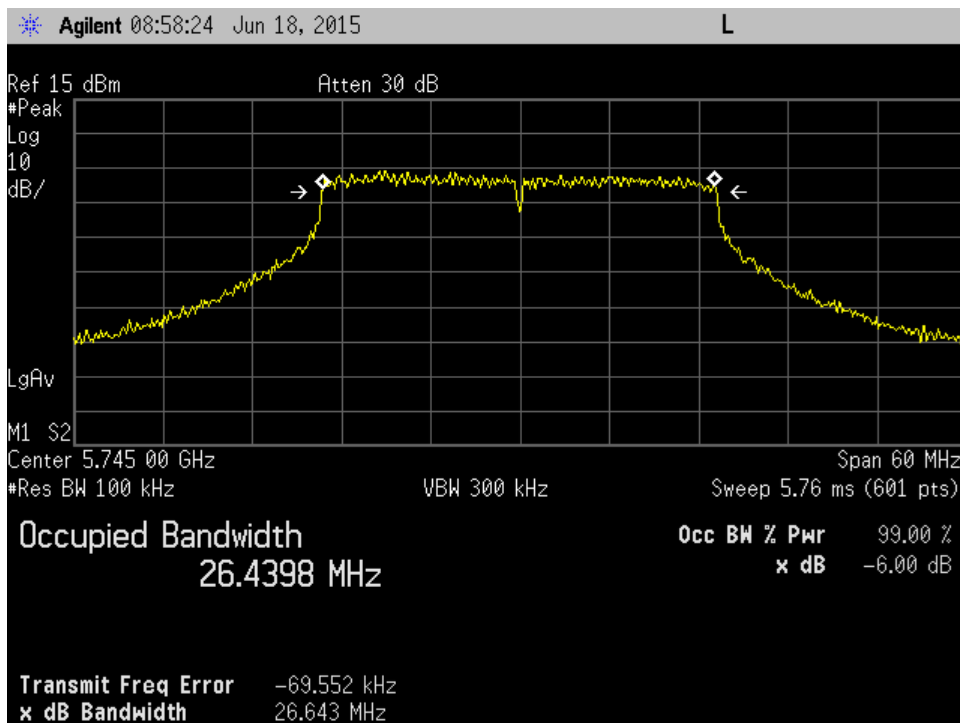




30MHz low ch_J1

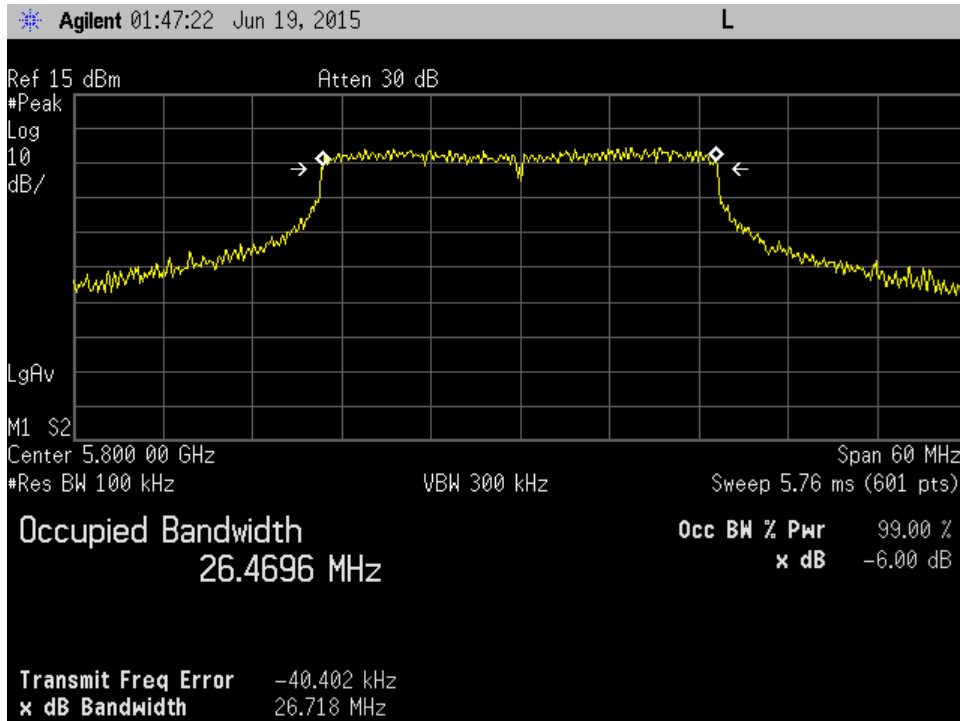


30MHz low ch_J3

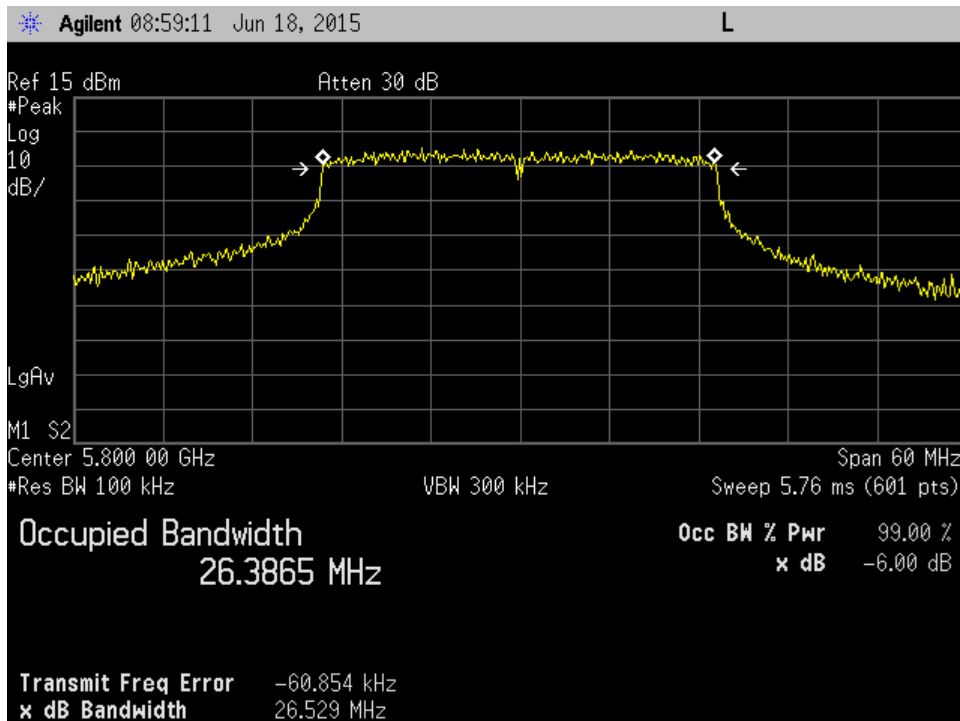




30MHz mid ch_J1

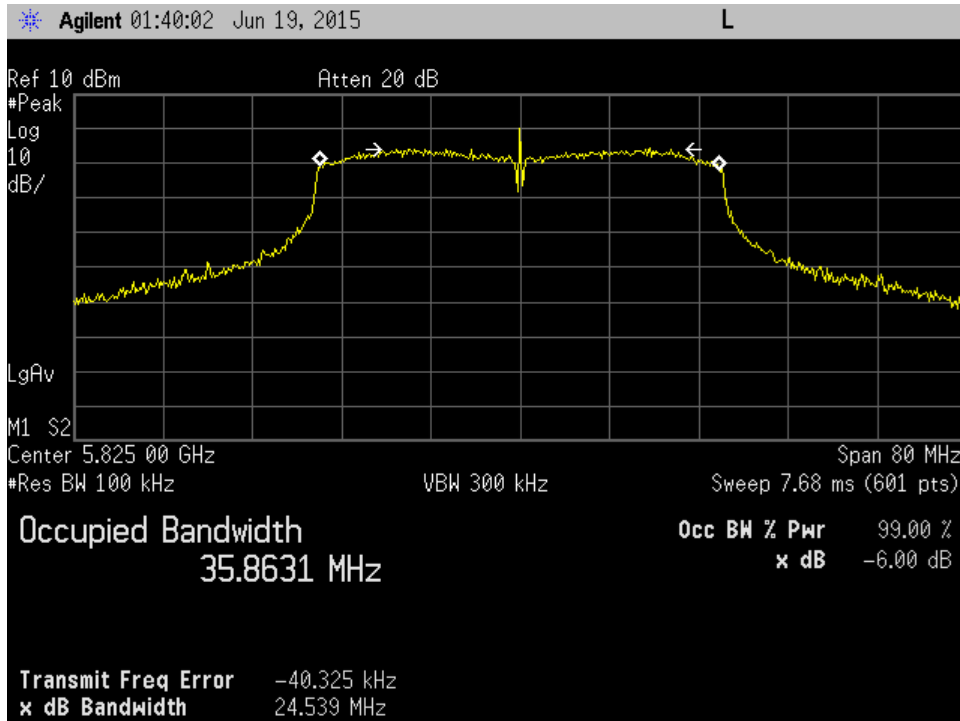


30MHz mid ch_J3

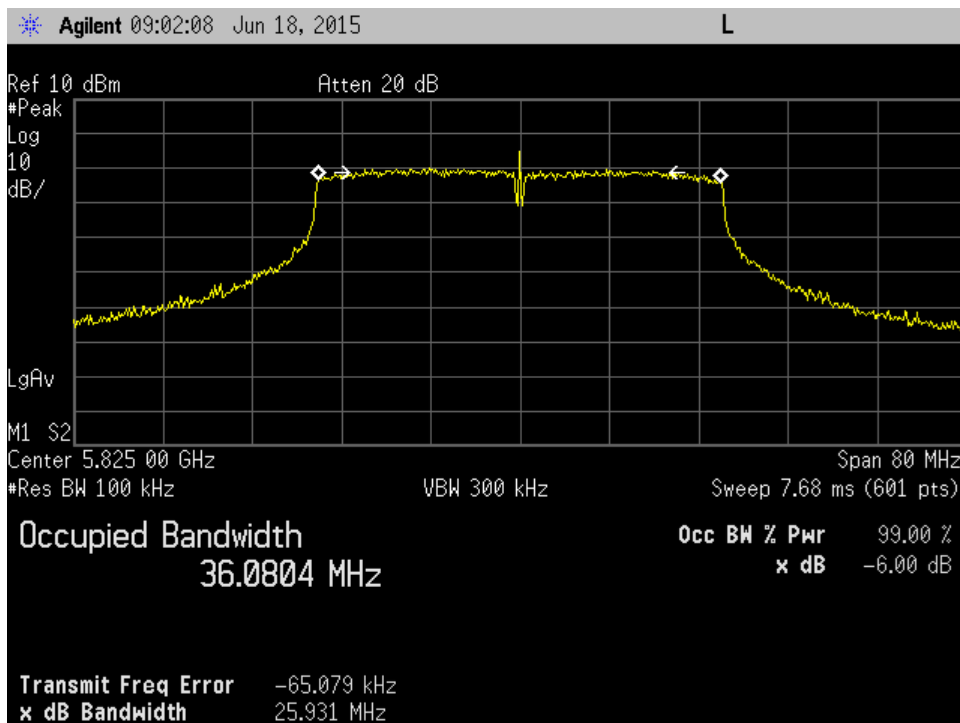




40MHz high ch_J1

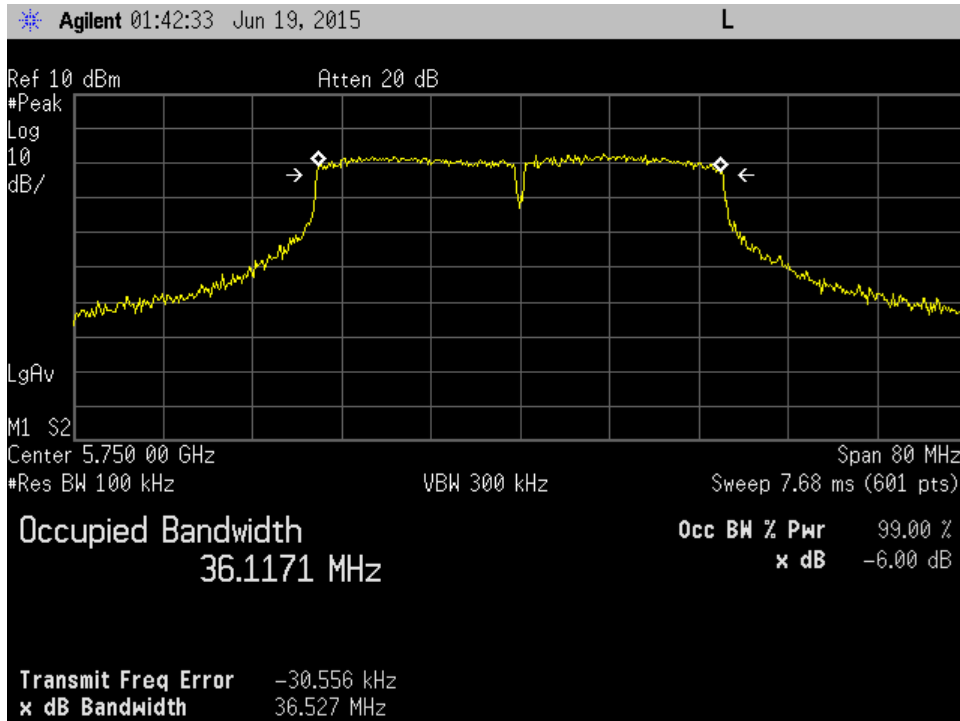


40MHz high ch_J3

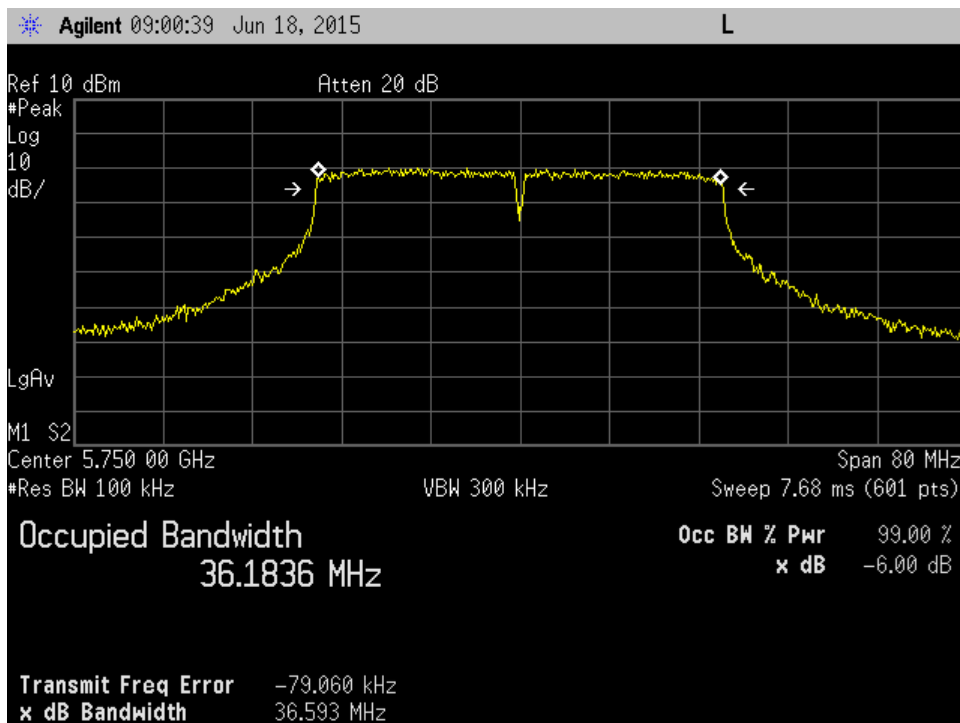




40MHz low ch_J1

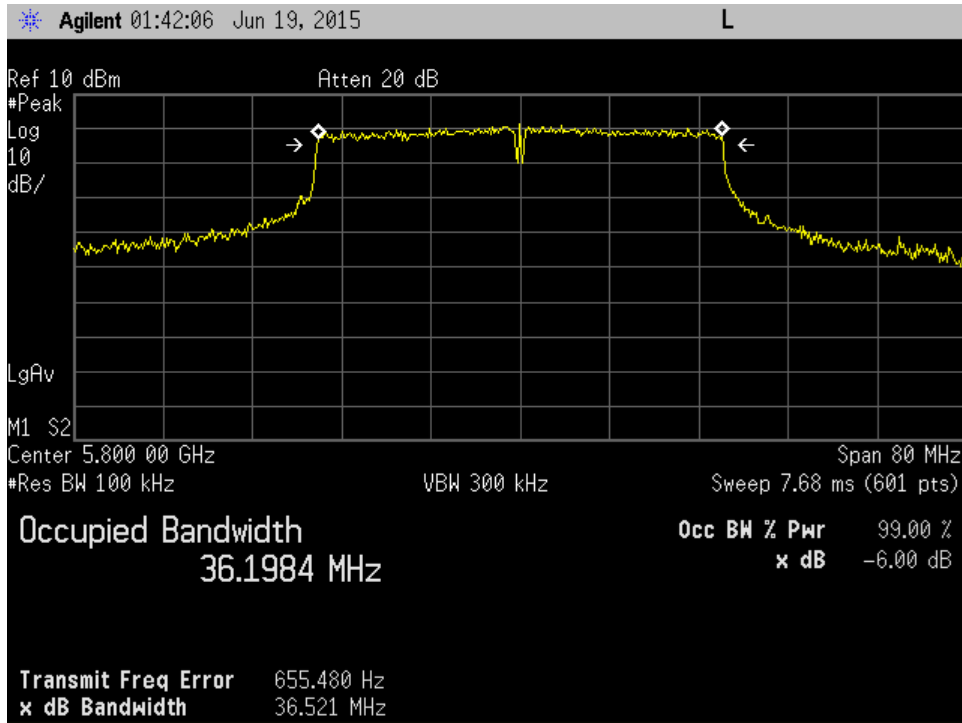


40MHz low ch_J3





40MHz mid ch_J1



40MHz mid ch_J3

