

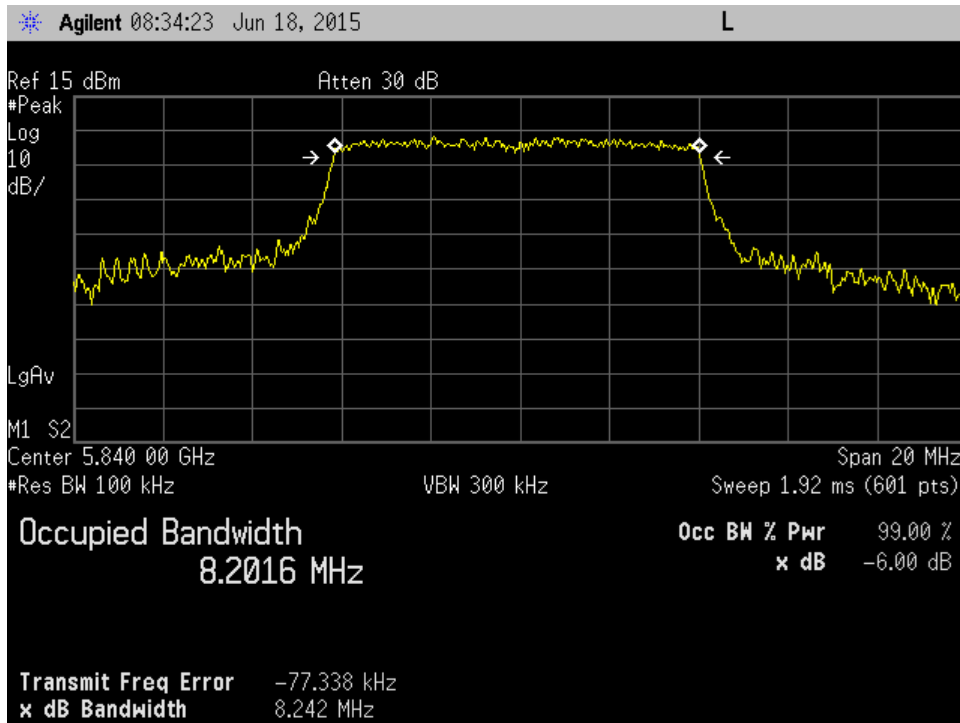


Annex C

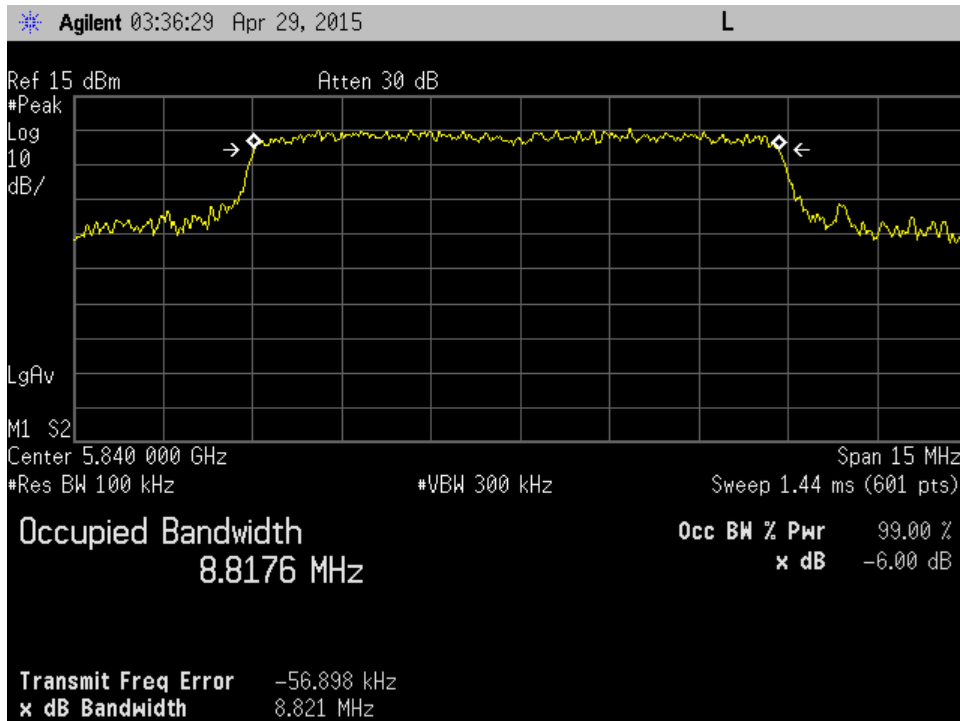
Occupied Bandwidth



10 MHz high ch_J1

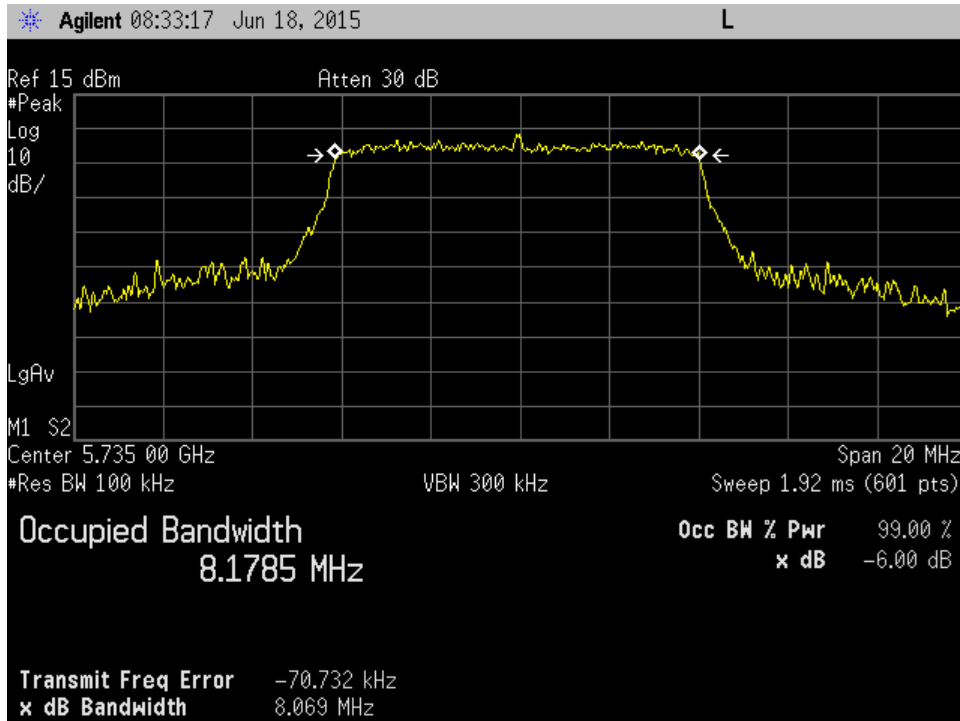


10 MHz high ch_J3

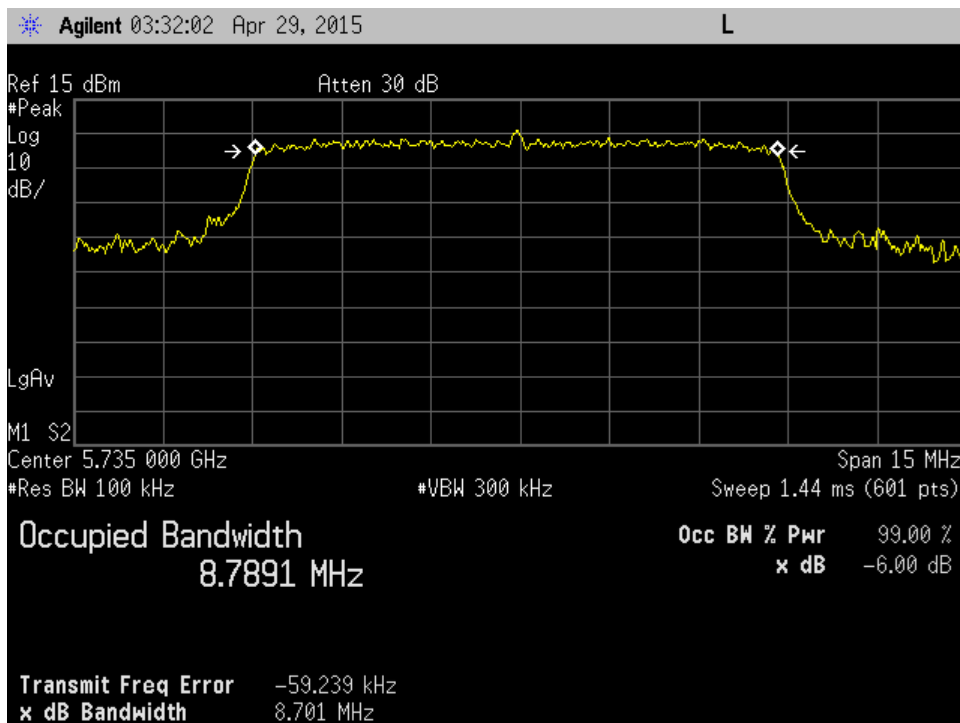




10 MHz low ch_J1

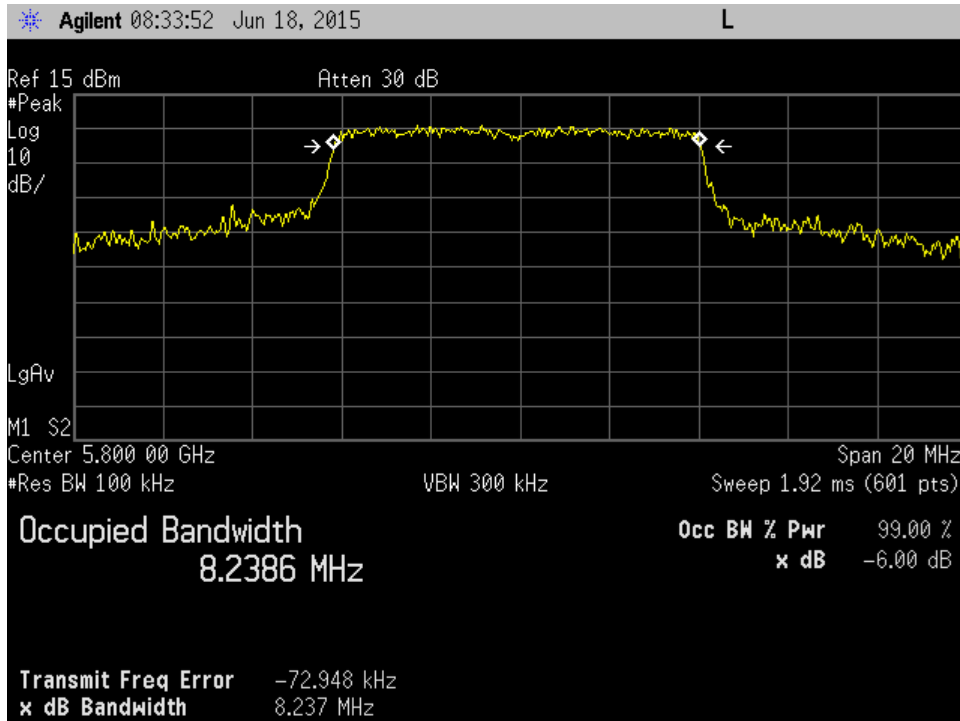


10 MHz low ch_J3

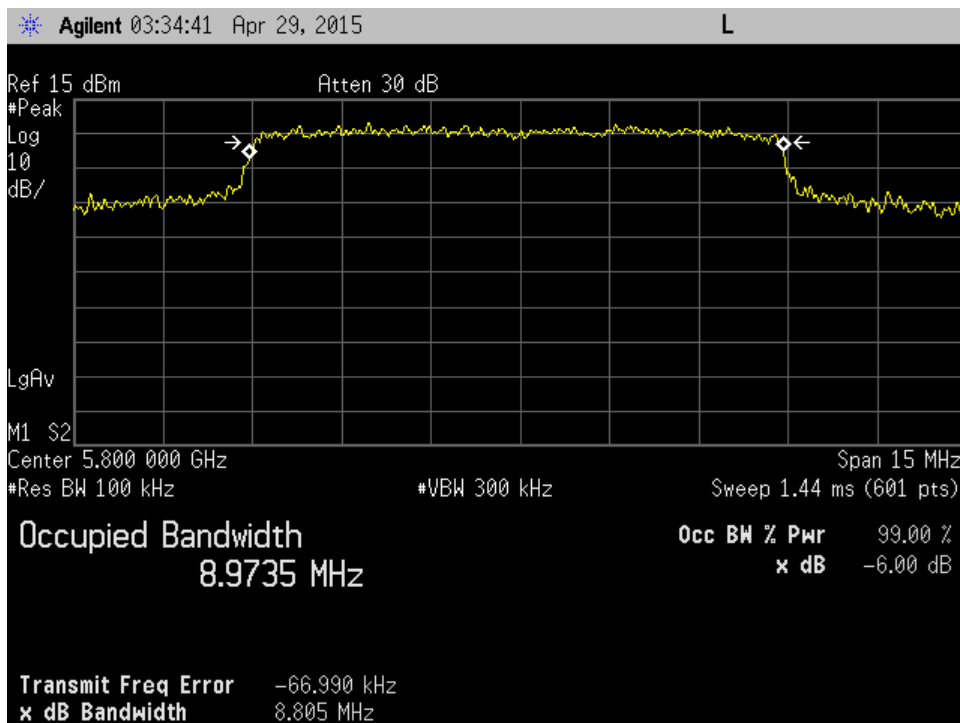




10 MHz mid ch_J1

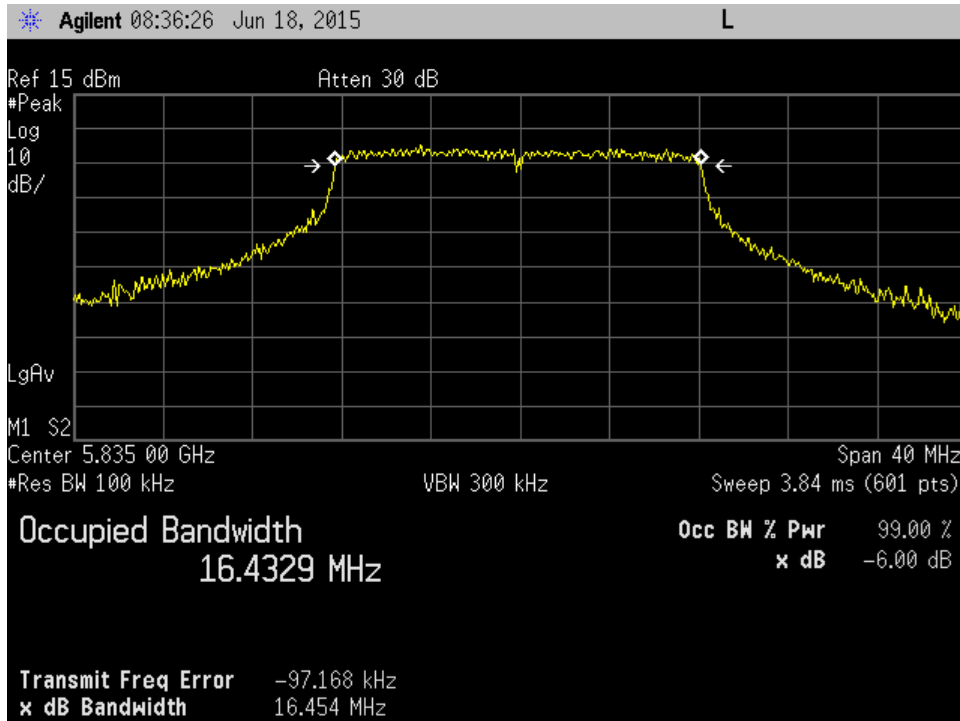


10 MHz mid ch_J3

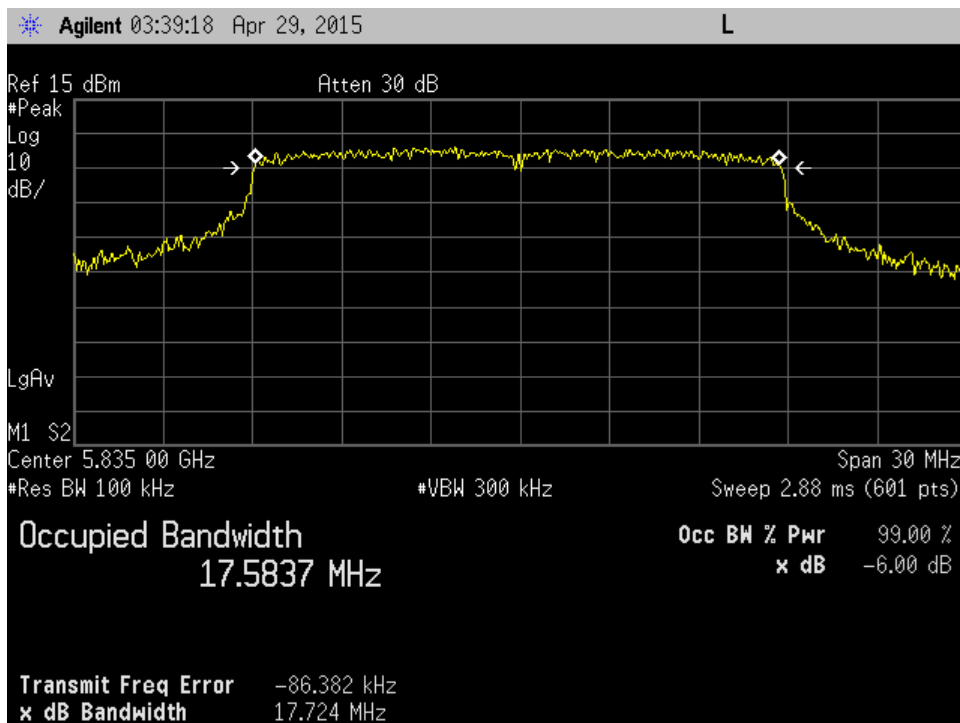




20 MHz high ch_J1

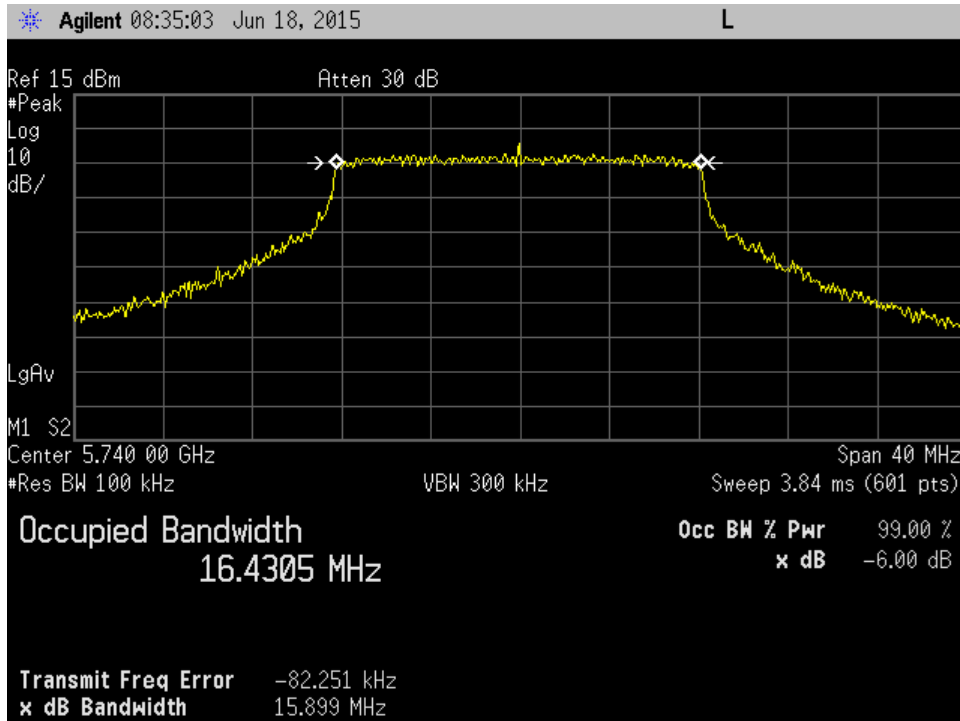


20 MHz high ch_J3

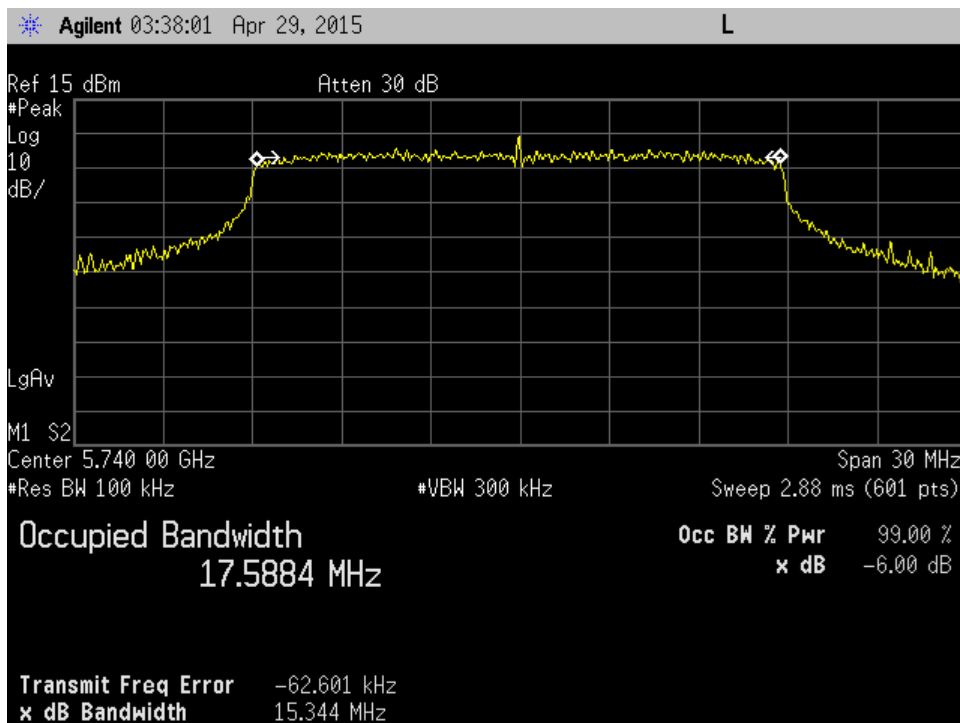




20 MHz low ch_J1

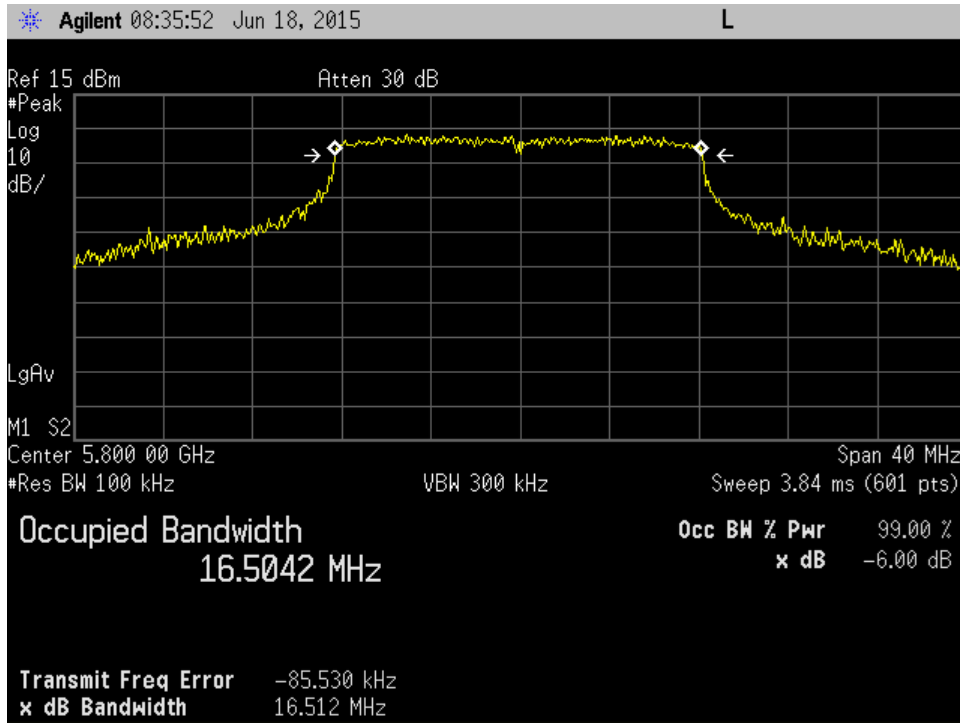


20 MHz low ch_J3

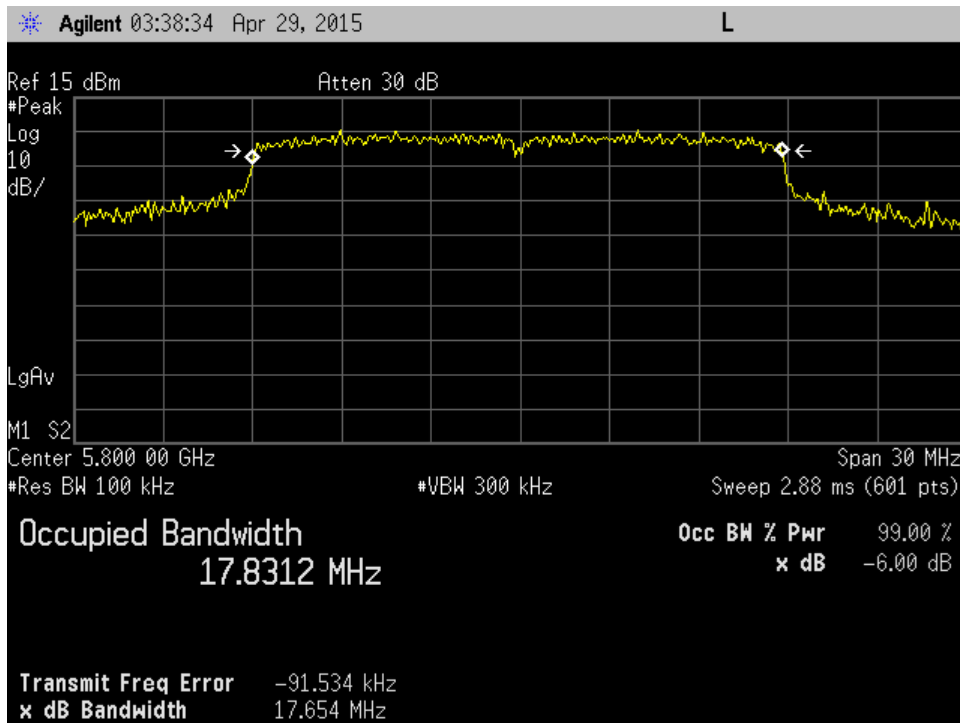




20 MHz mid ch_J1

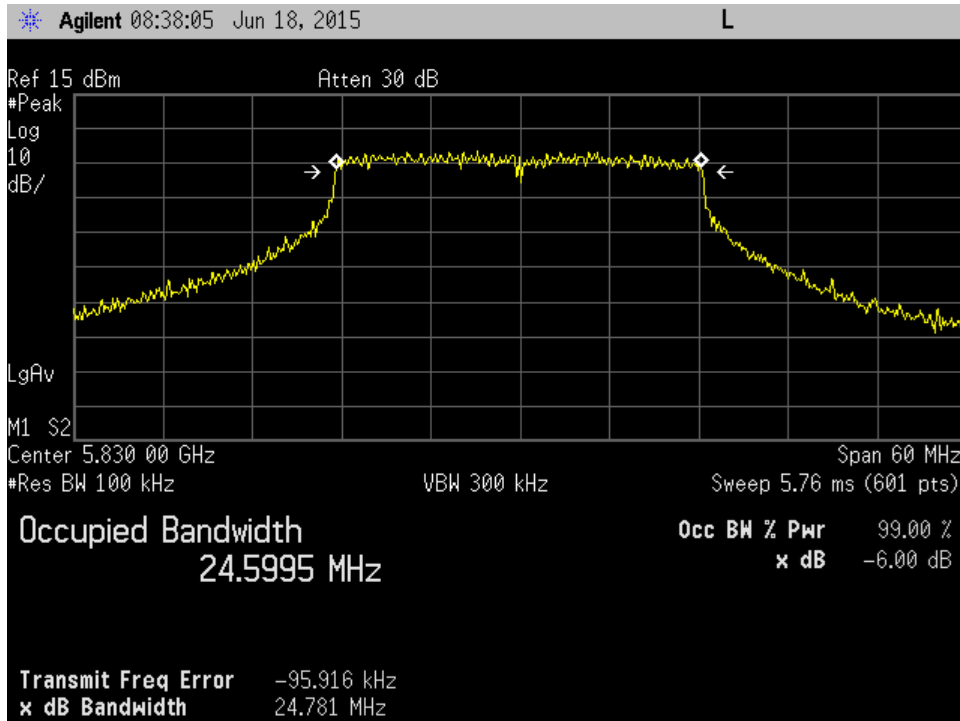


20 MHz mid ch_J3

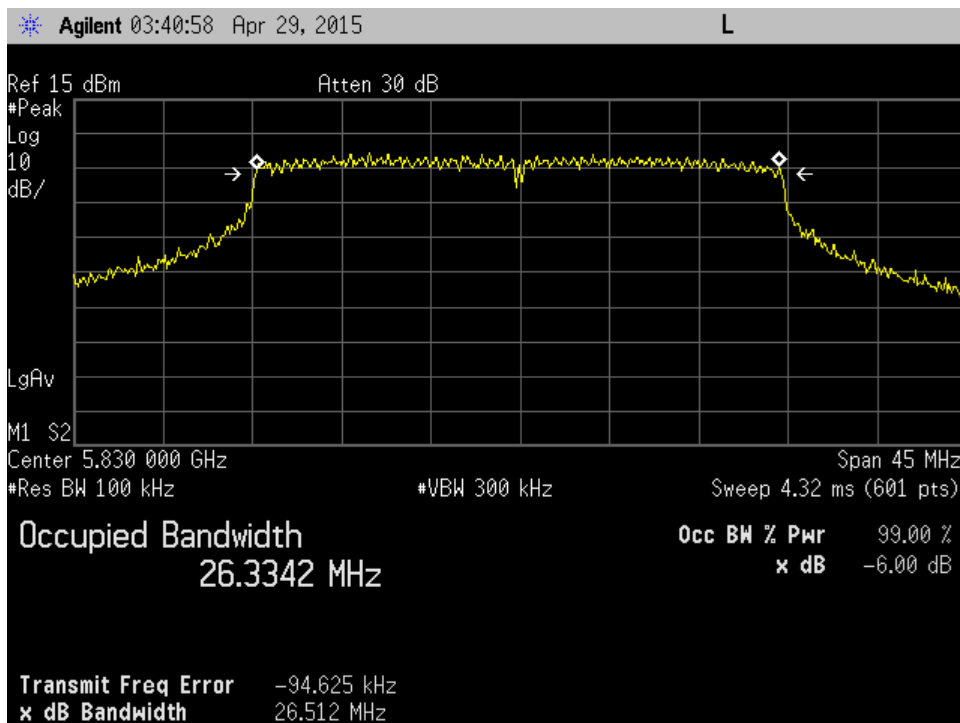




30 MHz high ch_J1

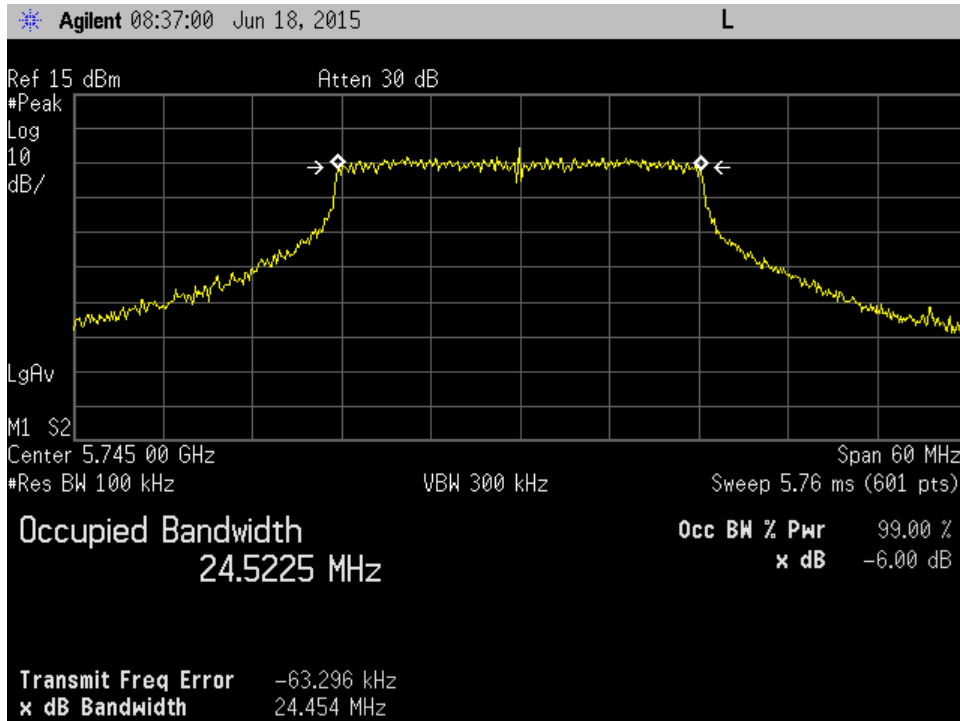


30 MHz high ch_J3

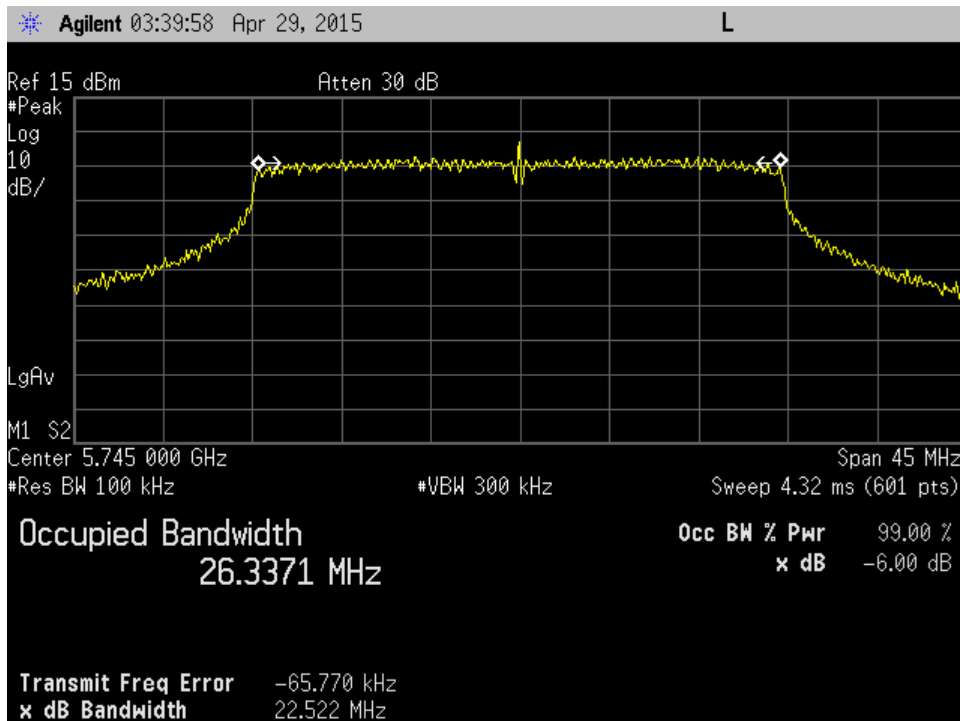




30 MHz low ch_J1

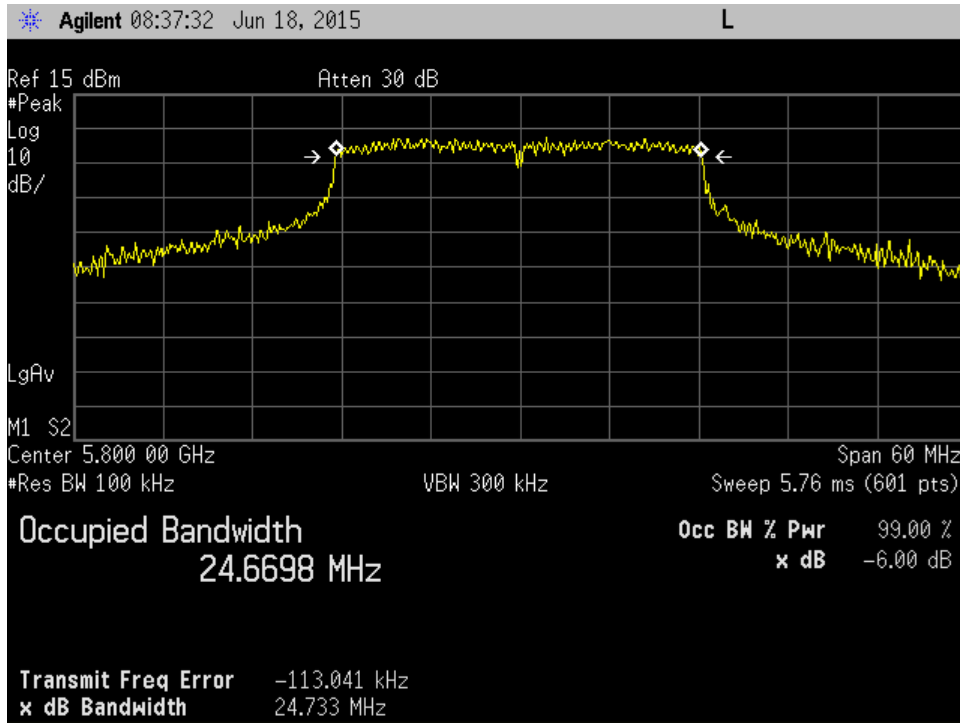


30 MHz low ch_J3

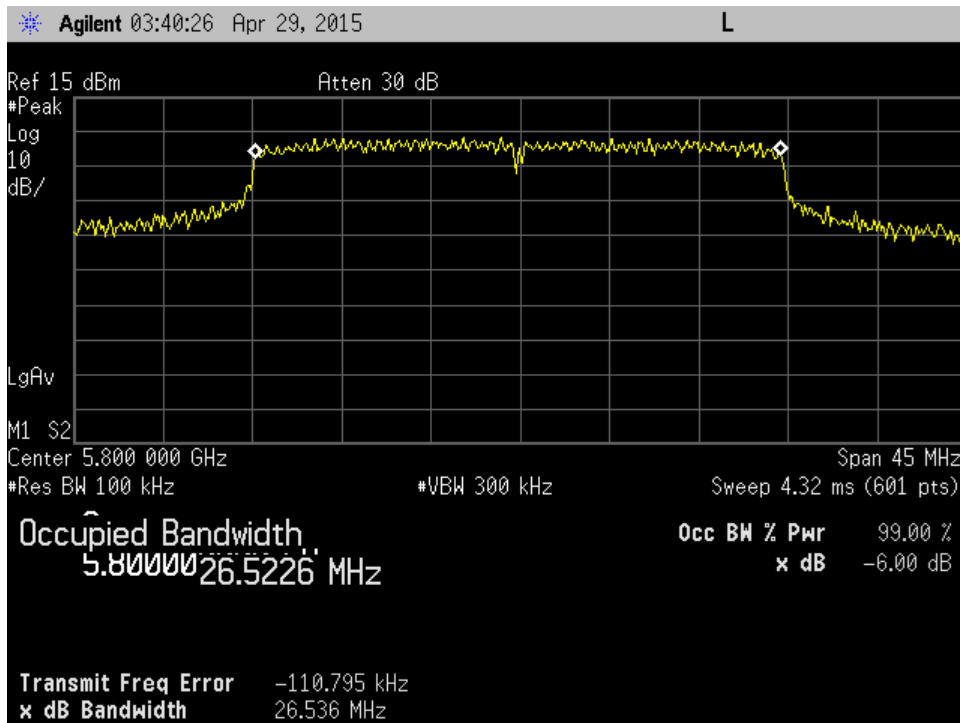




30 MHz mid ch_J1

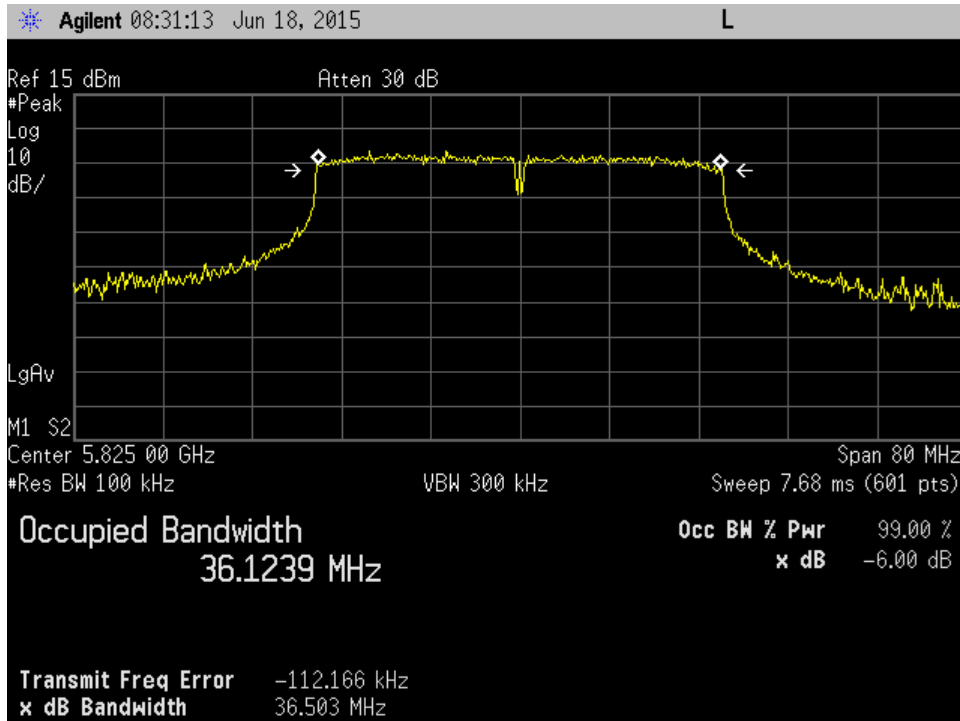


30 MHz mid ch_J3

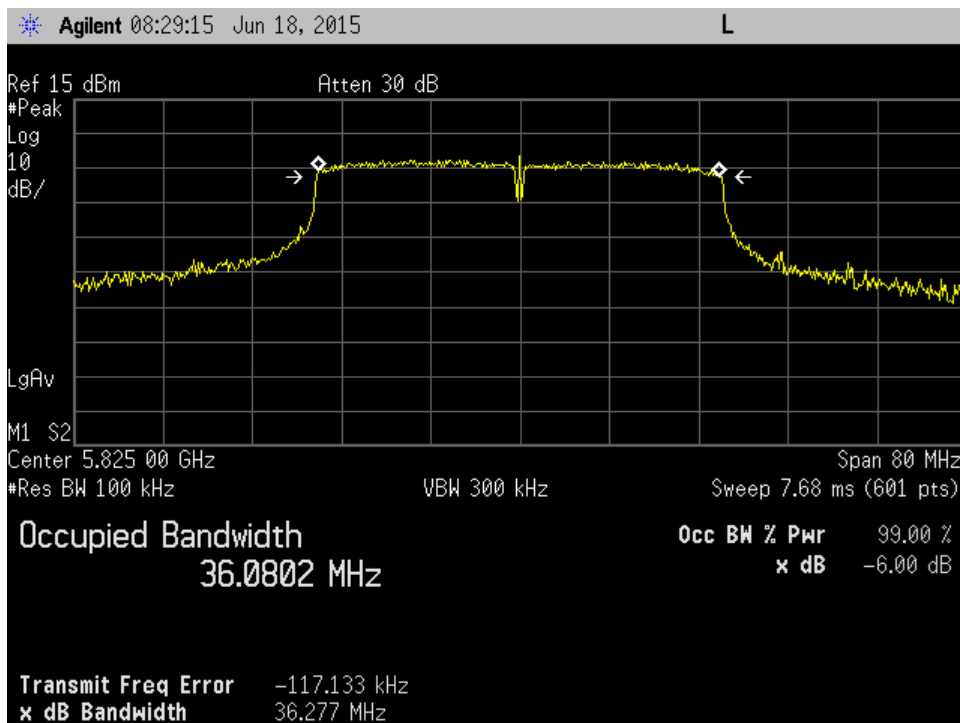




40 MHz high ch_J1

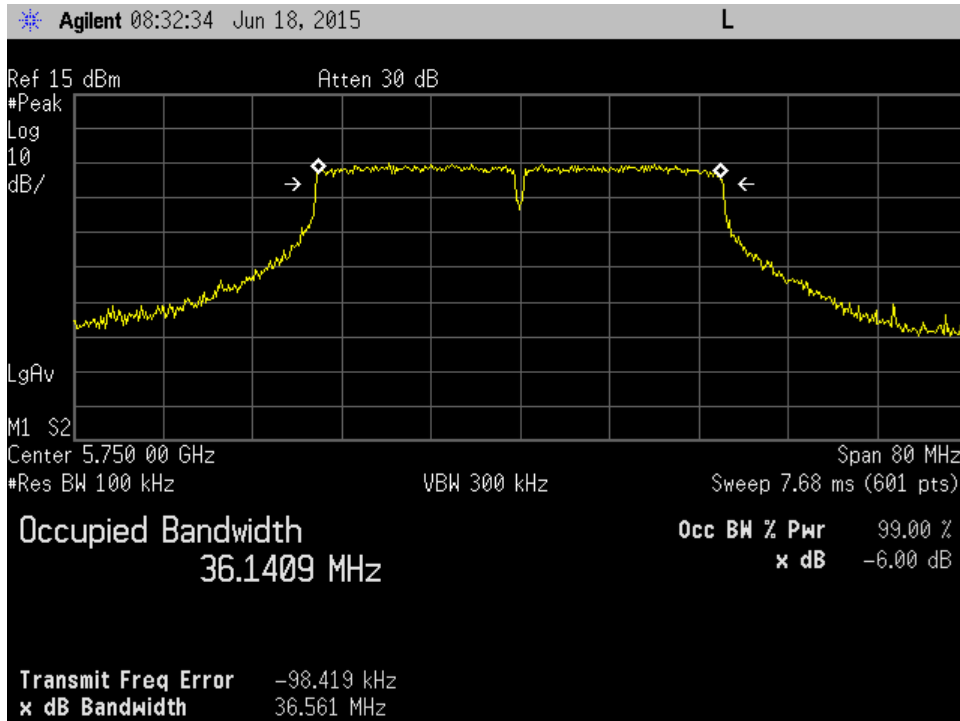


40 MHz high ch_J3

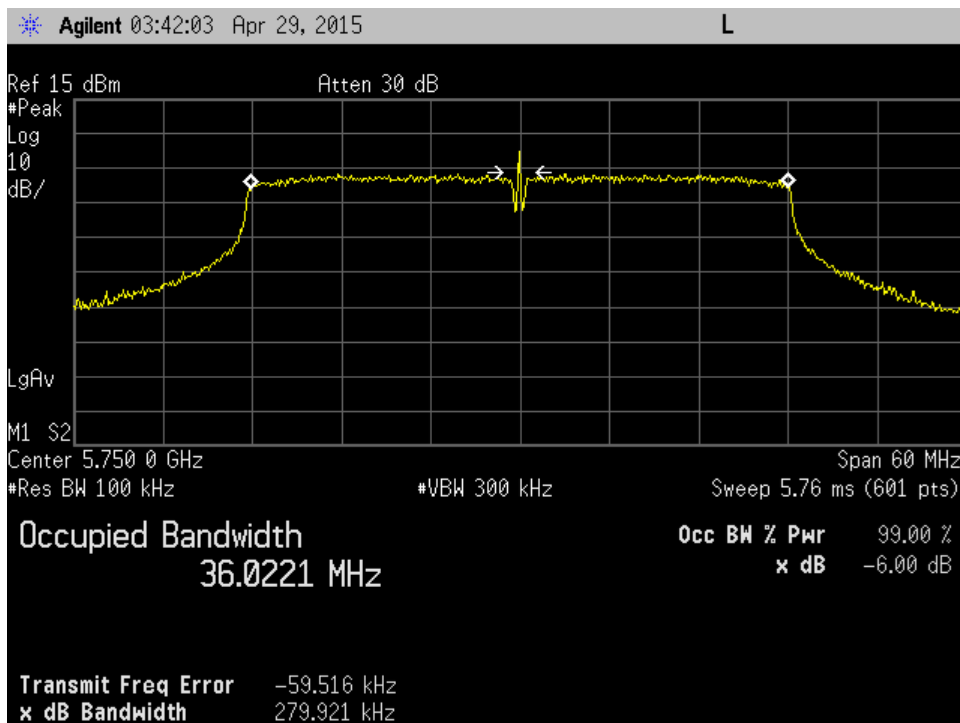




40 MHz low ch_J1

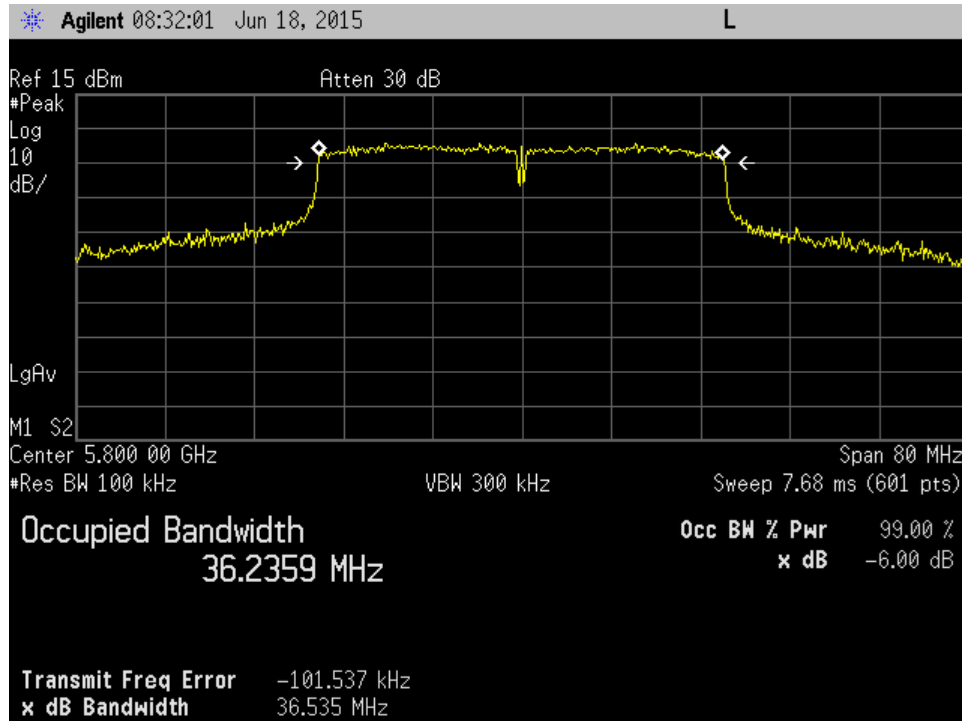


40 MHz low ch_J3_NEEDS ATTEN





40 MHz mid ch_J1



40 MHz mid ch_J3

