



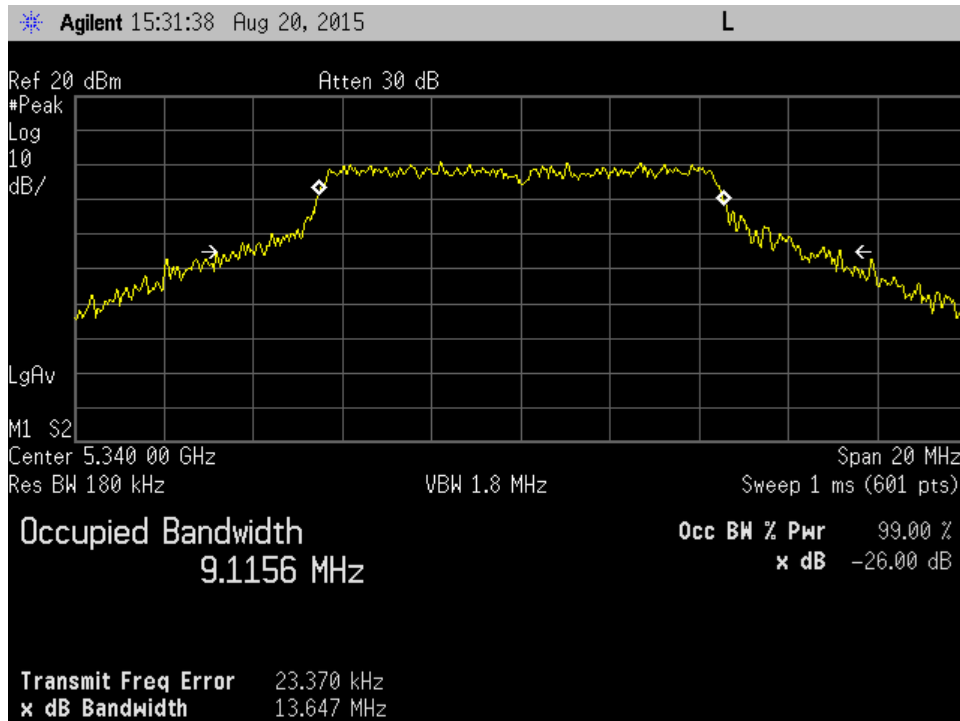
# **Annex C**

# **Occupied Bandwidth**

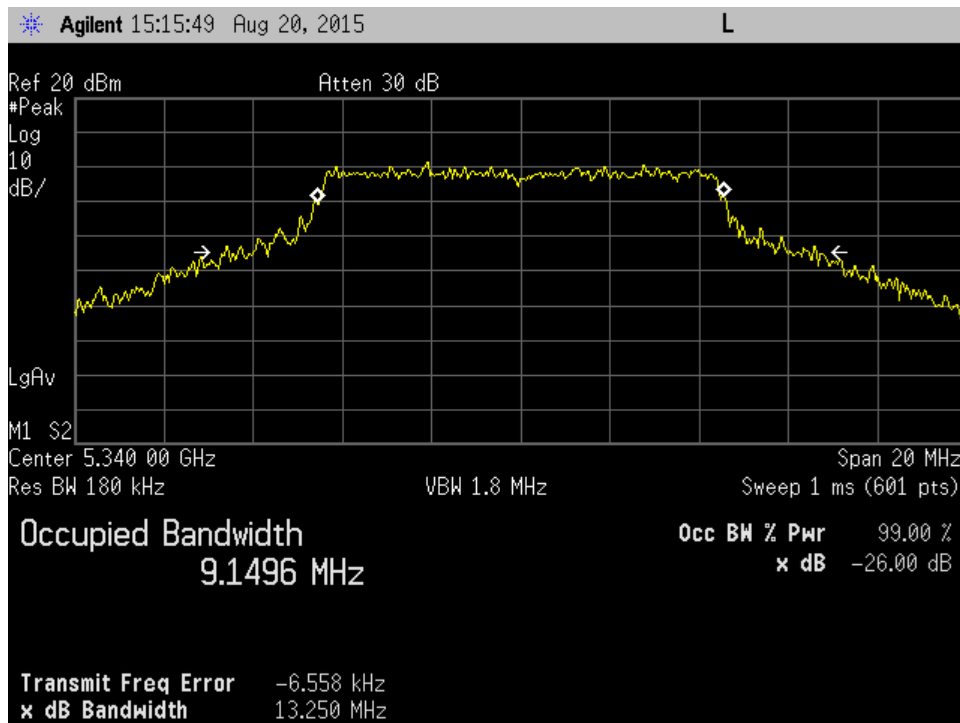


### UNII-2A

#### 10 MHz high ch\_J5

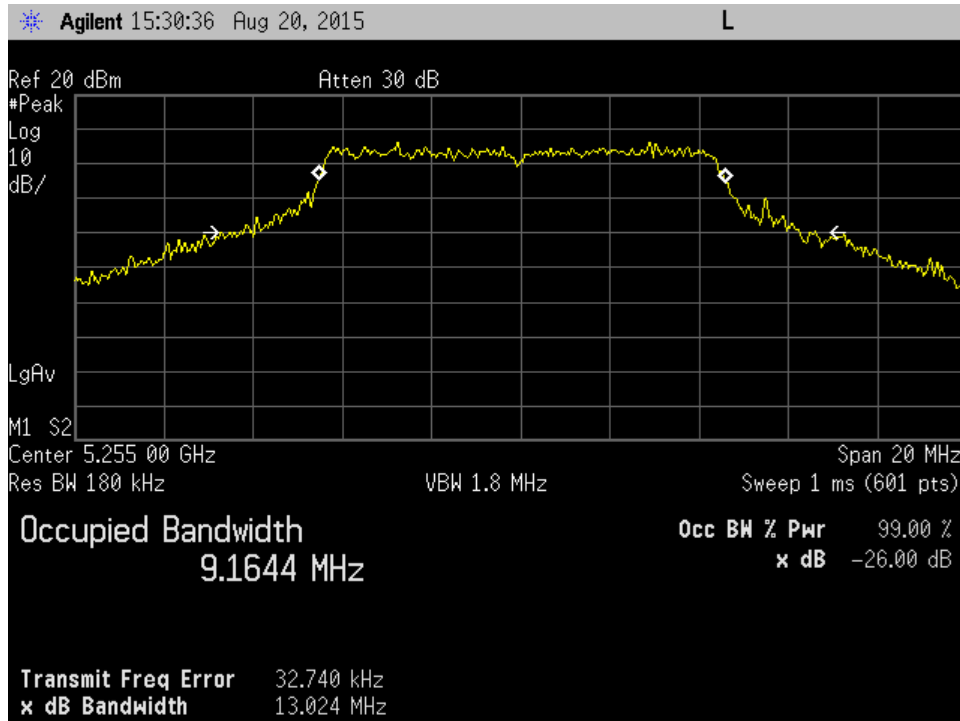


#### 10 MHz high ch\_J9

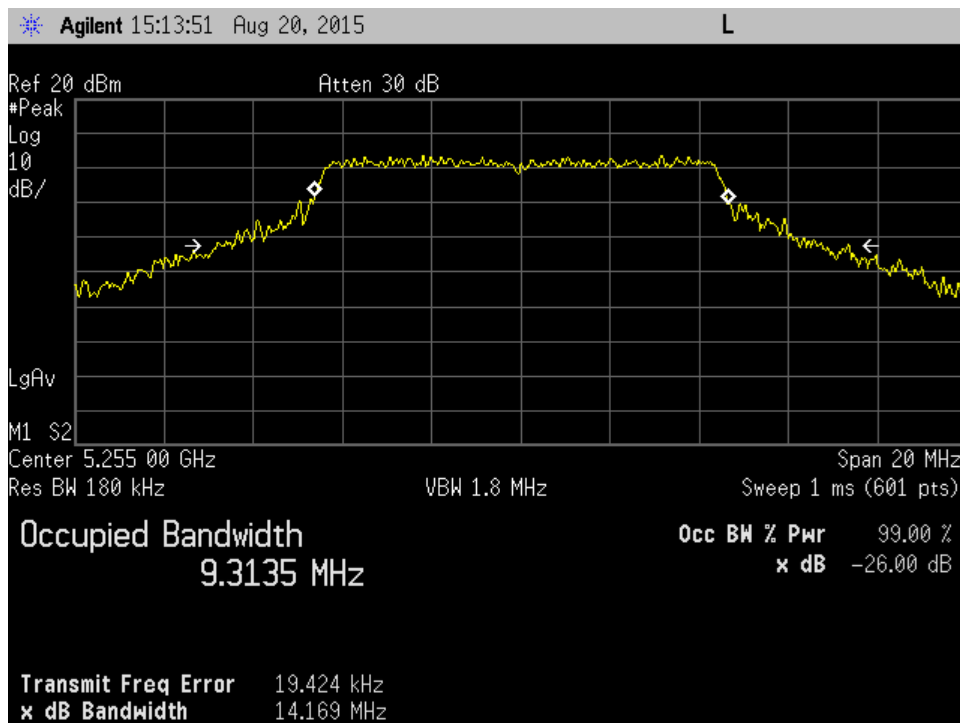




### 10 MHz low ch\_J5

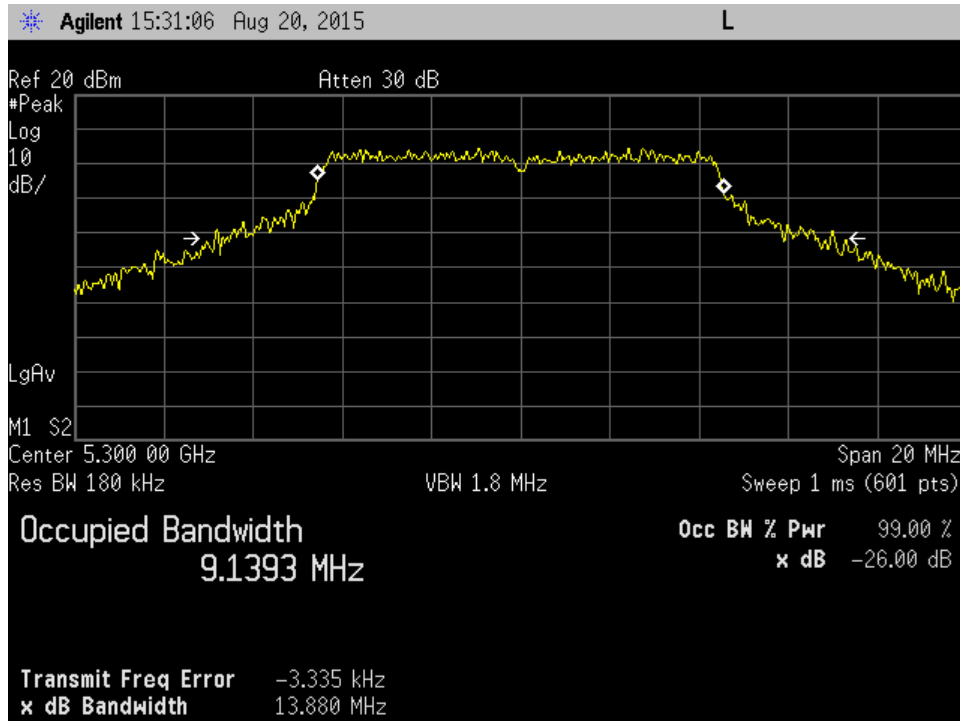


### 10 MHz low ch\_J9

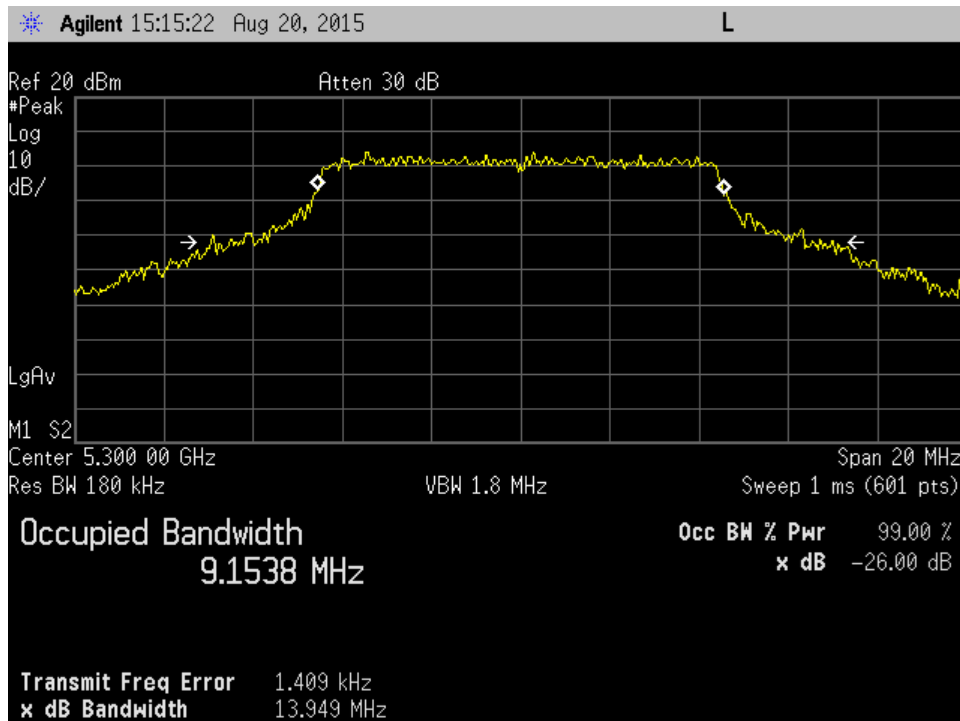




### 10 MHz mid ch\_J5

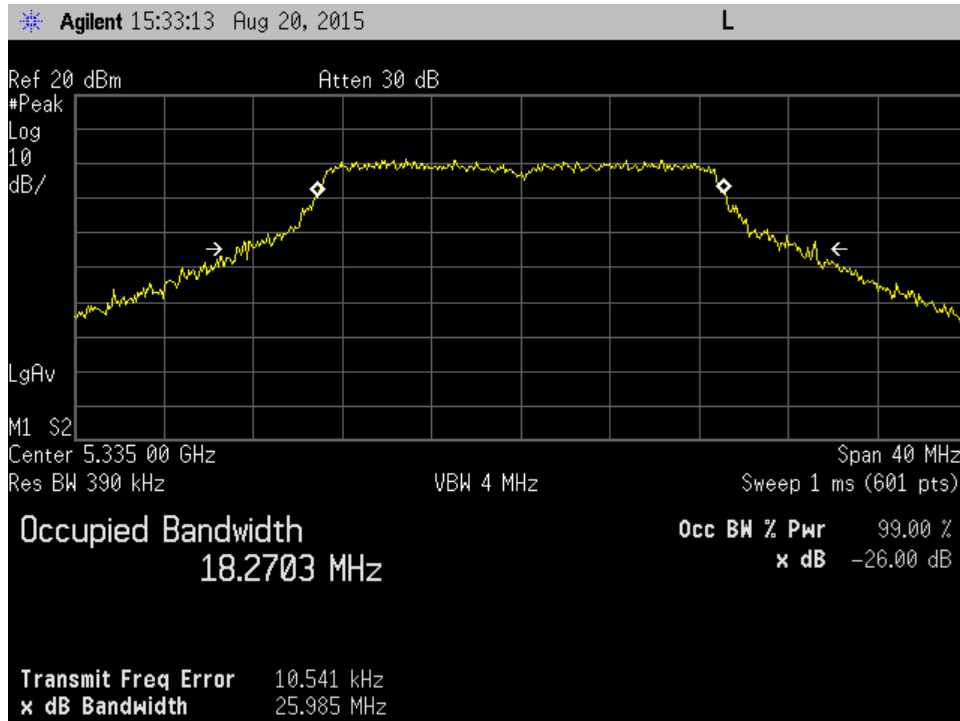


### 10 MHz mid ch\_J9

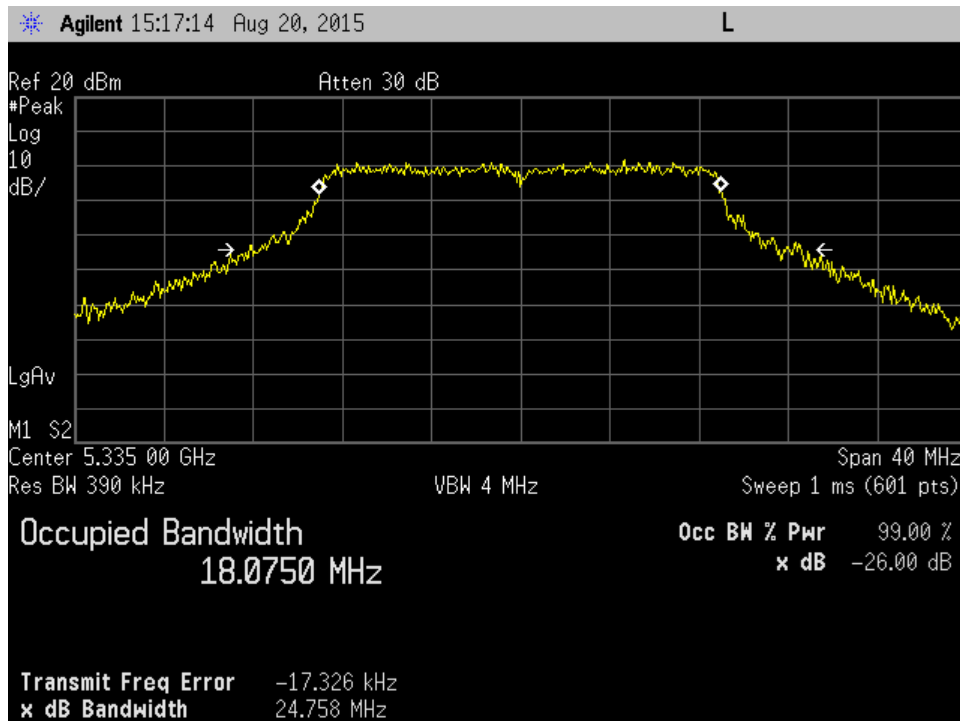




### 20 MHz high ch\_J5

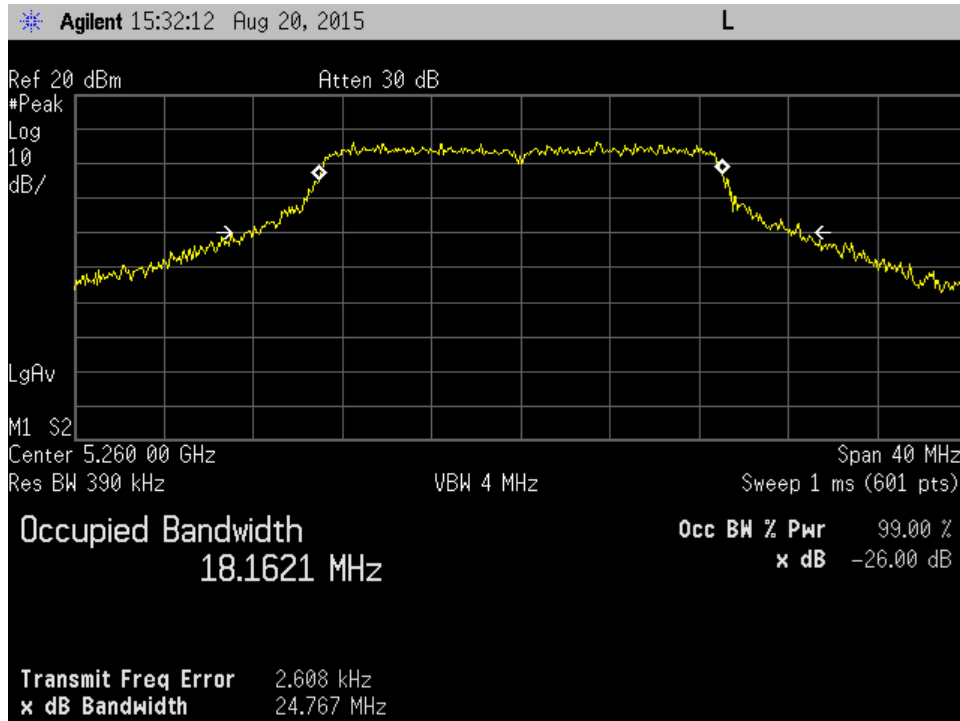


### 20 MHz high ch\_J9

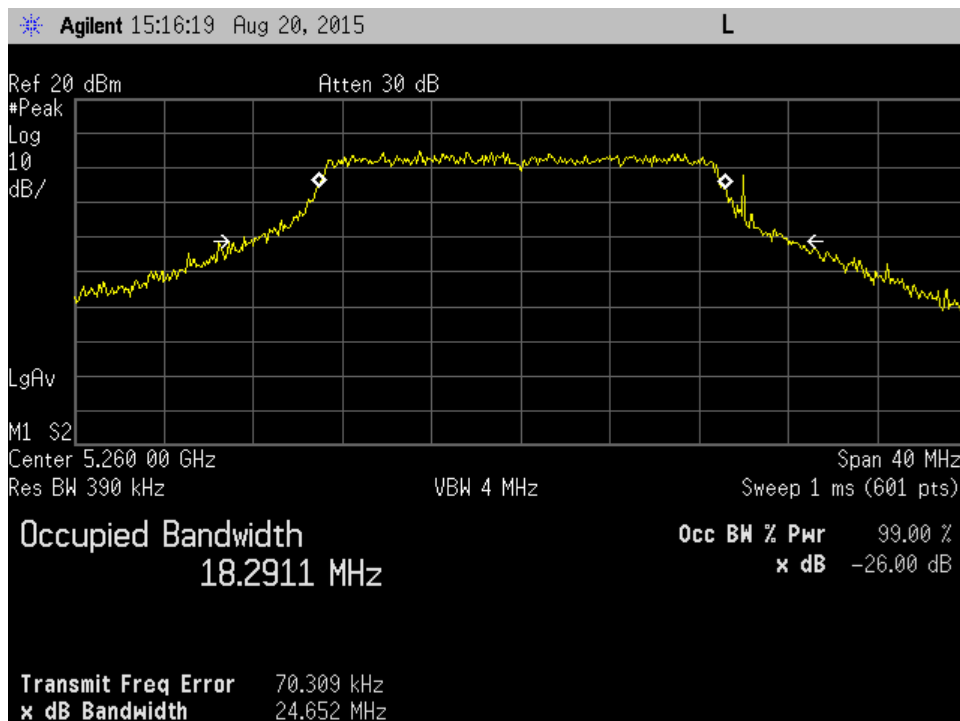




### 20 MHz low ch\_J5

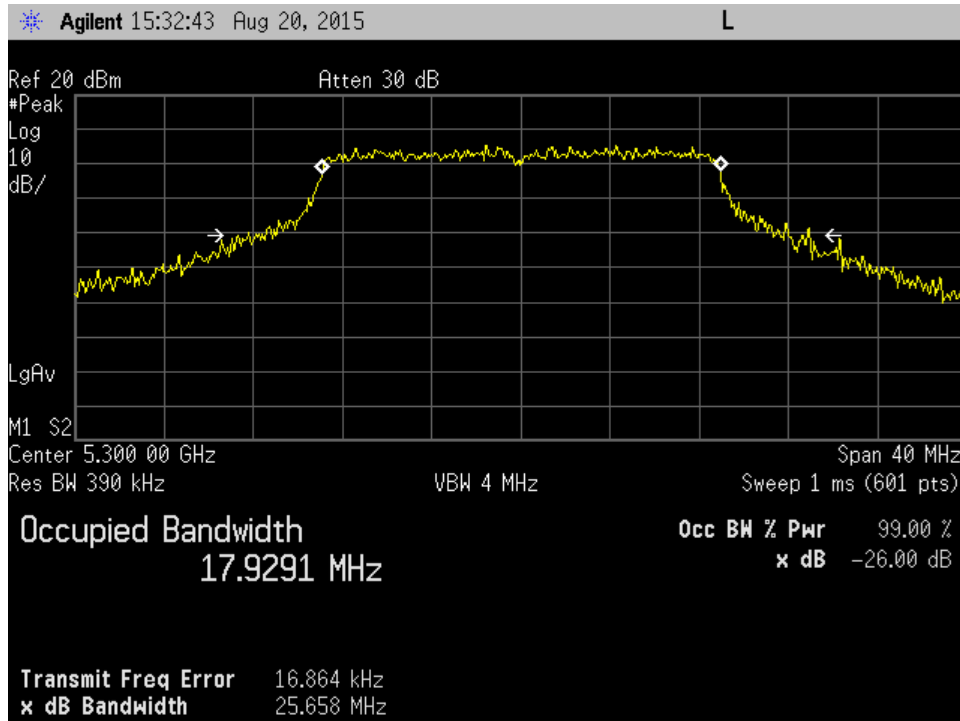


### 20 MHz low ch\_J9

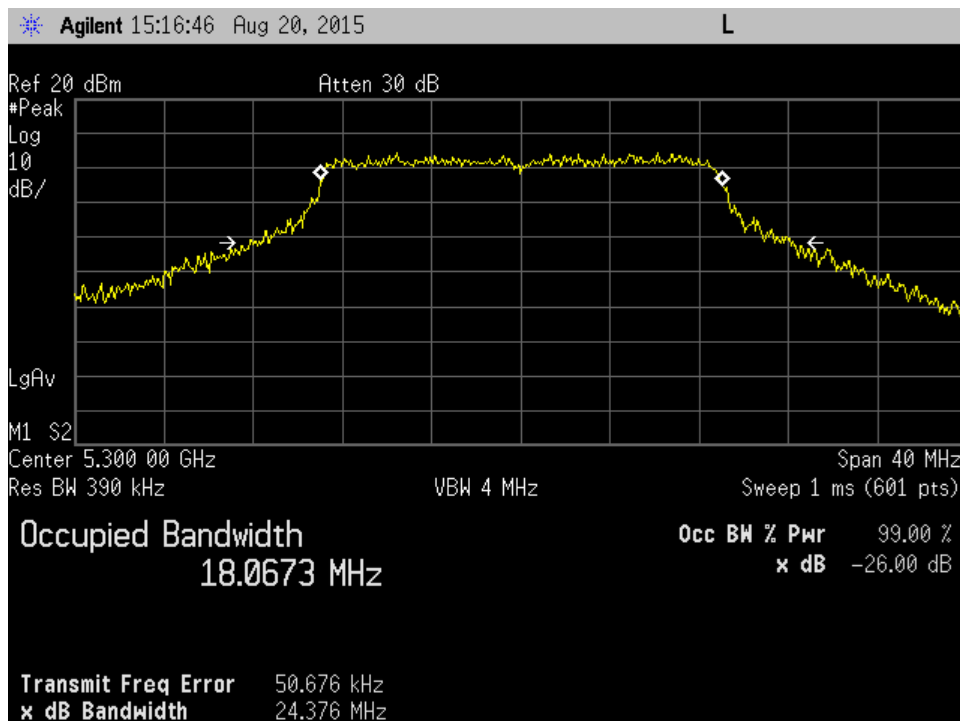




### 20 MHz mid ch\_J5

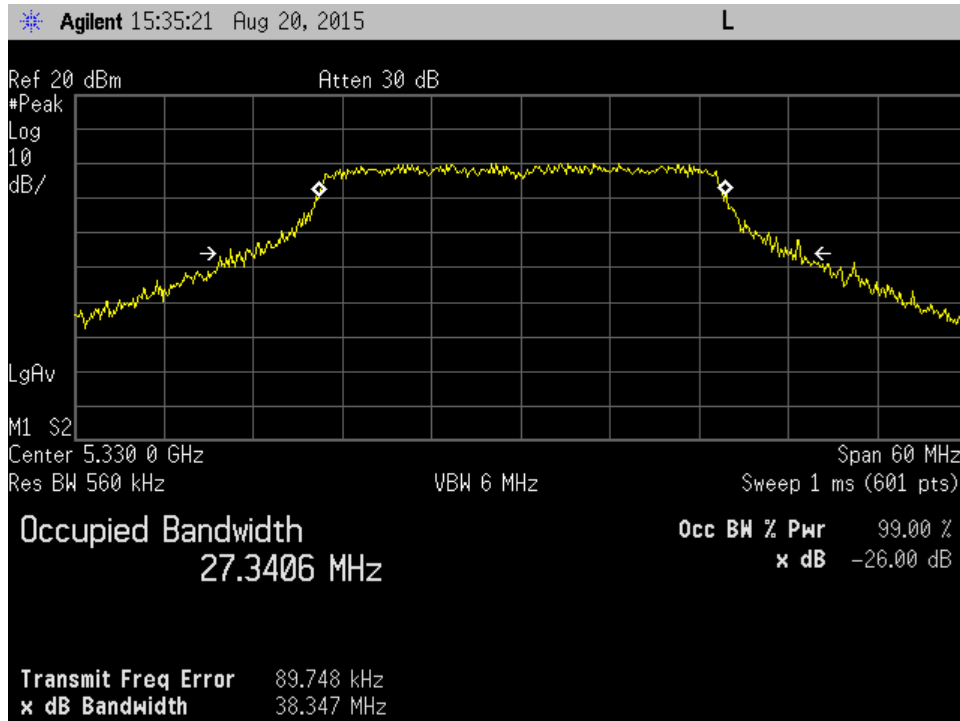


### 20 MHz mid ch\_J9

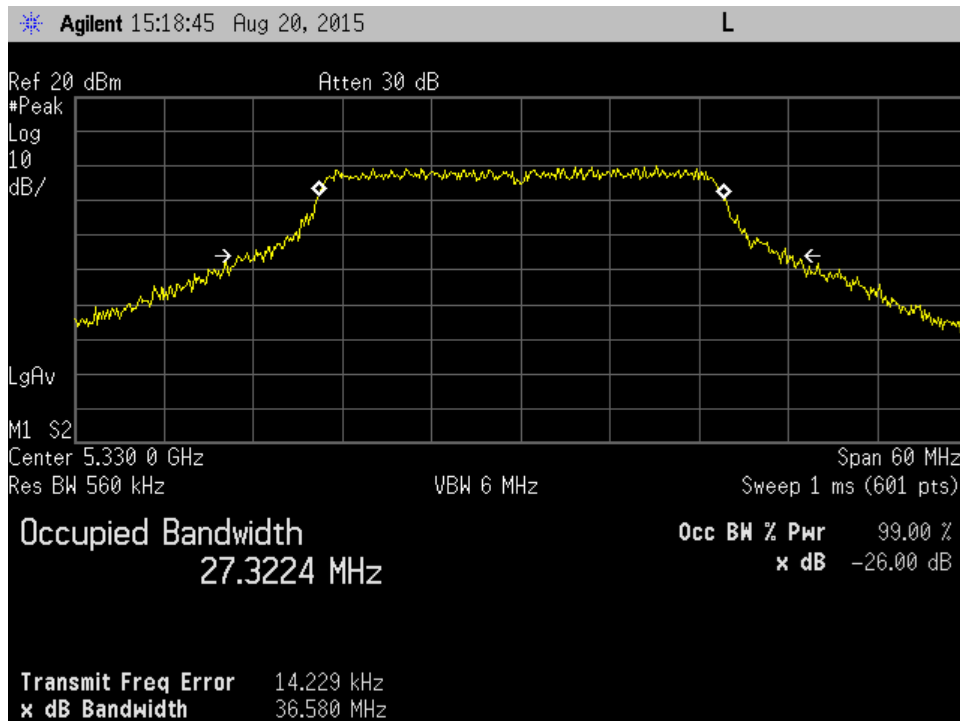




### 30 MHz high ch\_J5



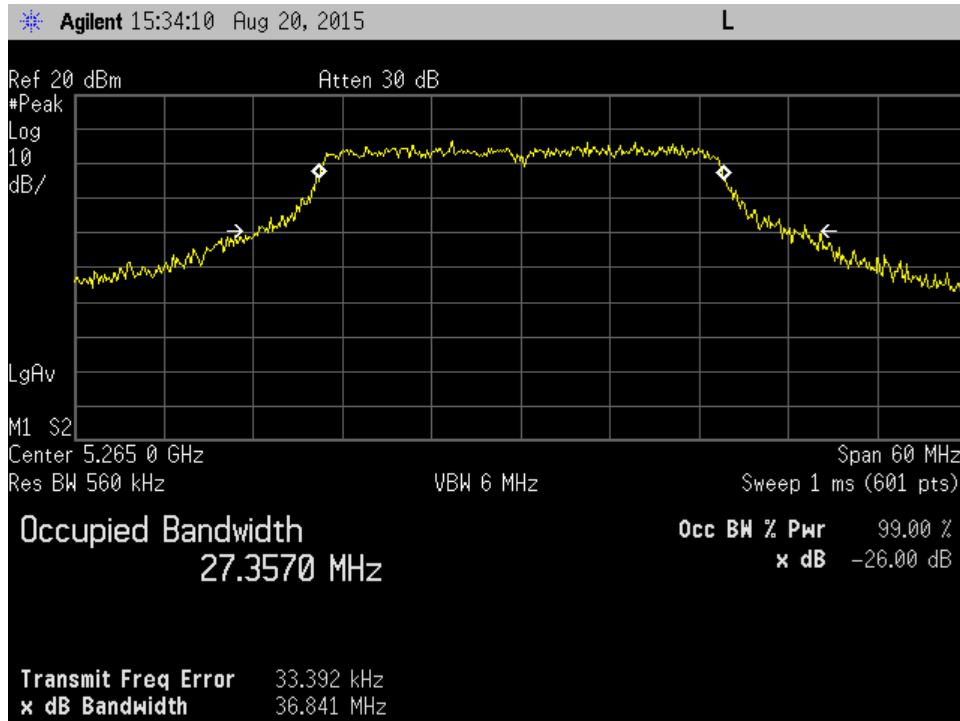
### 30 MHz high ch\_J9



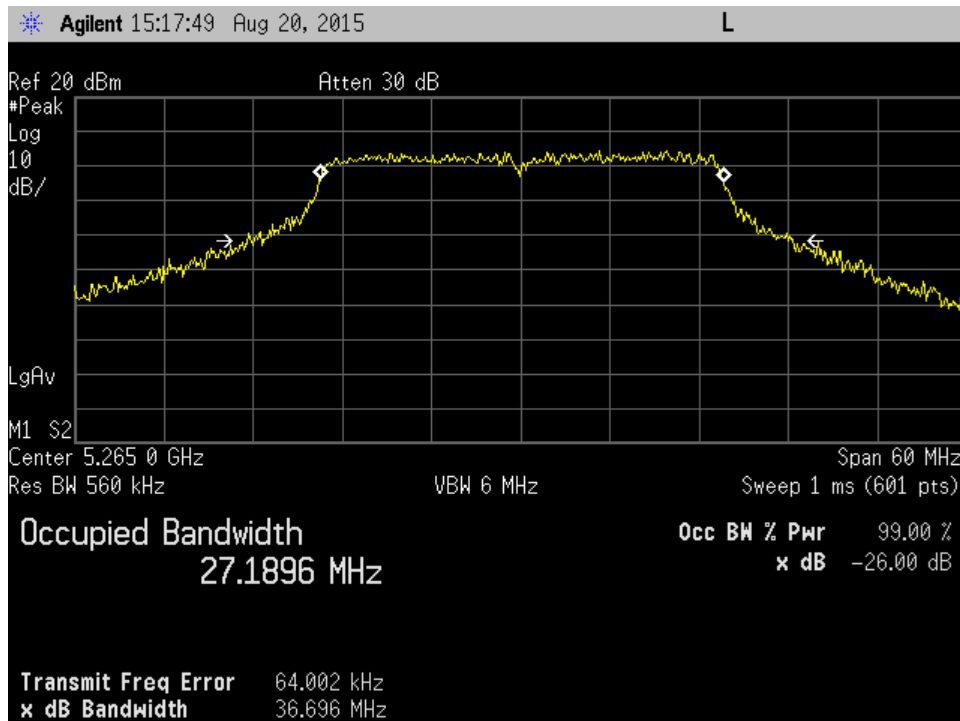




### 30 MHz low ch\_J5

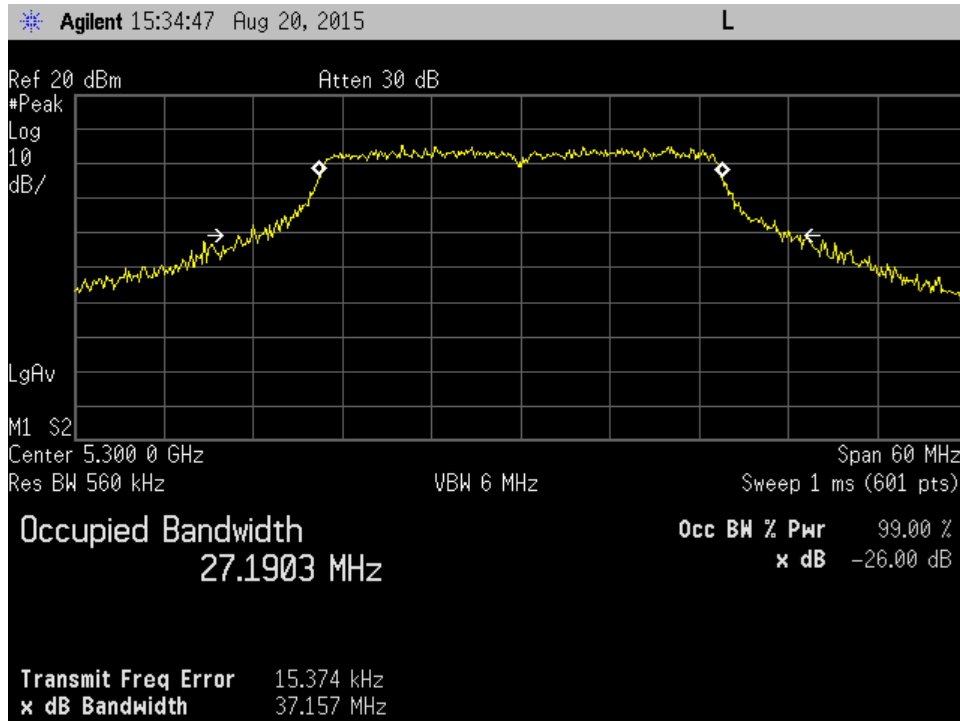


### 30 MHz low ch\_J9

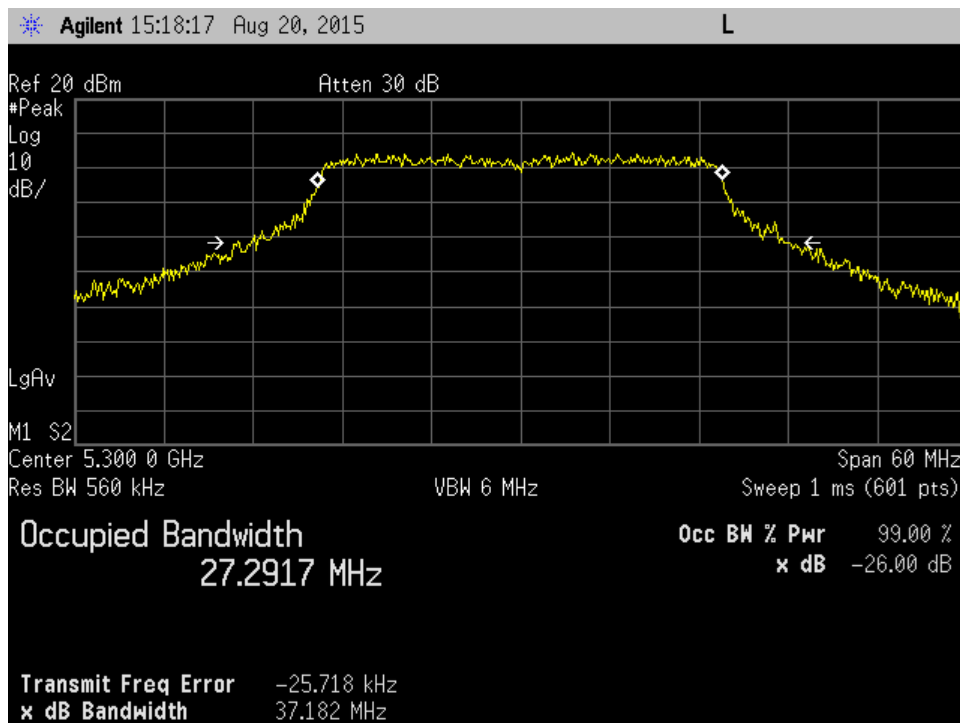




### 30 MHz mid ch\_J5

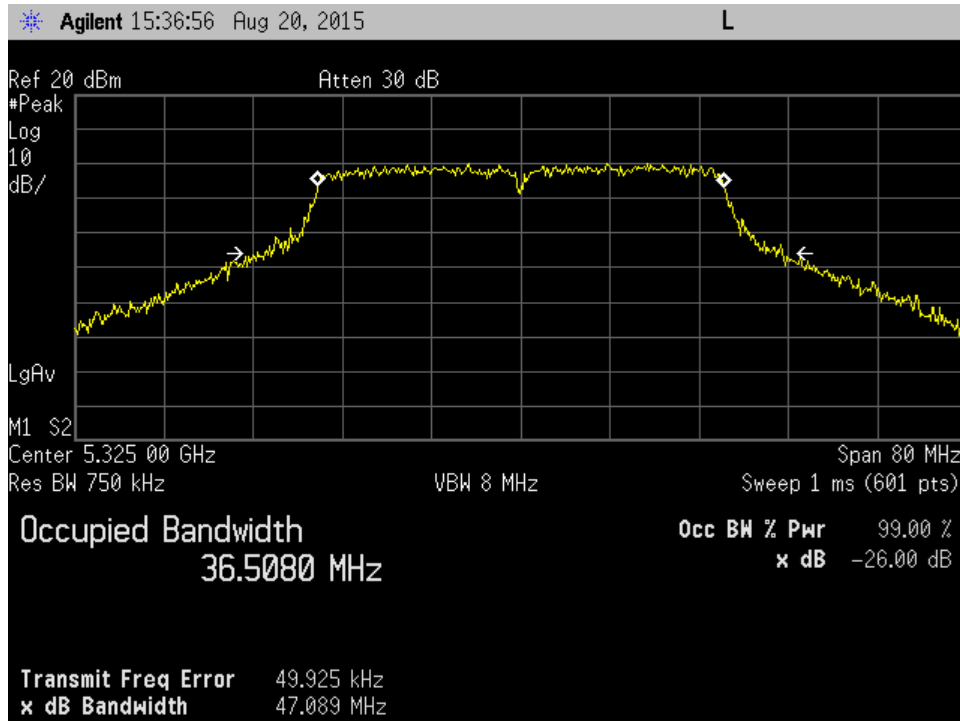


### 30 MHz mid ch\_J9

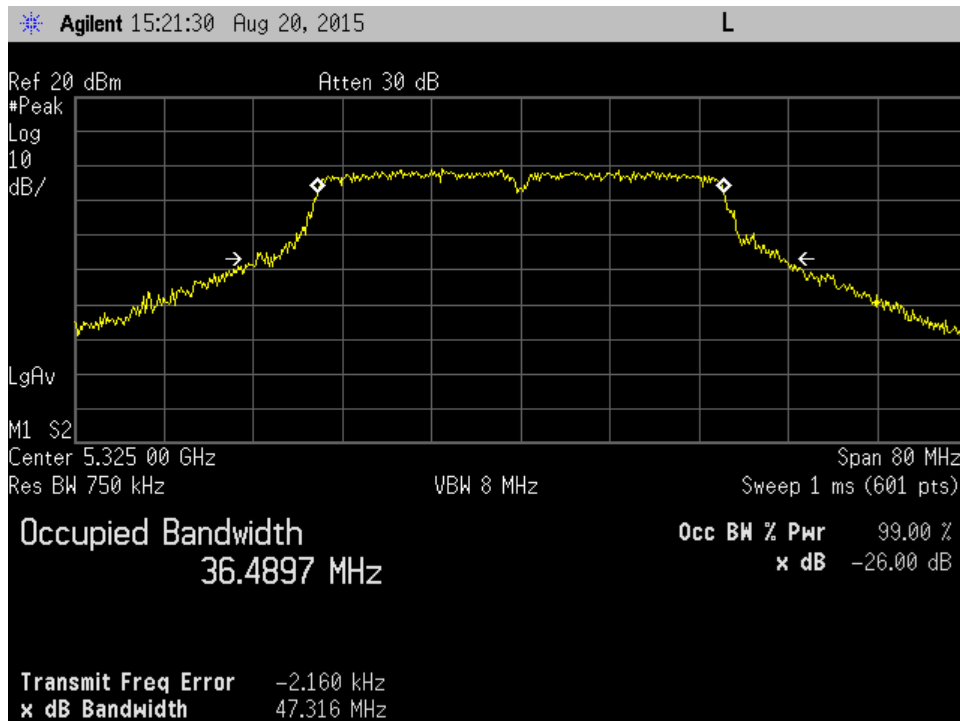




### 40 MHz high ch\_J5

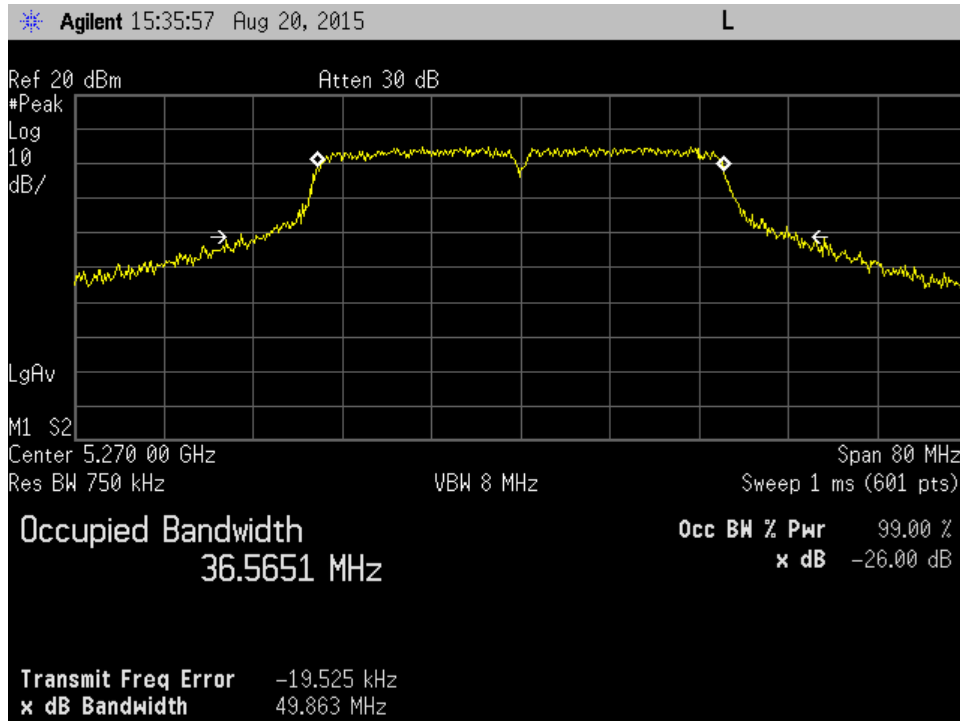


### 40 MHz high ch\_J9

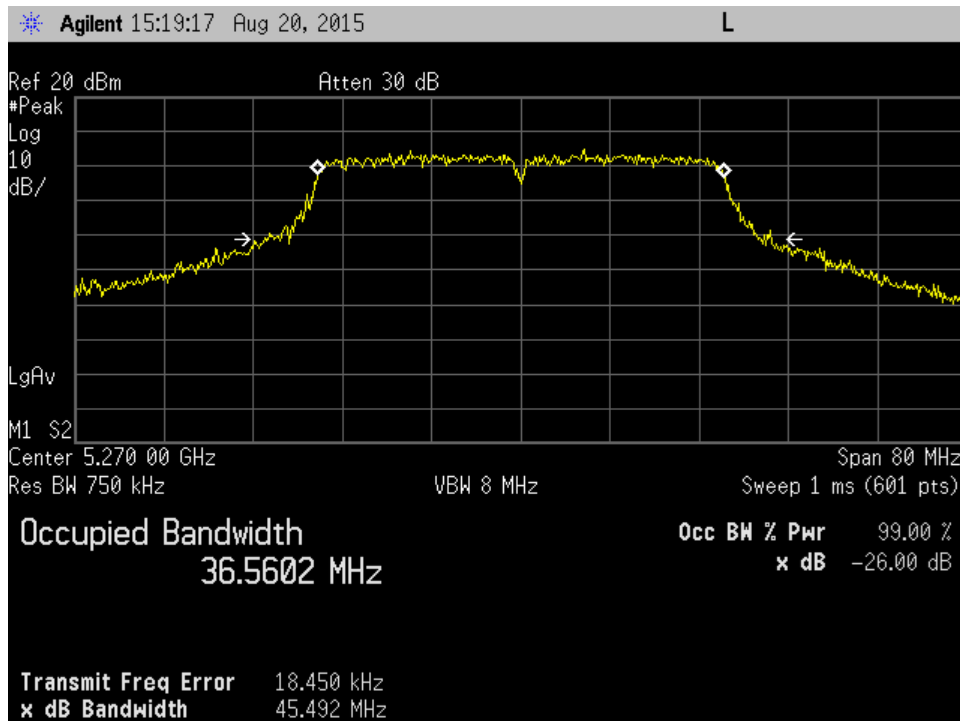




### 40 MHz low ch\_J5

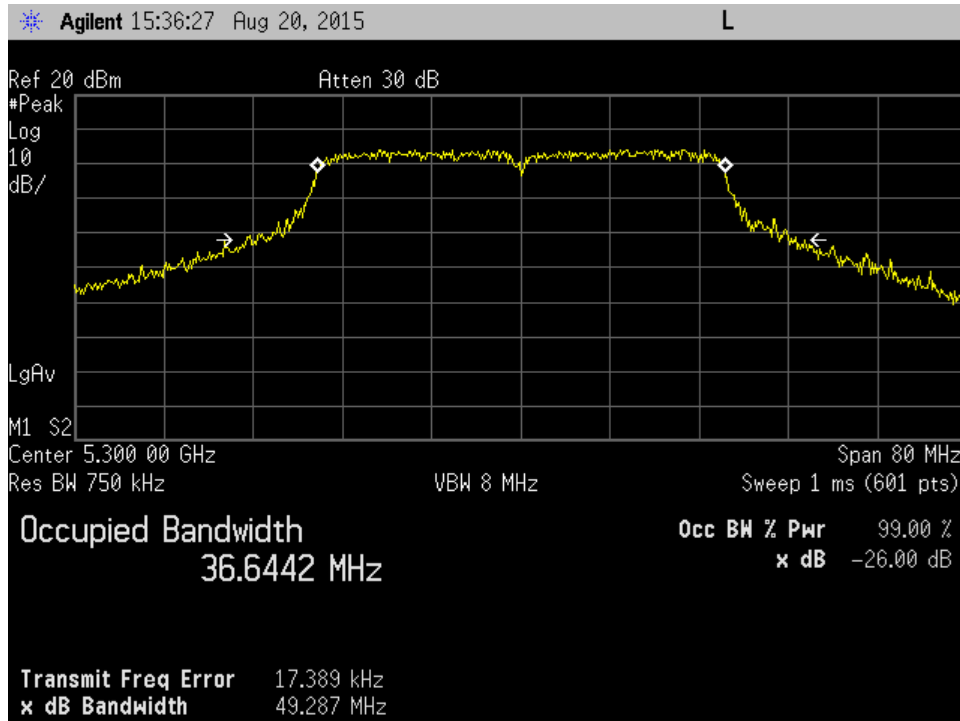


### 40 MHz low ch\_J9

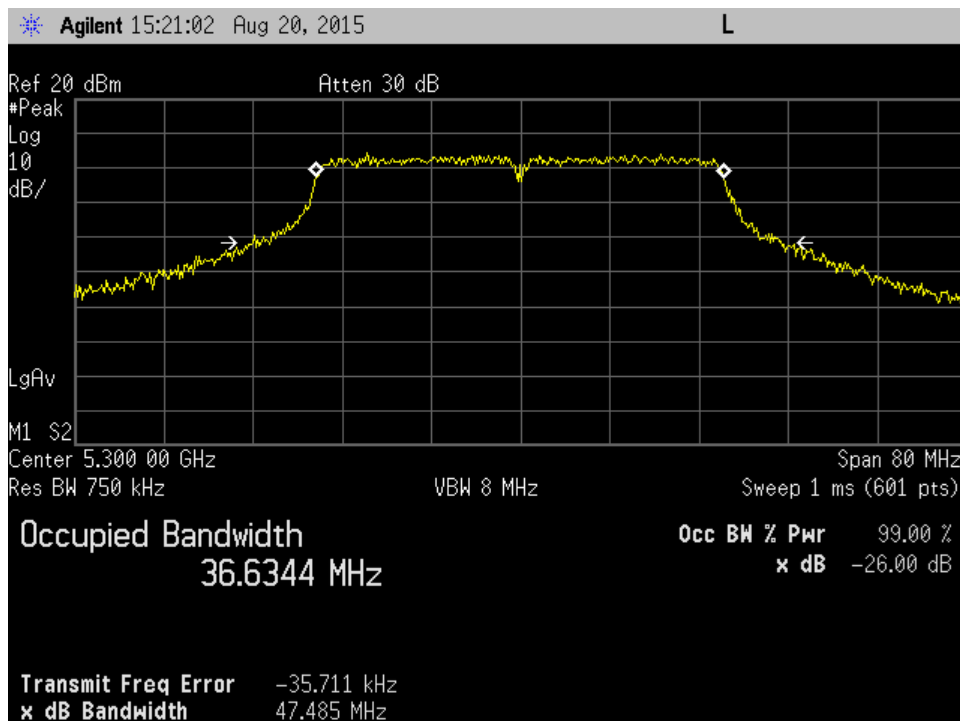




### 40 MHz mid ch\_J5

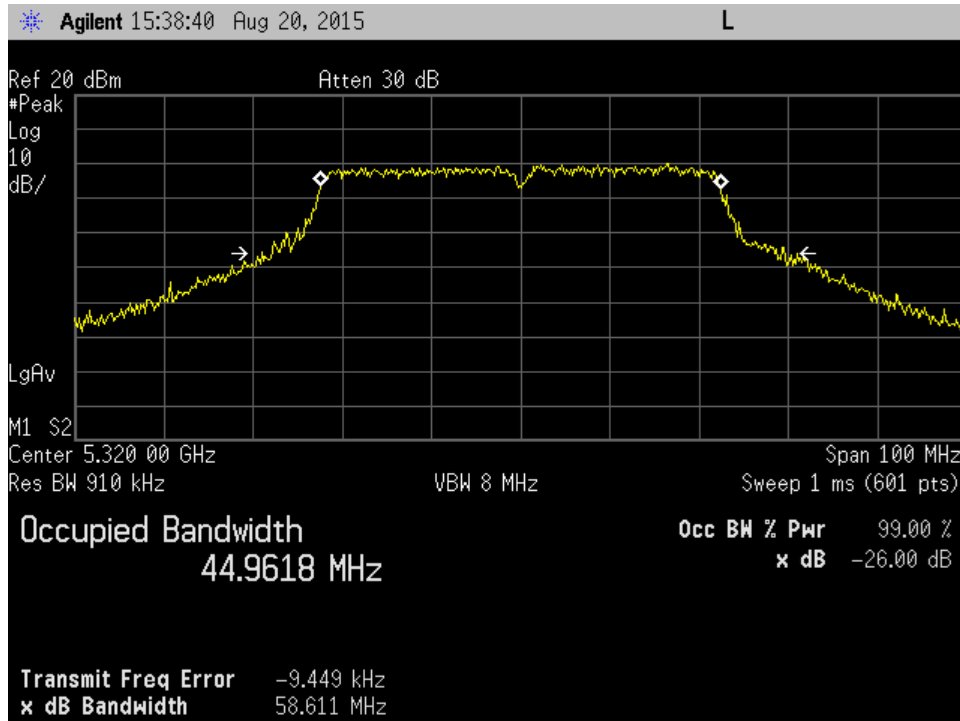


### 40 MHz mid ch\_J9

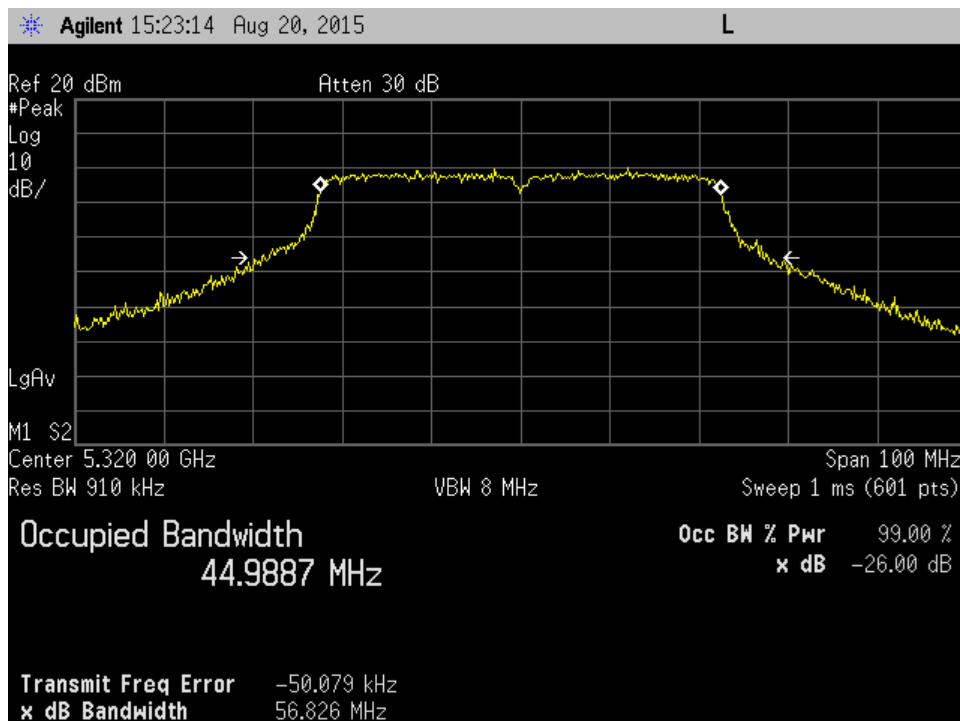




### 50 MHz high ch\_J5

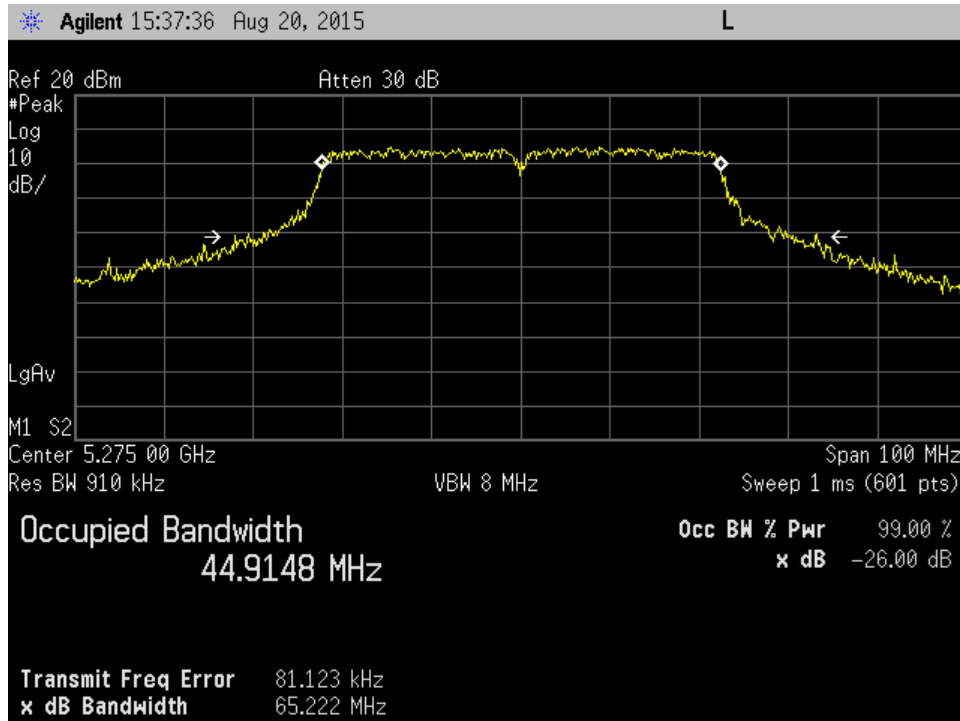


### 50 MHz high ch\_J9

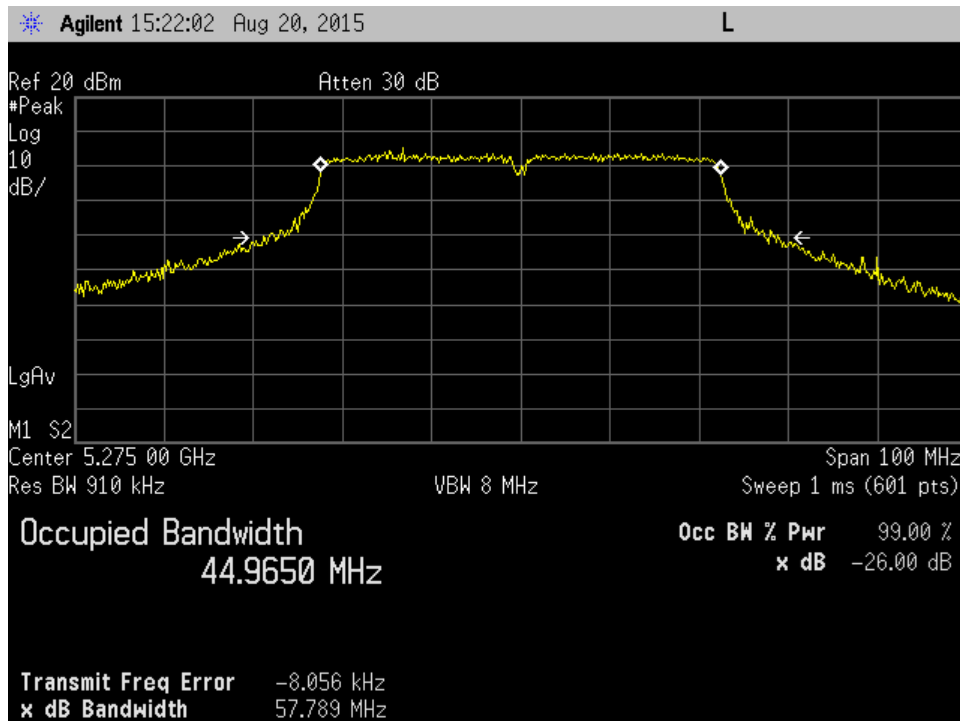




### 50 MHz low ch\_J5

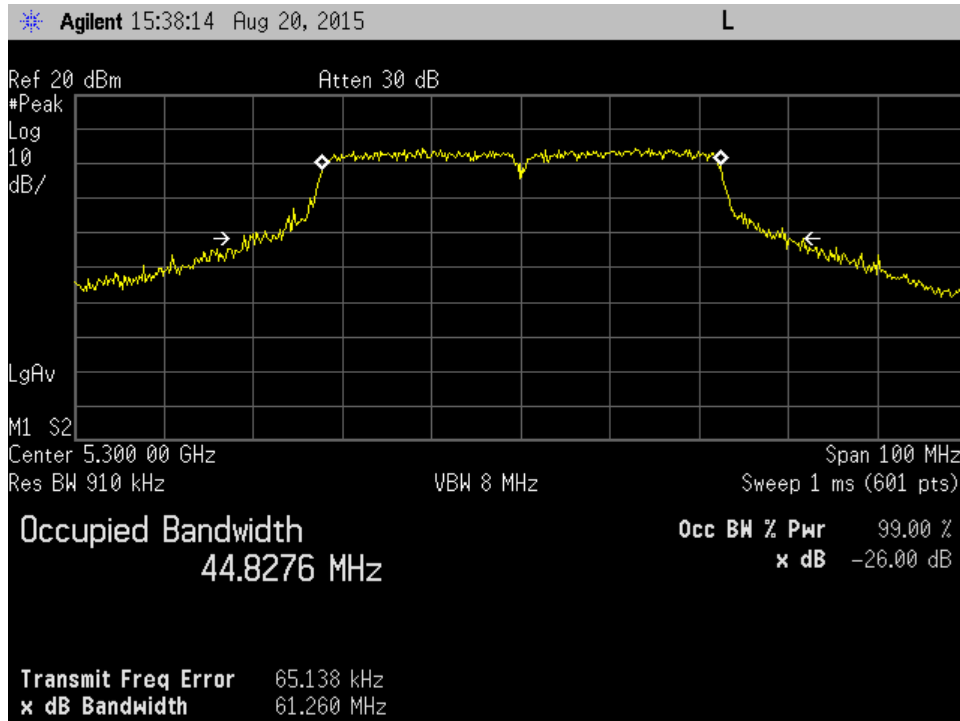


### 50 MHz low ch\_J9

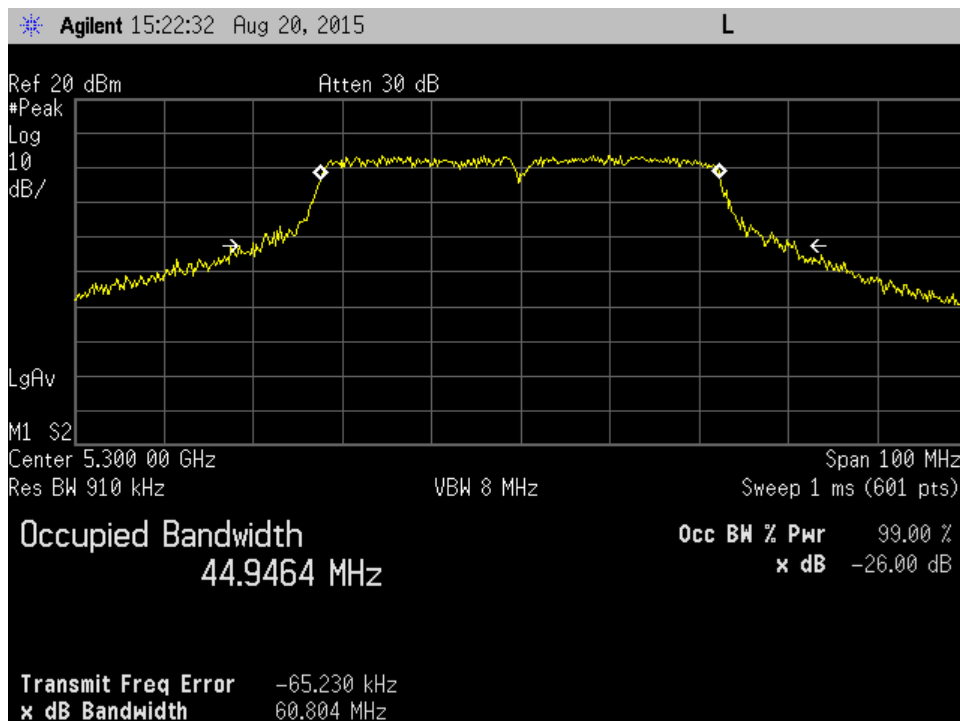




### 50 MHz mid ch\_J5



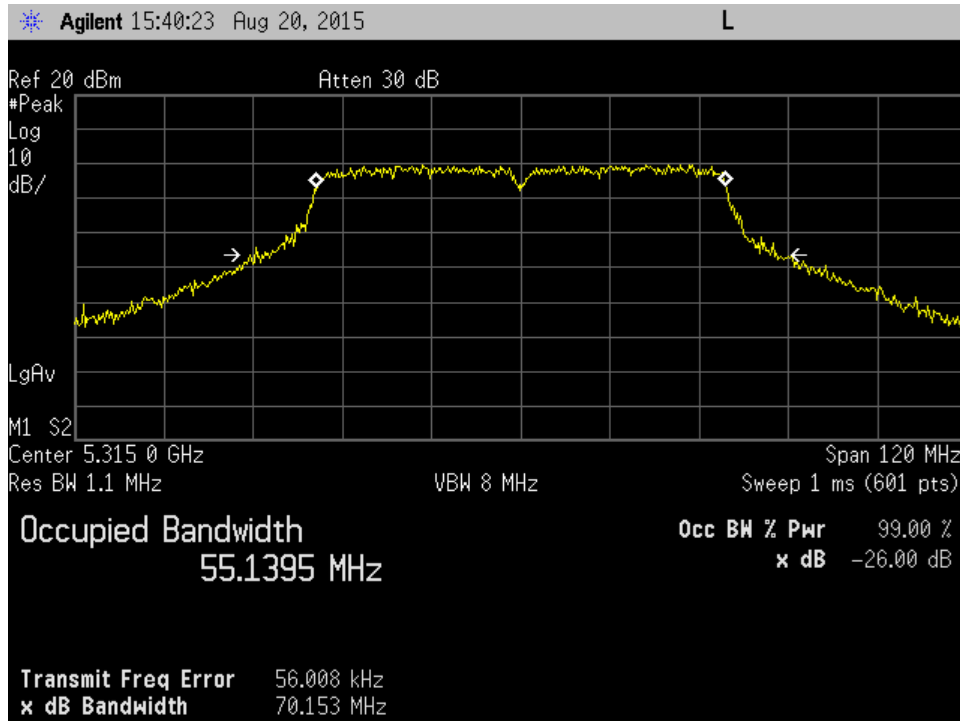
### 50 MHz mid ch\_J9



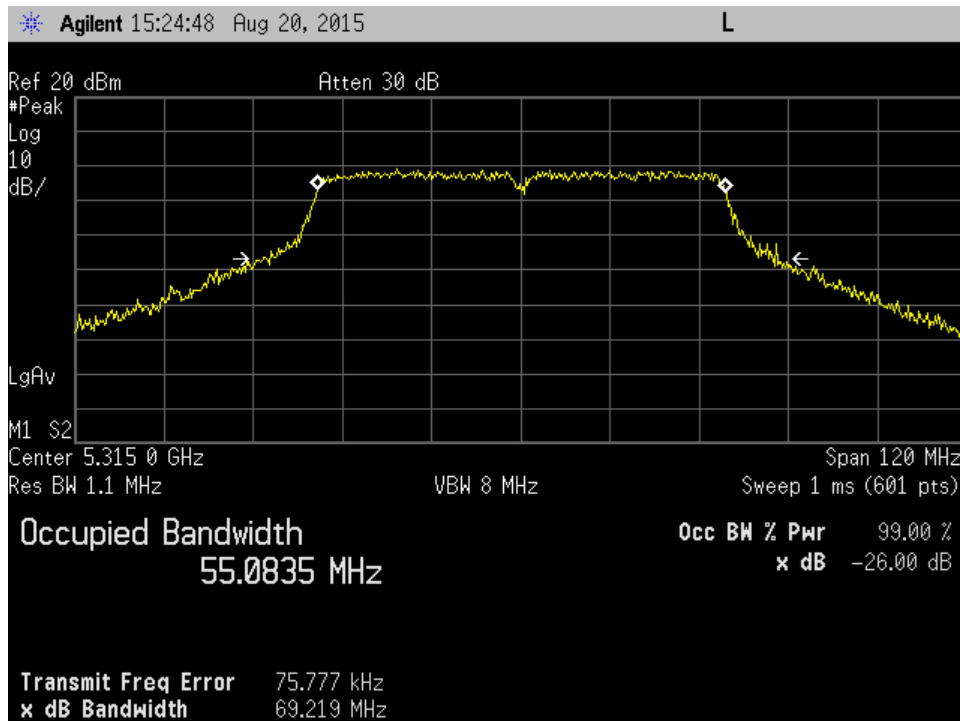




### 60 MHz high ch\_J5

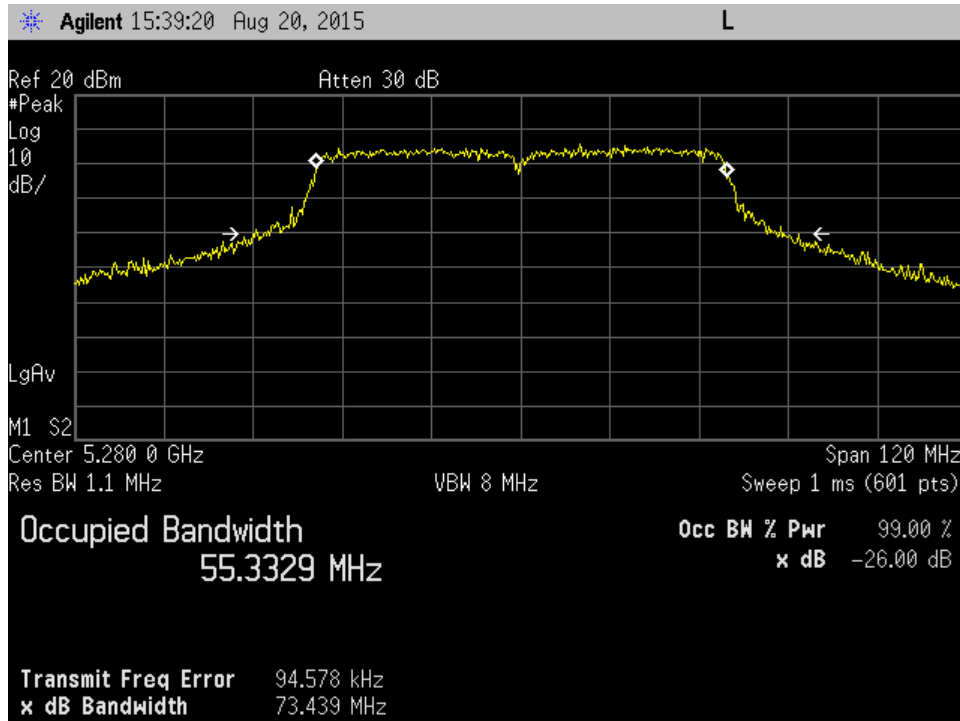


### 60 MHz high ch\_J9

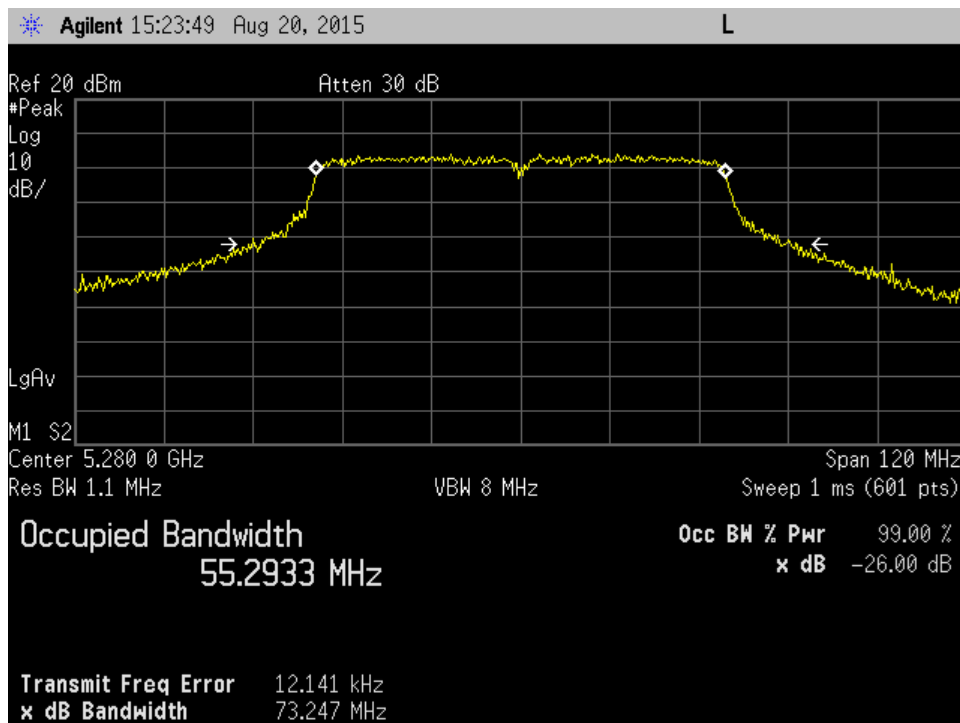




### 60 MHz low ch\_J5

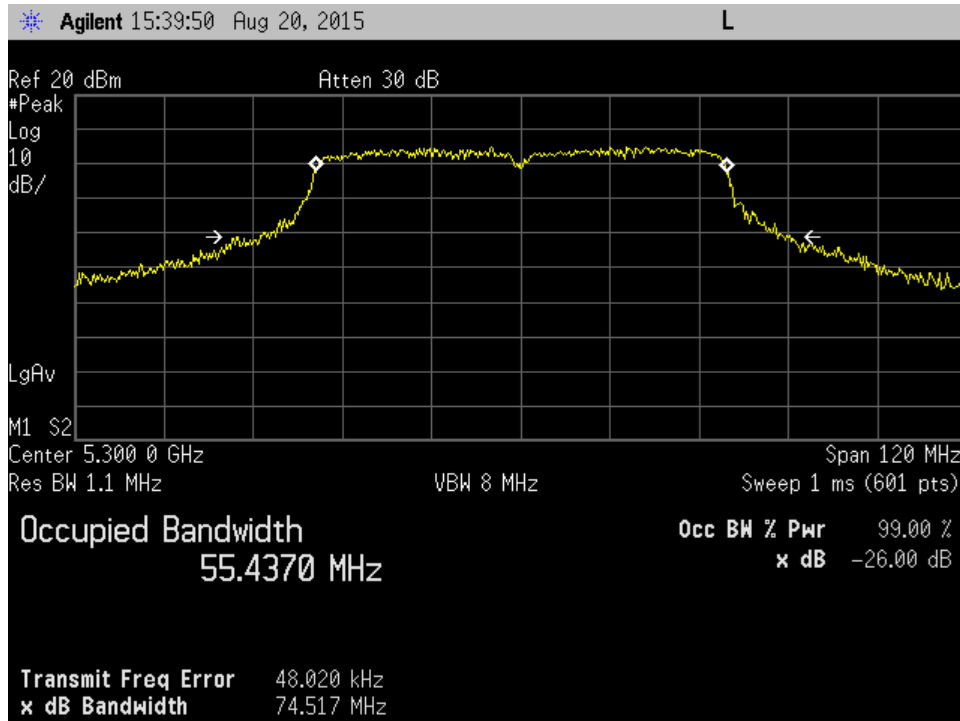


### 60 MHz low ch\_J9

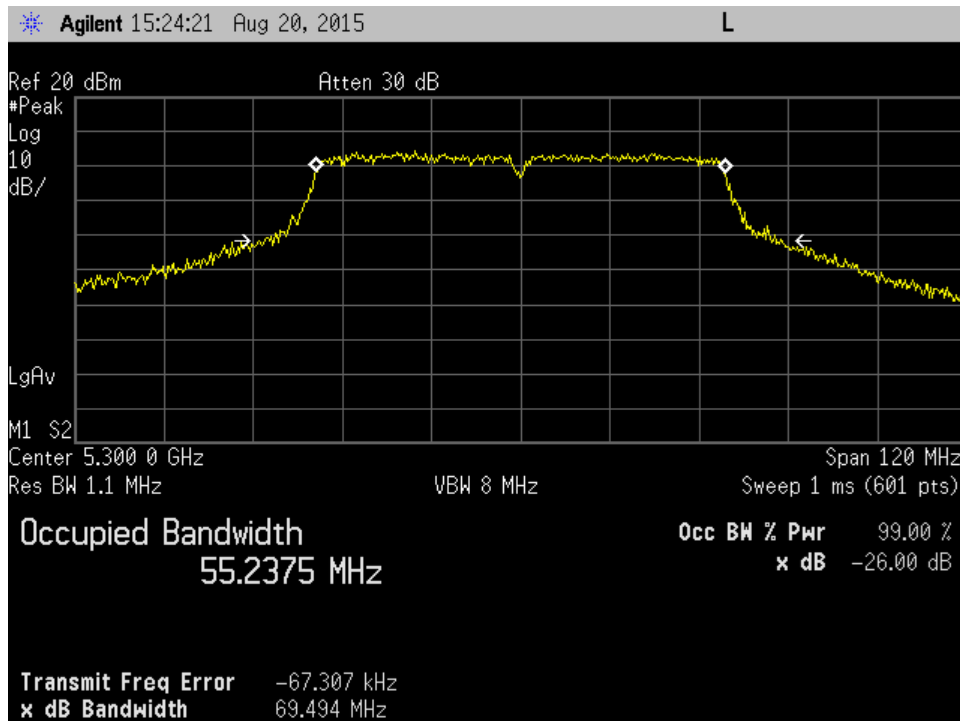




### 60 MHz mid ch\_J5



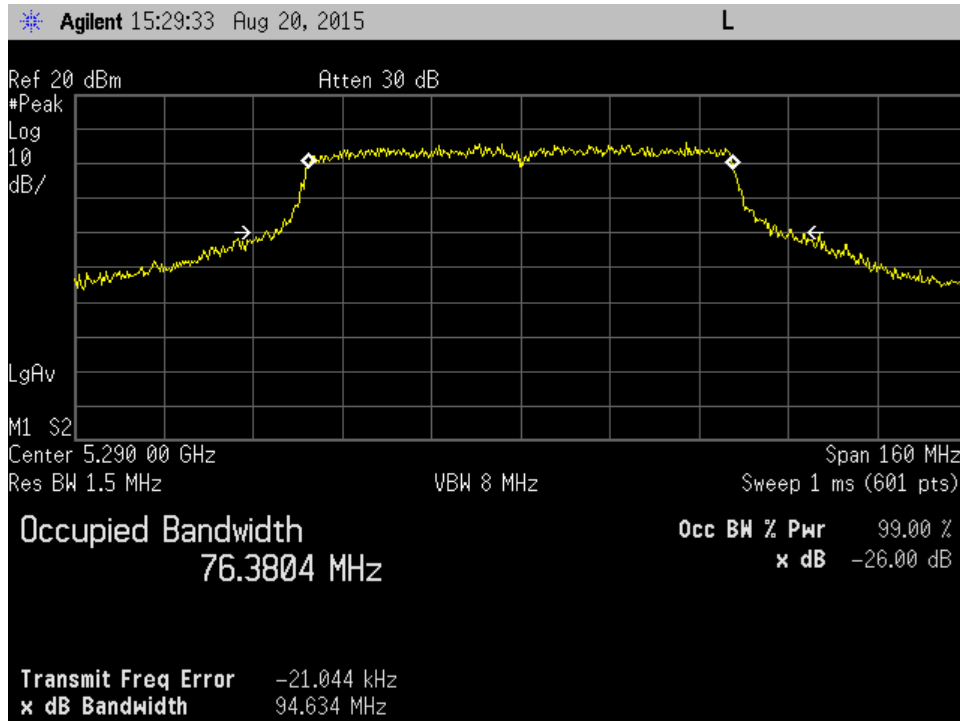
### 60 MHz mid ch\_J9



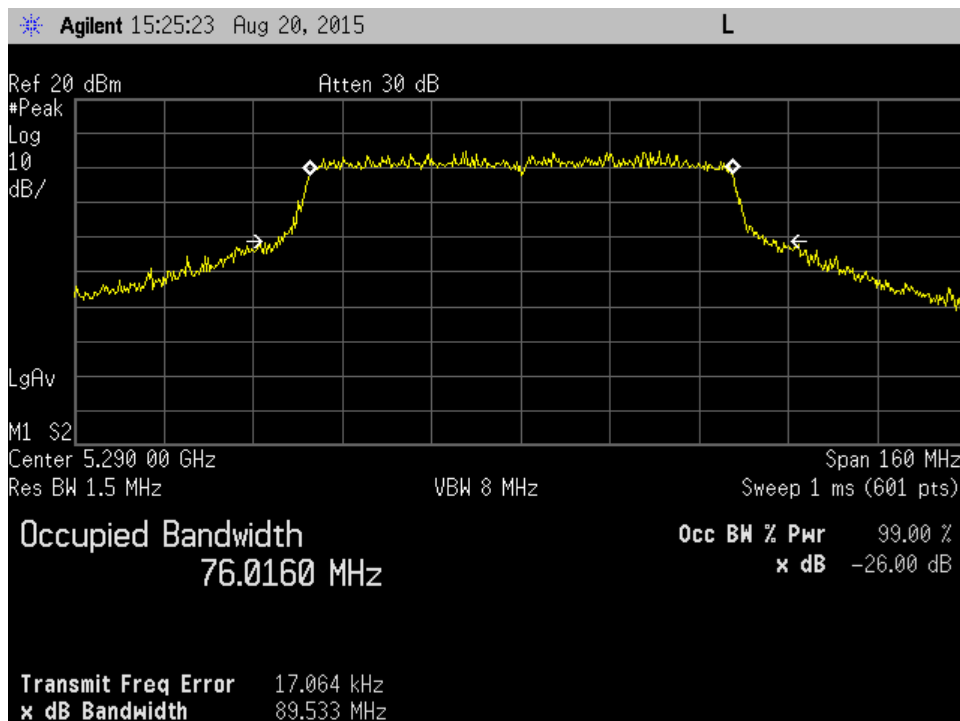




### 80 MHz low ch\_J5

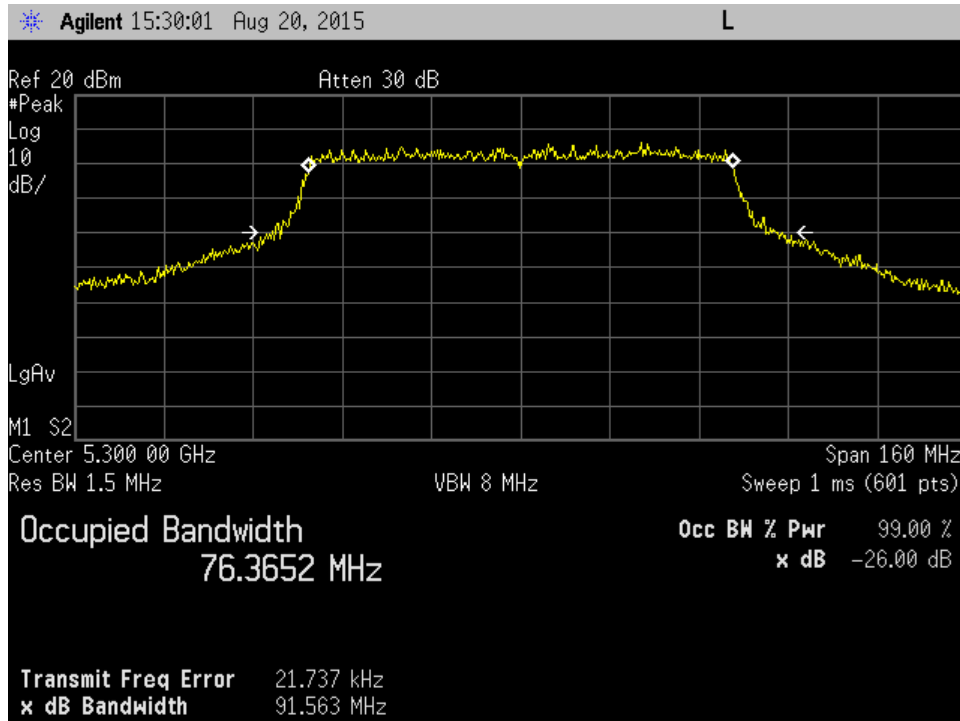


### 80 MHz low ch\_J9

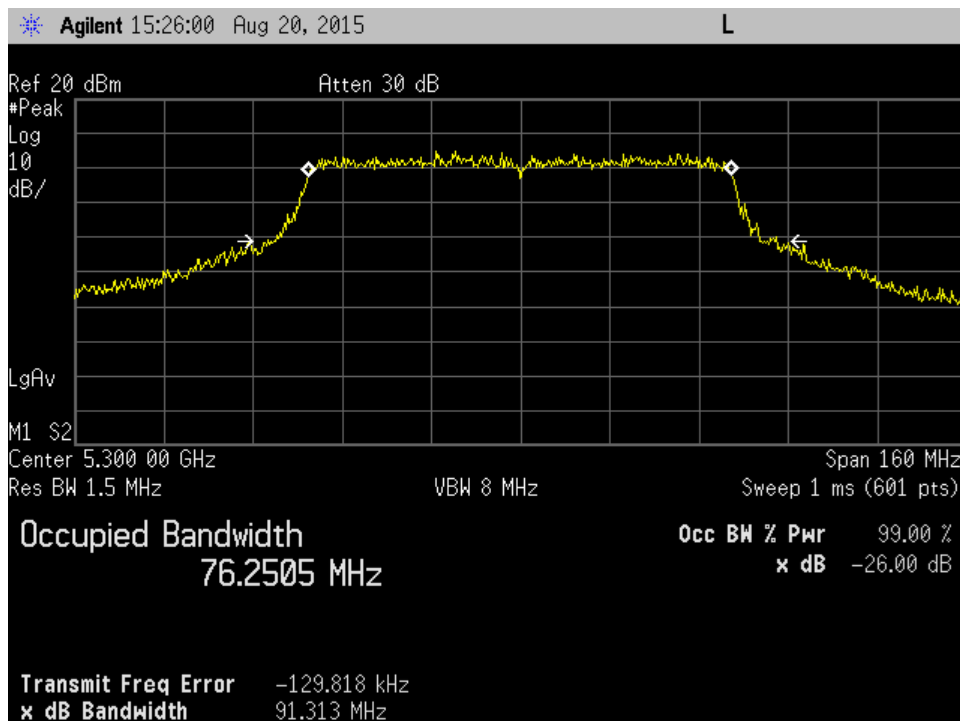




### 80 MHz mid ch\_J5



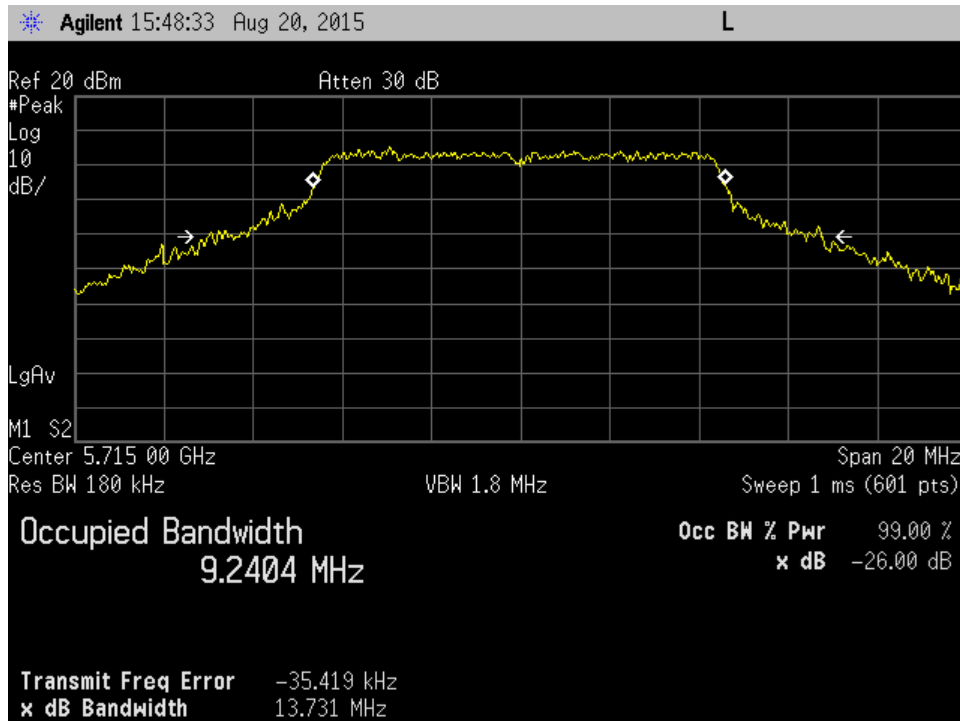
### 80 MHz mid ch\_J9



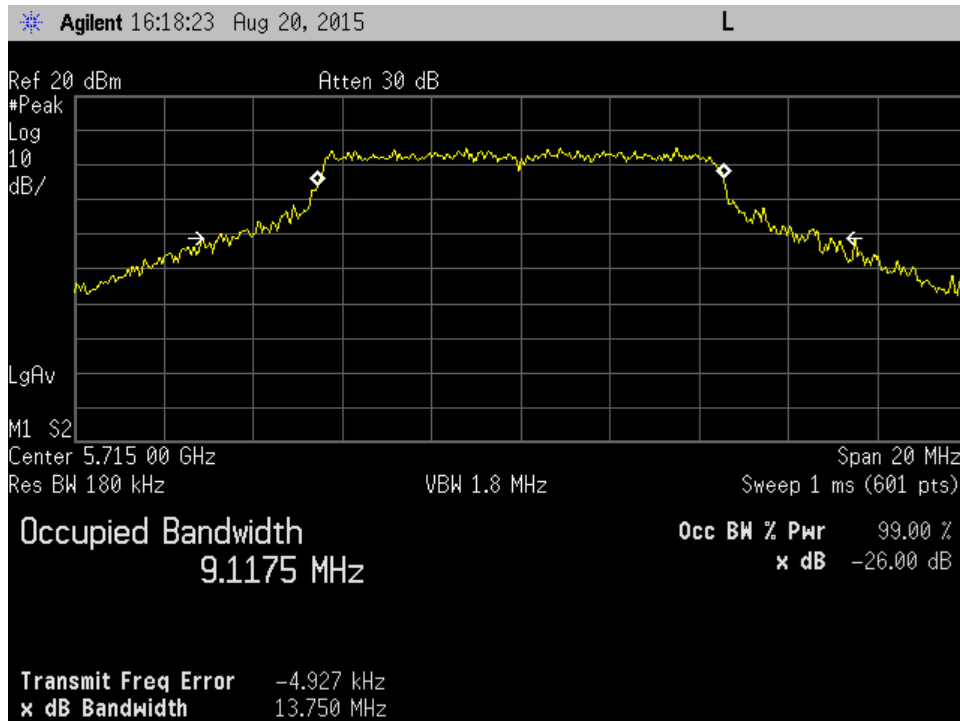


UNII-2C Data

10 MHz high ch\_J5

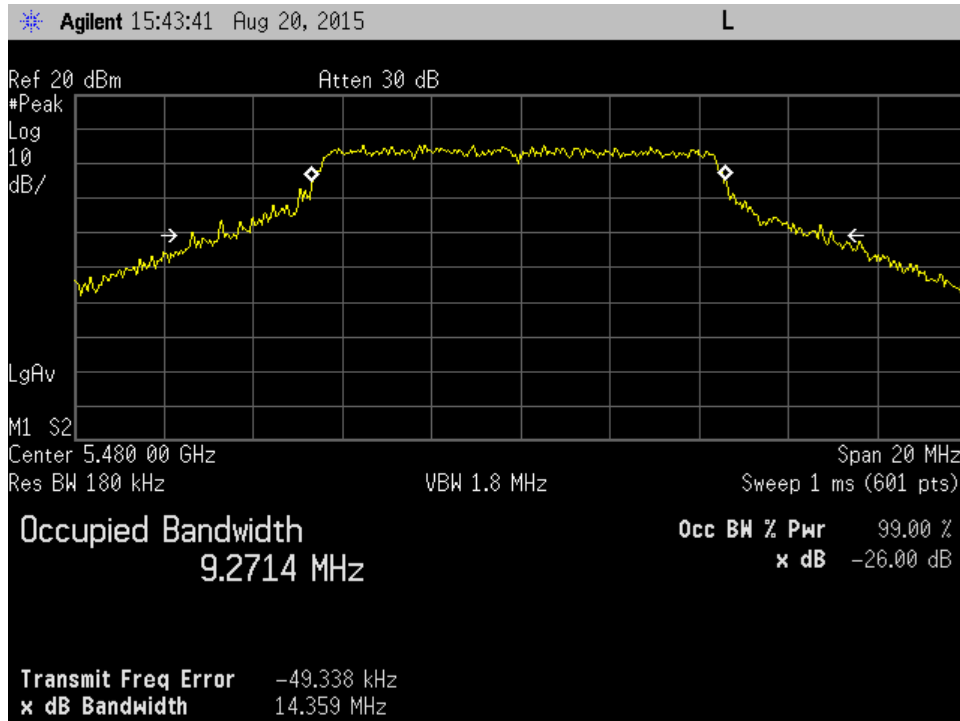


10 MHz high ch\_J9

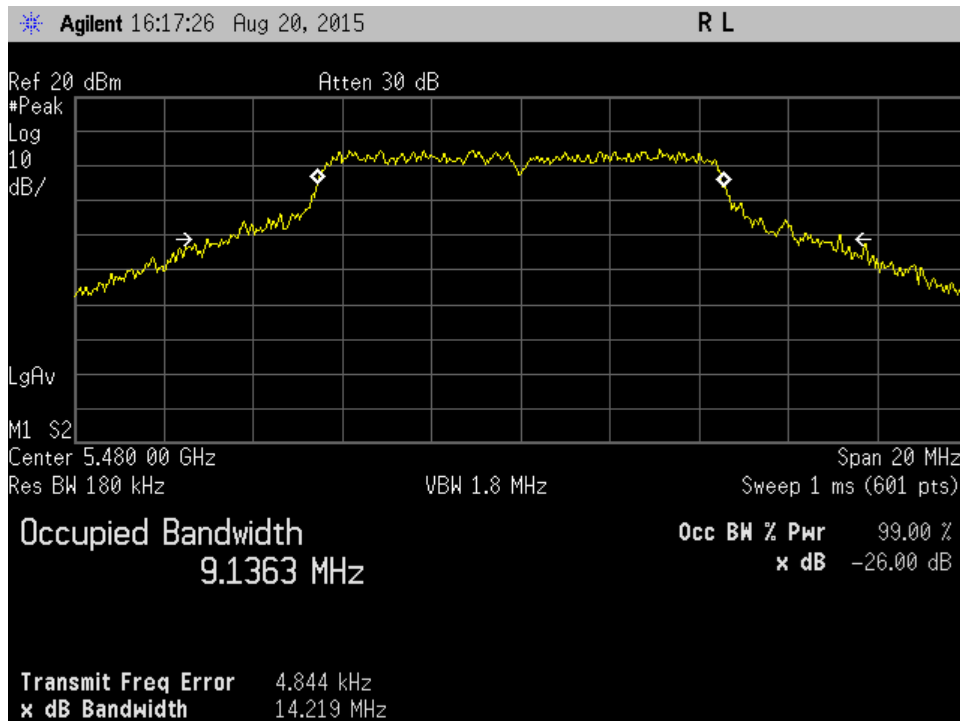




### 10 MHz low ch\_J5



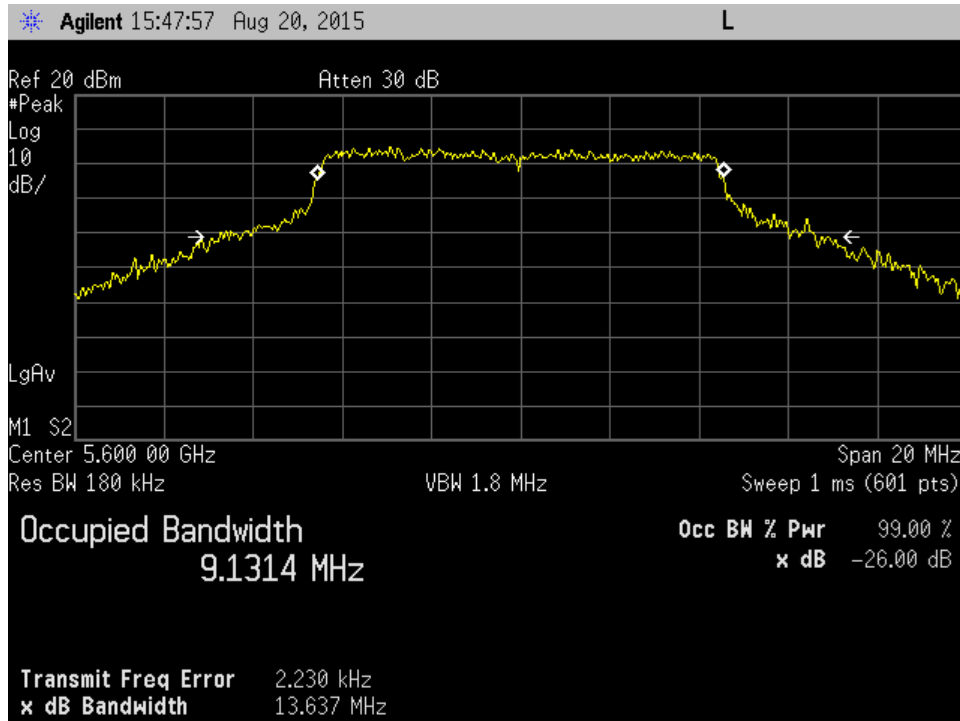
### 10 MHz low ch\_J9



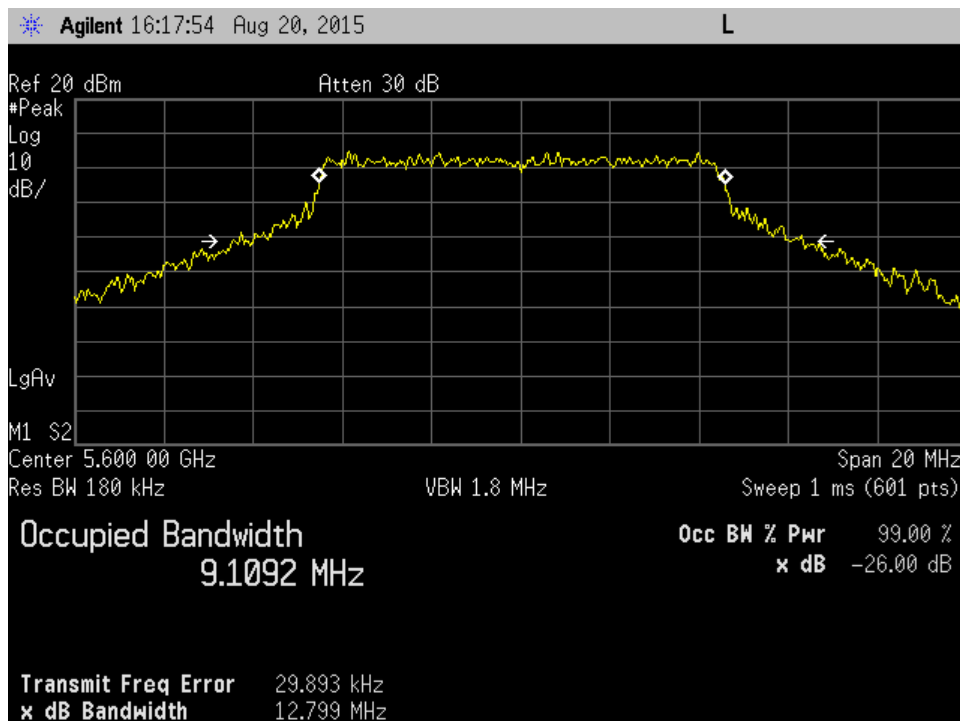




### 10 MHz mid ch\_J5

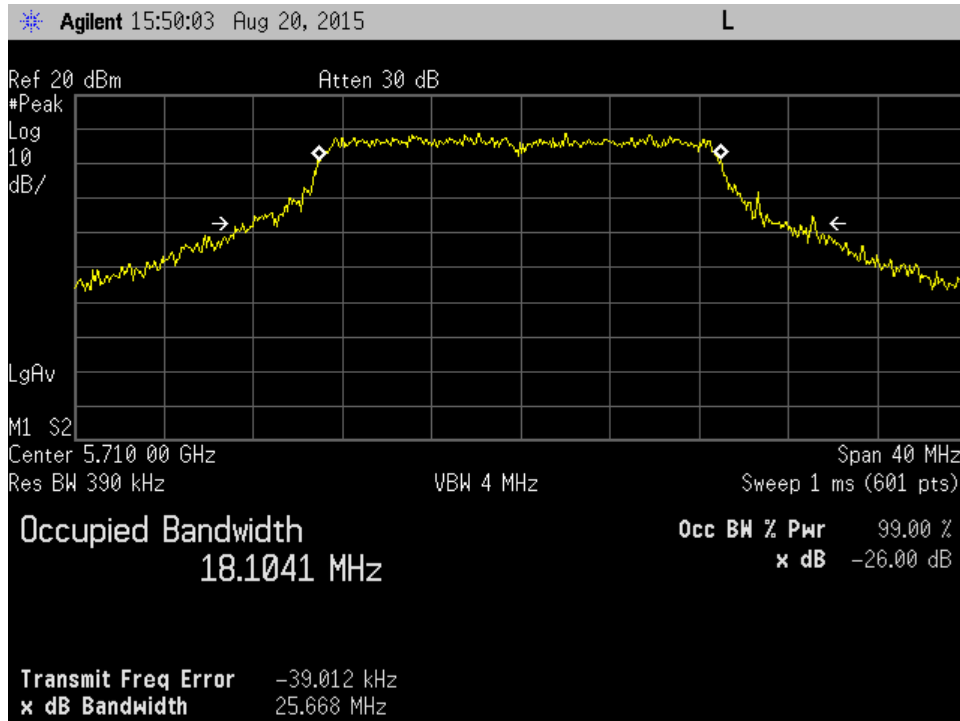


### 10 MHz mid ch\_J9

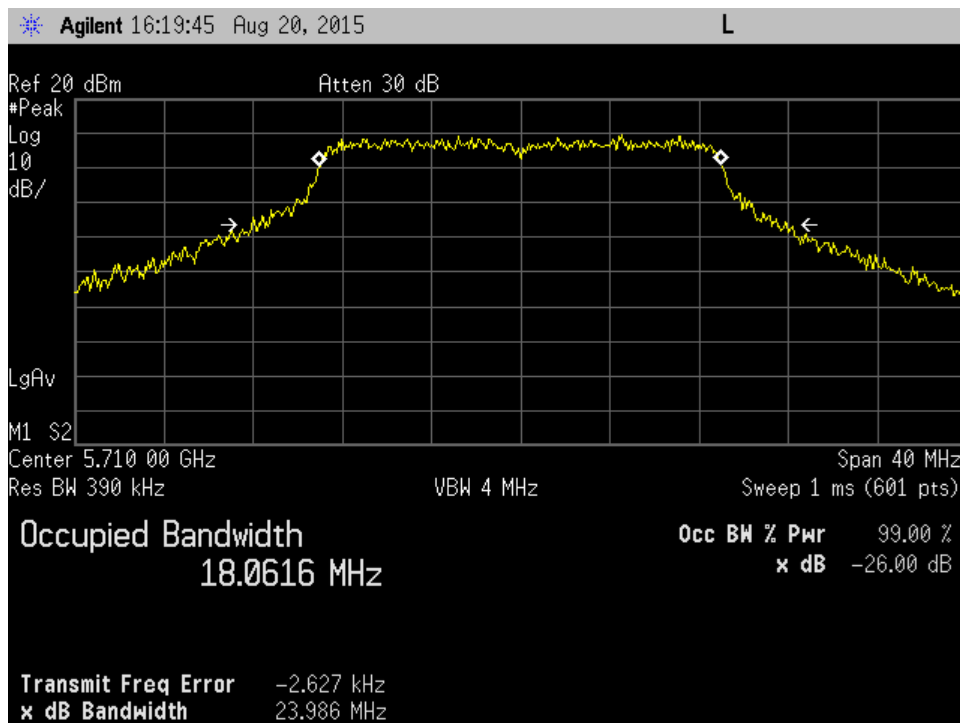




### 20 MHz high ch\_J5

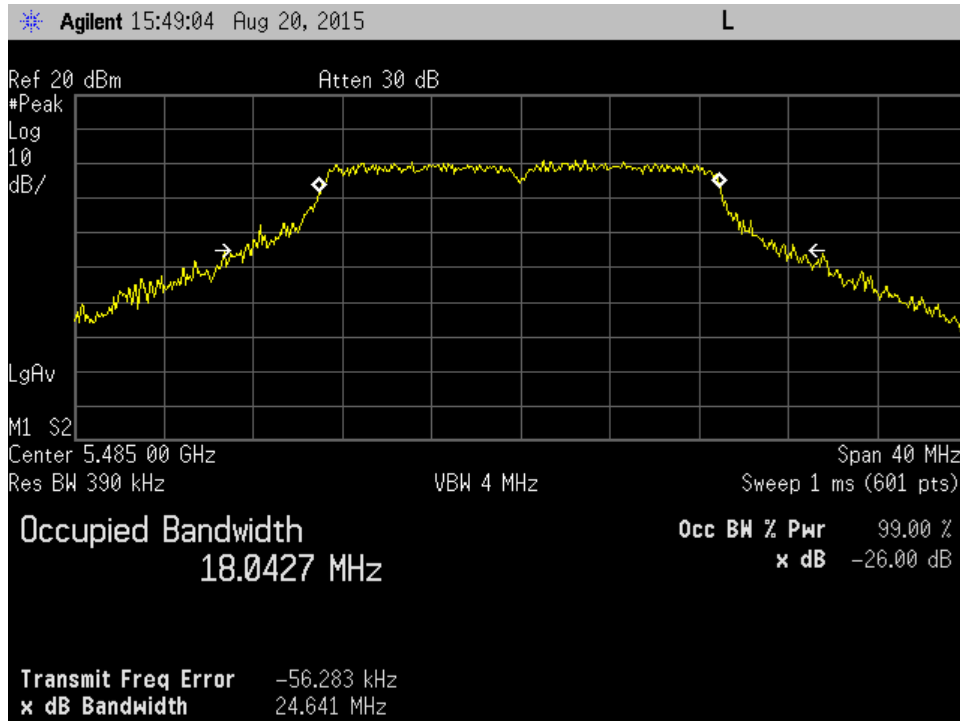


### 20 MHz high ch\_J9

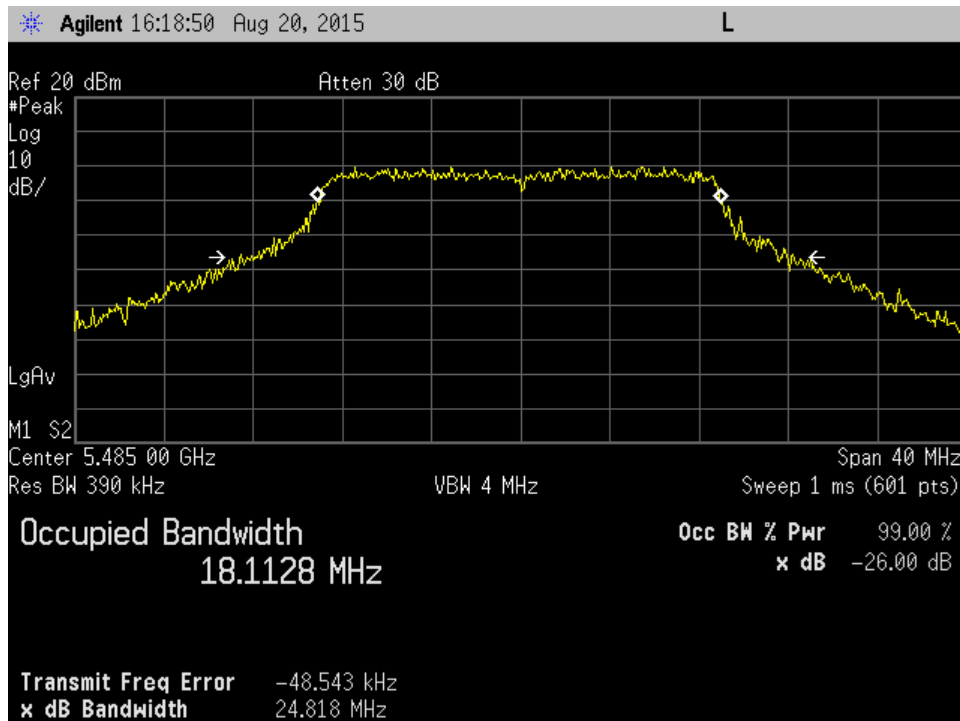




### 20 MHz low ch\_J5

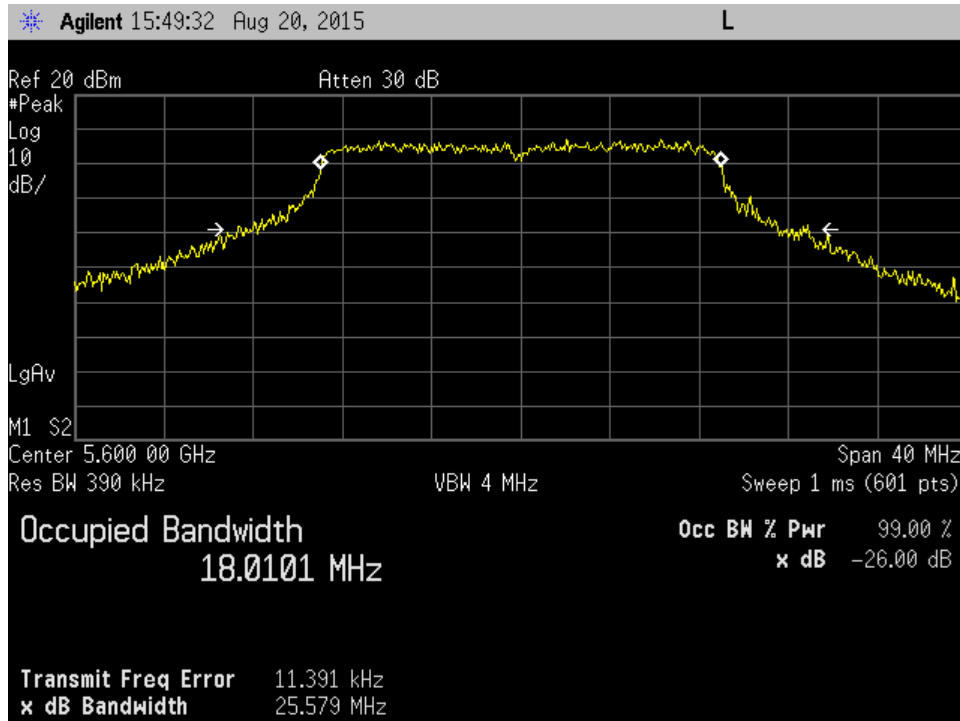


### 20 MHz low ch\_J9

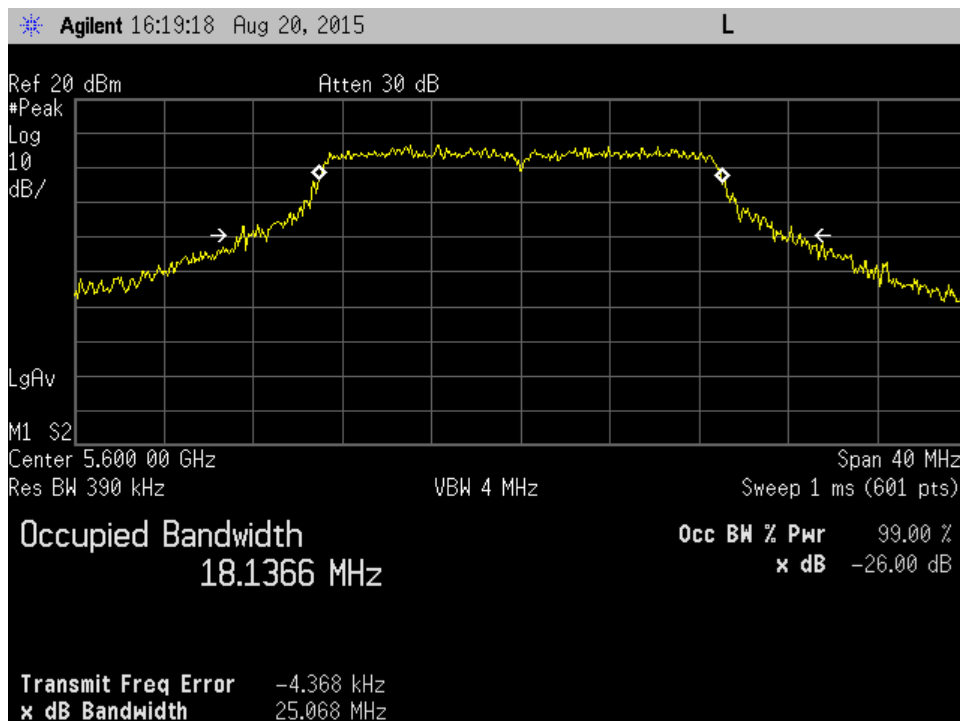




### 20 MHz mid ch\_J5

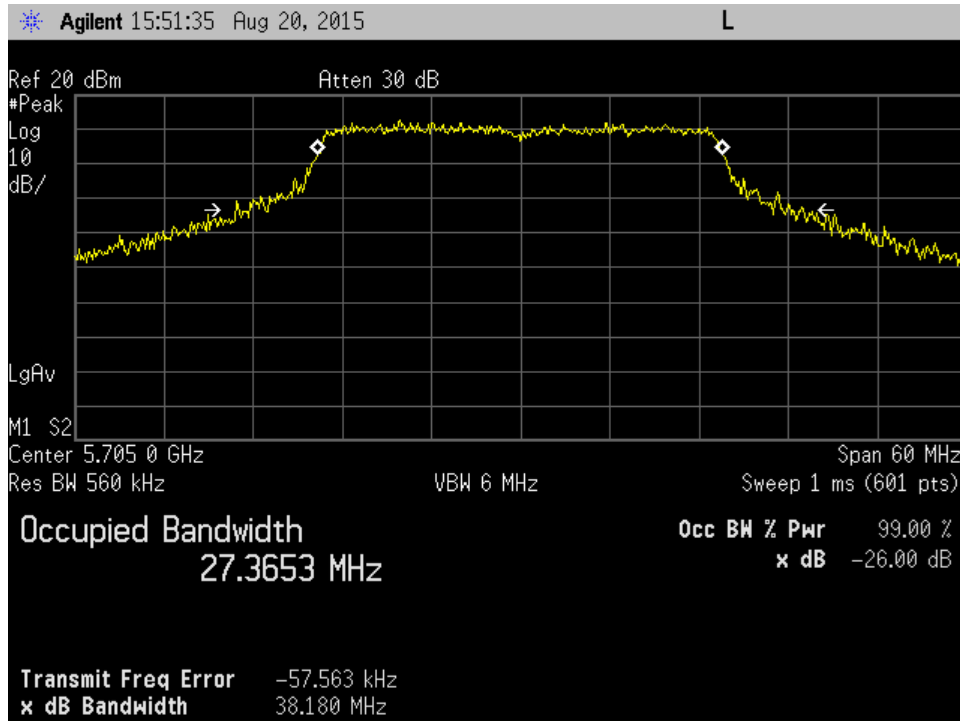


### 20 MHz mid ch\_J9

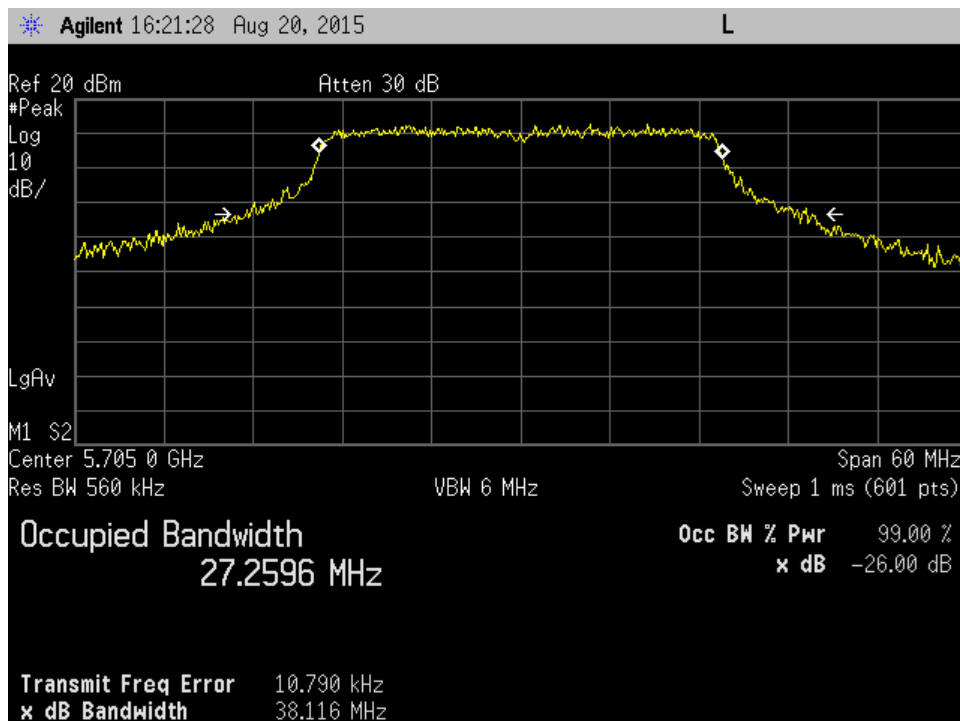




### 30 MHz high ch\_J5

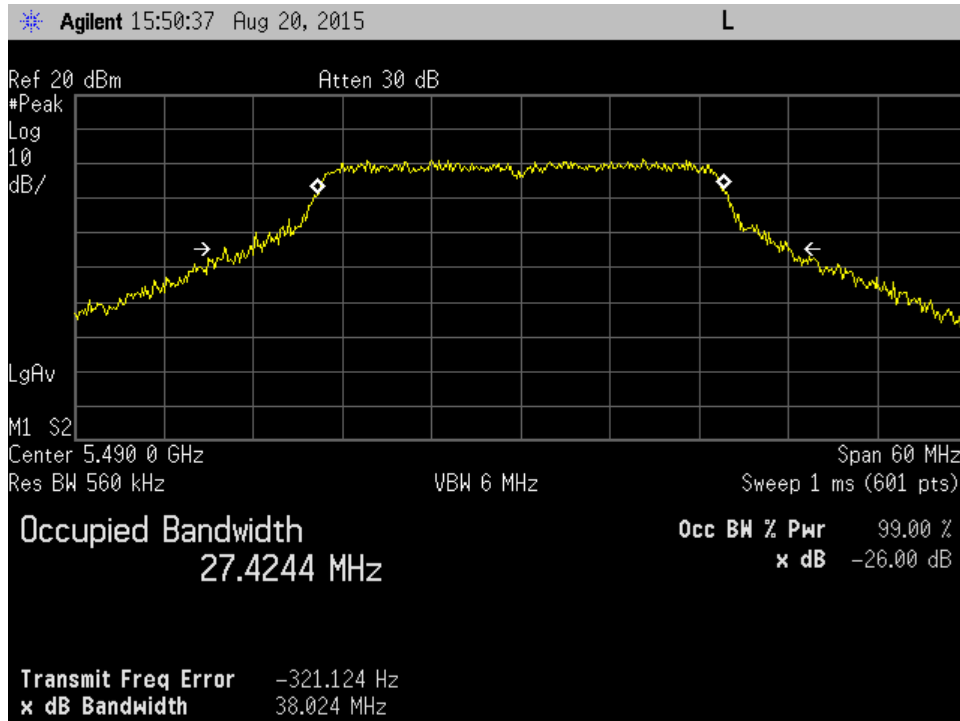


### 30 MHz high ch\_J9

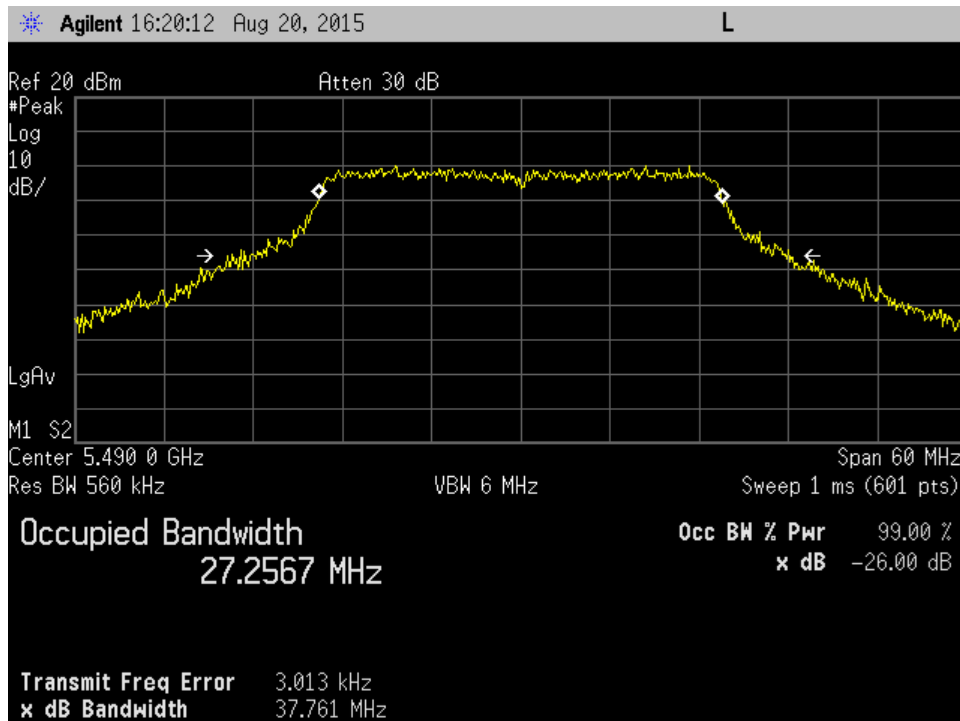




### 30 MHz low ch\_J5

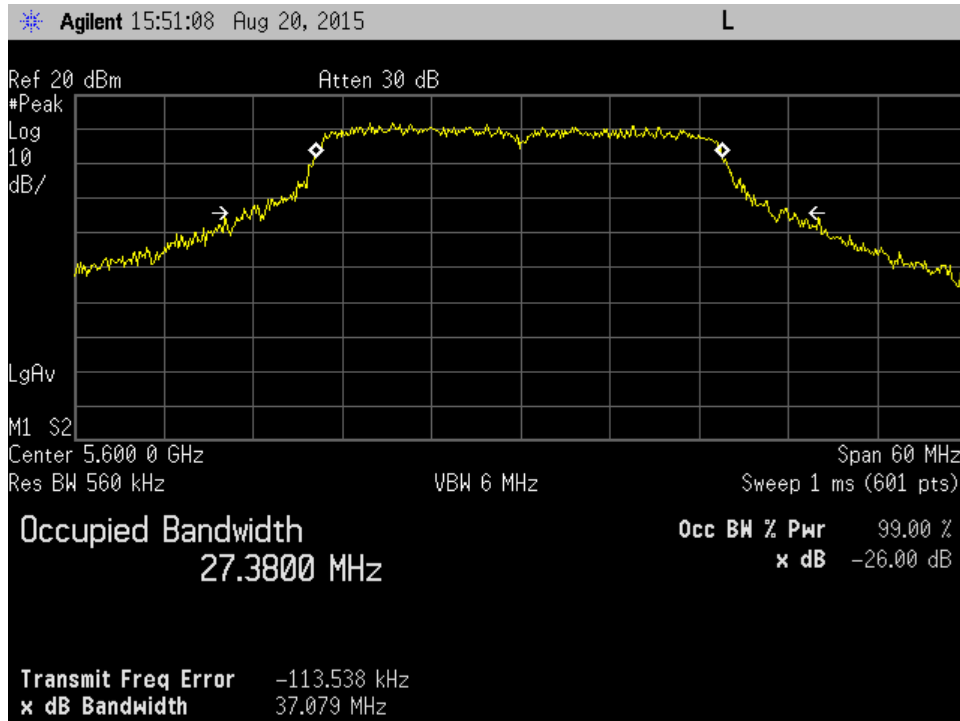


### 30 MHz low ch\_J9

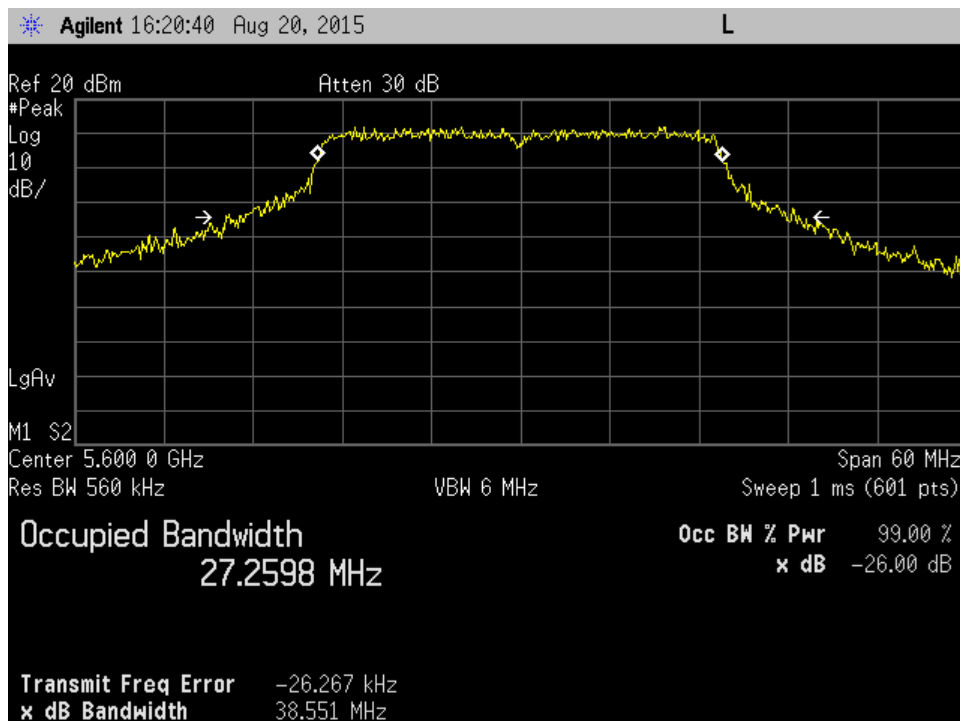




### 30 MHz mid ch\_J5

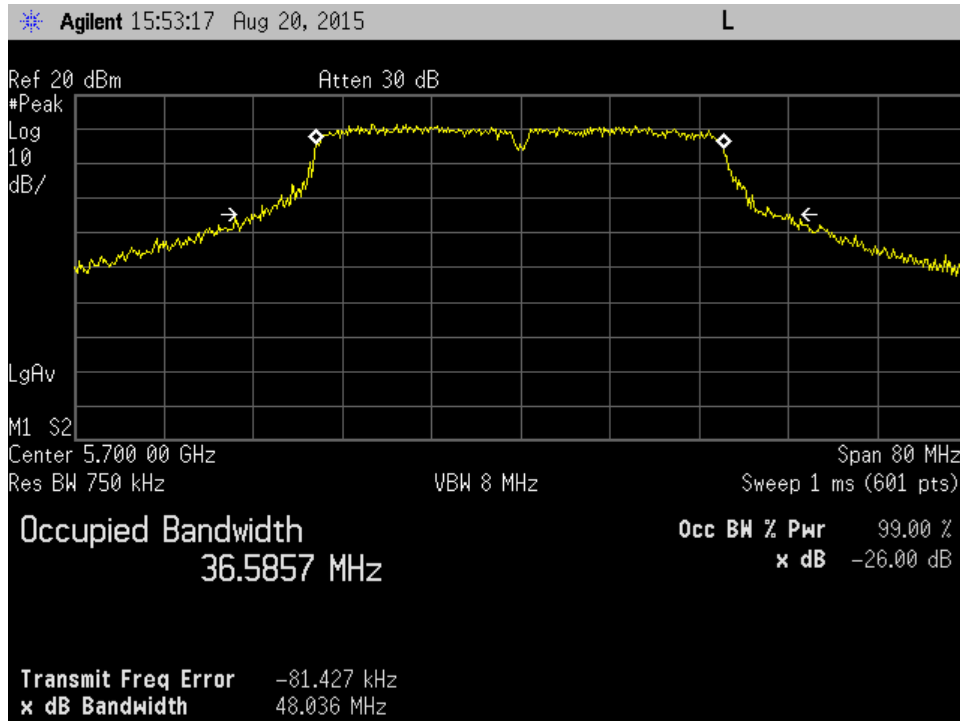


### 30 MHz mid ch\_J9

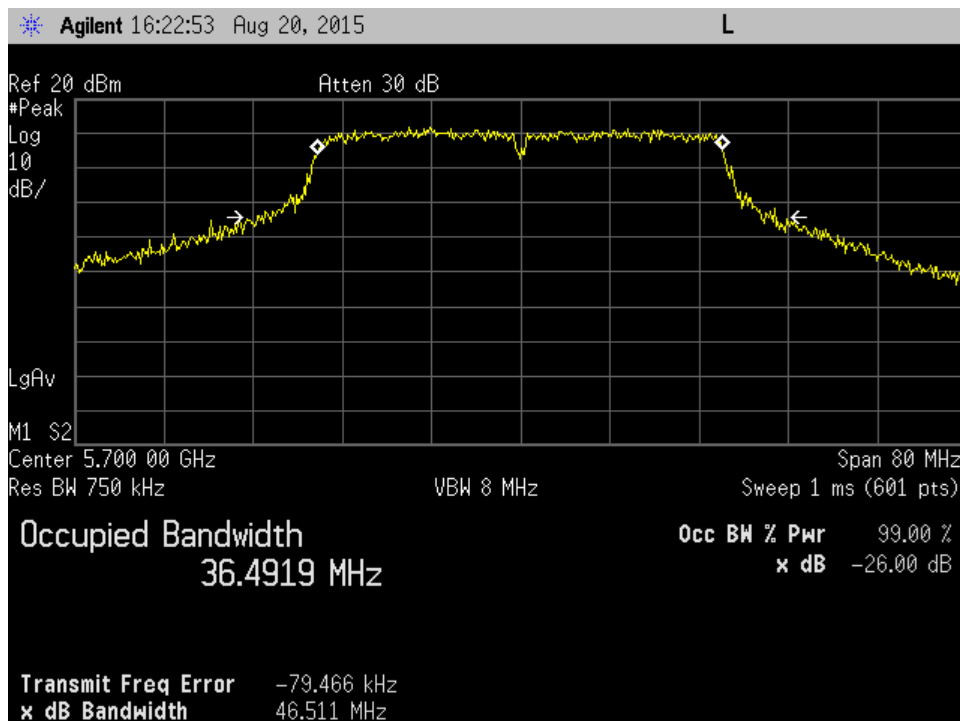




### 40 MHz high ch\_J5



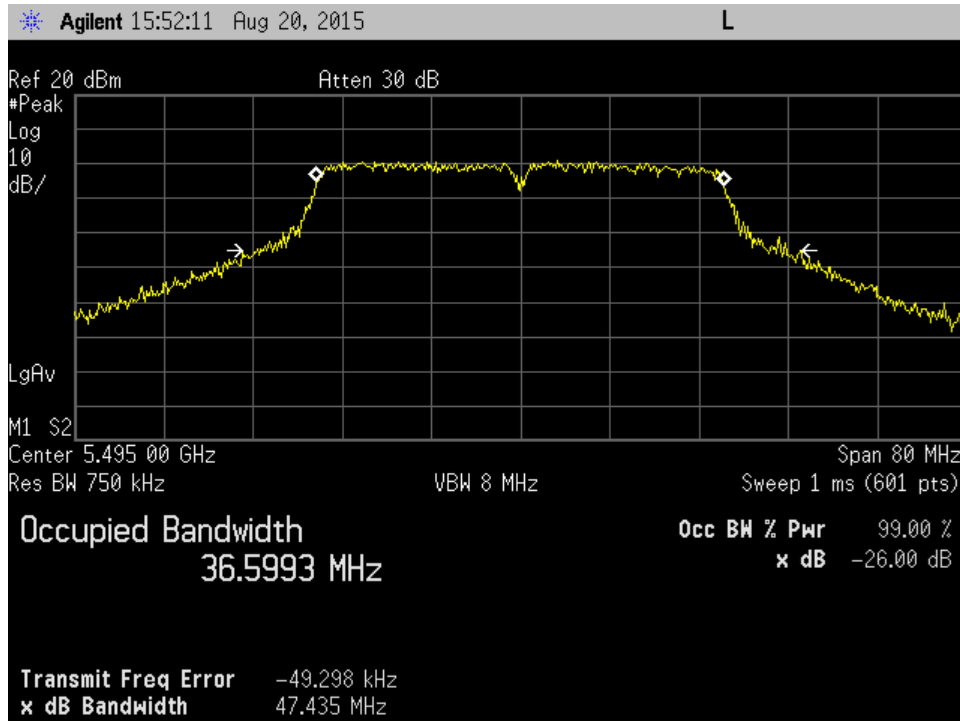
### 40 MHz high ch\_J9



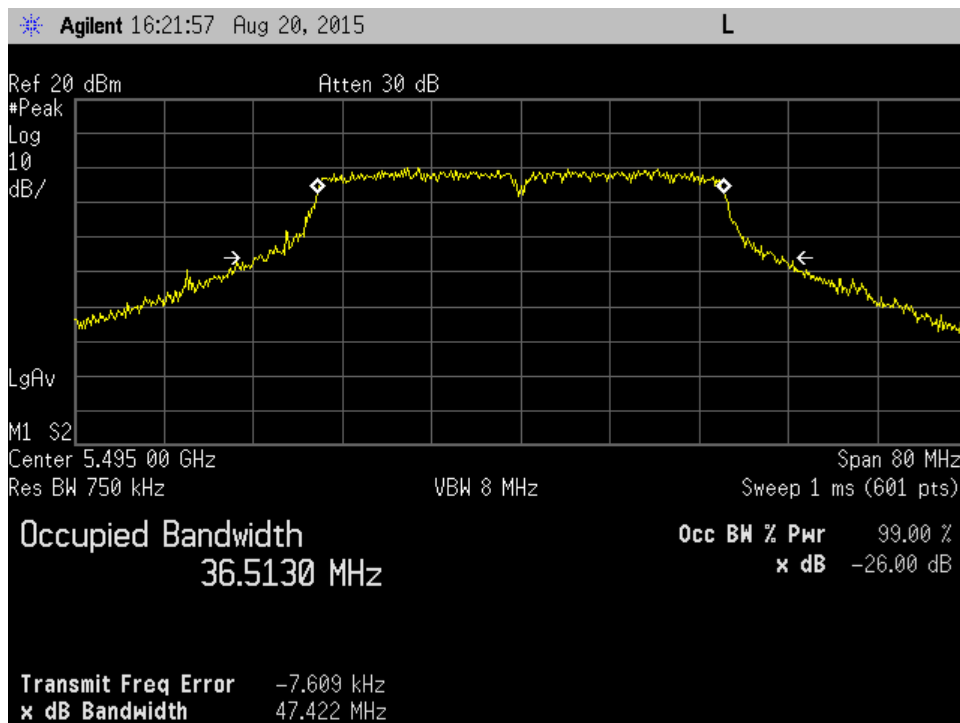




### 40 MHz low ch\_J5

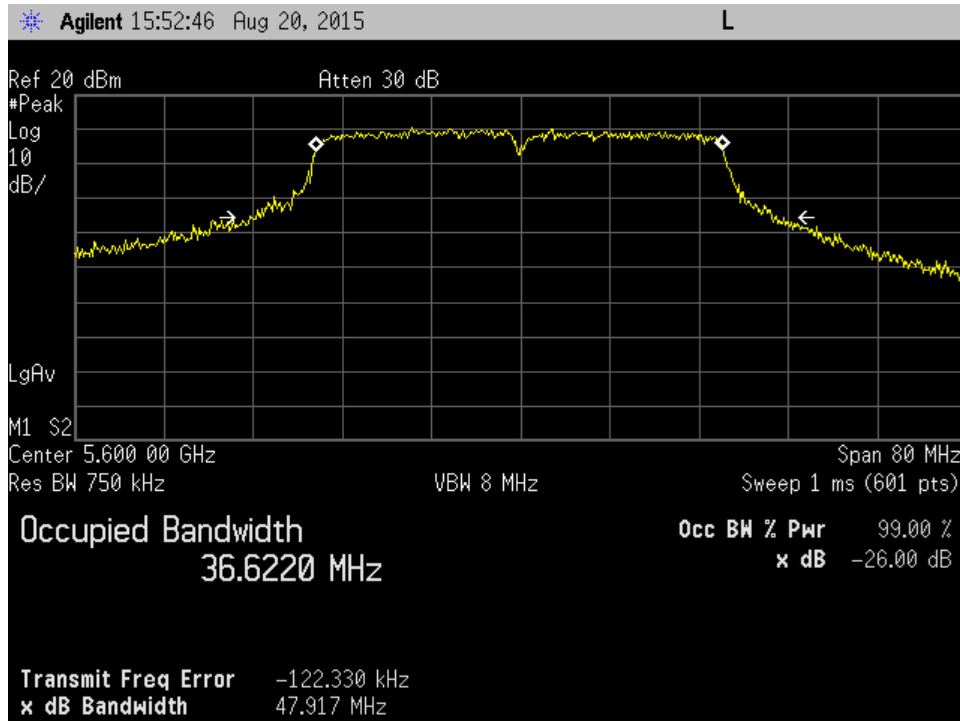


### 40 MHz low ch\_J9

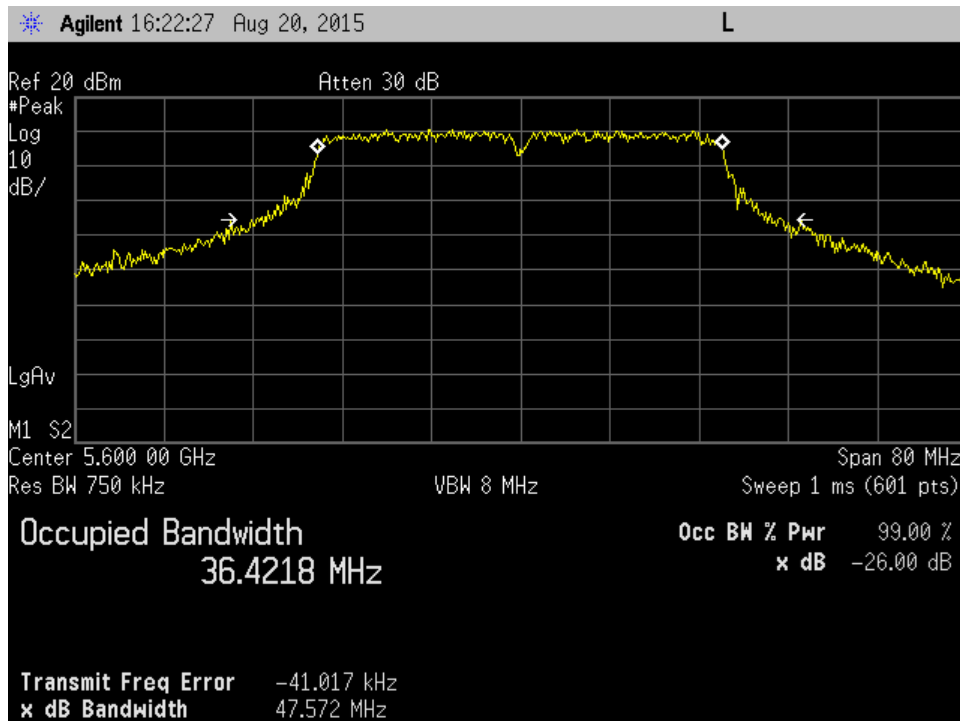




### 40 MHz mid ch\_J5

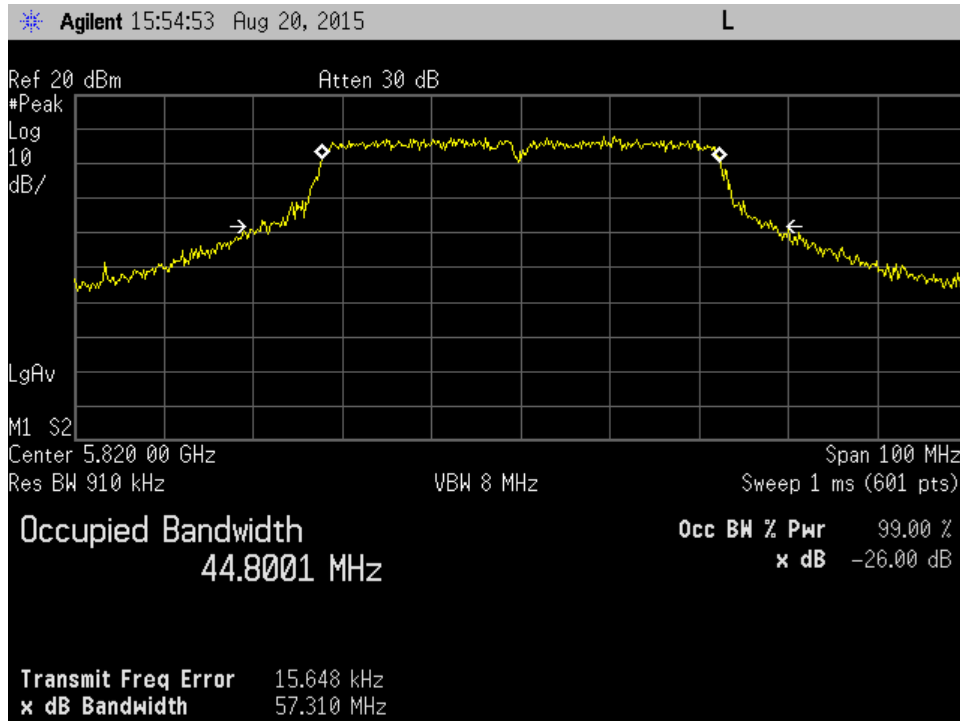


### 40 MHz mid ch\_J9

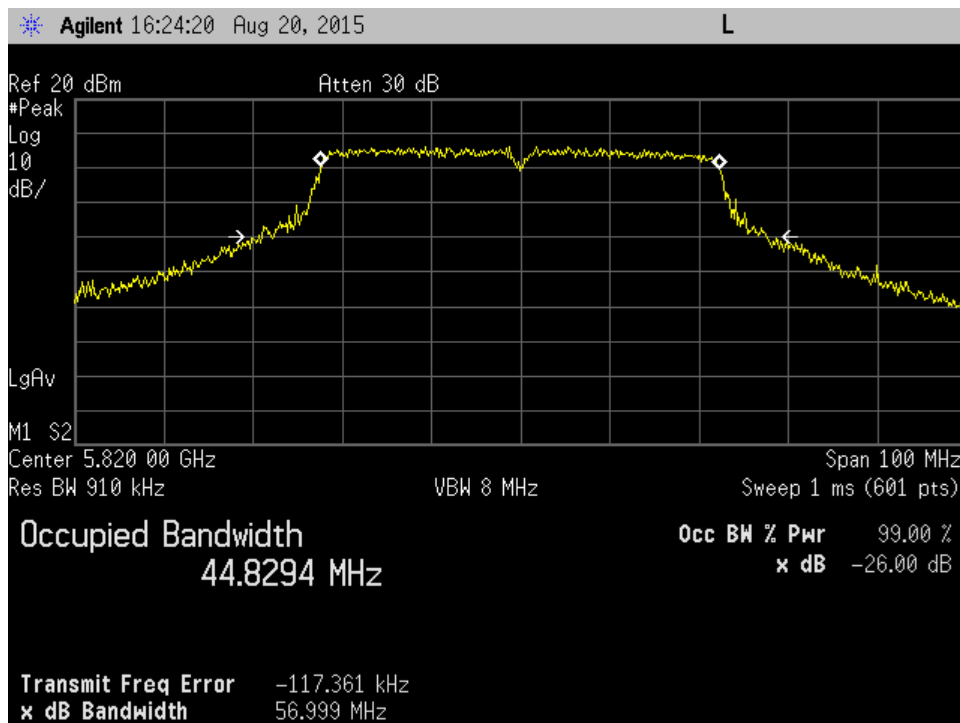




### 50 MHz high ch\_J5

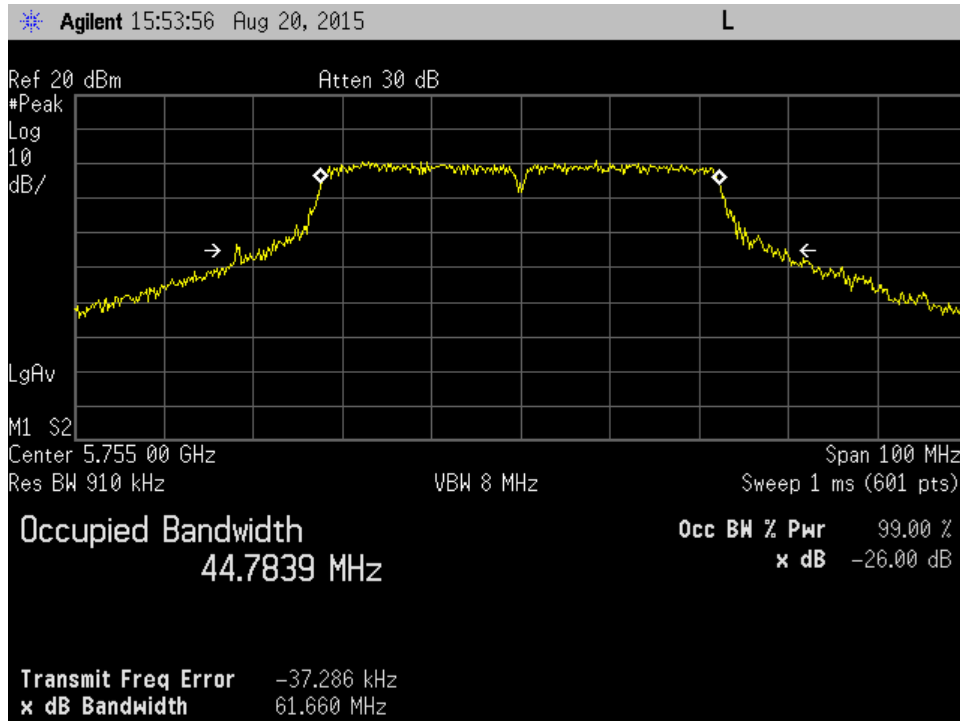


### 50 MHz high ch\_J9

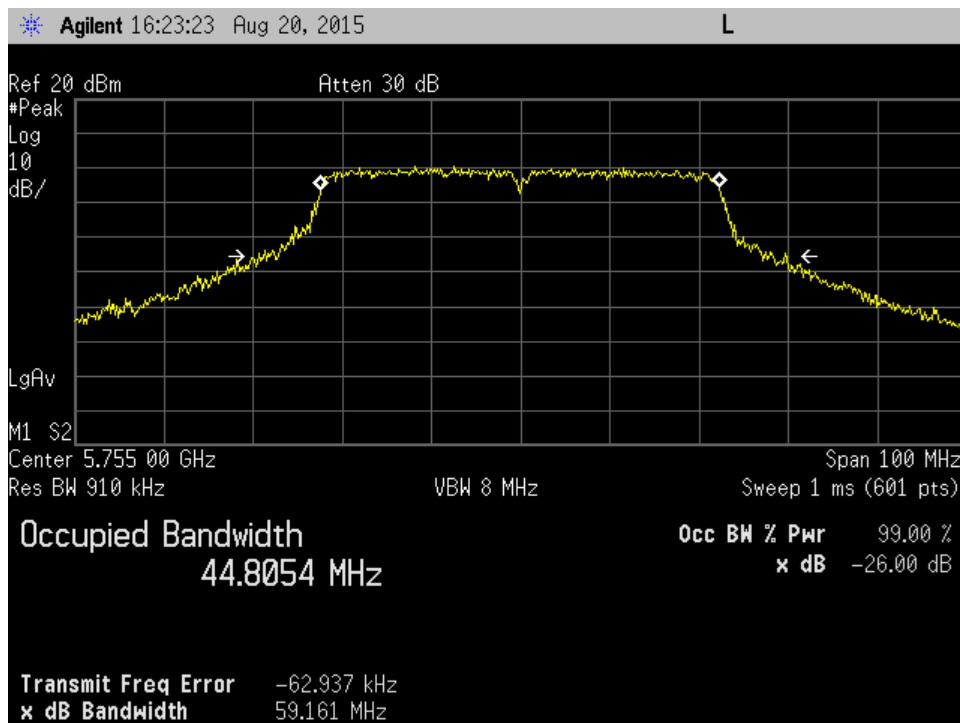




### 50 MHz low ch\_J5

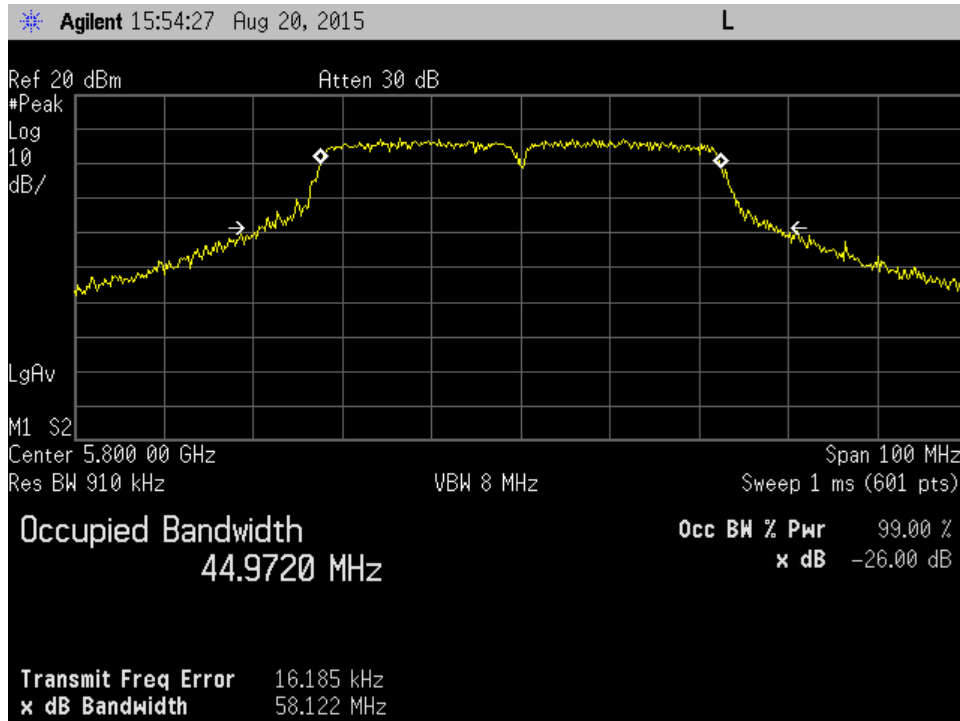


### 50 MHz low ch\_J9

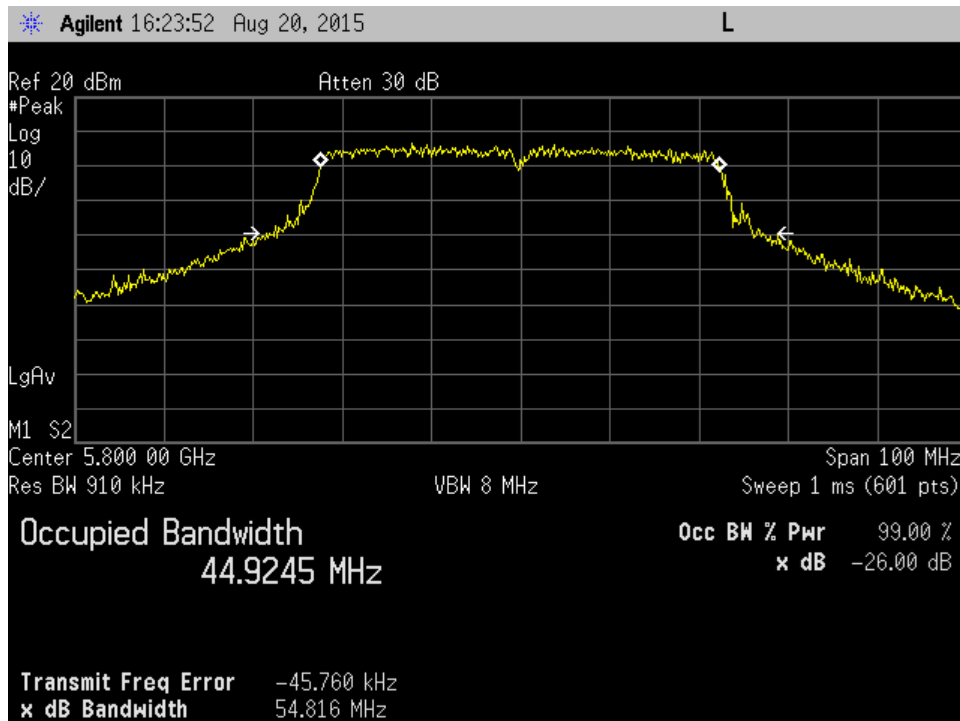




### 50 MHz mid ch\_J5

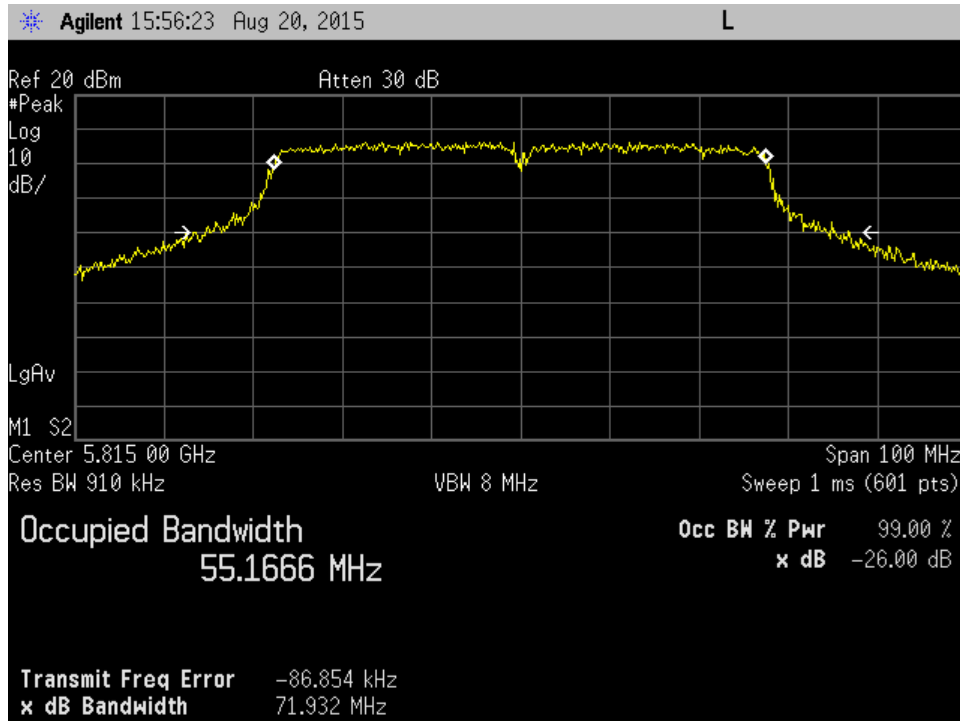


### 50 MHz mid ch\_J9

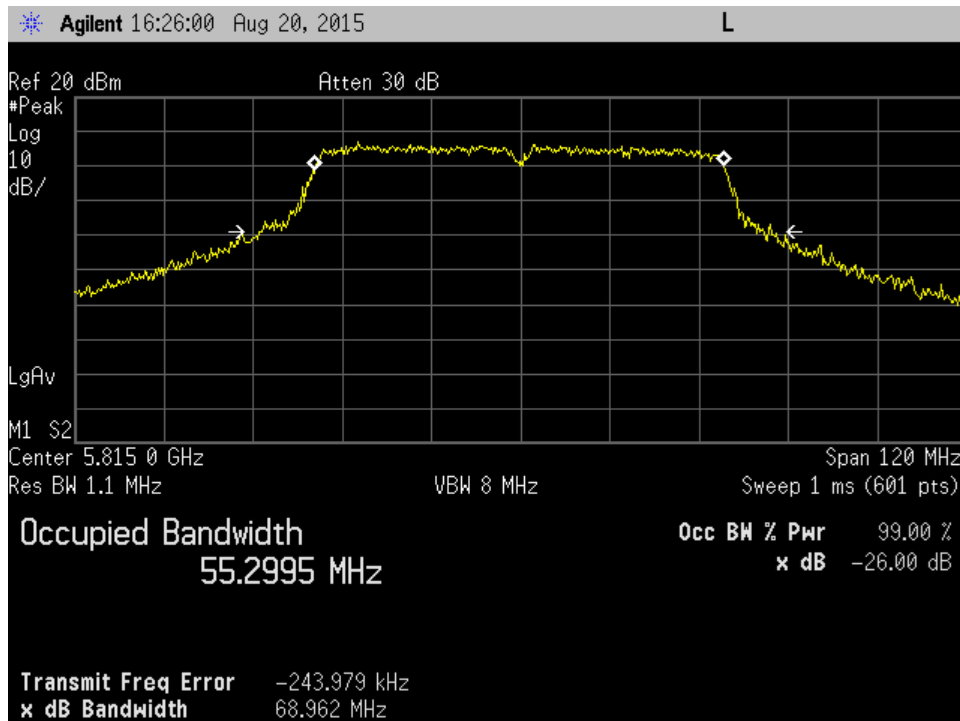




### 60 MHz high ch\_J5

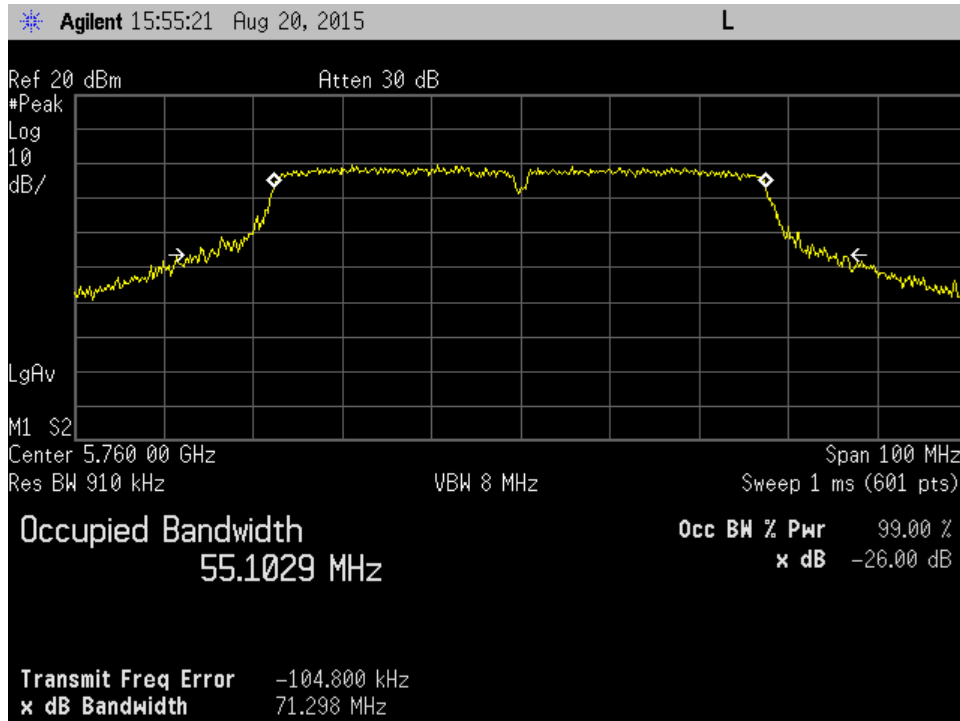


### 60 MHz high ch\_J9

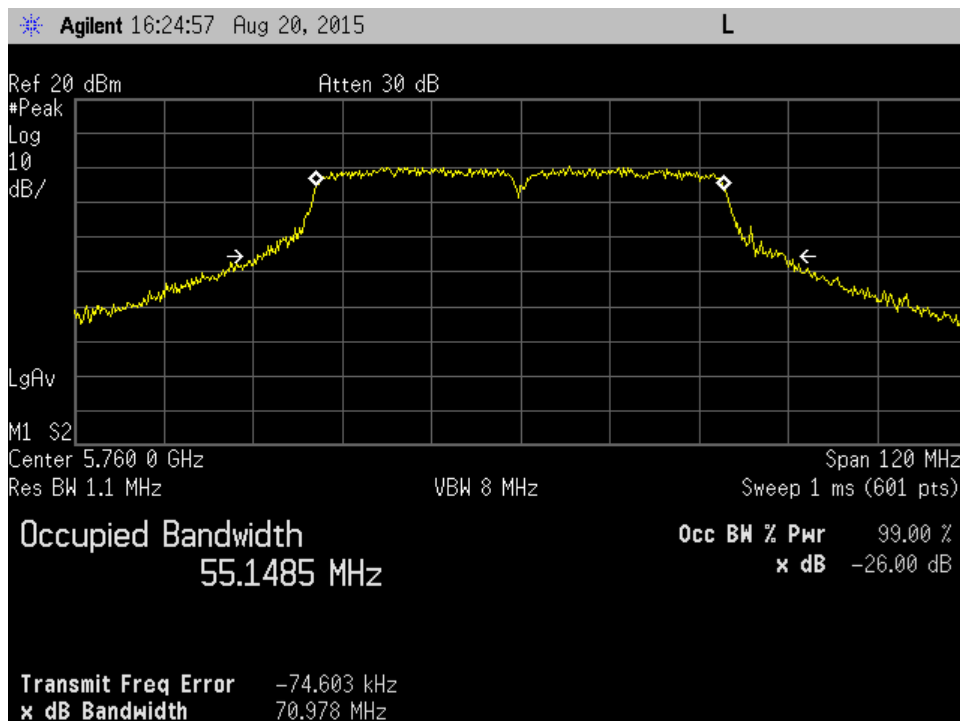




### 60 MHz low ch\_J5

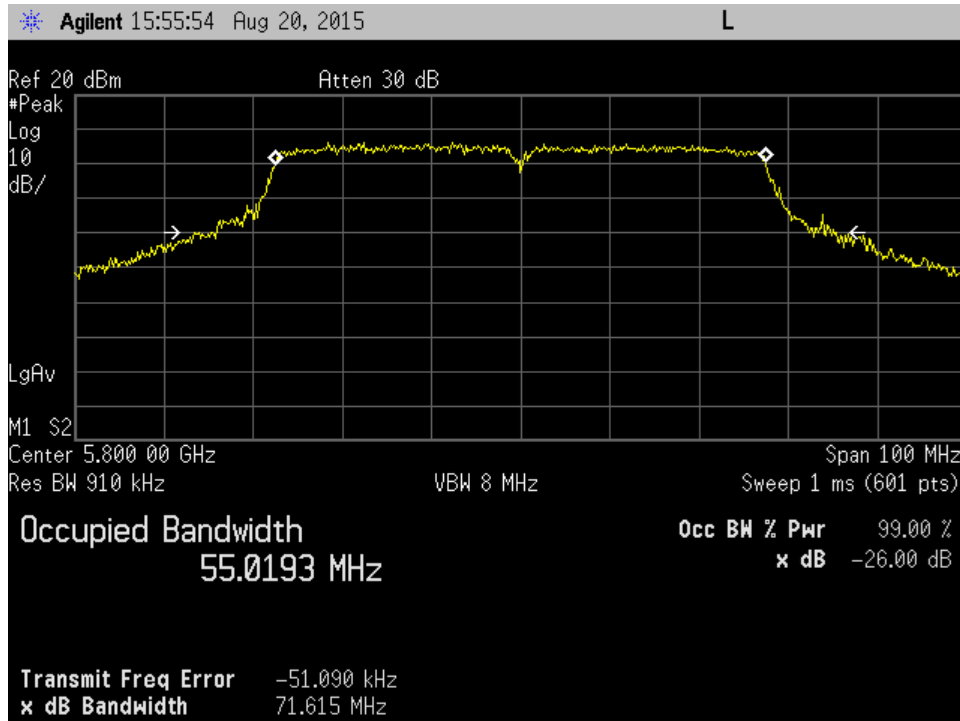


### 60 MHz low ch\_J9

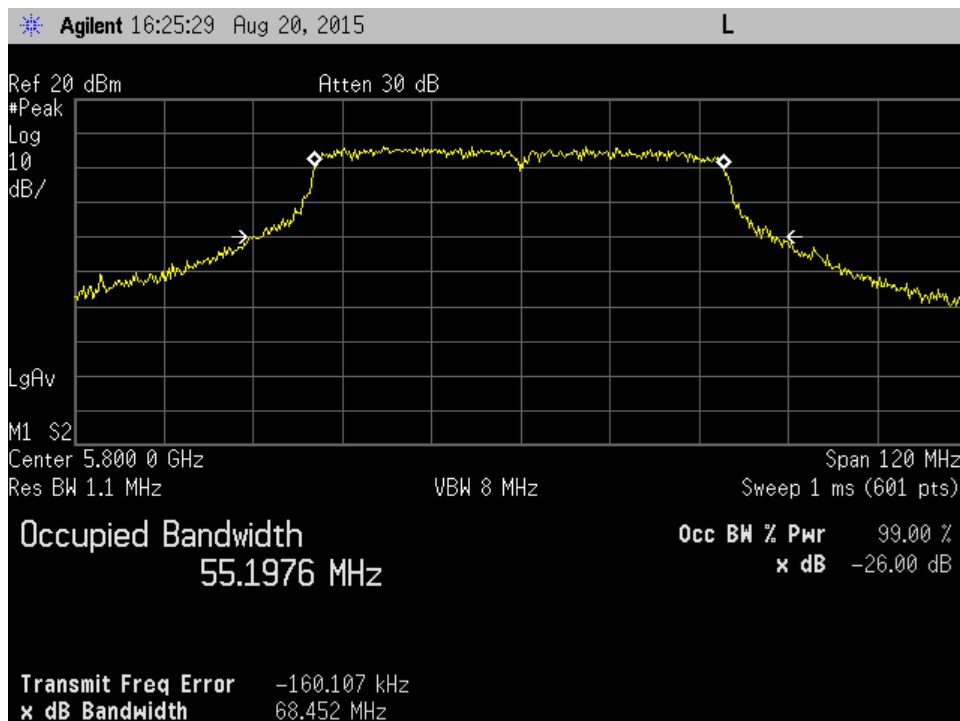




### 60 MHz mid ch\_J5



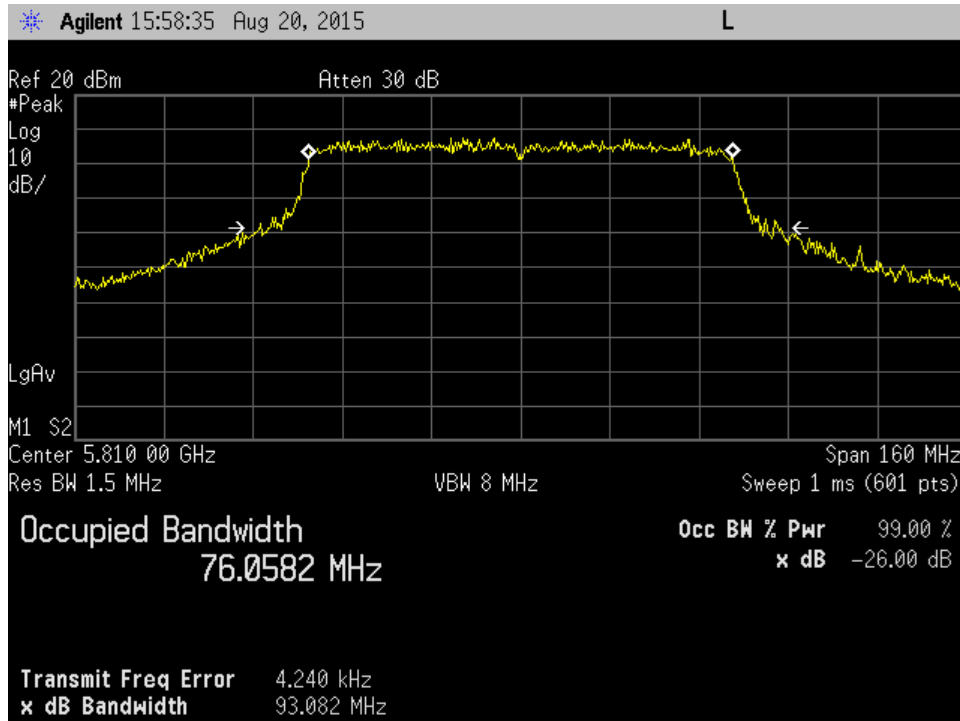
### 60 MHz mid ch\_J9



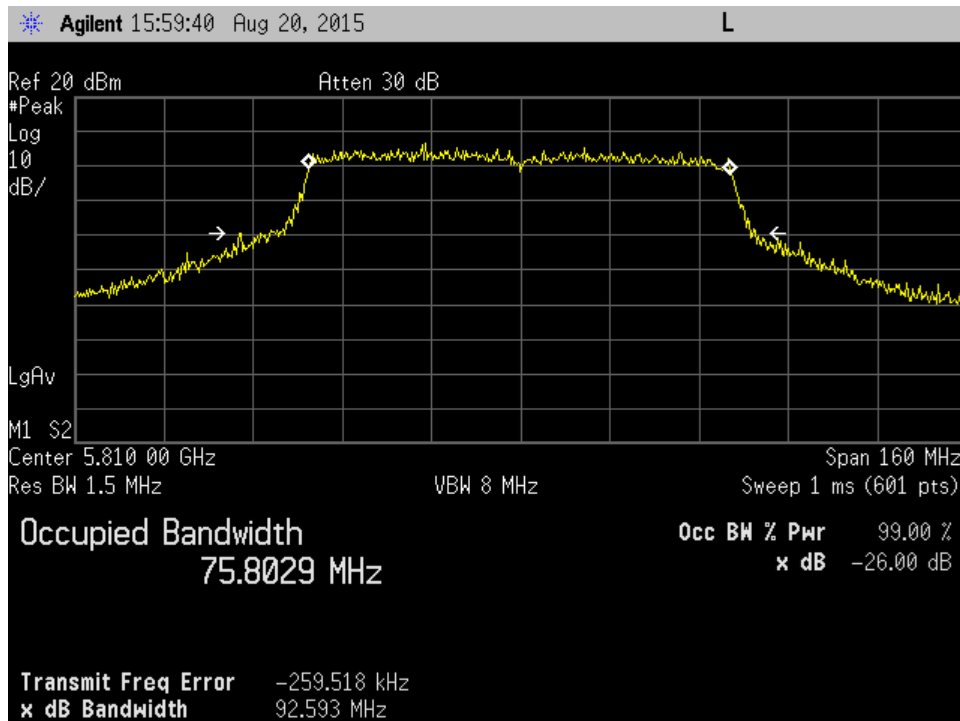




### 80 MHz high ch\_J5

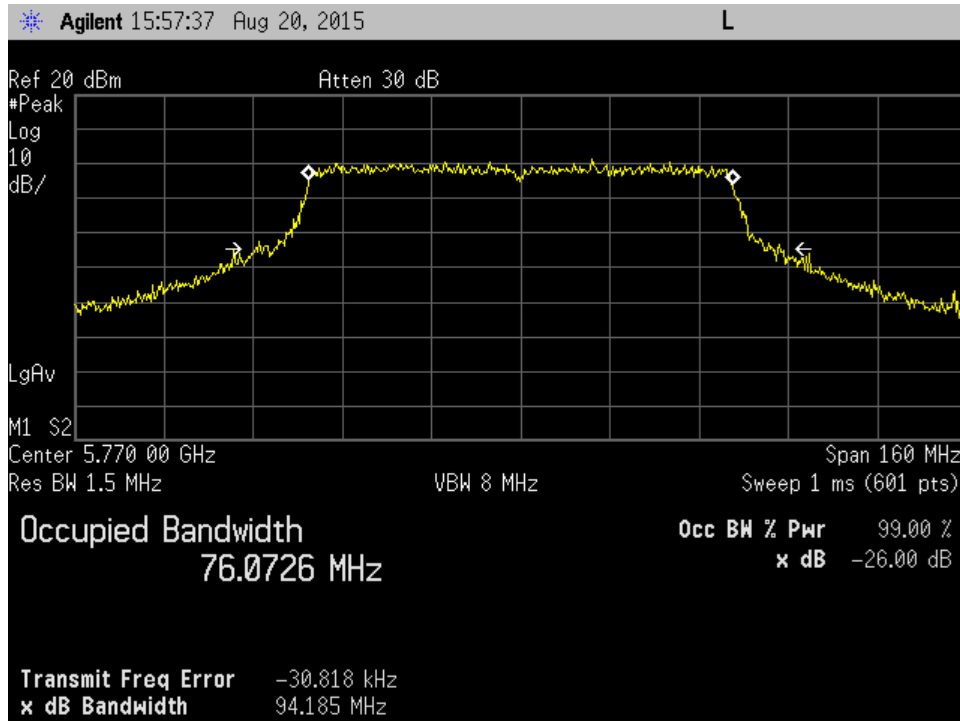


### 80 MHz high ch\_J9

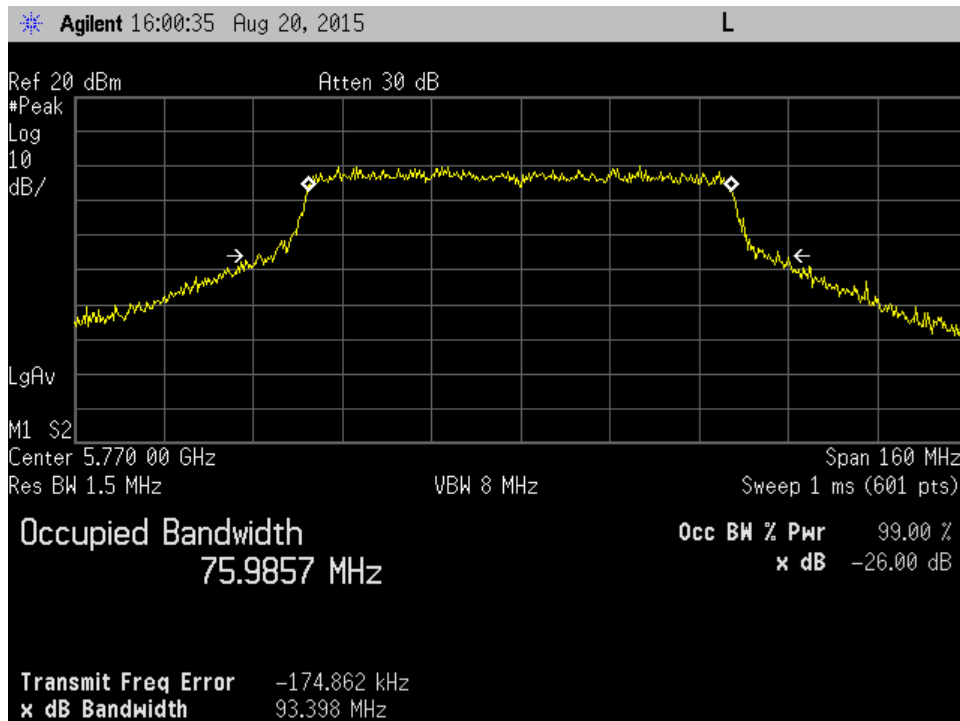




### 80 MHz low ch\_J5

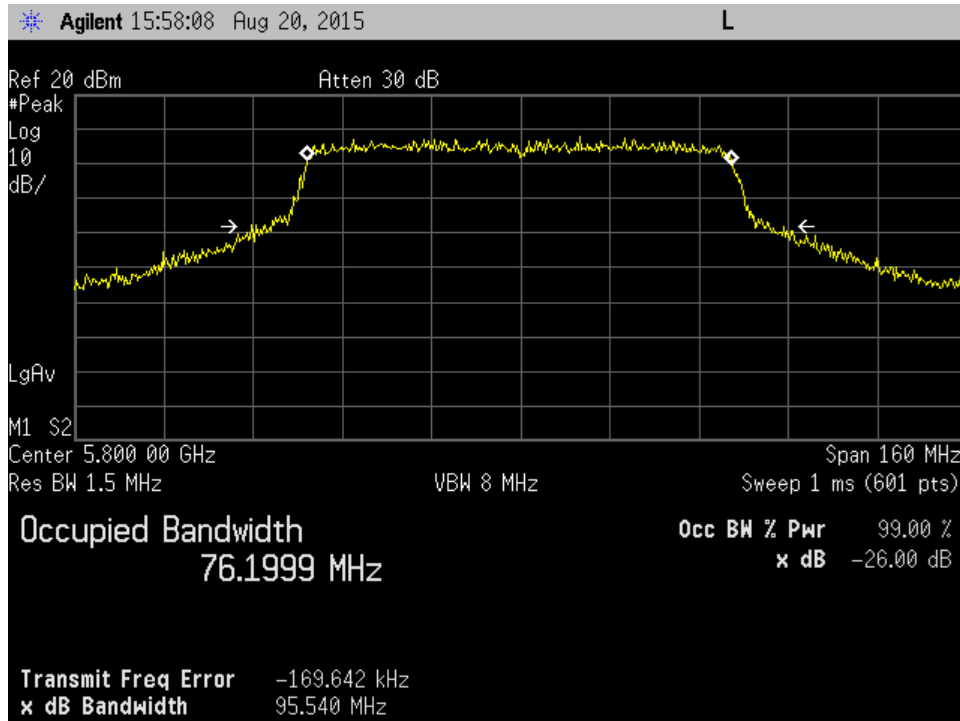


### 80 MHz low ch\_J9





### 80 MHz mid ch\_J5



### 80 MHz mid ch\_J9

