

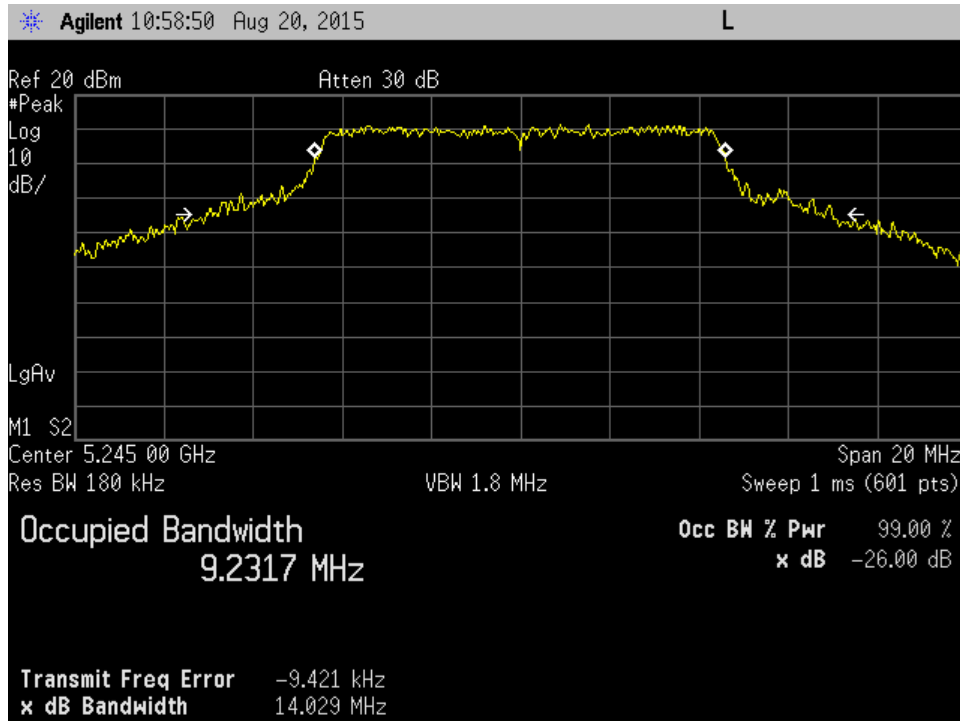


Annex C

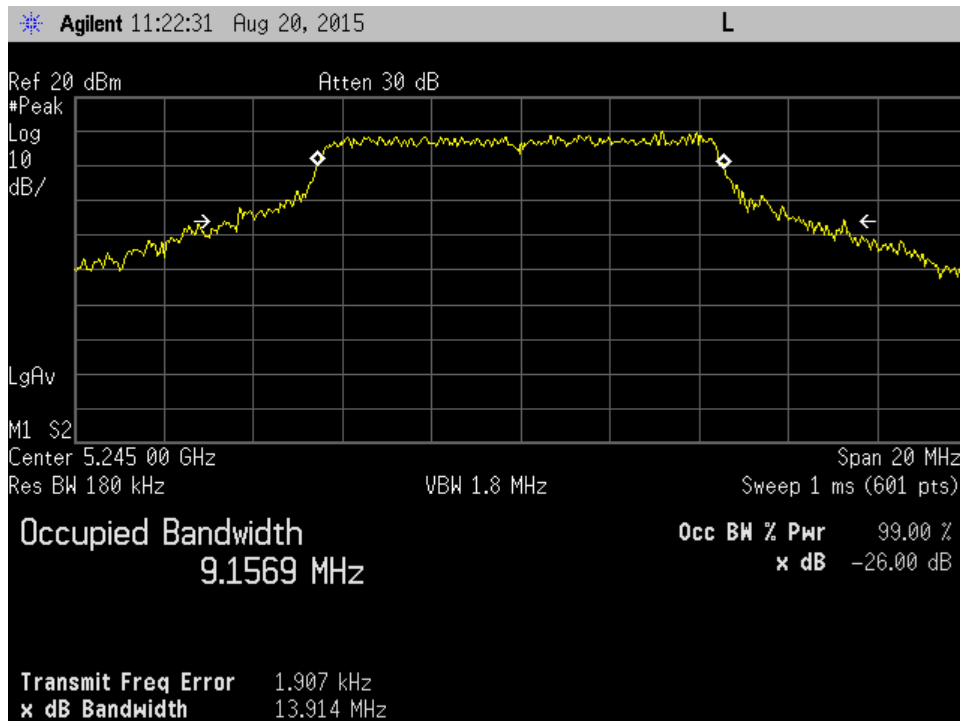
Occupied Bandwidth



10 MHz high ch_J5



10 MHz high ch_J9

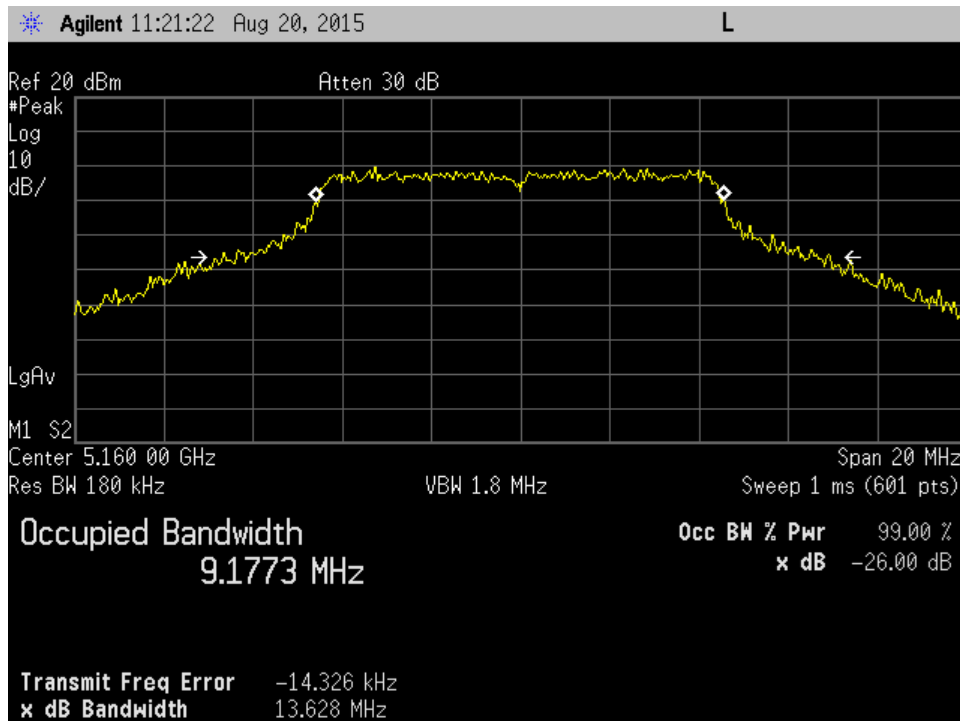




10 MHz low ch_J5

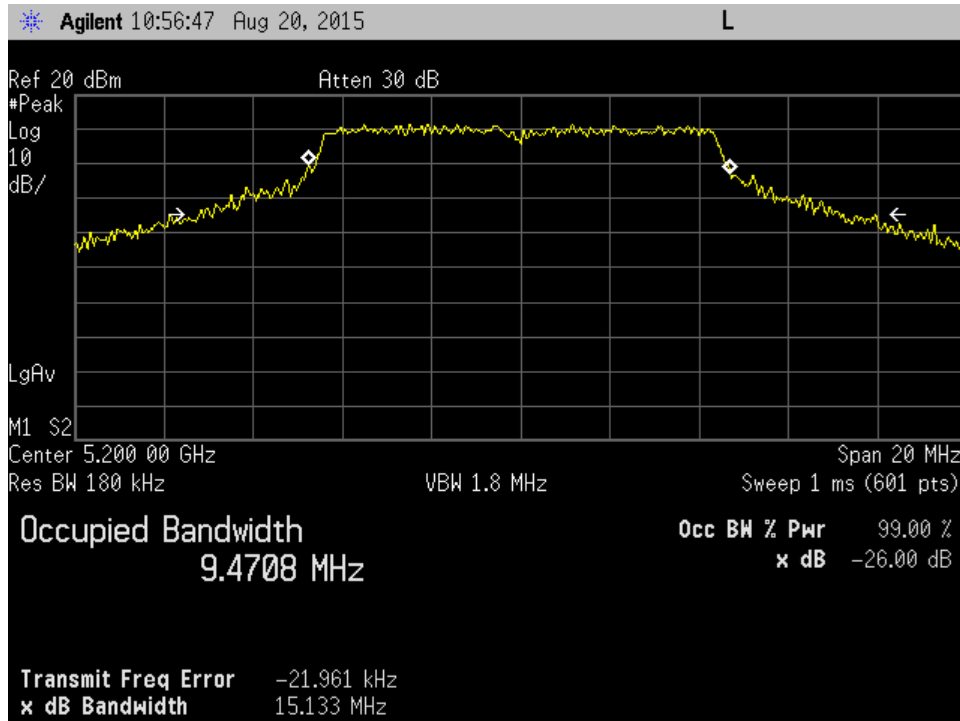


10 MHz low ch_J9

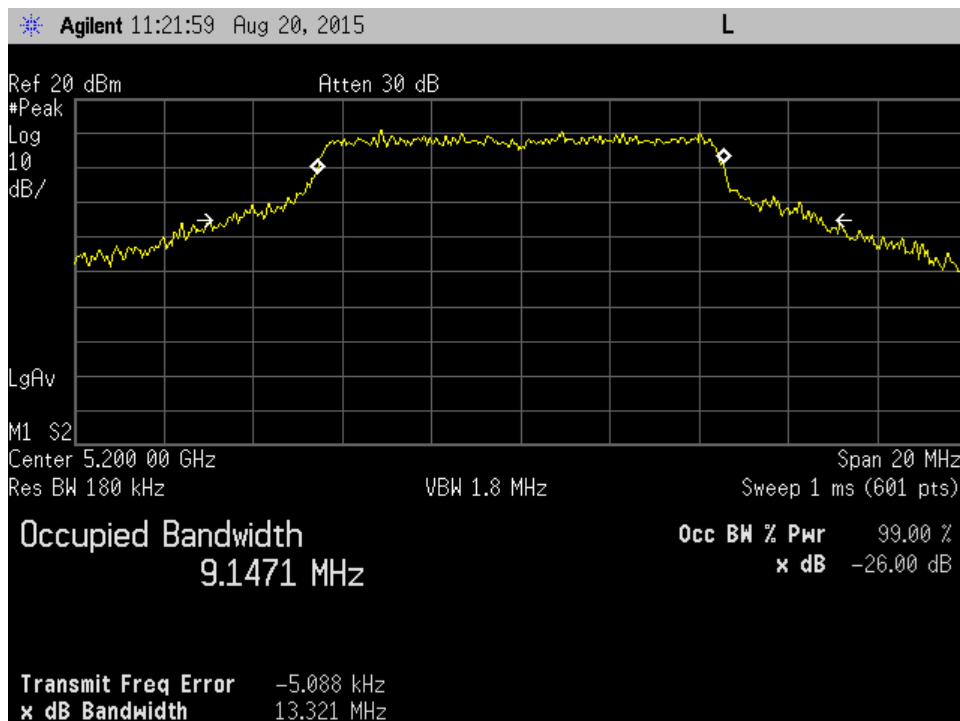




10 MHz mid ch_J5

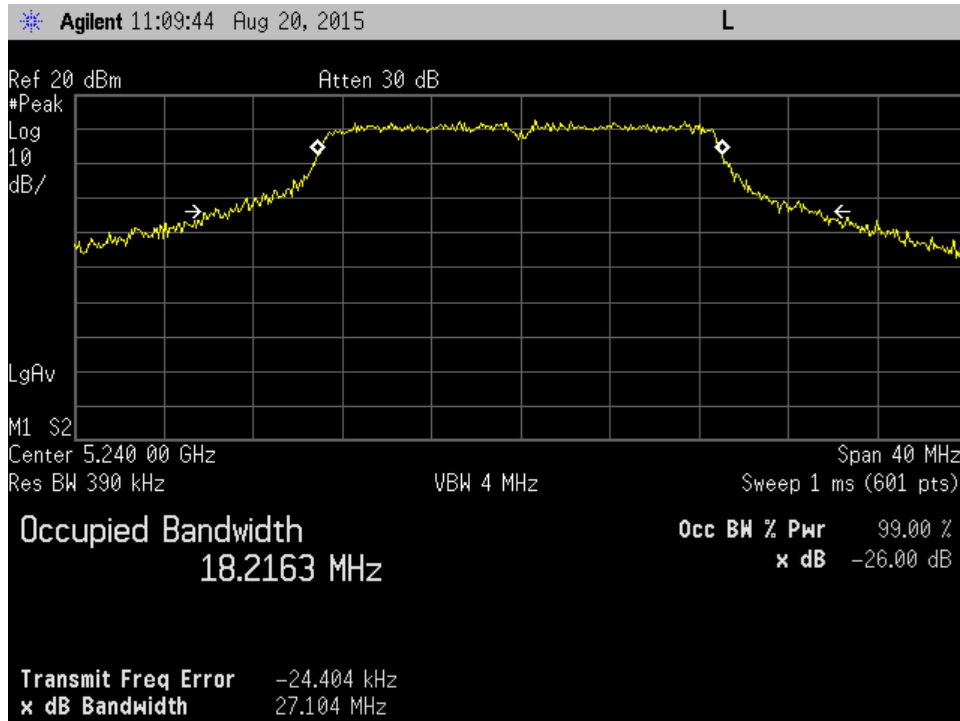


10 MHz mid ch_J9

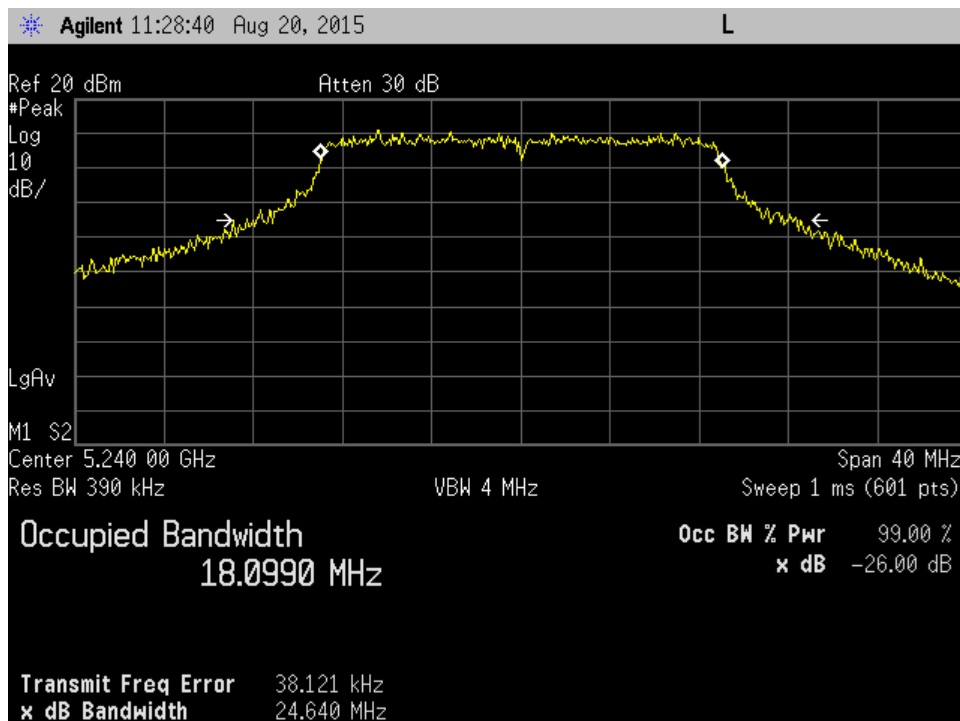




20 MHz high ch_J5

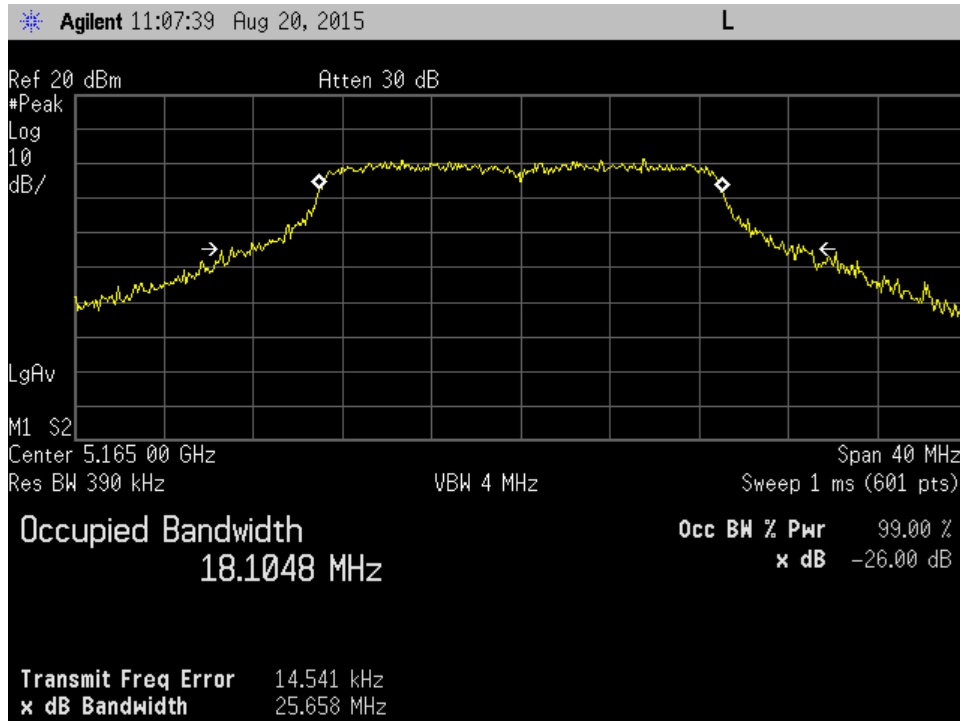


20 MHz high ch_J9

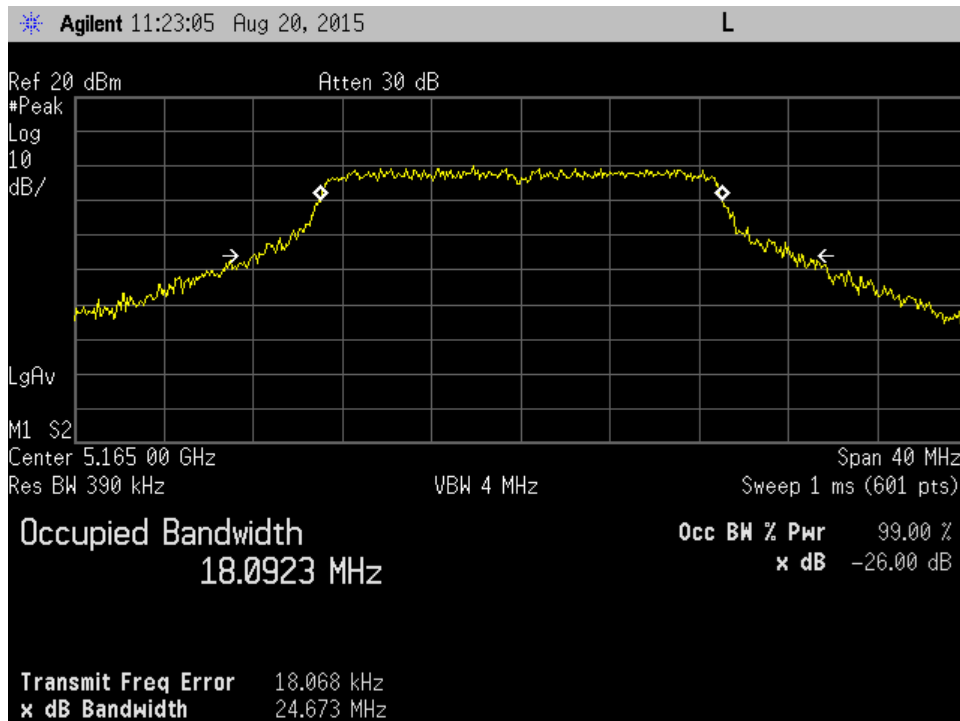




20 MHz low ch_J5

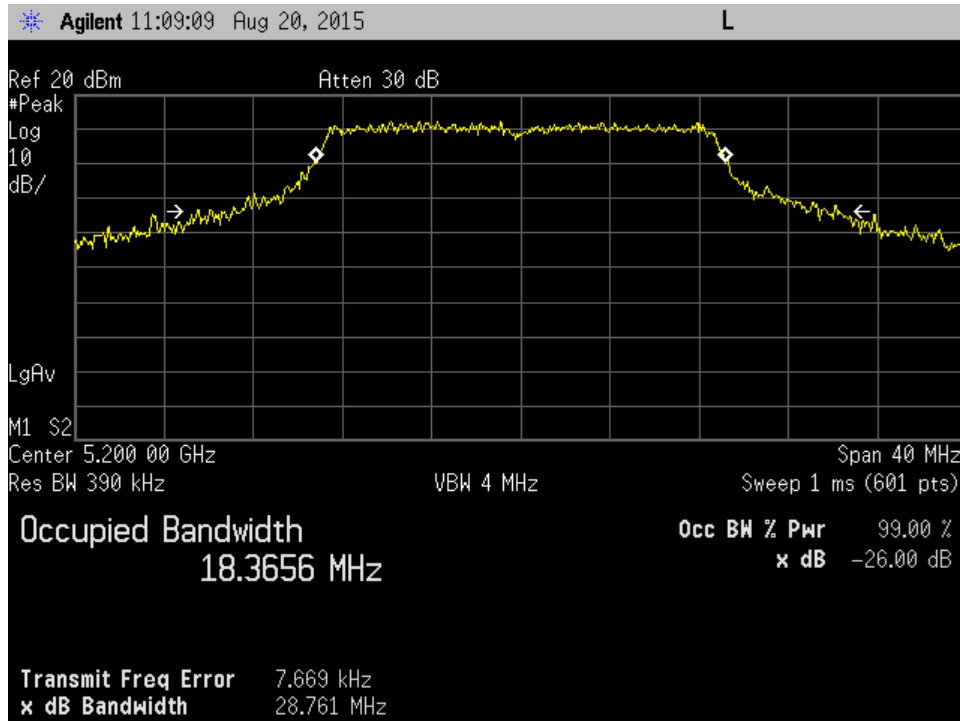


20 MHz low ch_J9

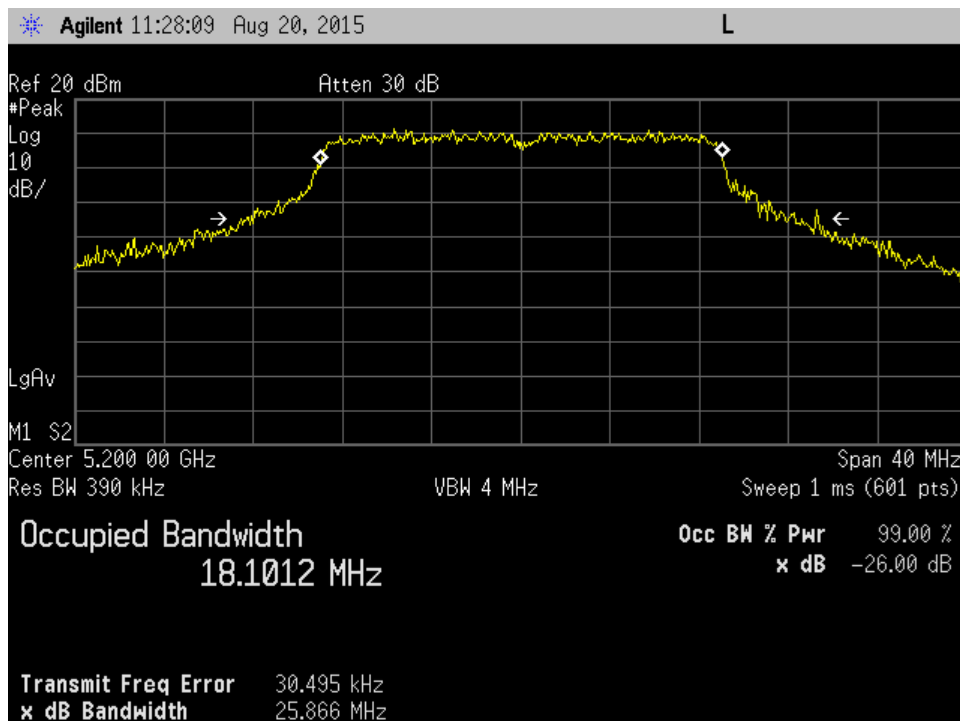




20 MHz mid ch_J5

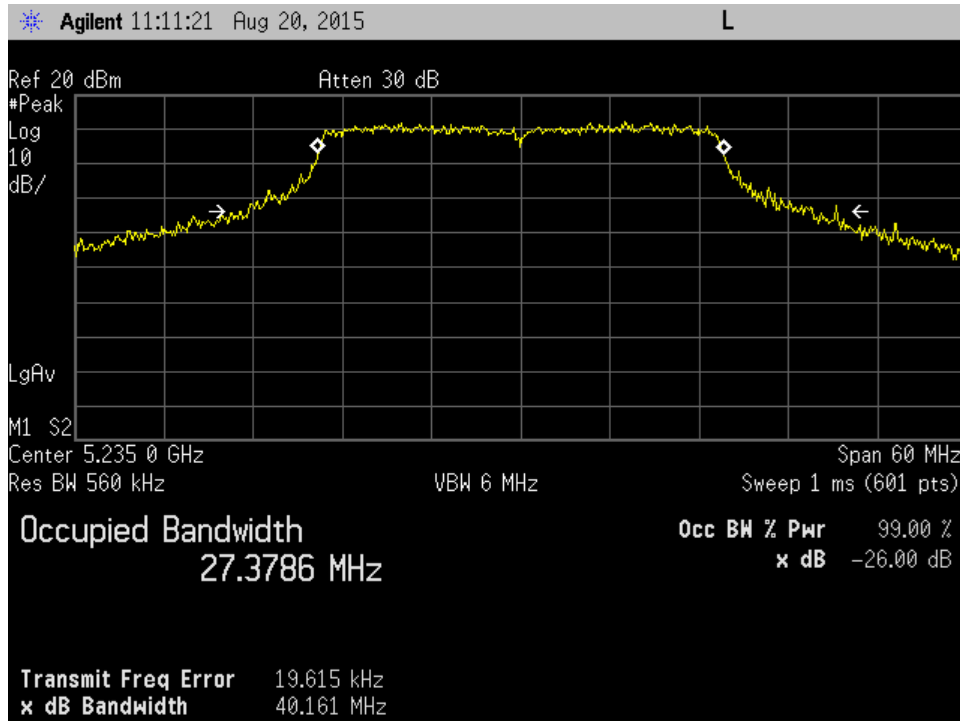


20 MHz mid ch_J9

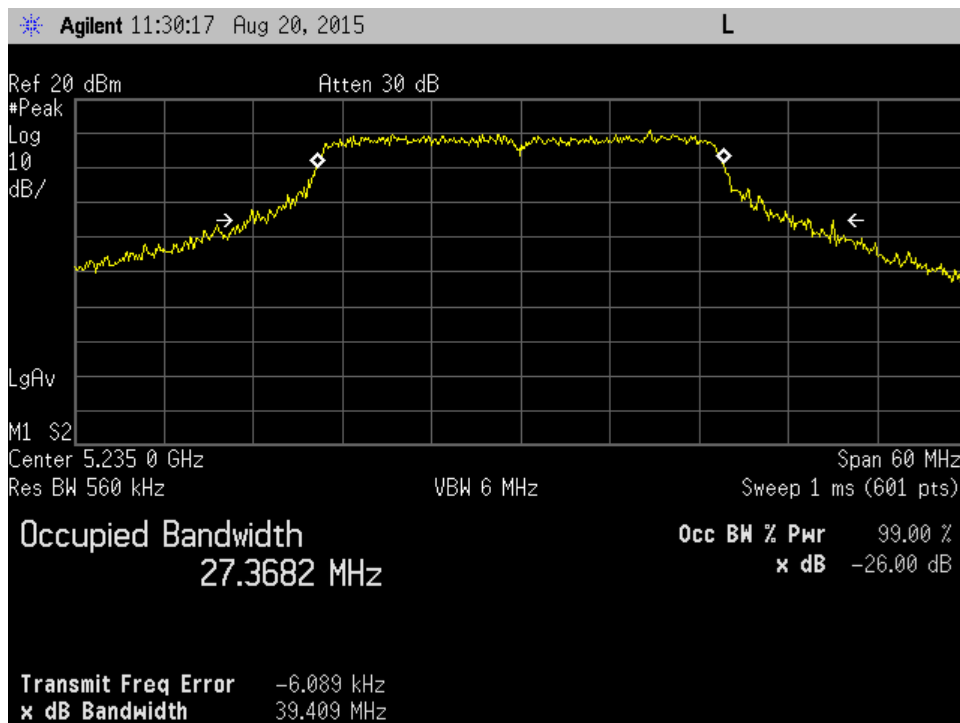




30 MHz high ch_J5

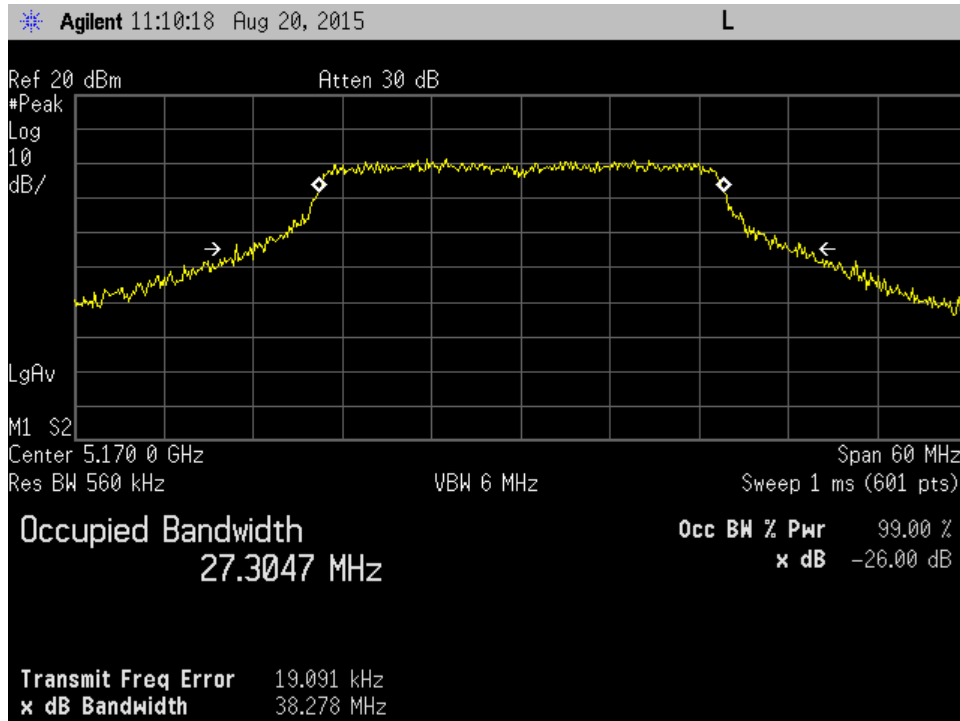


30 MHz high ch_J9

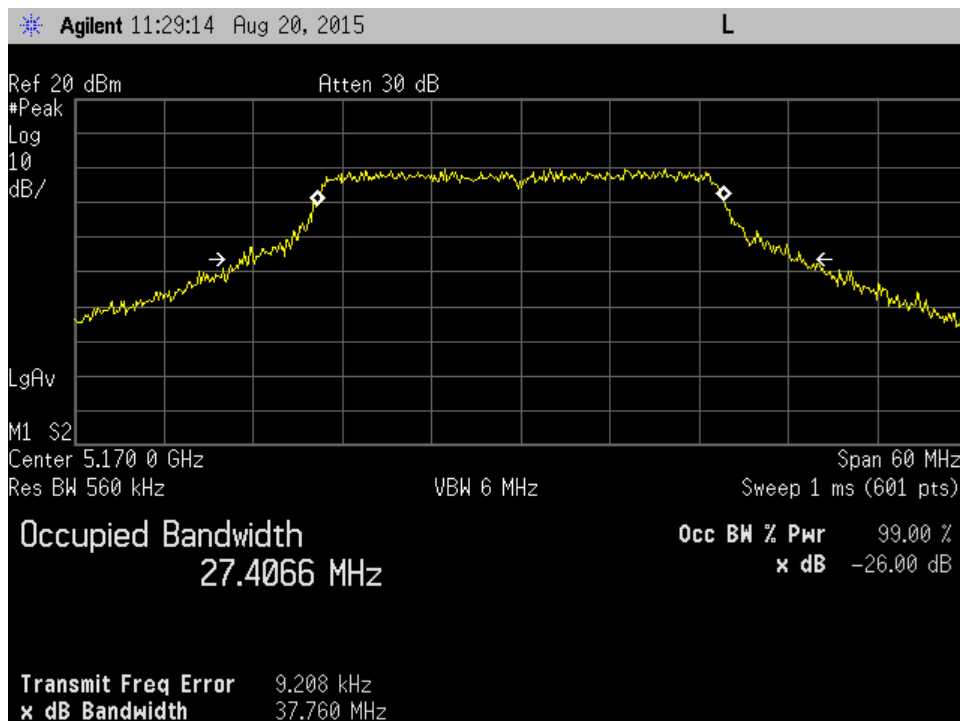




30 MHz low ch_J5

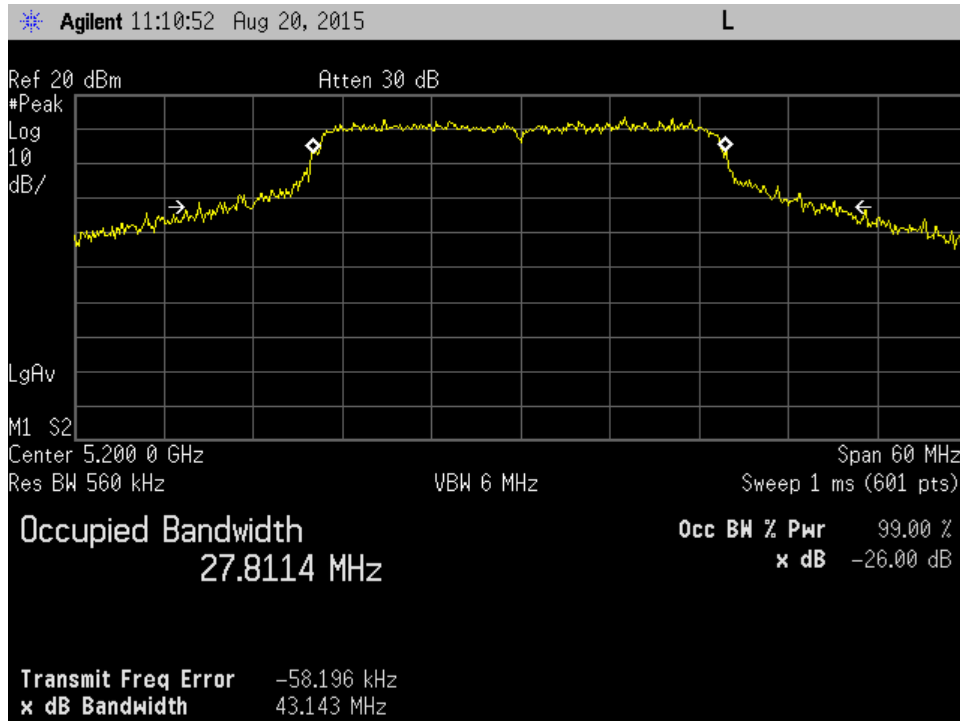


30 MHz low ch_J9

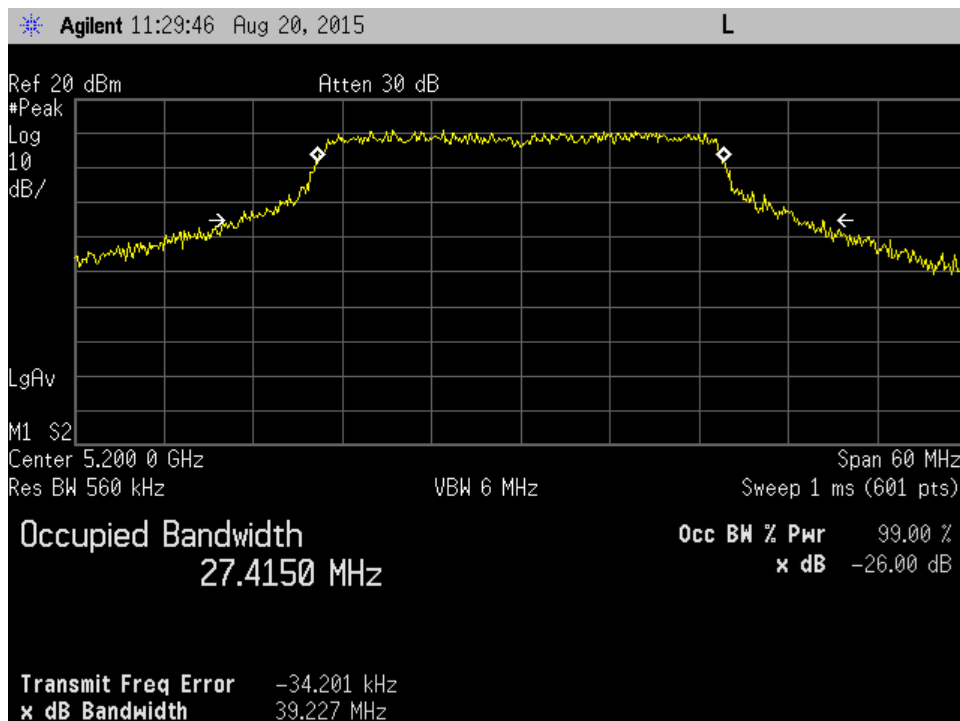




30 MHz mid ch_J5

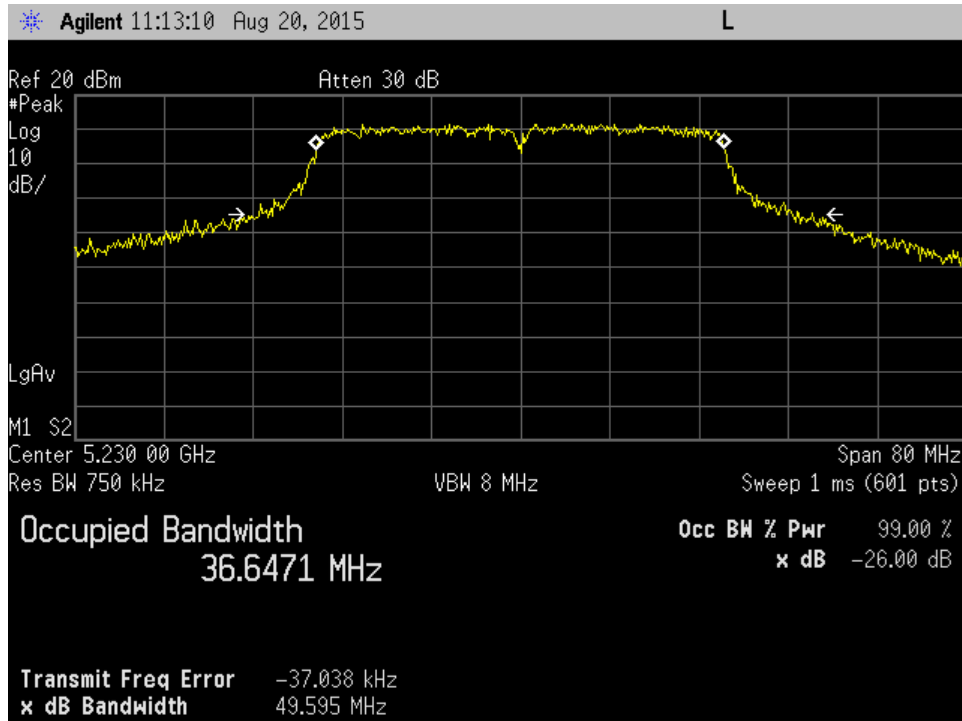


30 MHz mid ch_J9

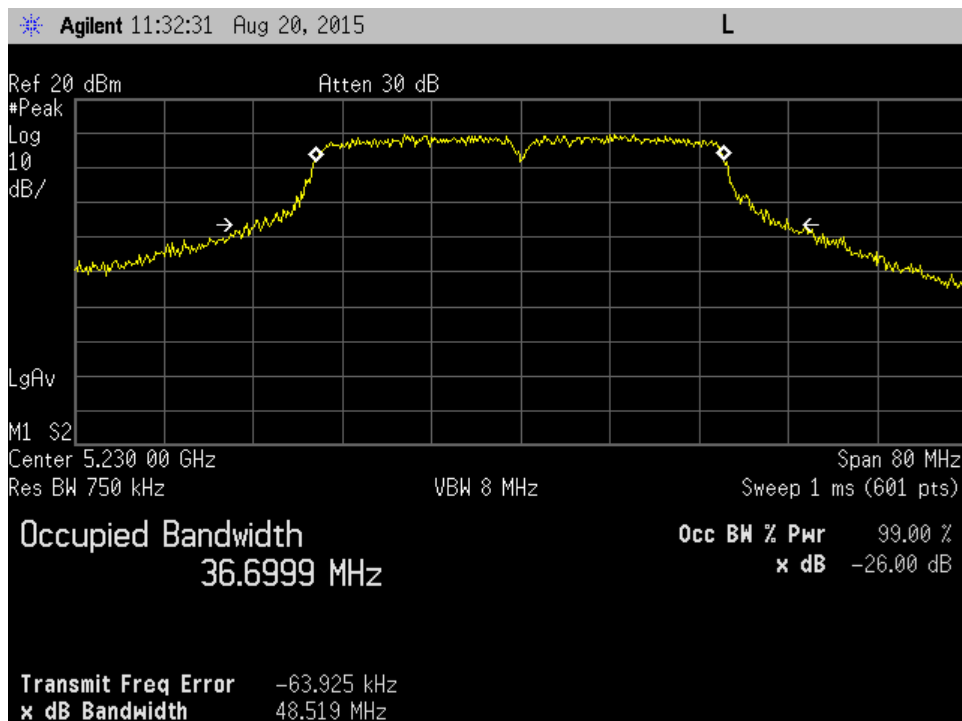




40 MHz high ch_J5

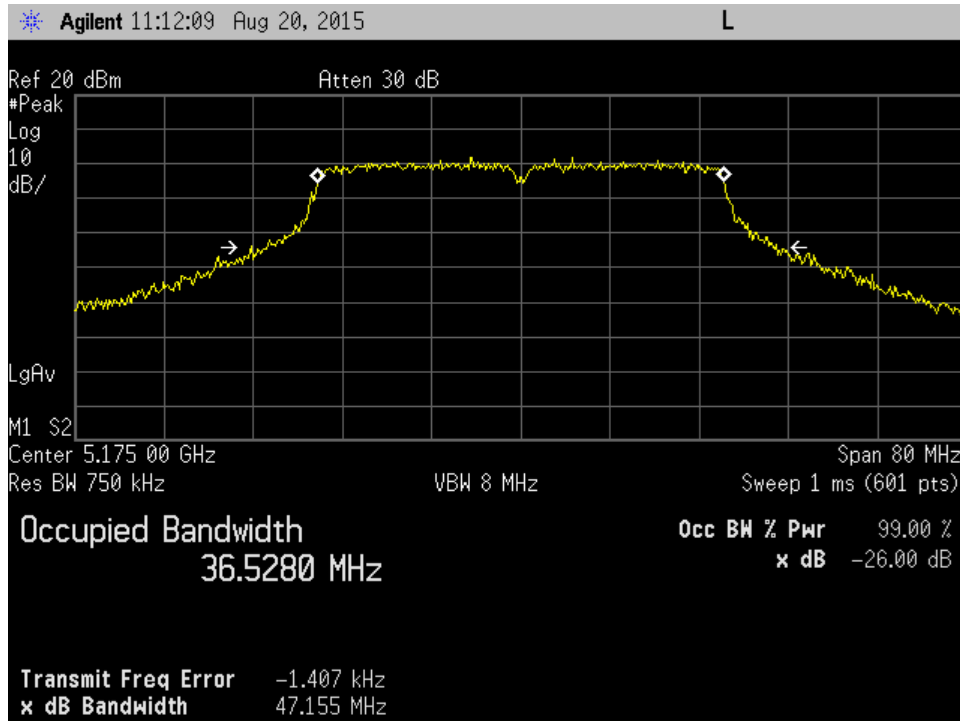


40 MHz high ch_J9

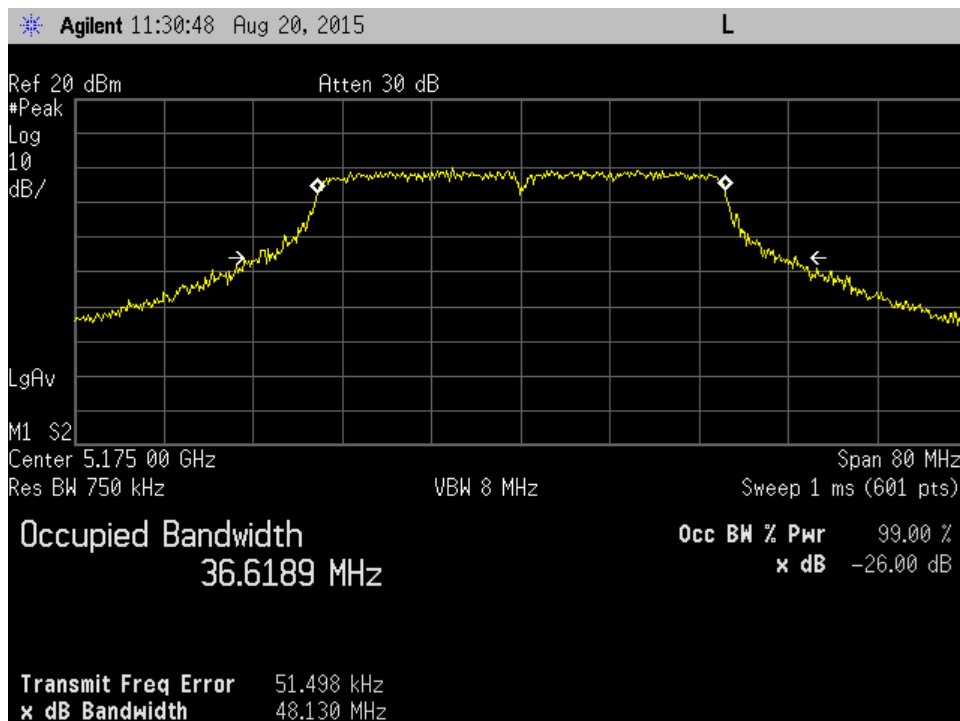




40 MHz low ch_J5

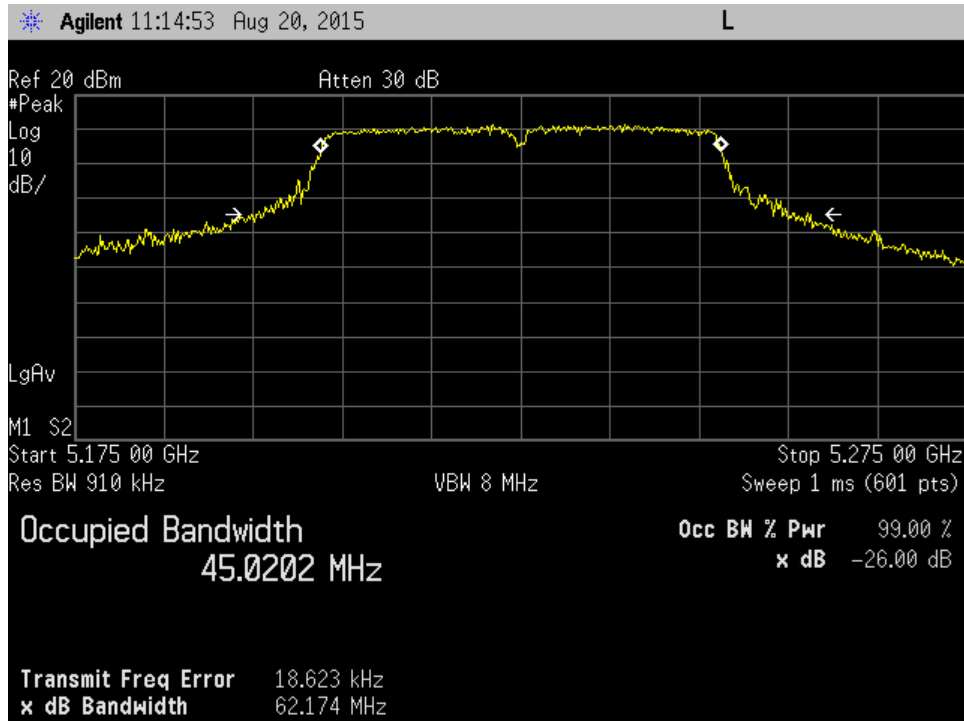


40 MHz low ch_J9

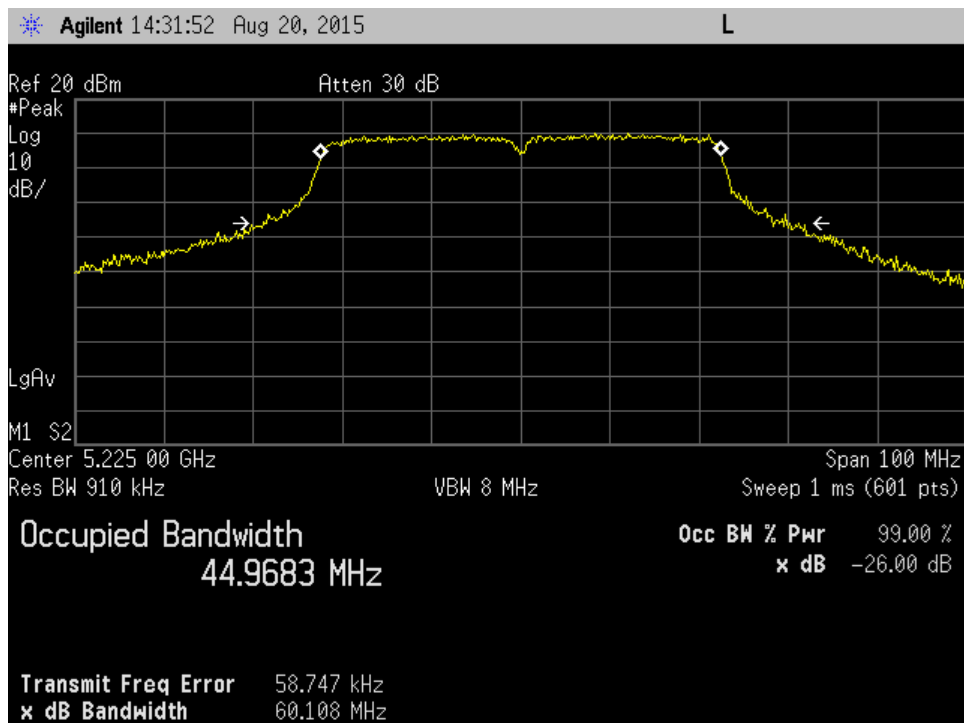




50 MHz high ch_J5

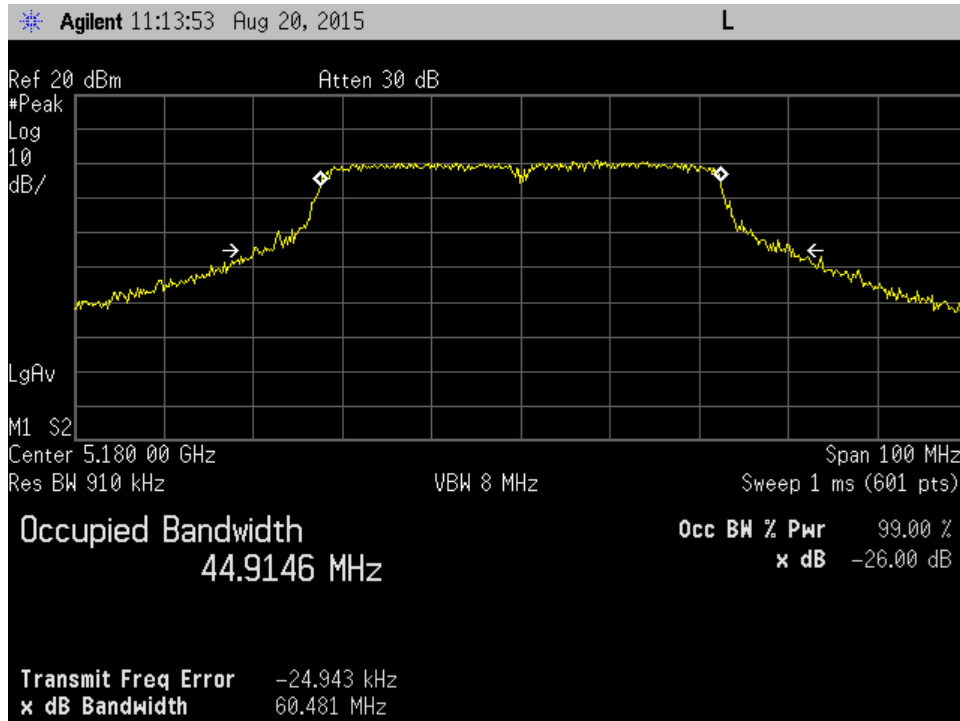


50 MHz high ch_J9

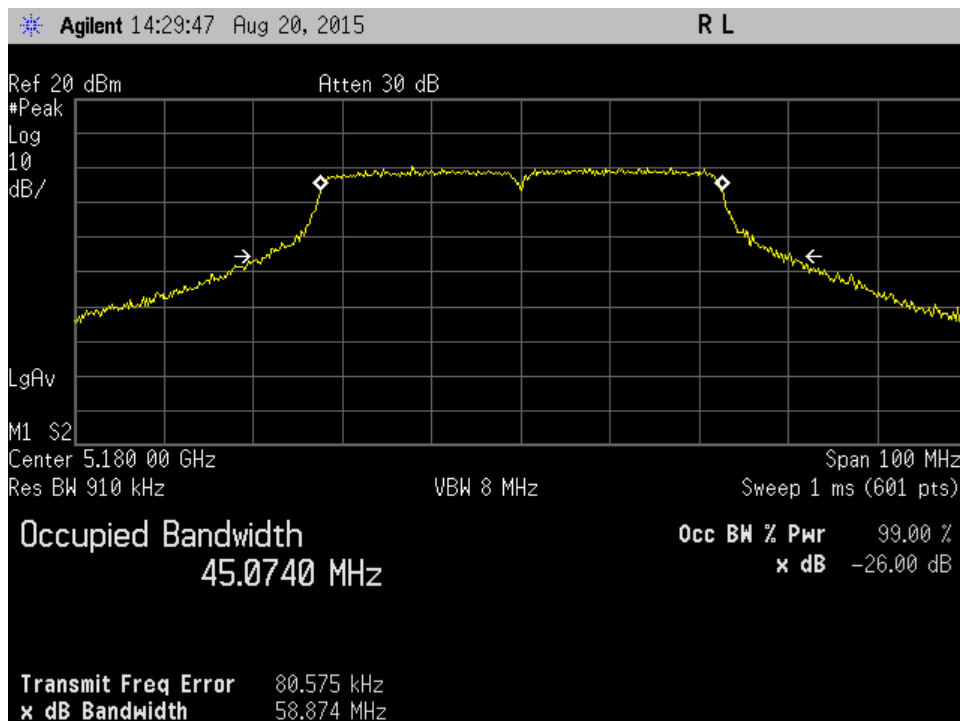




50 MHz low ch_J5

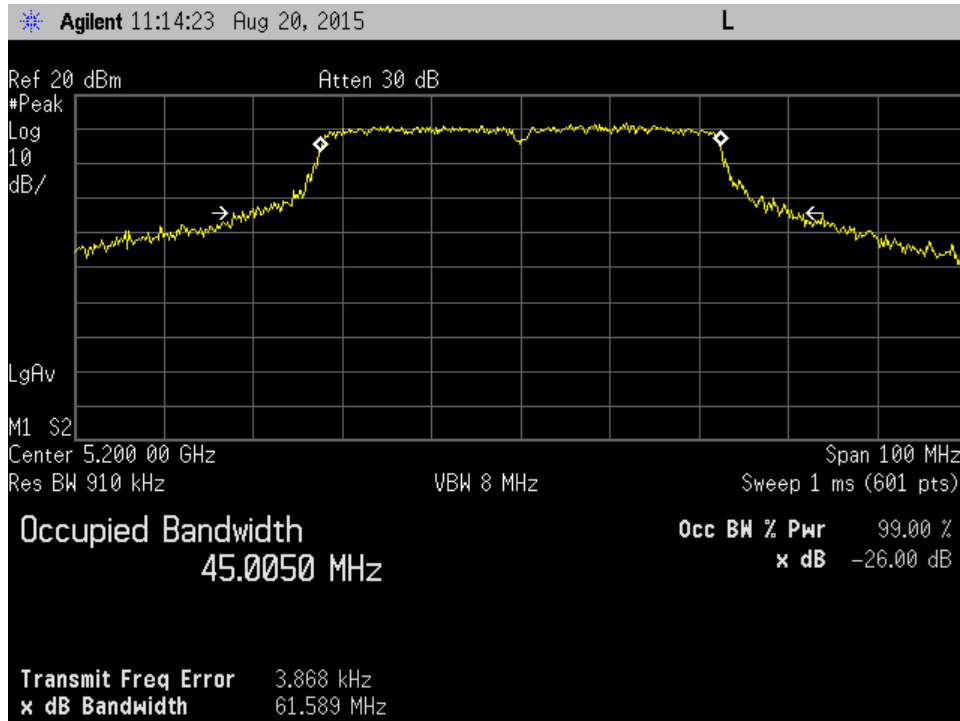


50 MHz low ch_J9

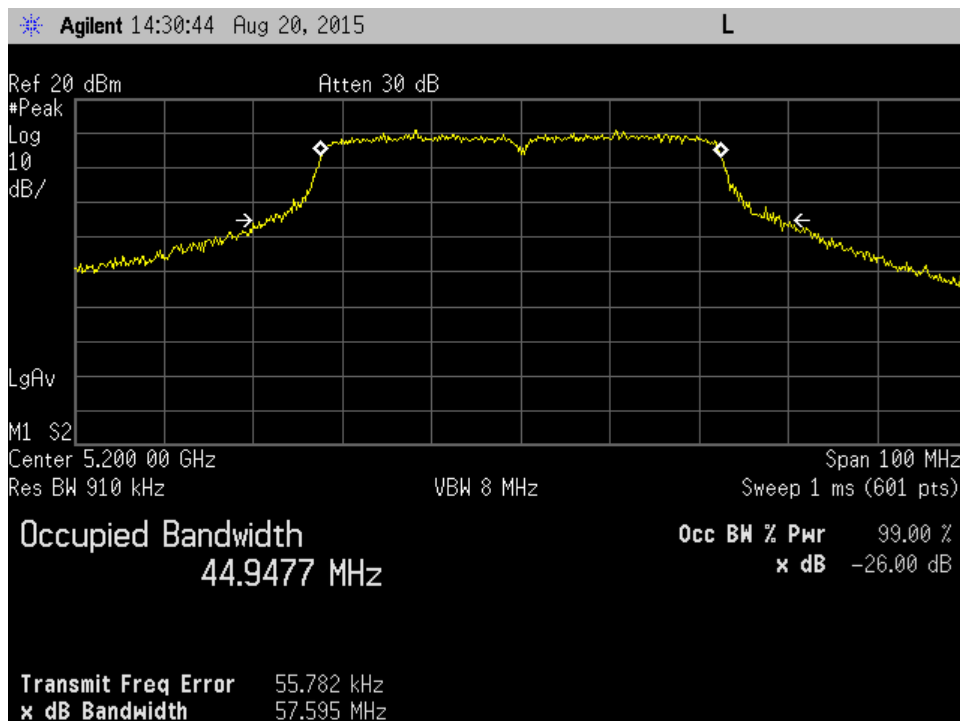




50 MHz mid ch_J5

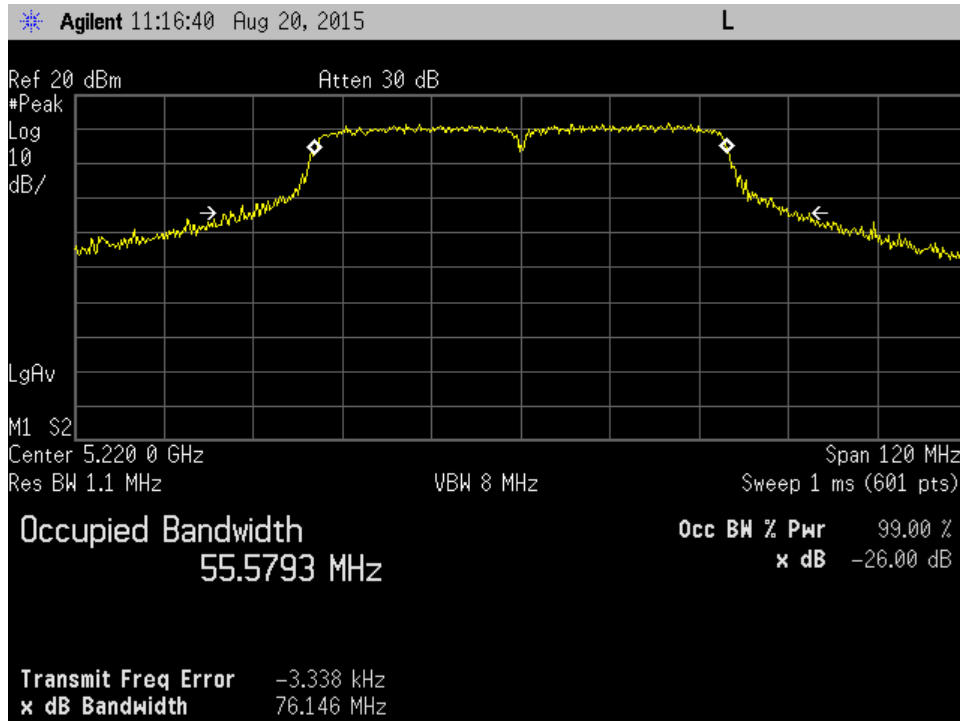


50 MHz mid ch_J9

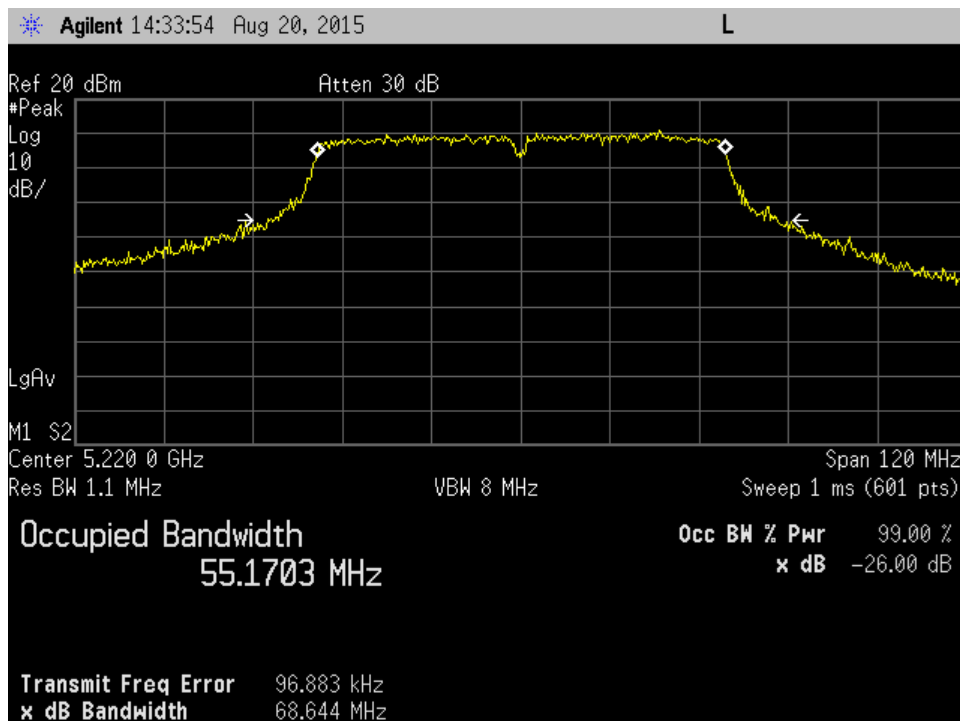




60 MHz high ch_J5

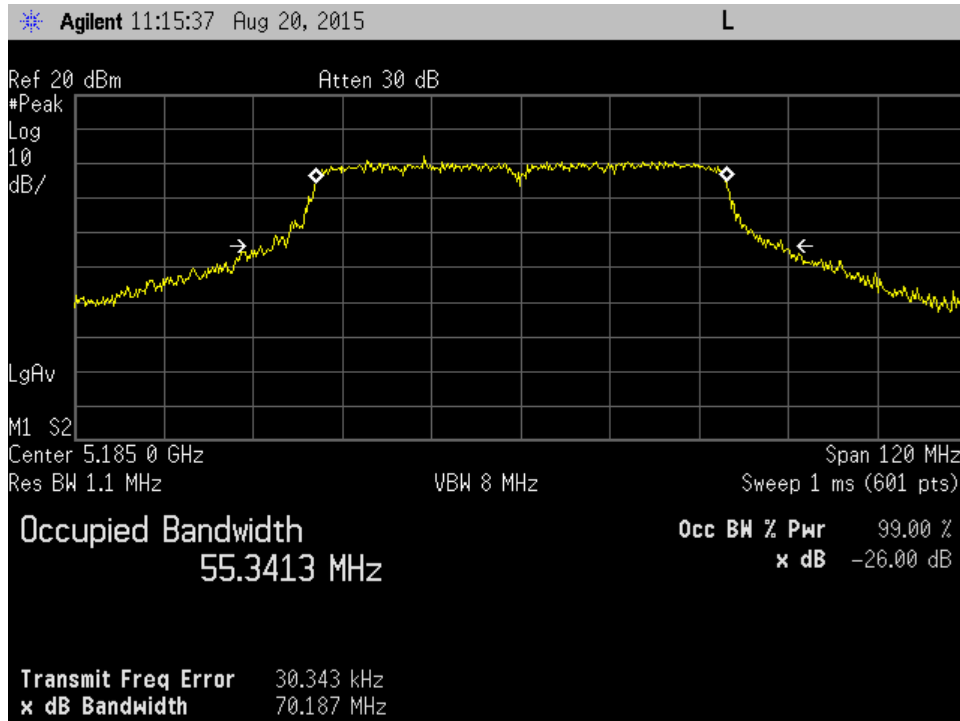


60 MHz high ch_J9

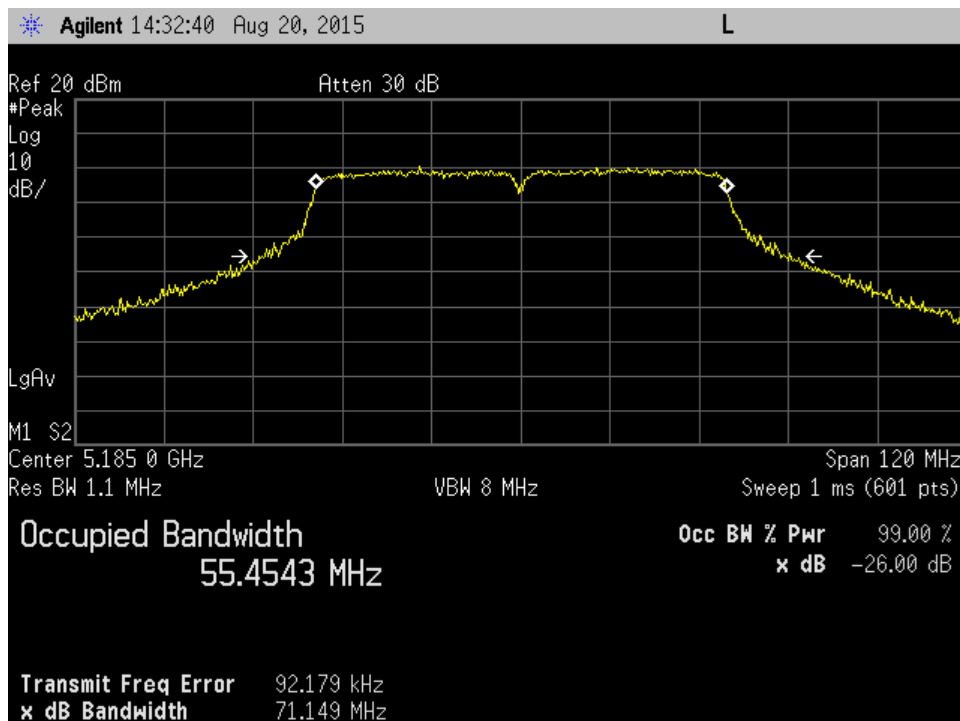




60 MHz low ch_J5

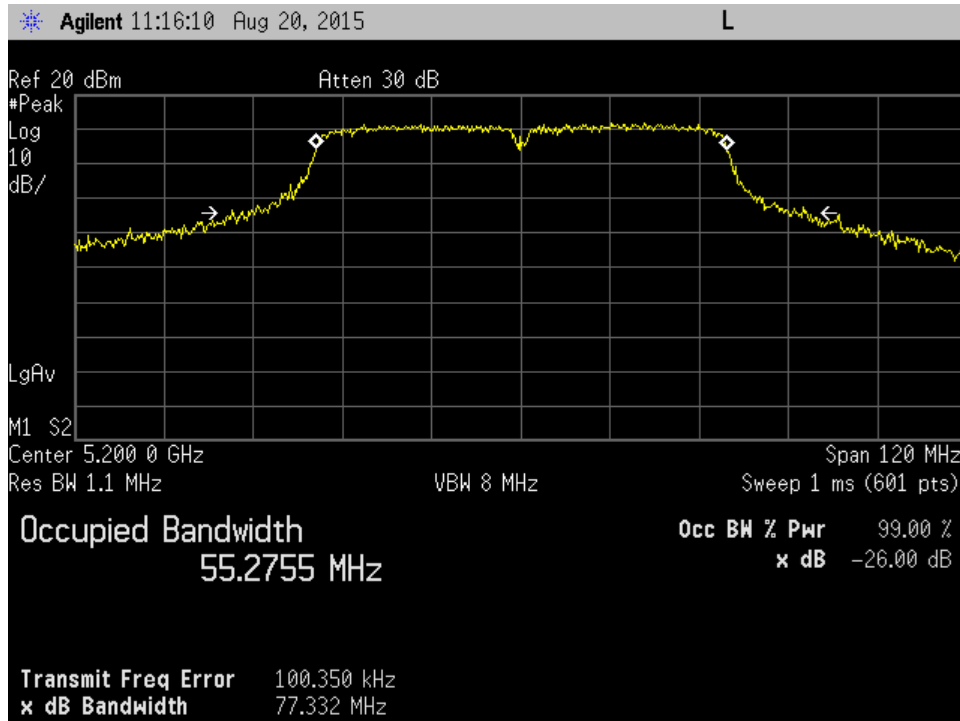


60 MHz low ch_J9

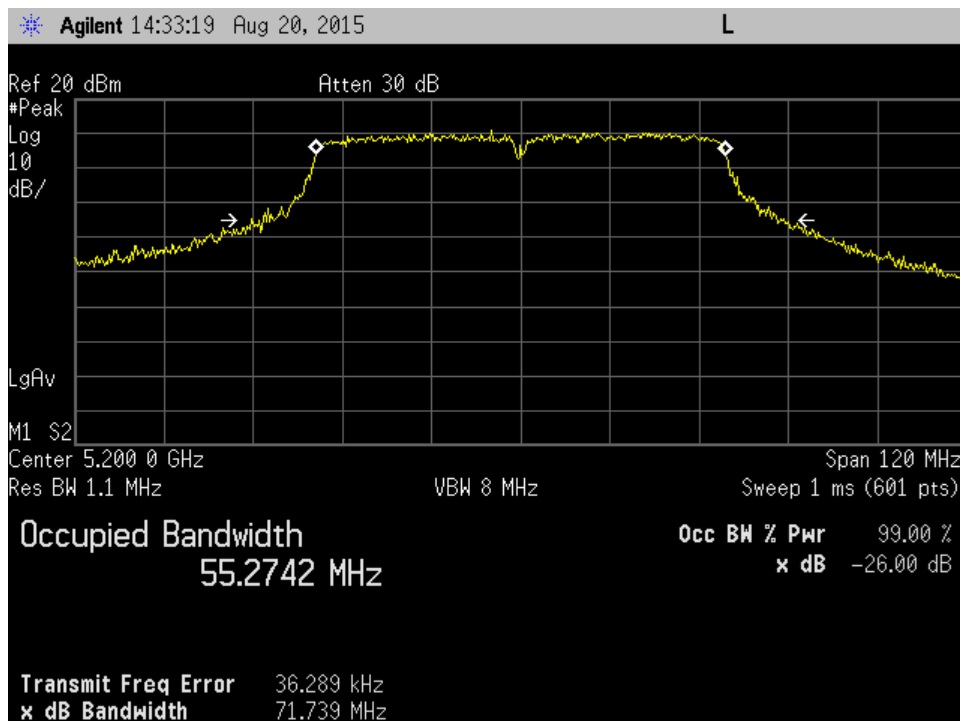




60 MHz mid ch_J5

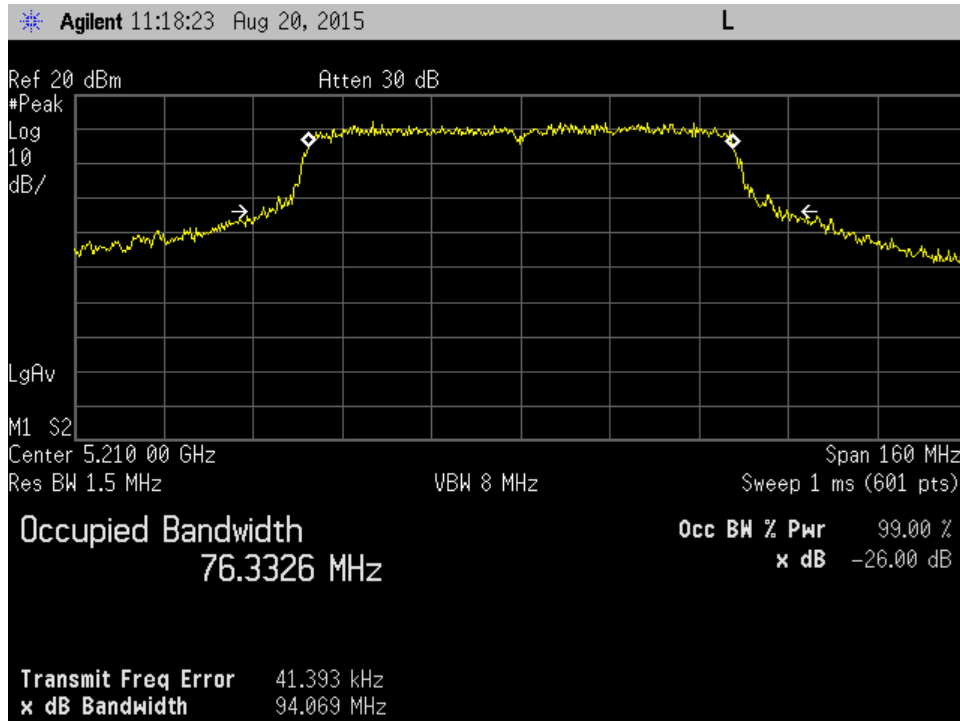


60 MHz mid ch_J9

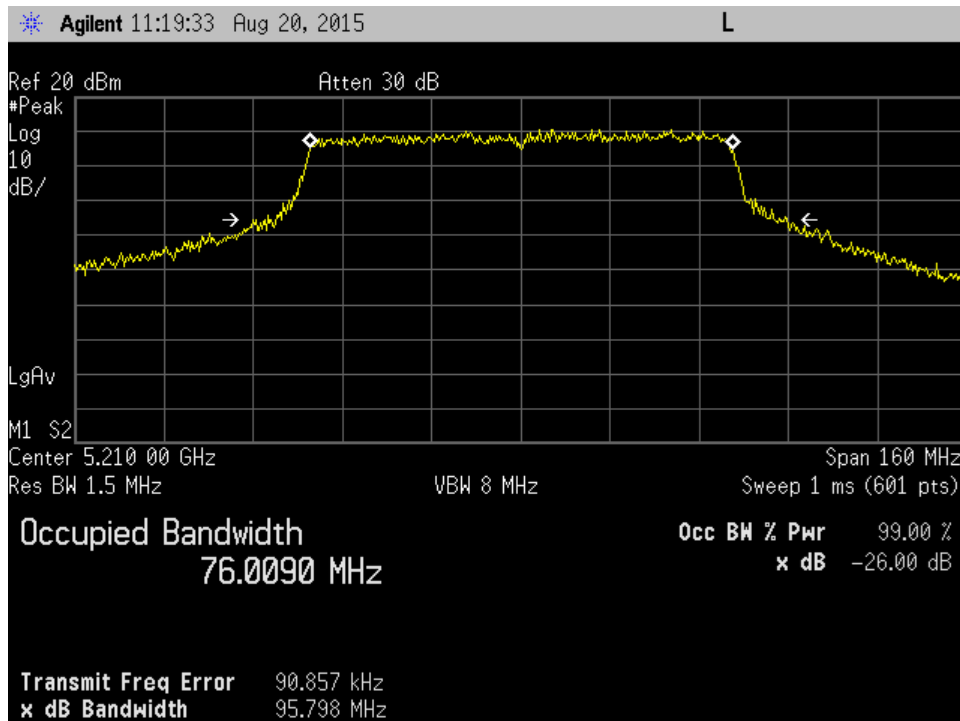




80 MHz high ch_J5

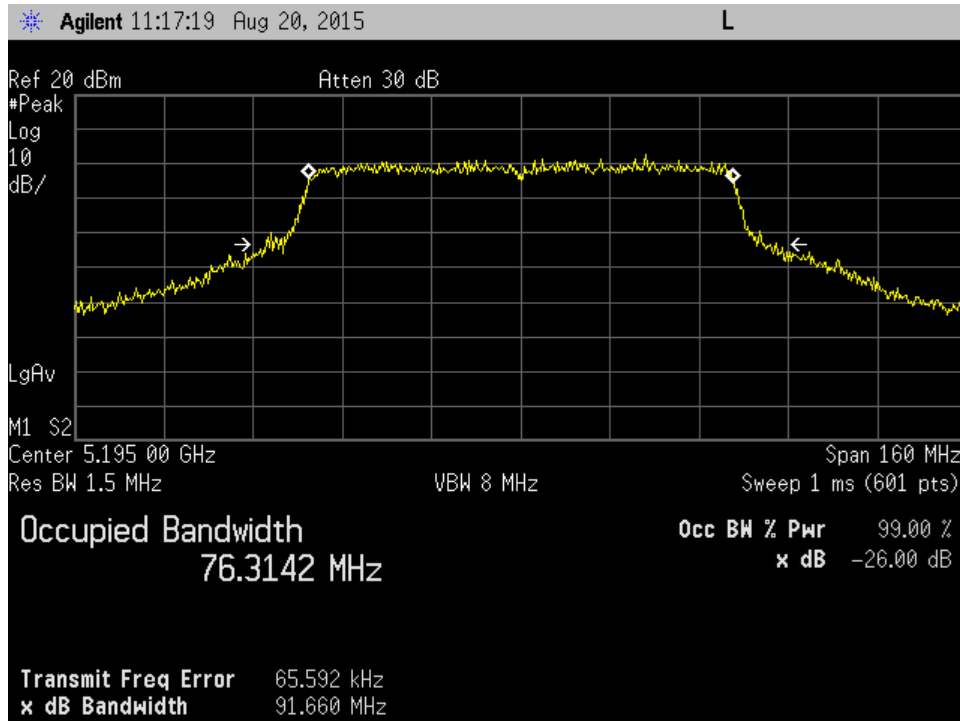


80 MHz high ch_J9

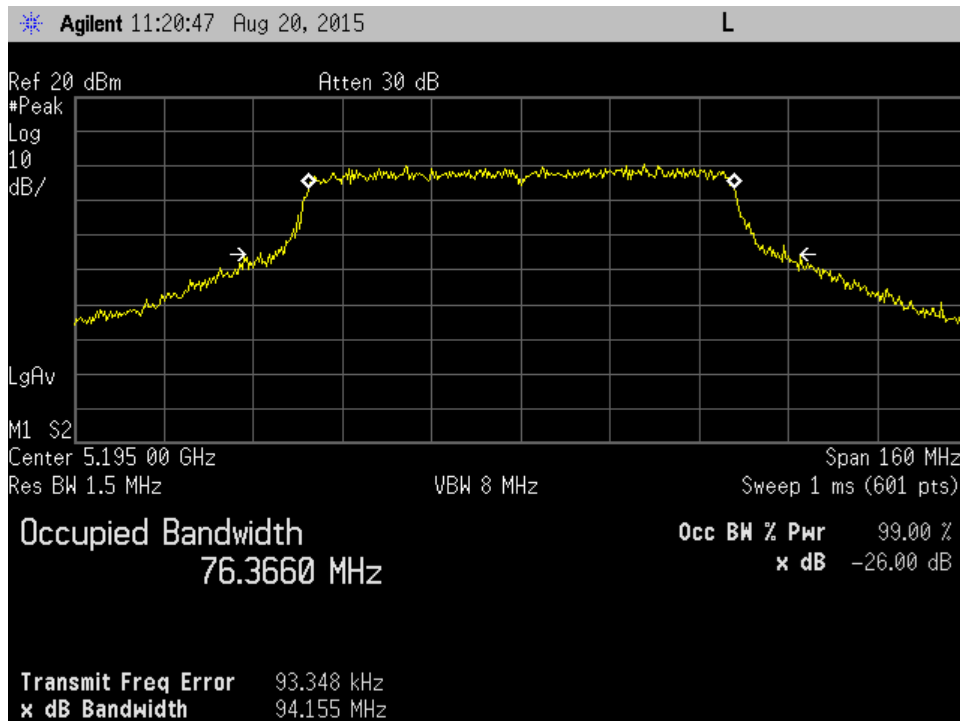




80 MHz low ch_J5

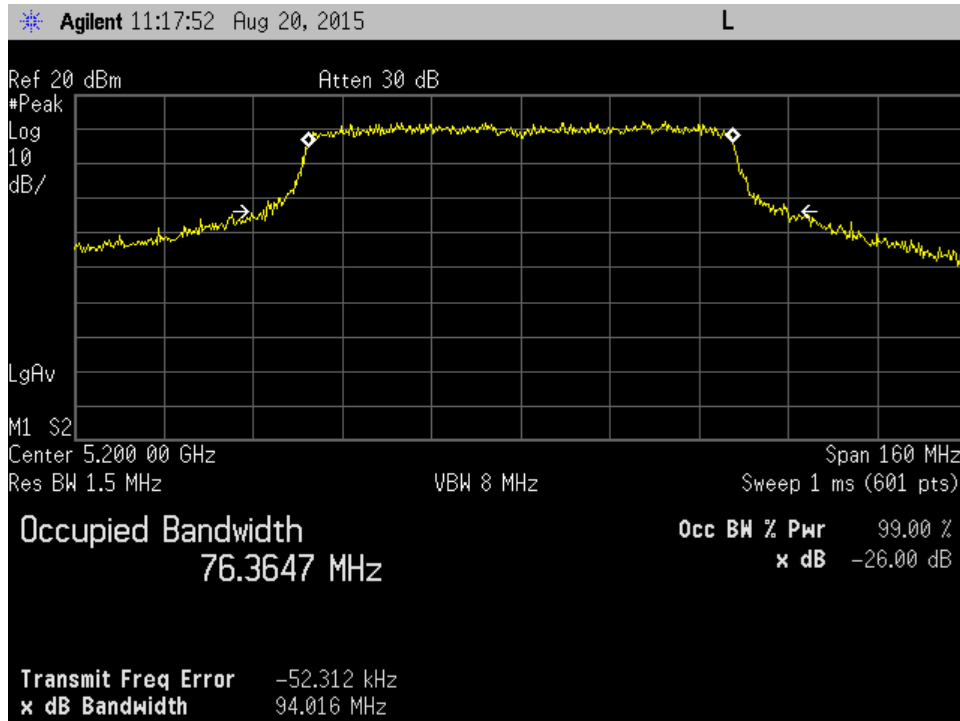


80 MHz low ch_J9





80 MHz mid ch_J5



80 MHz mid ch_J9

