



Annex A

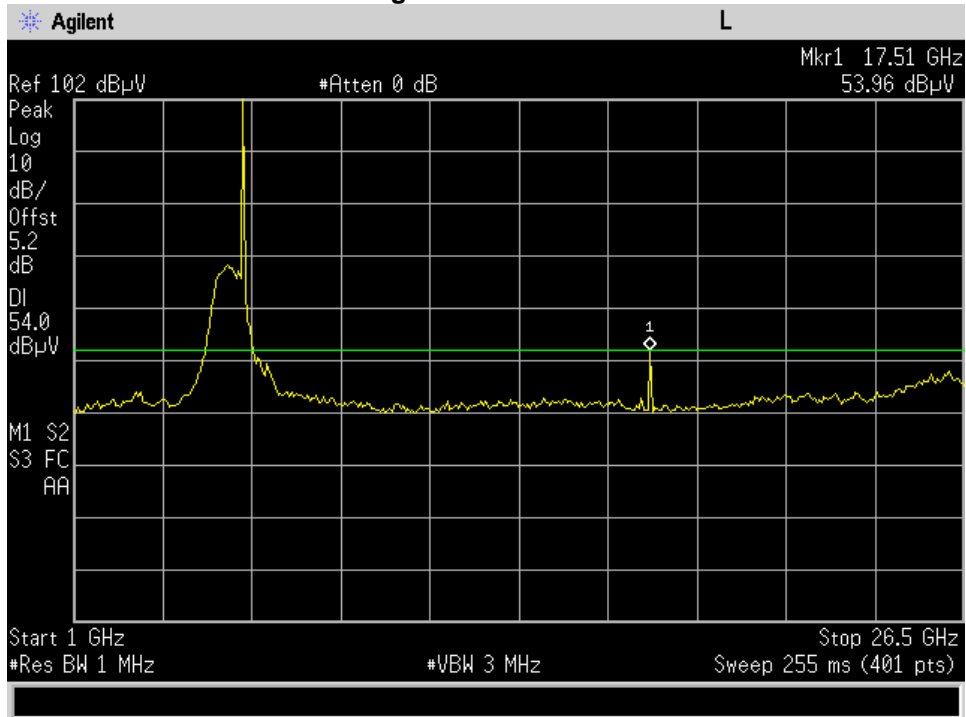
Undesirable Emissions Conducted

UNII-3

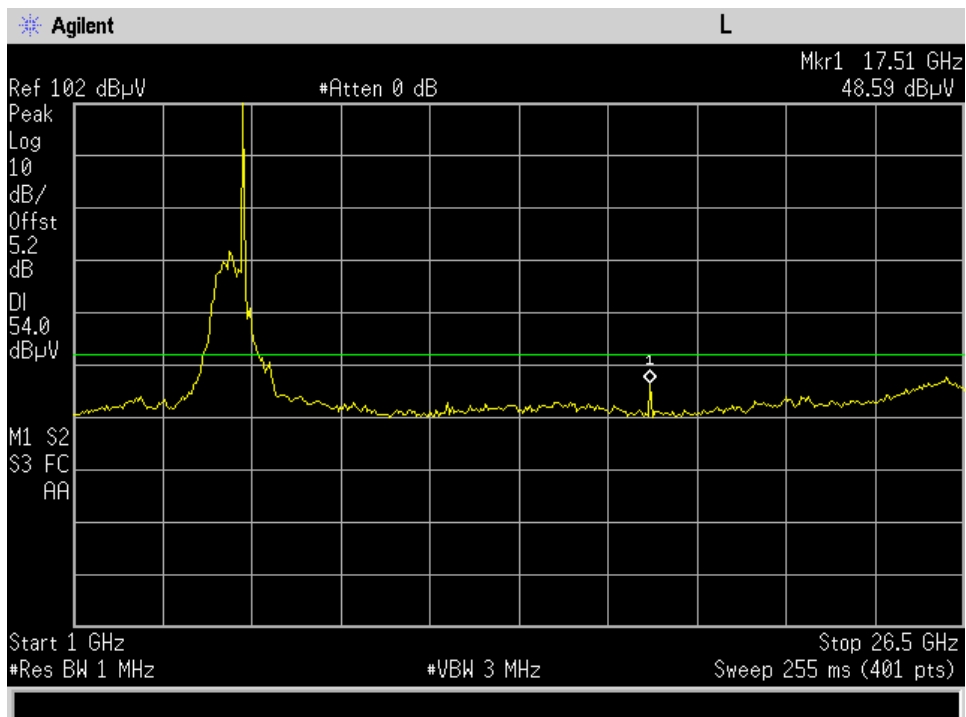


Conducted measurements in lieu of Radiated

10MHz High Ch 1-26.5GHz Port J4

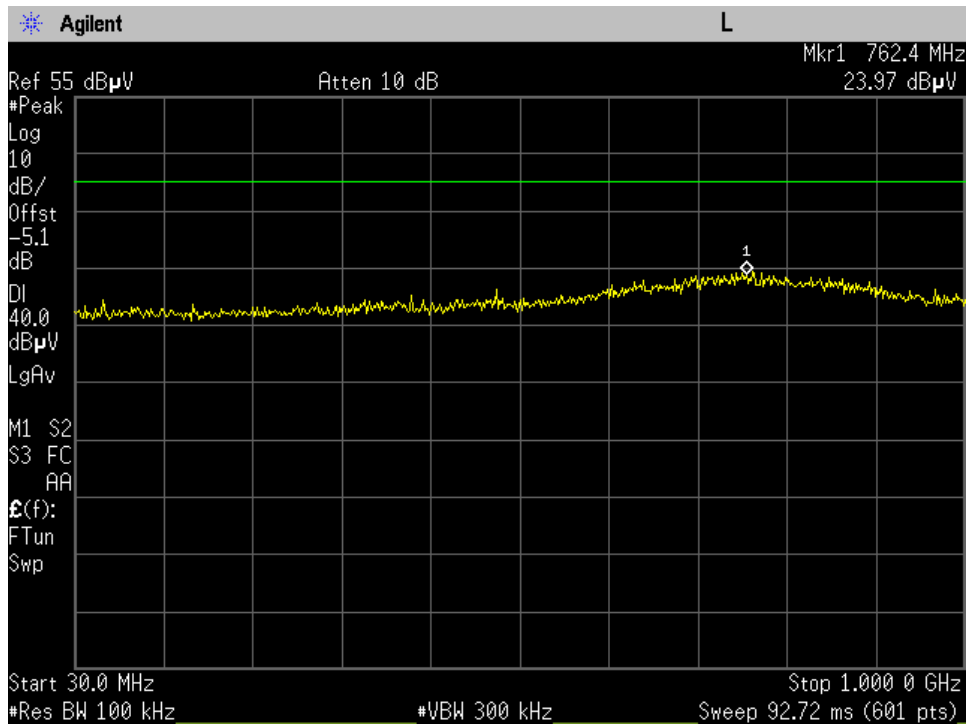


10MHz High Ch 1-26.5GHz Port J7

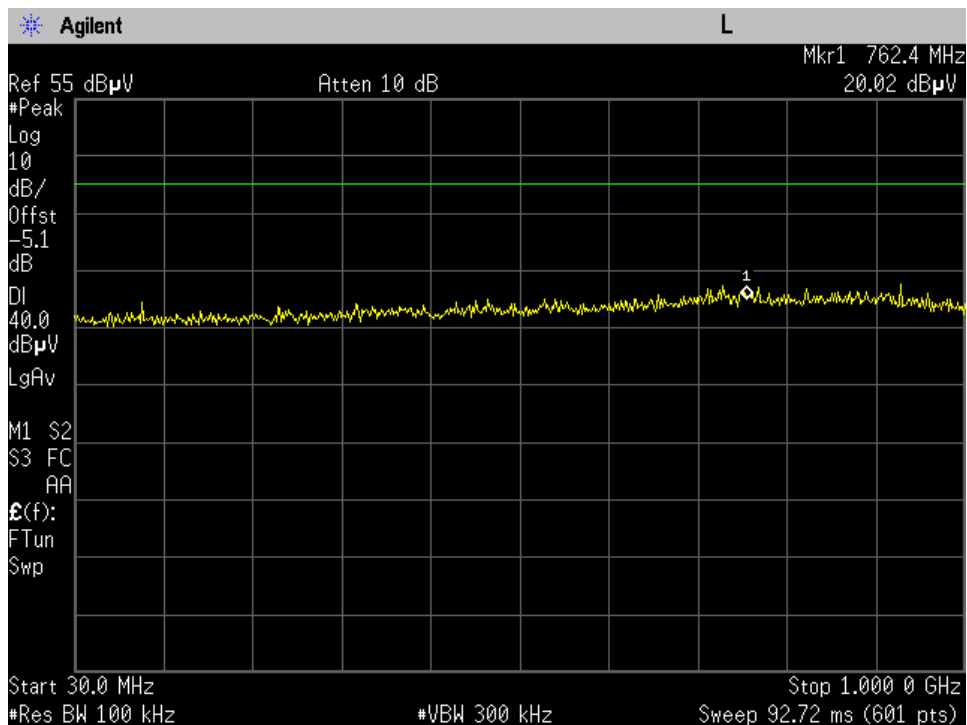




10MHz High Ch 30 - 1000MHz Port J4

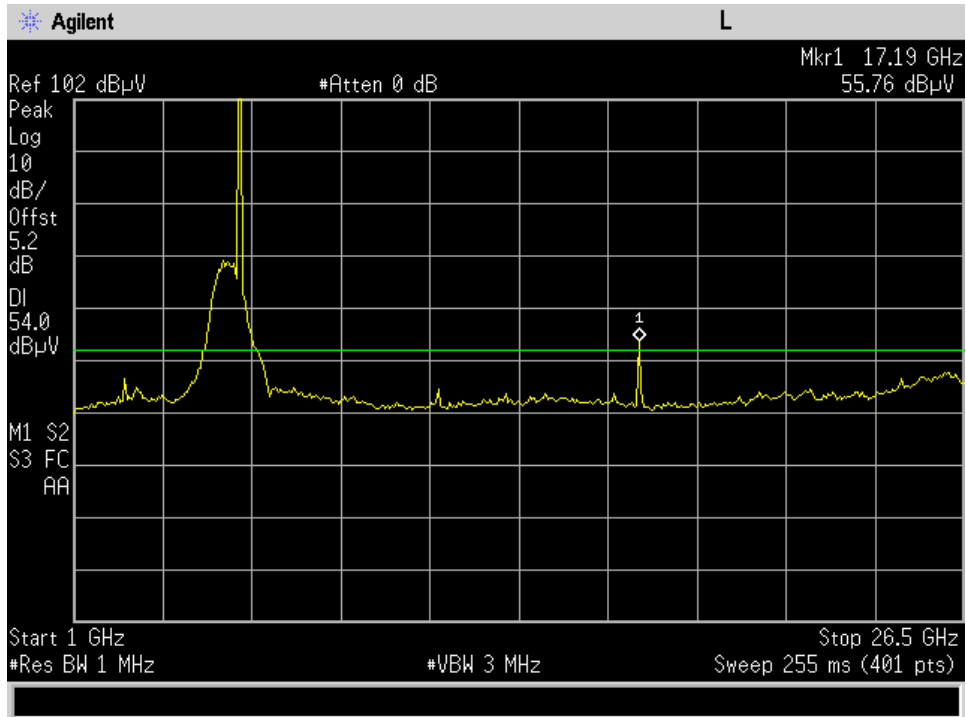


10MHz High Ch 30 - 1000MHz Port J7

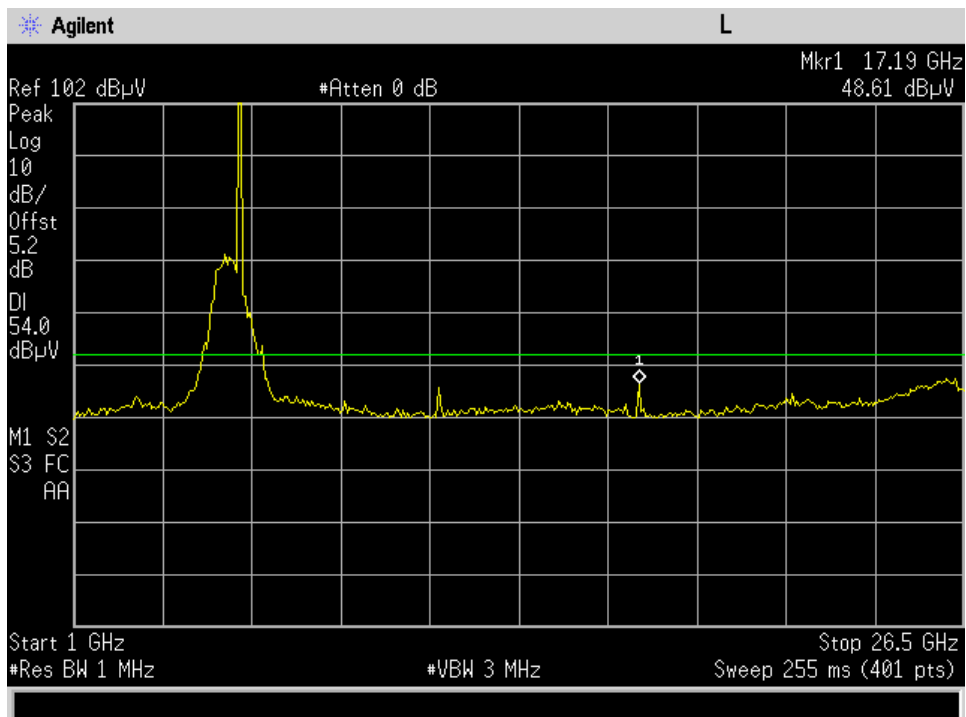




10MHz Low Ch 1-26.5GHz Port J4

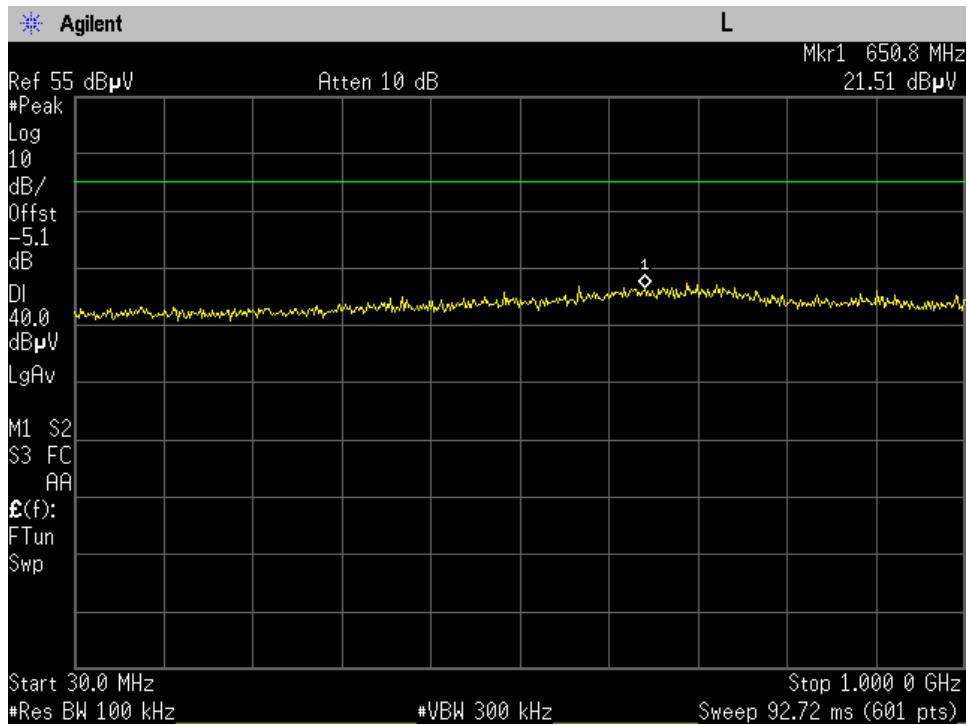


10MHz Low Ch 1-26.5GHz Port J7

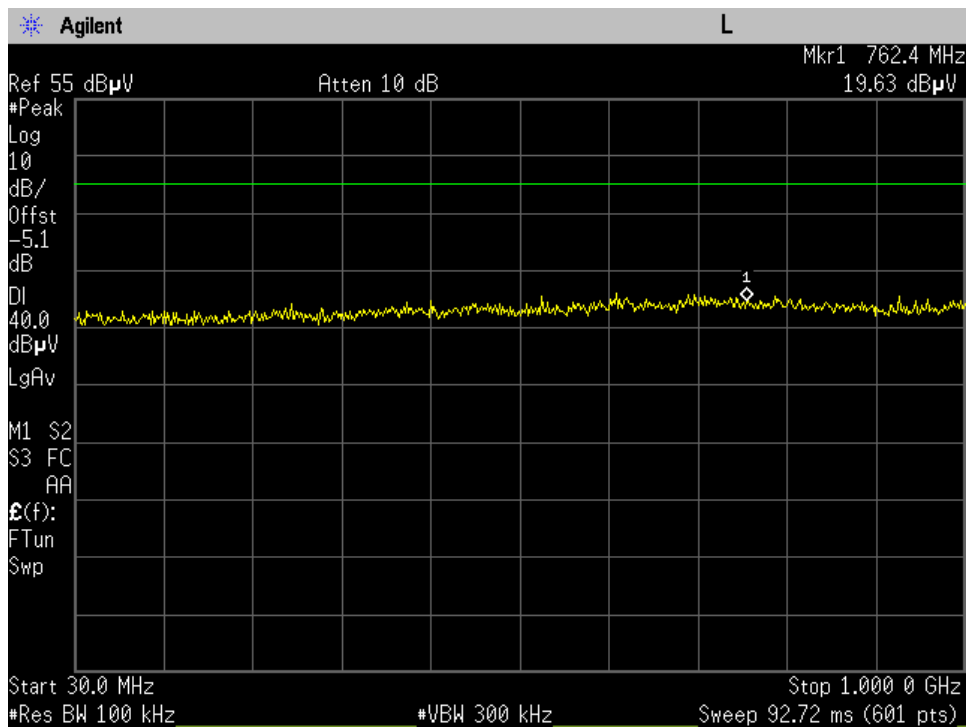




10MHz Low Ch 30 - 1000MHz Port J4

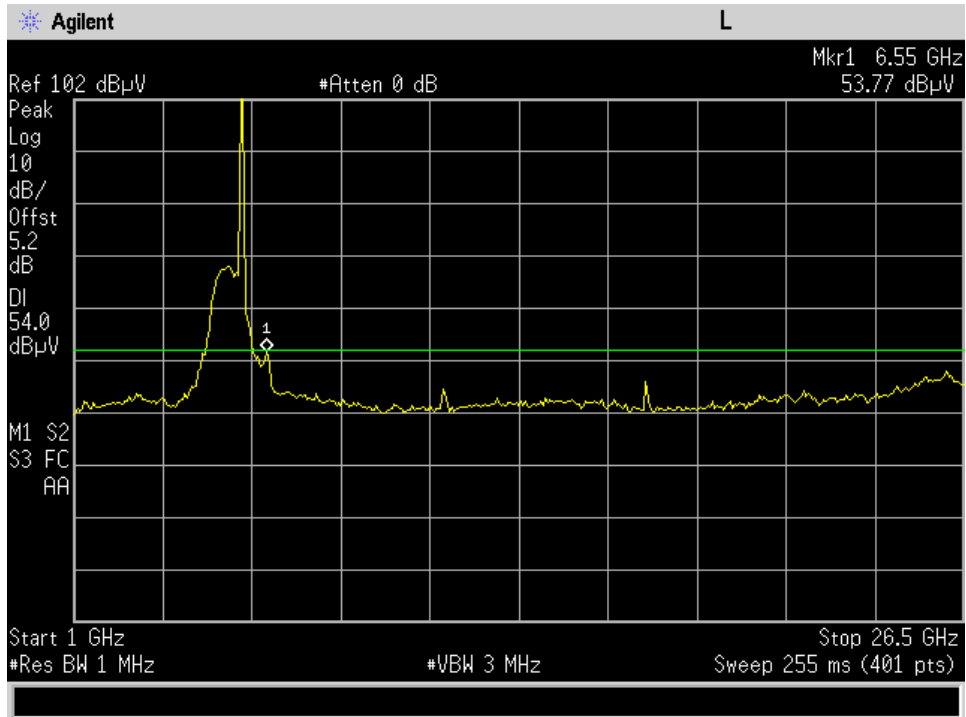


10MHz Low Ch 30 - 1000MHz Port J7

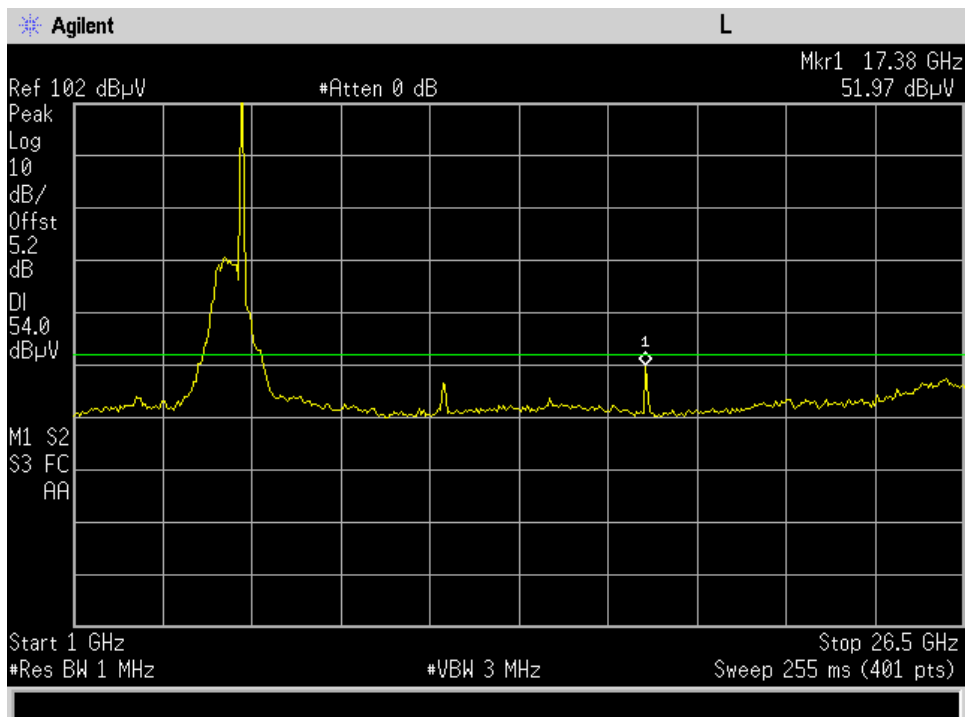




10MHz Mid Ch 1-26.5GHz Port J4

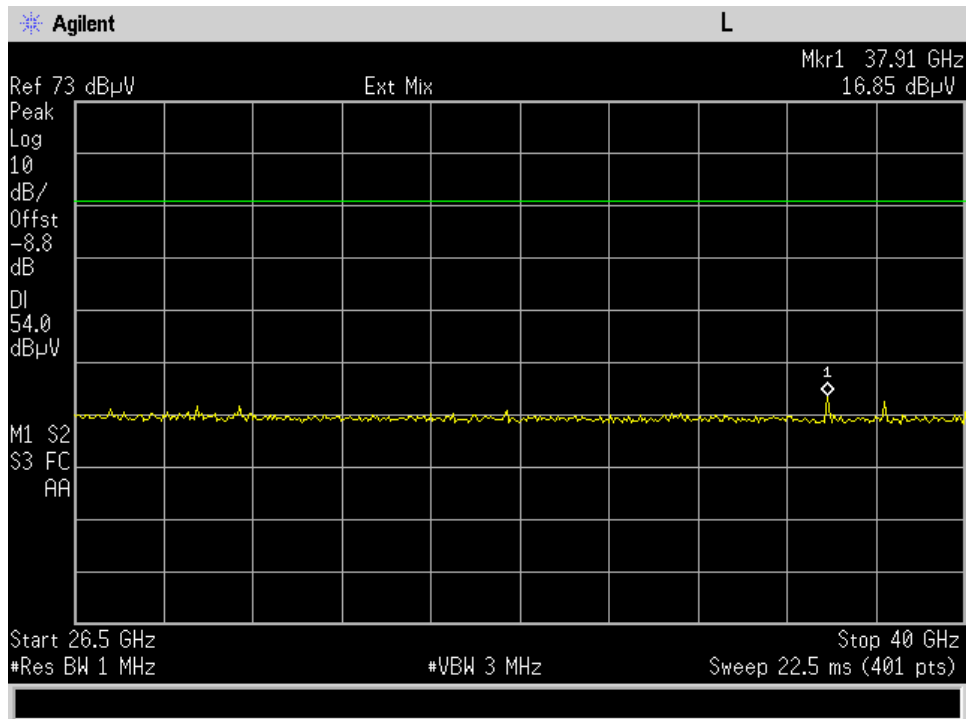


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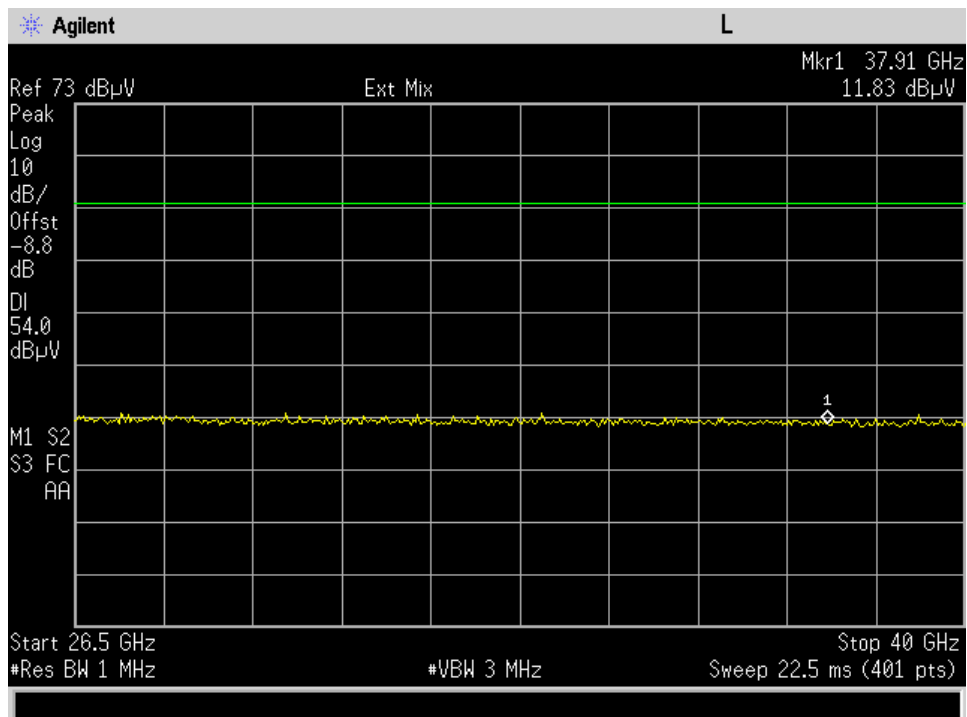




10MHz Mid Ch 26.5-40GHz Port J4

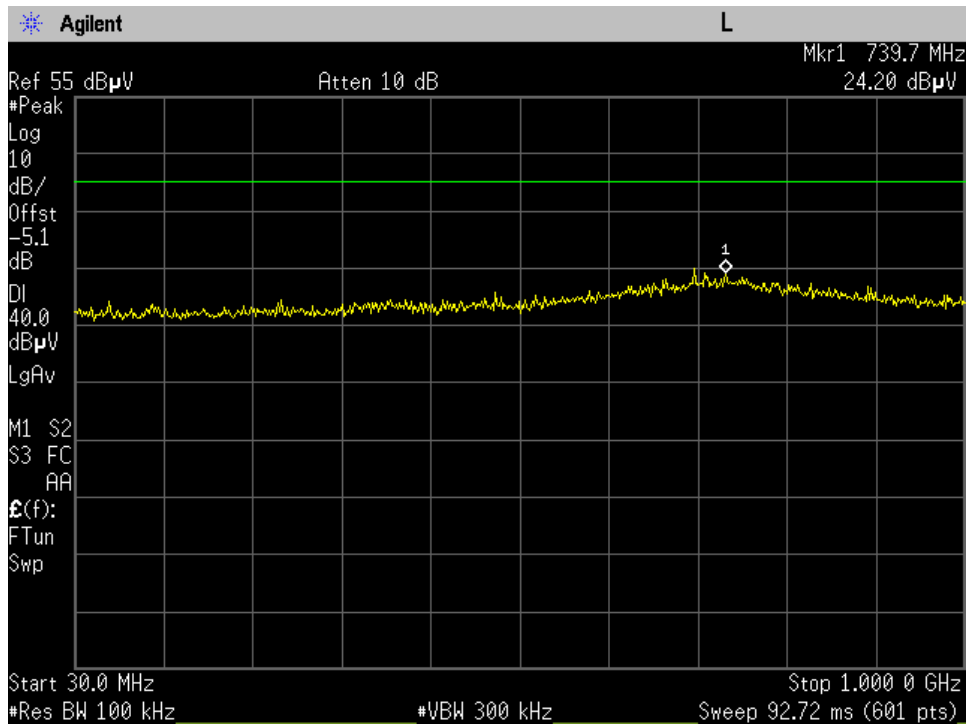


10MHz Mid Ch 26.5-40GHz Port J7

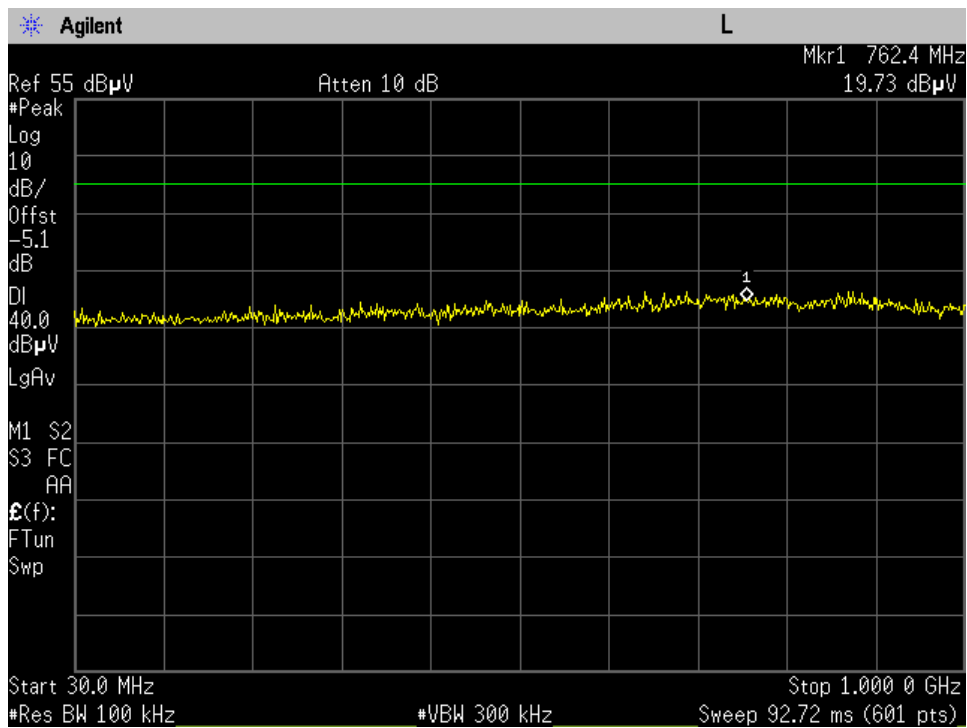




10MHz Mid Ch 30 - 1000MHz Port J4

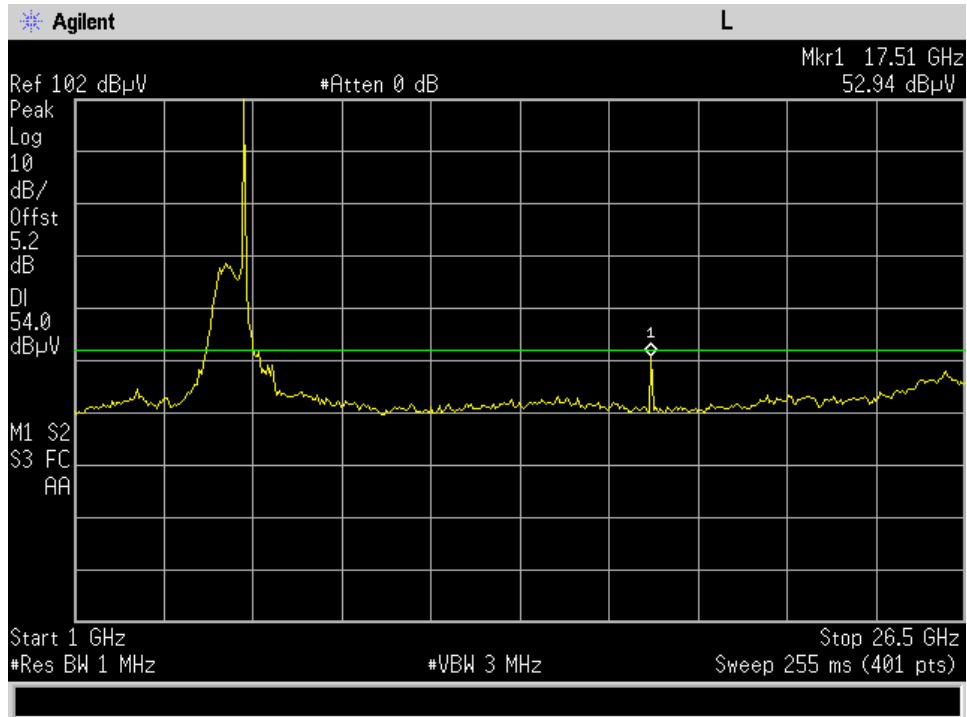


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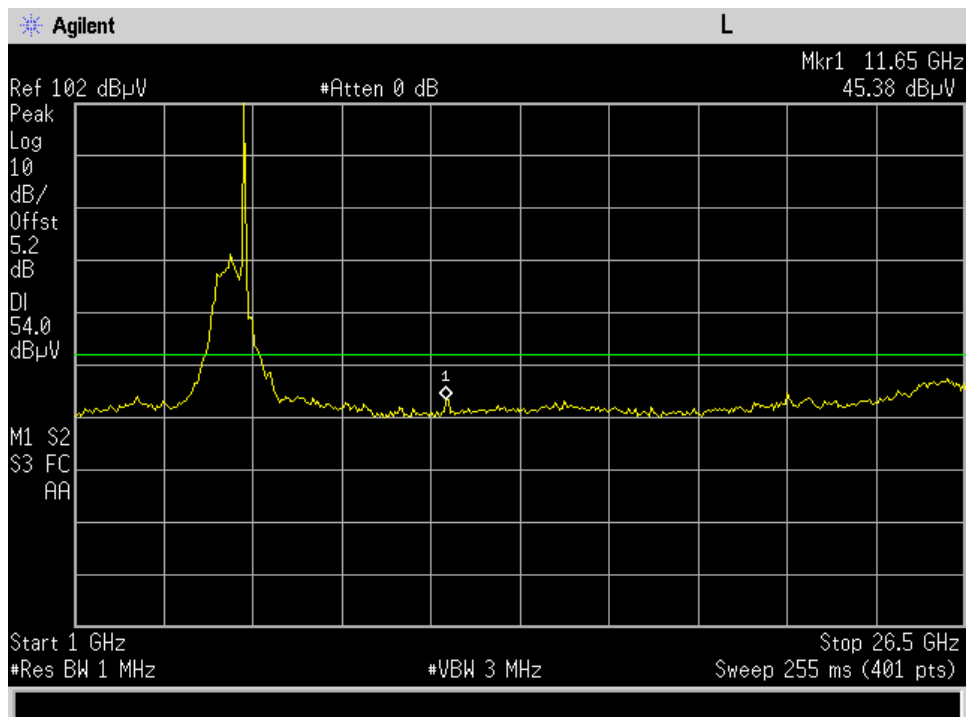




20MHz High Ch 1-26.5GHz Port J4

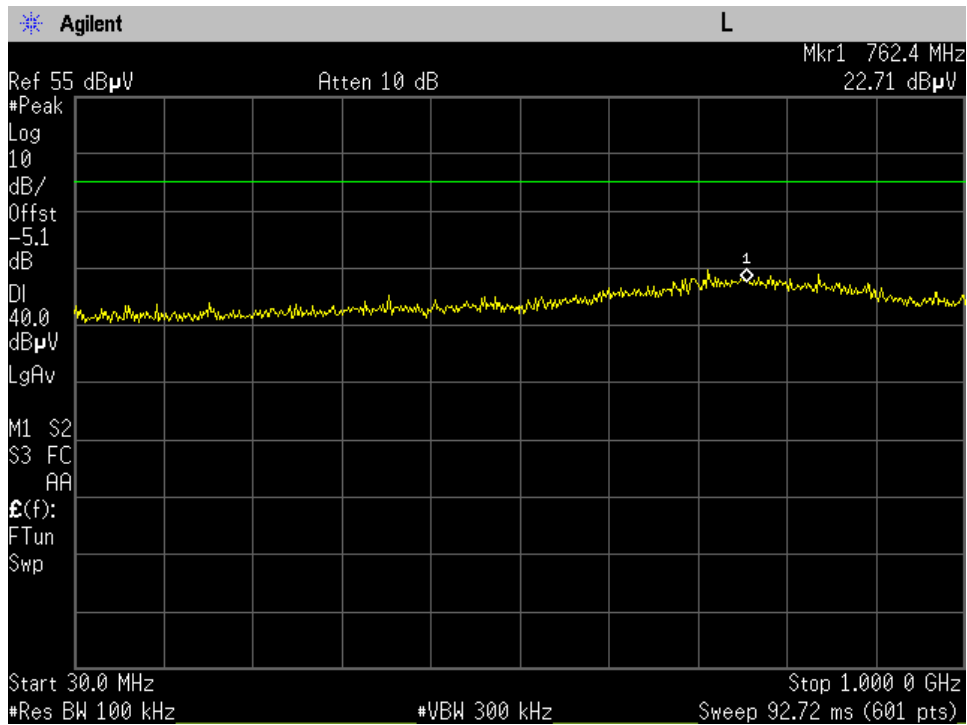


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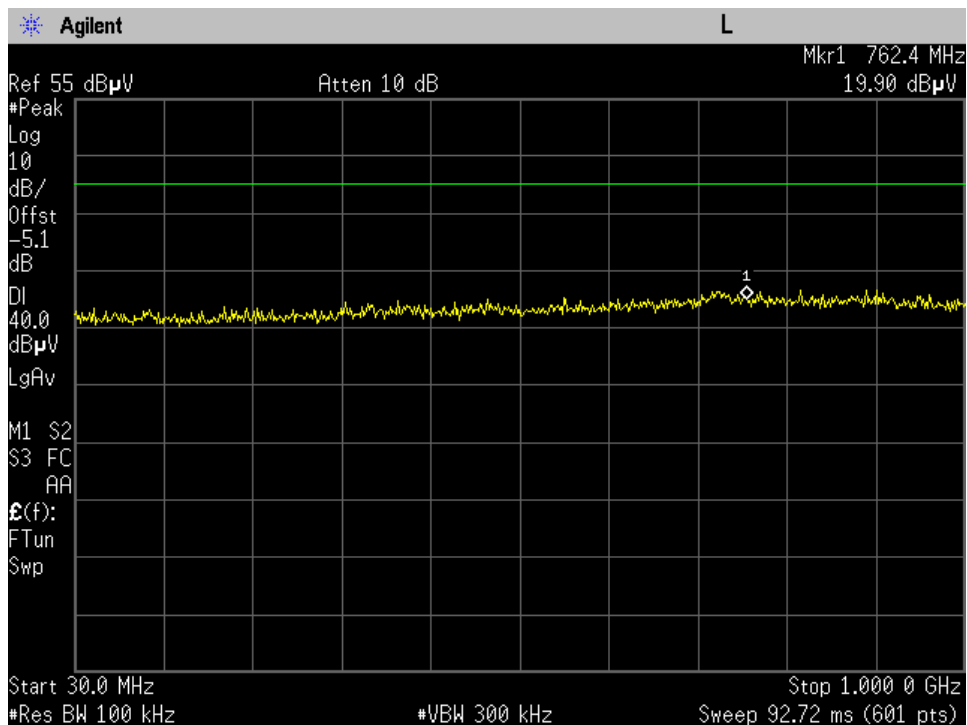




20MHz High Ch 30 - 1000MHz Port J4

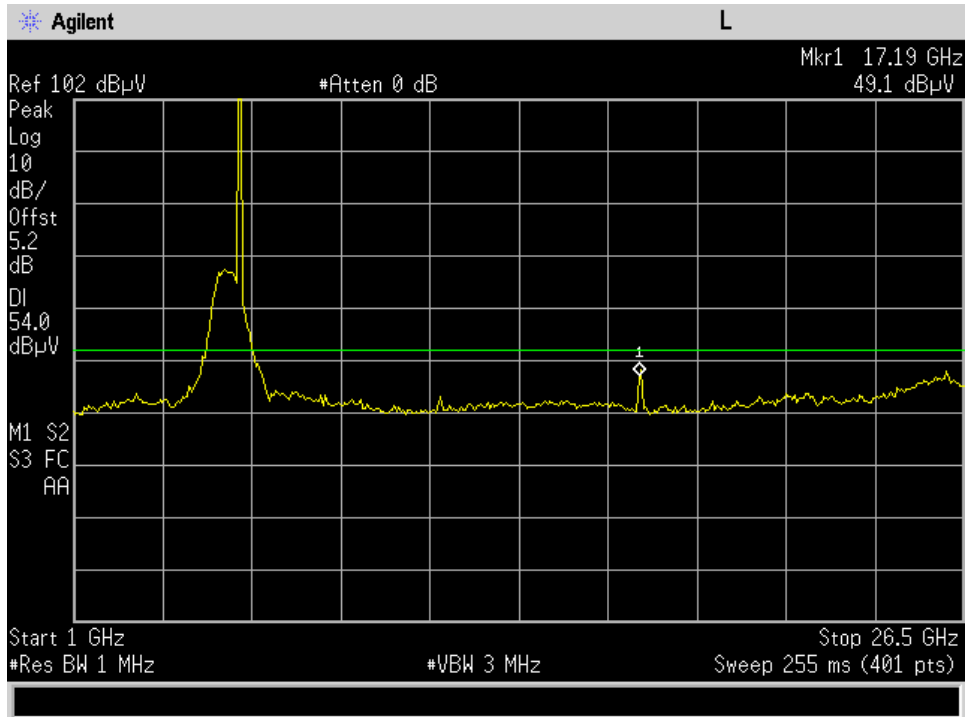


20MHz High Ch 30 - 1000MHz Port J7

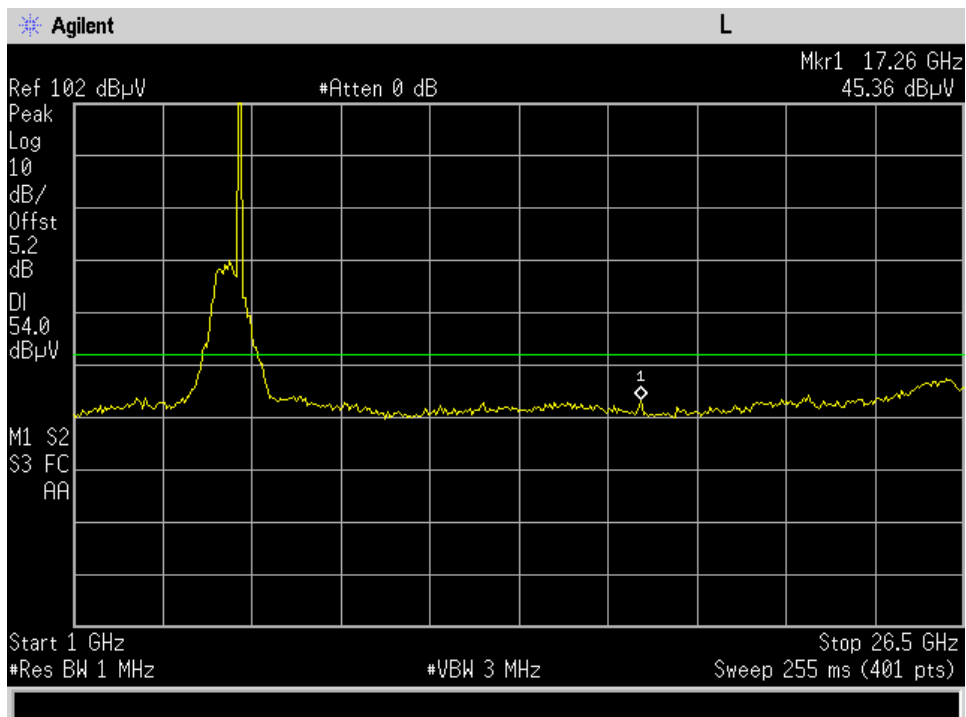




20MHz Low Ch 1-26.5GHz Port J4

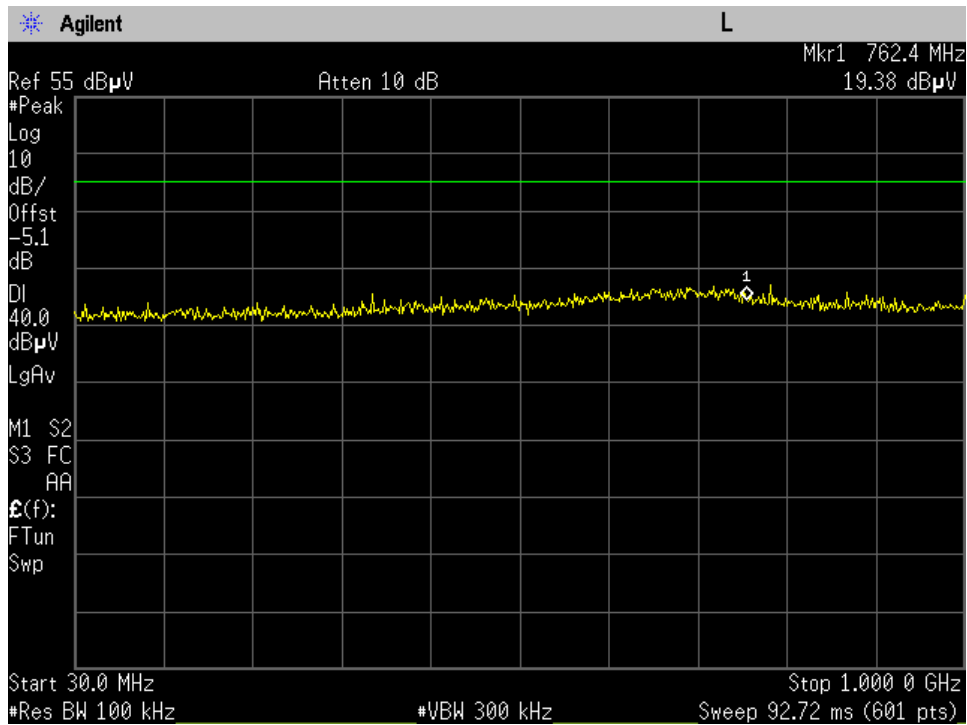


20MHz Low Ch 1-26.5GHz Port J7

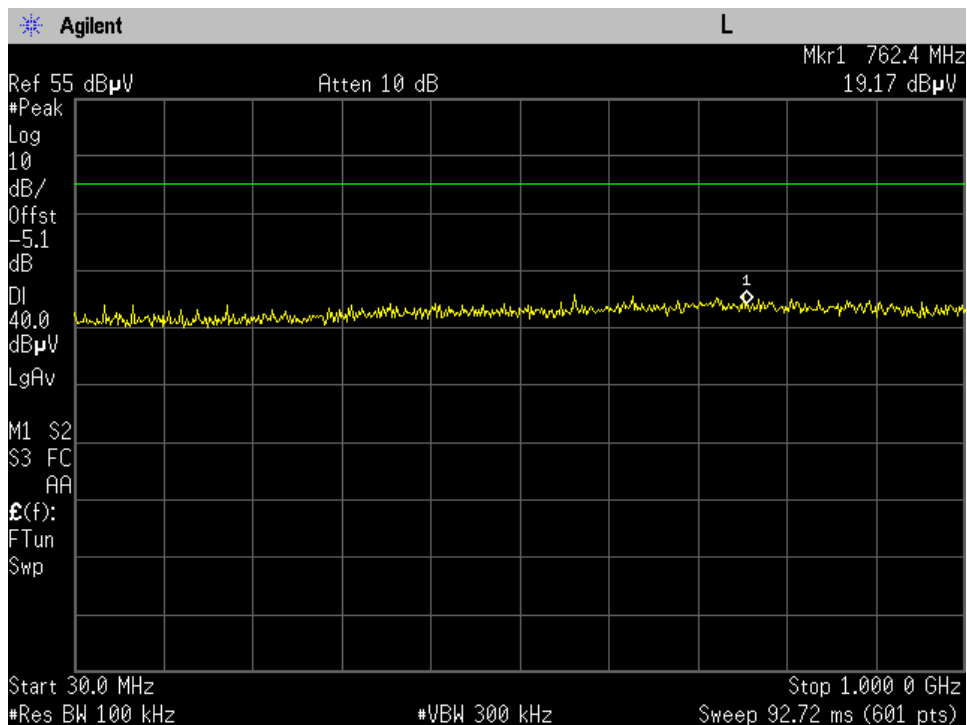




20MHz Low Ch 30 - 1000MHz Port J4

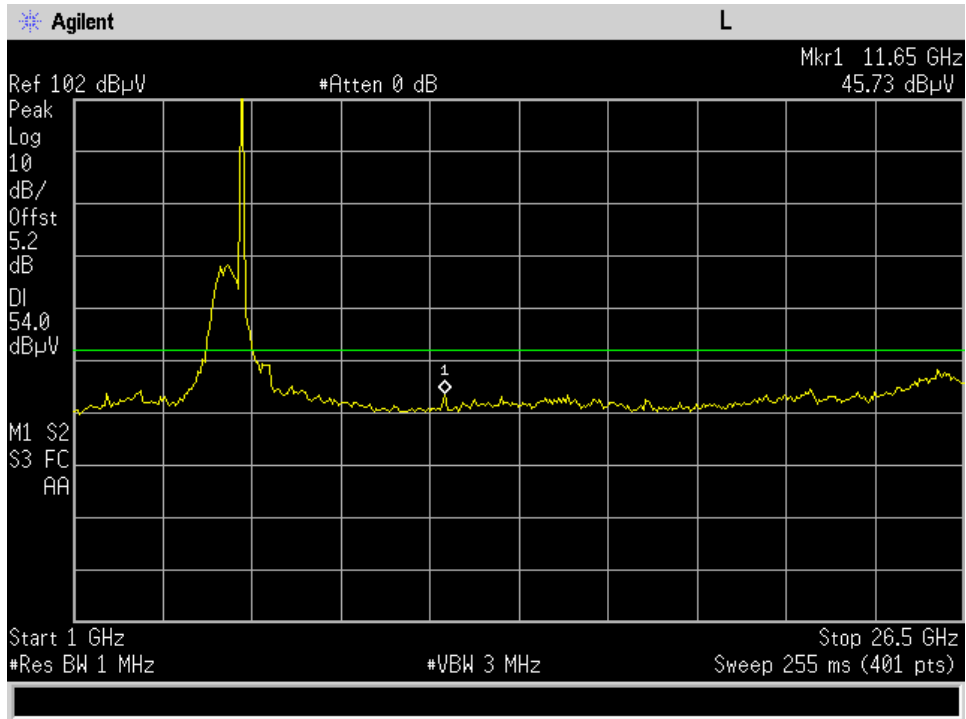


20MHz Low Ch 30 - 1000MHz Port J7

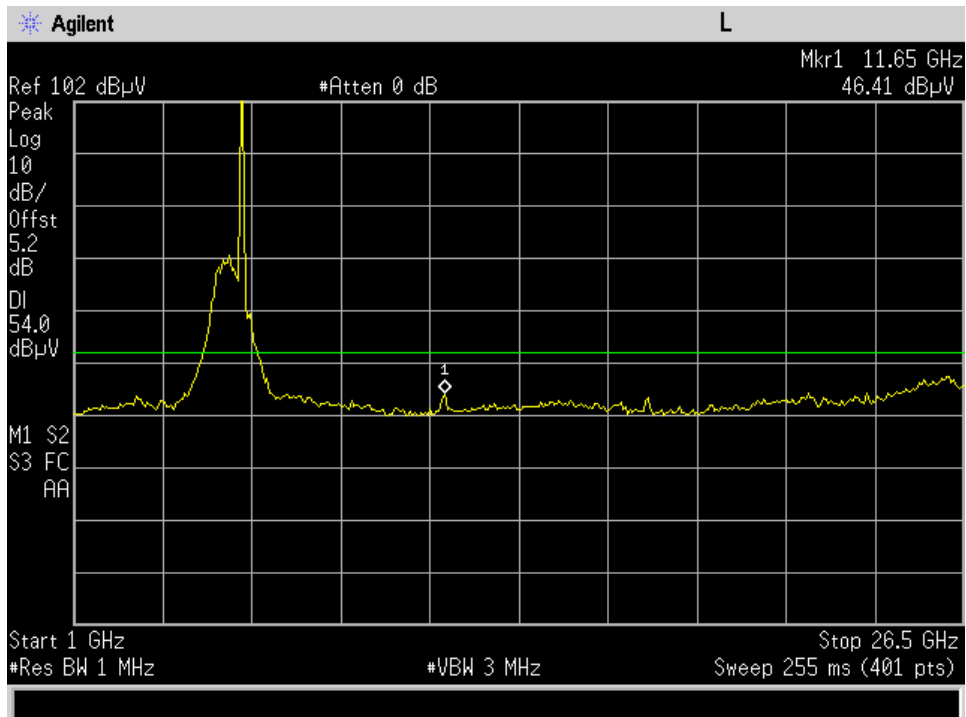




20MHz Mid Ch 1-26.5GHz Port J4

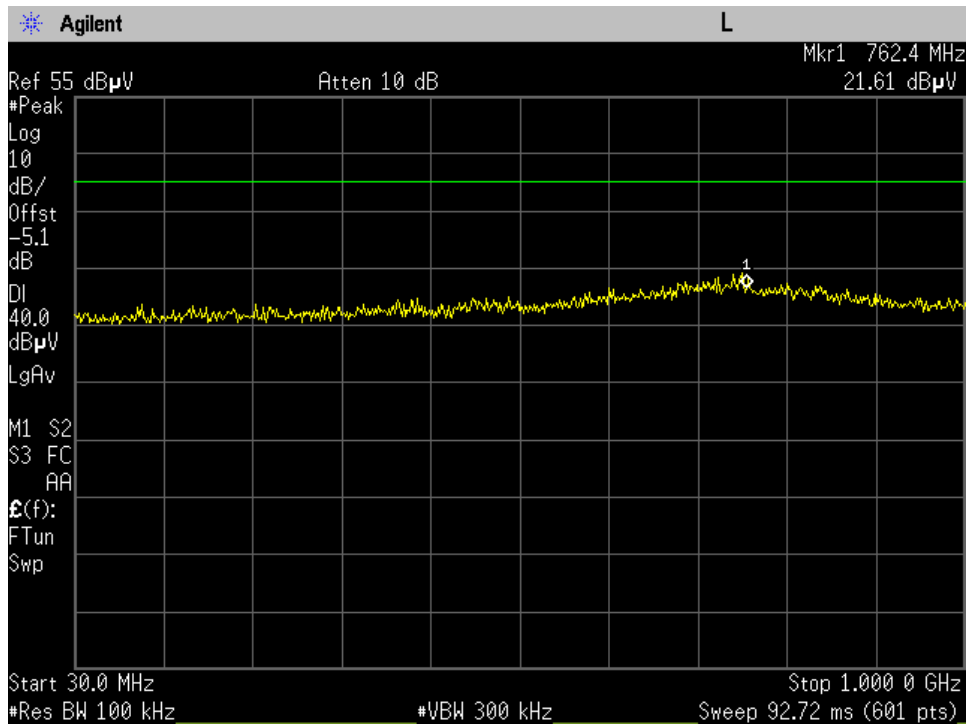


20MHz Mid Ch 1-26.5GHz Port J7

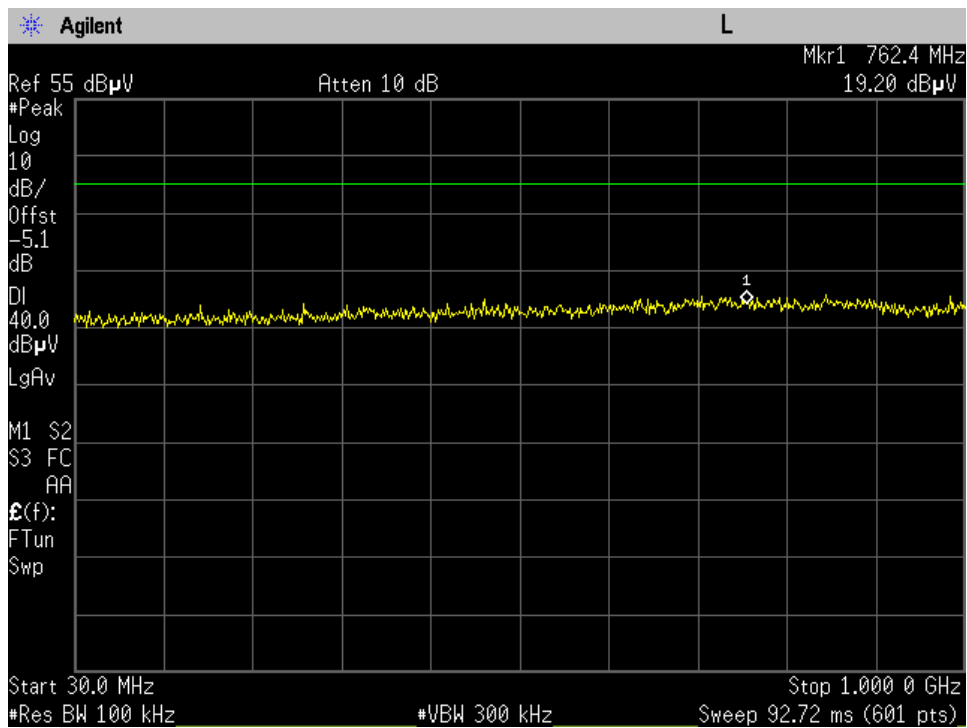




20MHz Mid Ch 30 - 1000MHz Port J4

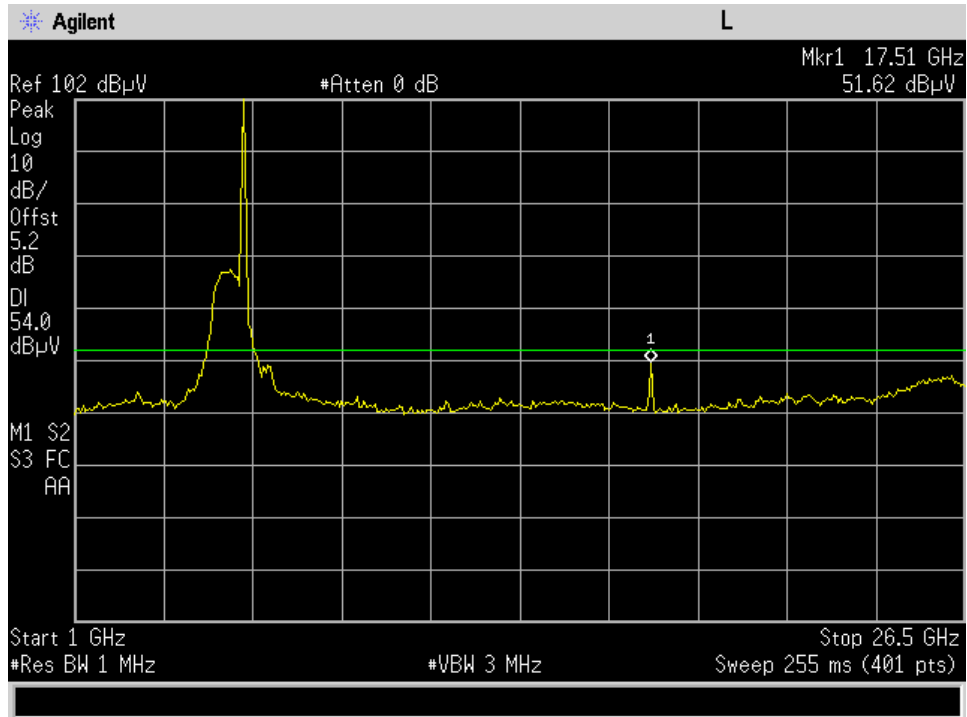


20MHz Mid Ch 30 - 1000MHz Port J7

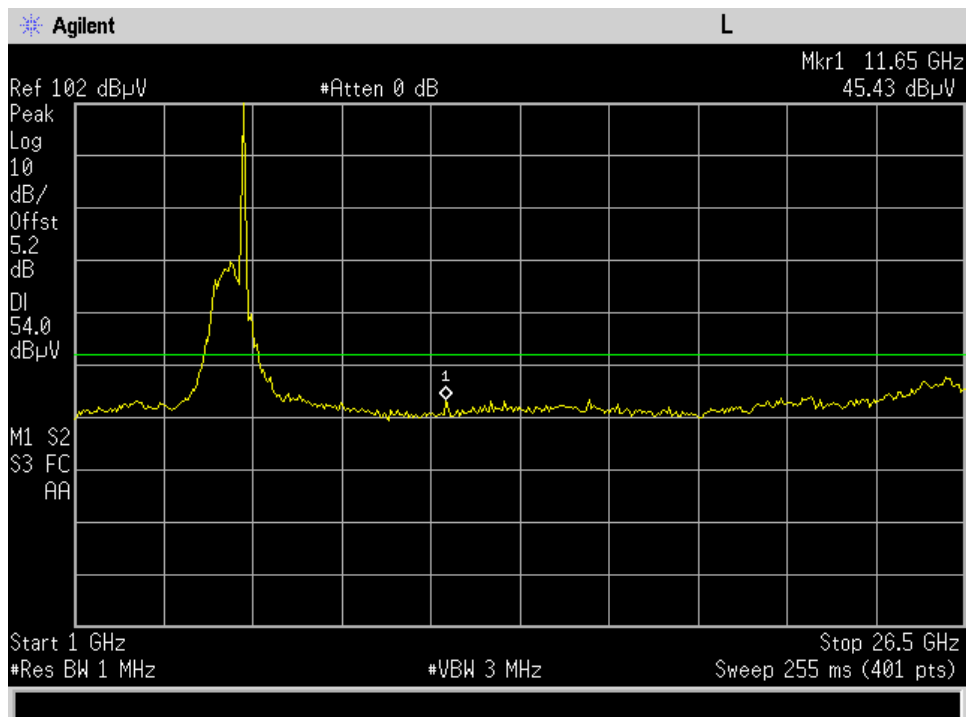




30MHz High Ch 1-26.5GHz Port J4

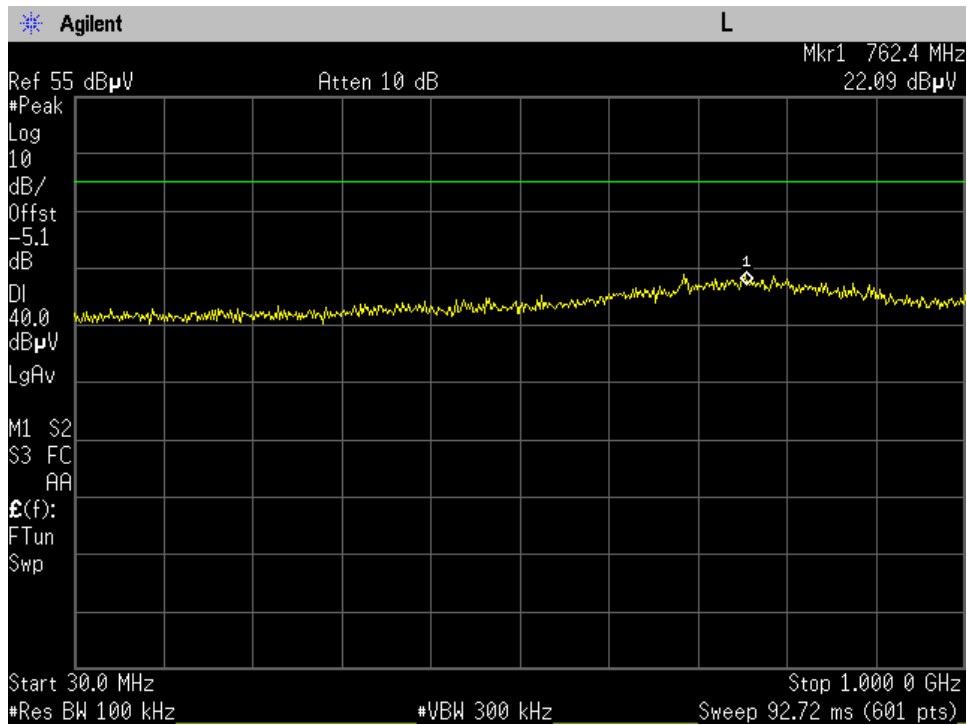


30MHz High Ch 1-26.5GHz Port J7

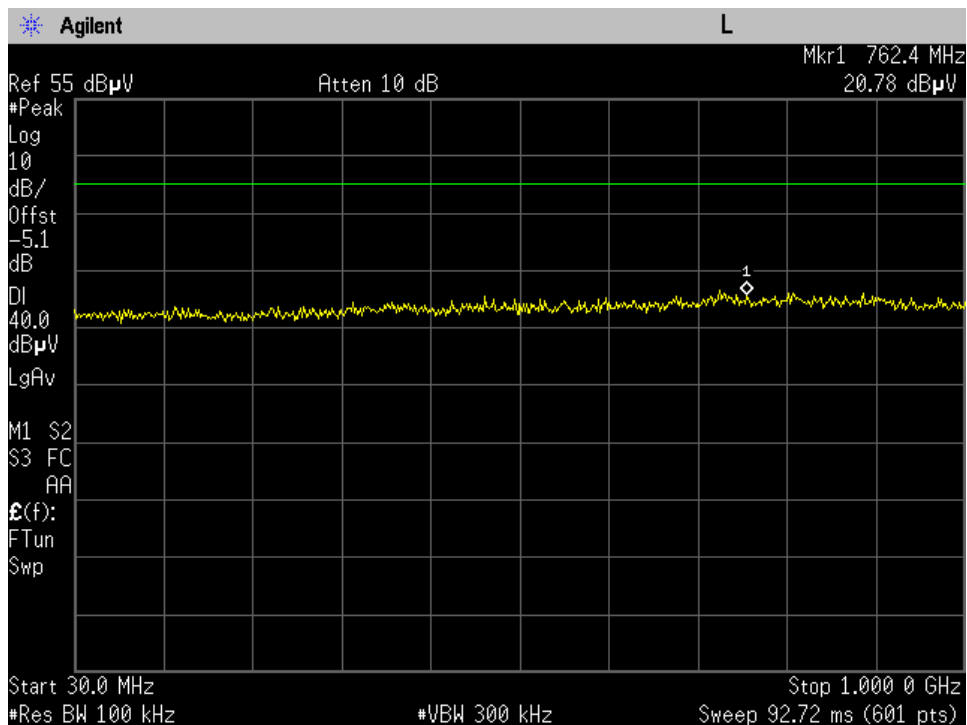




30MHz High Ch 30 - 1000MHz Port J4

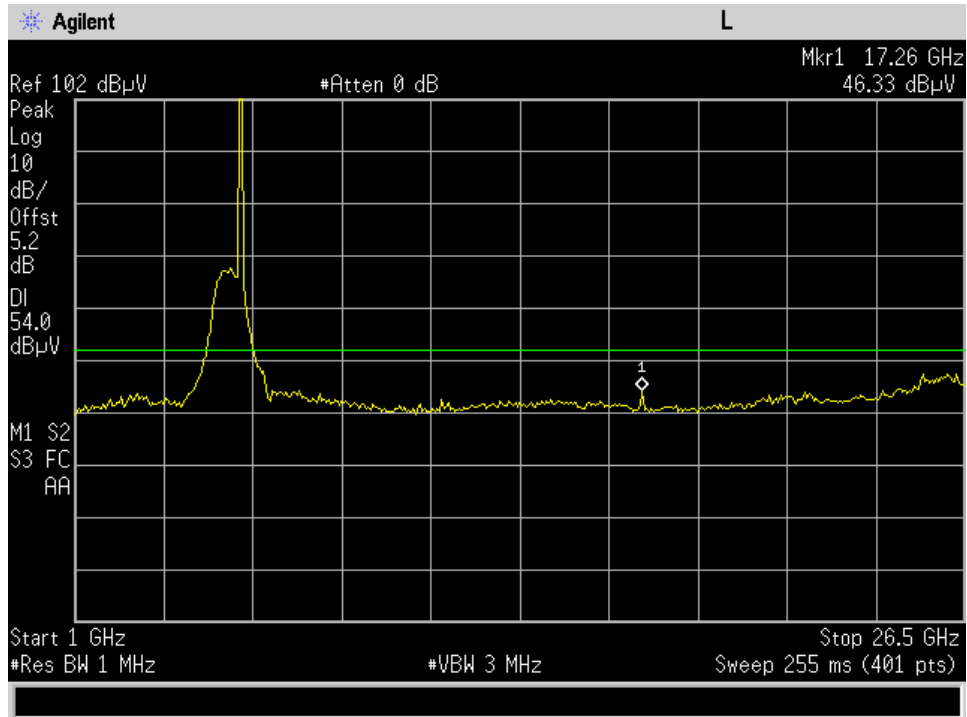


30MHz High Ch 30 - 1000MHz Port J7

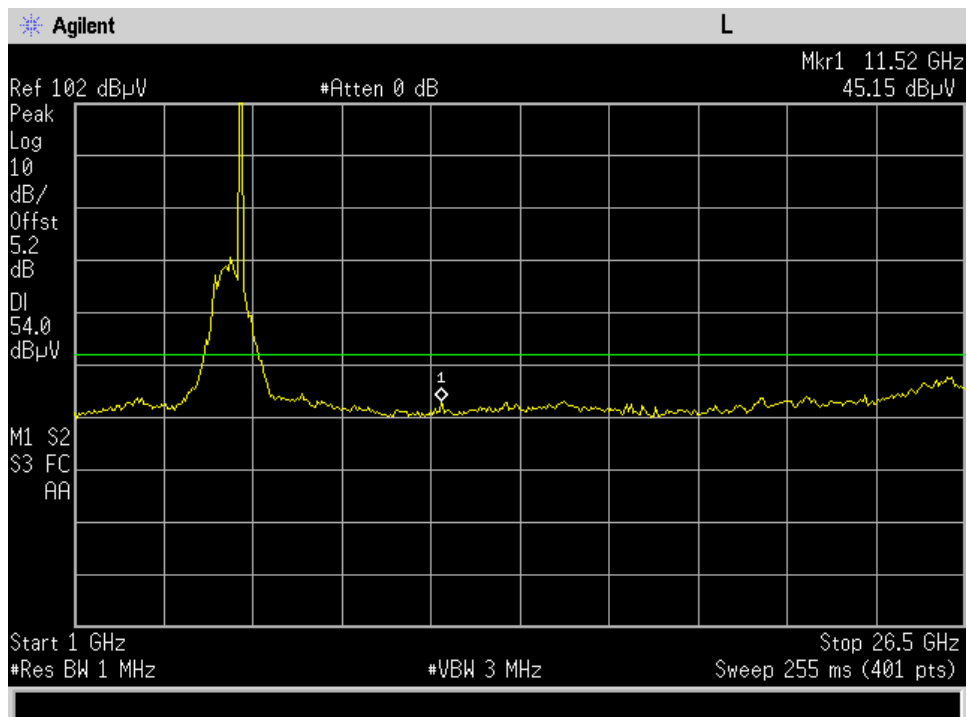




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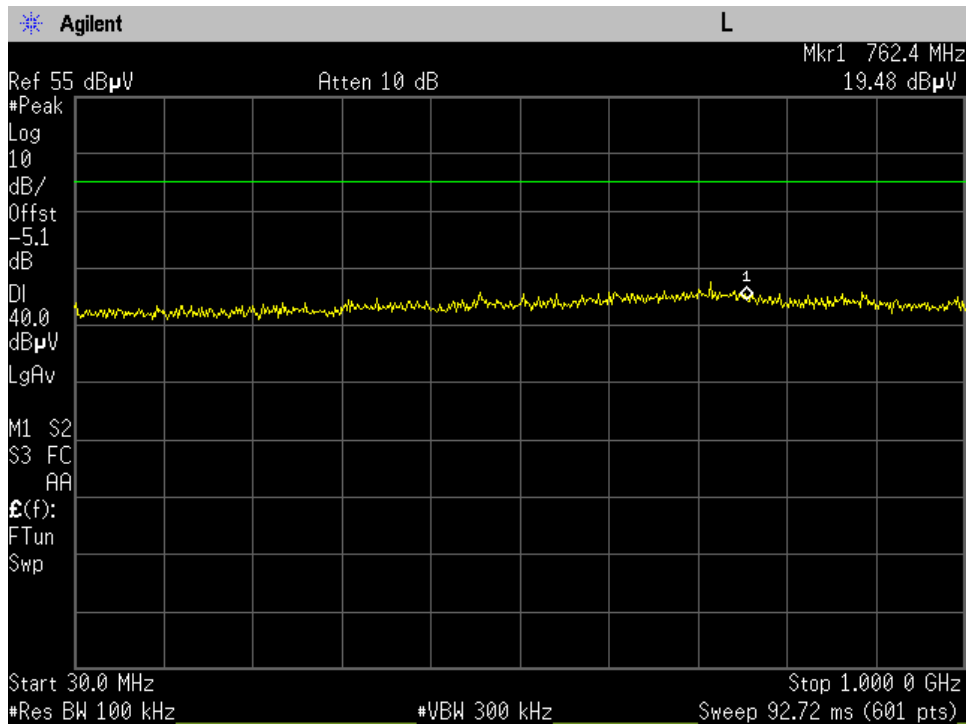


30MHz Low Ch 1-26.5GHz Port J7

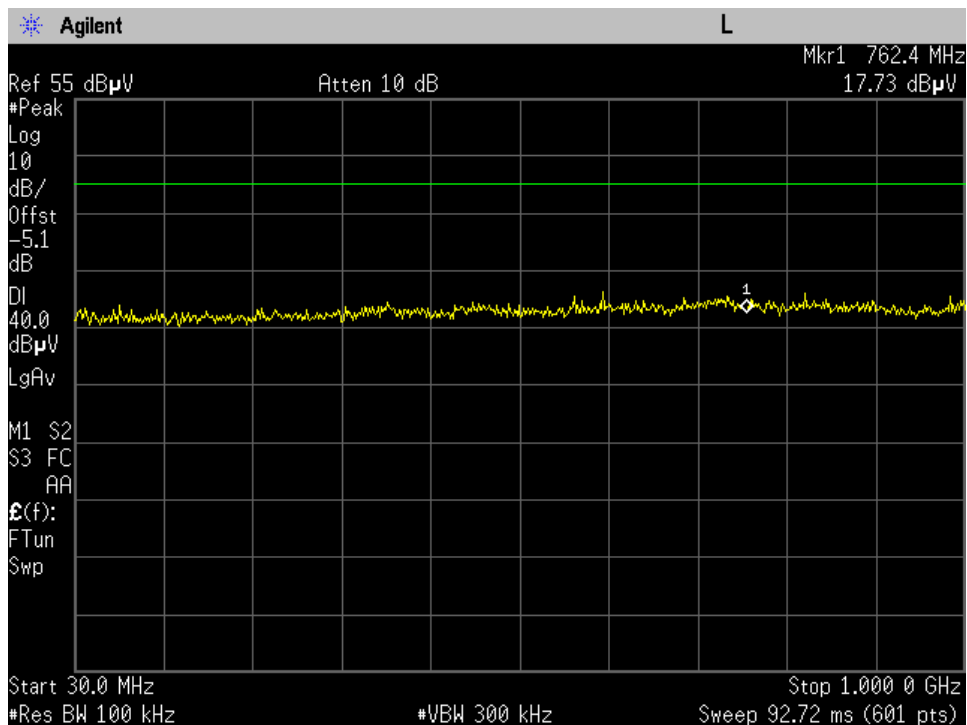




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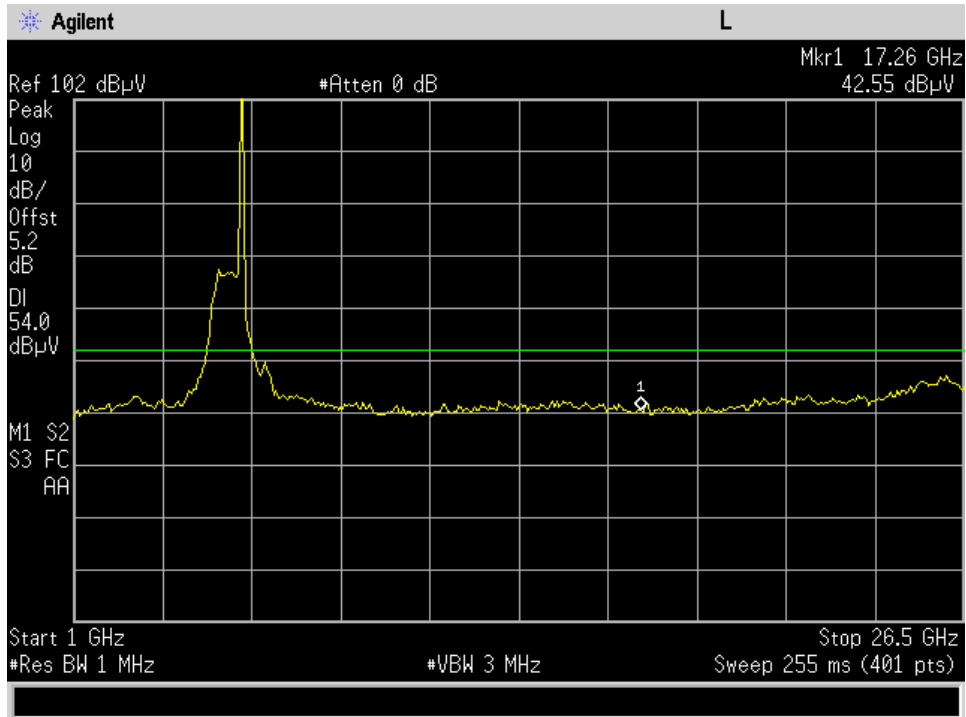


30MHz Low Ch 30 - 1000MHz Port J7

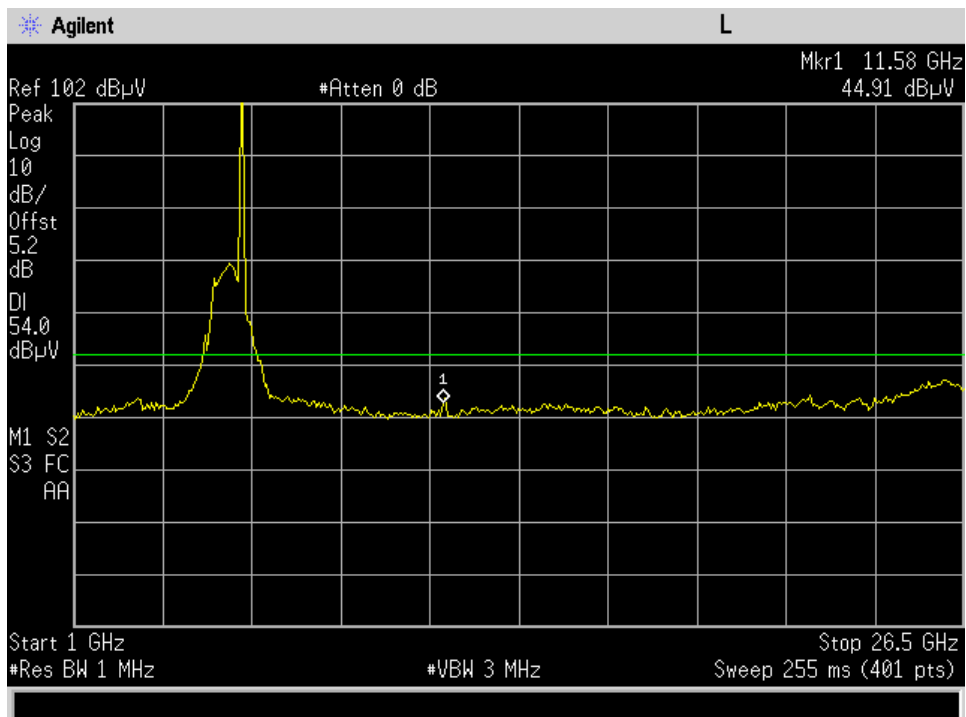




30MHz Mid Ch 1-26.5GHz Port J4

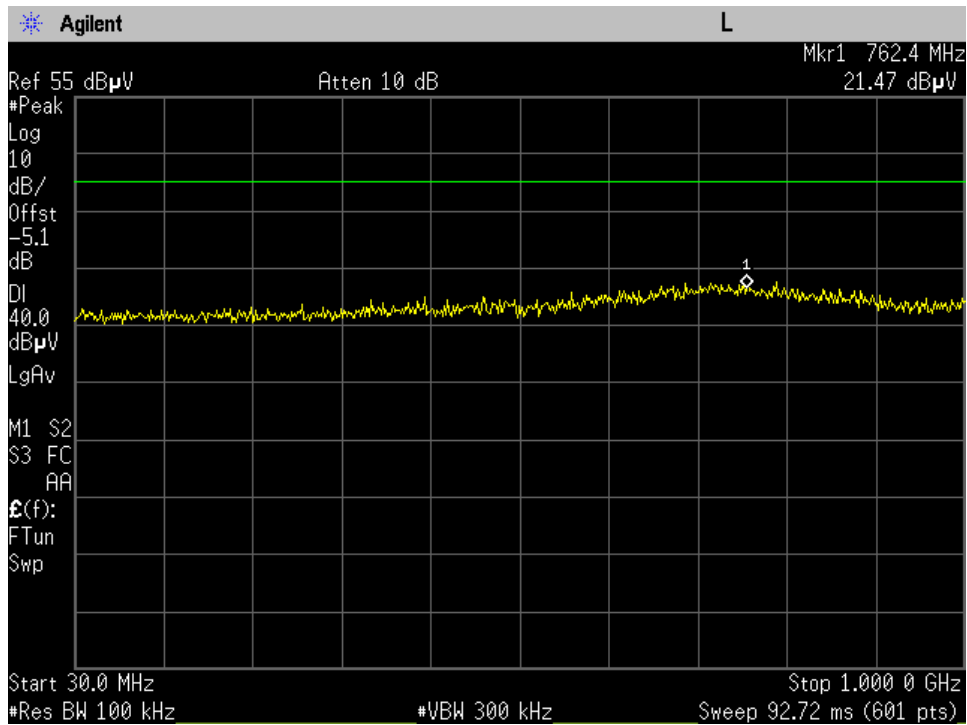


30MHz Mid Ch 1-26.5GHz Port J7

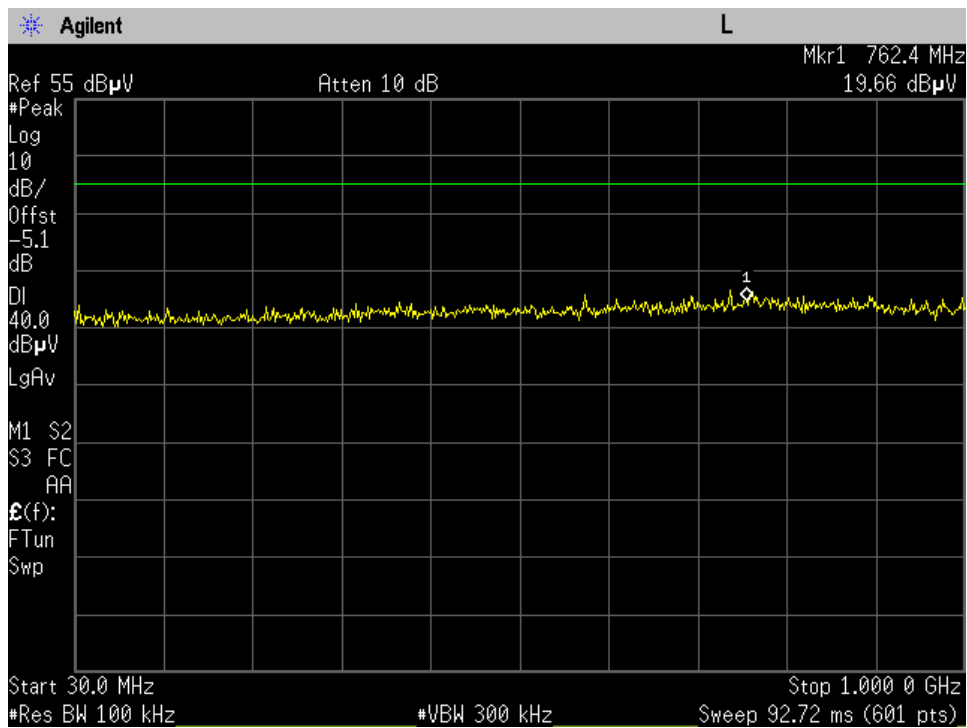




30MHz Mid Ch 30 - 1000MHz Port J4

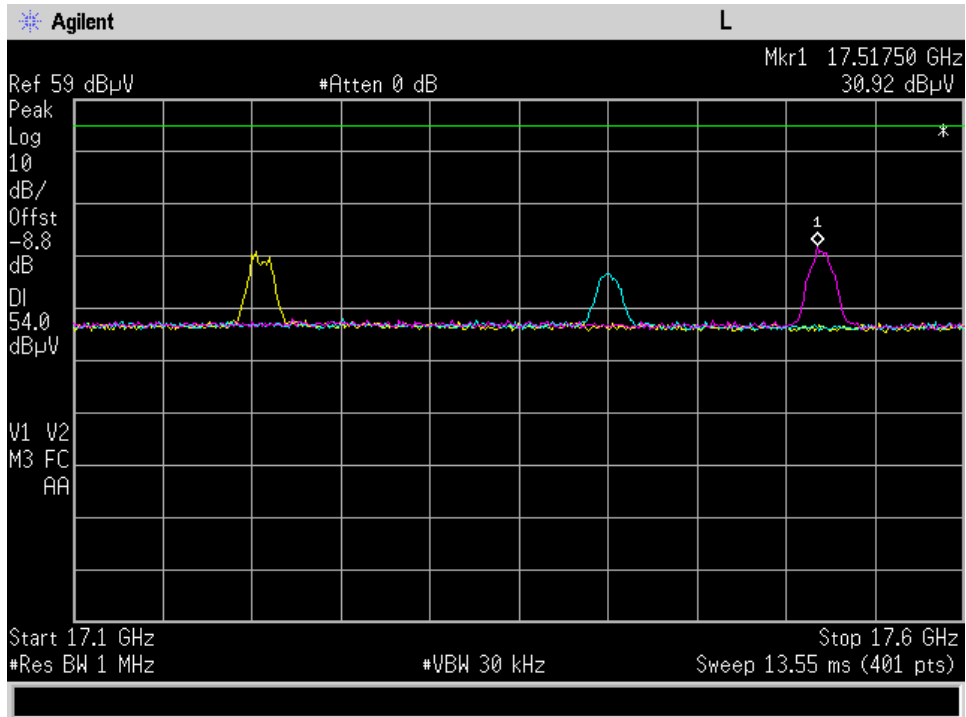


30MHz Mid Ch 30 - 1000MHz Port J7

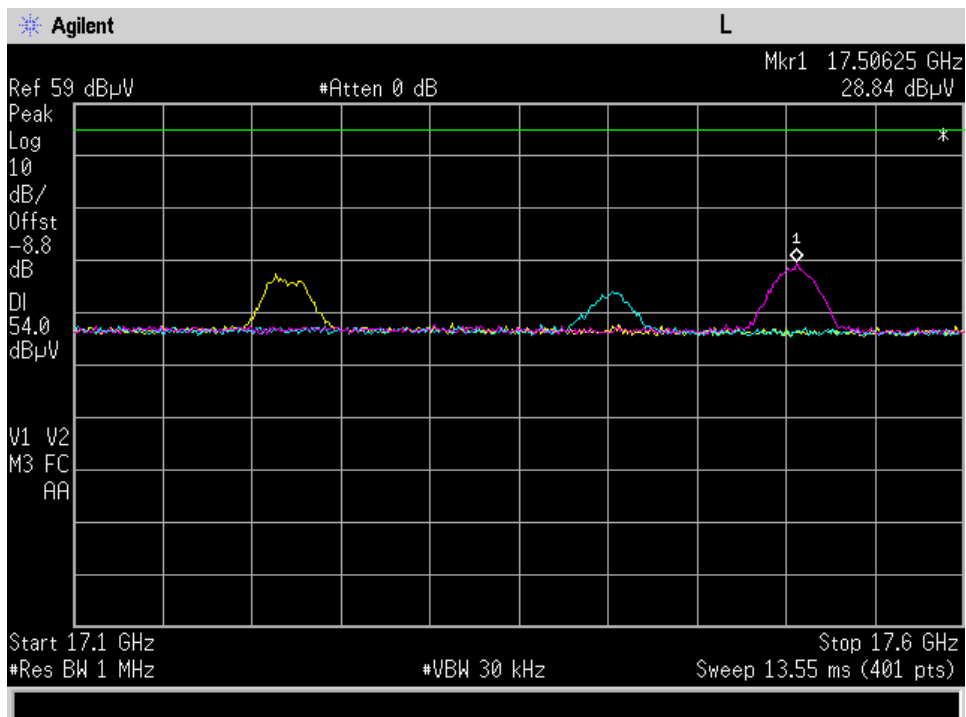




3rd Harmonic 10MHz Low Mid High Ch Port J4

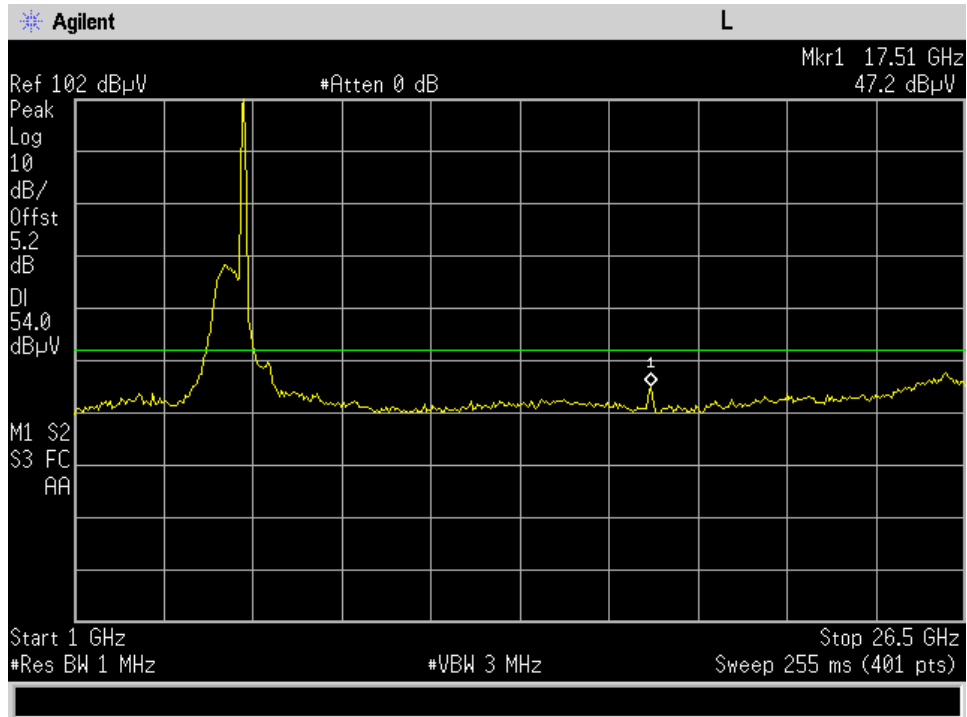


3rd Harmonic 20MHz Low Mid High Ch Port J4

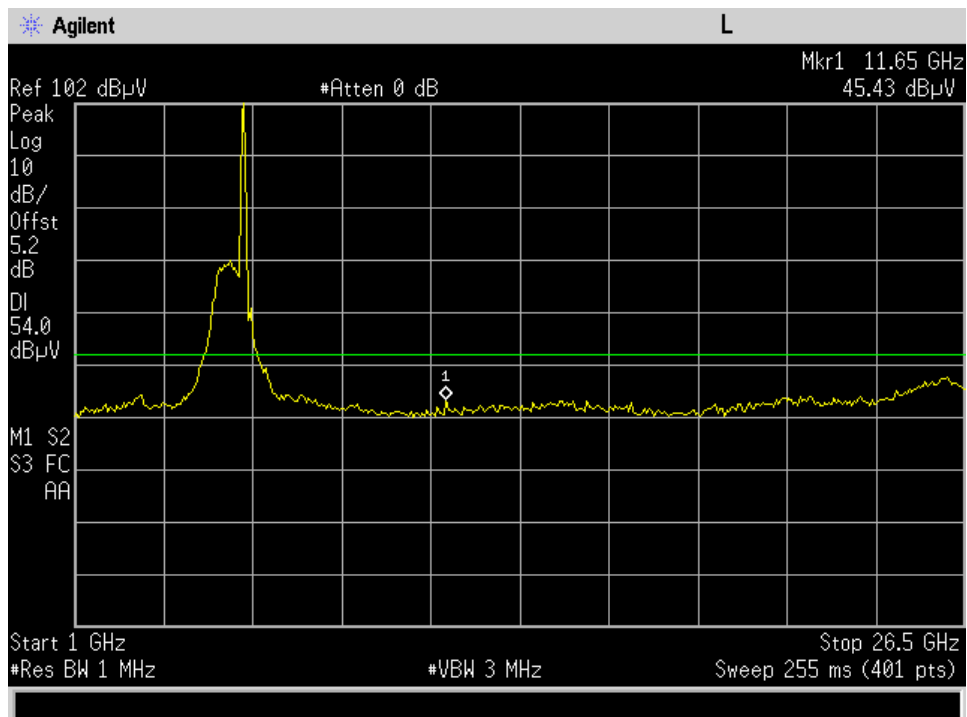




40MHz High Ch 1-26.5GHz Port J4

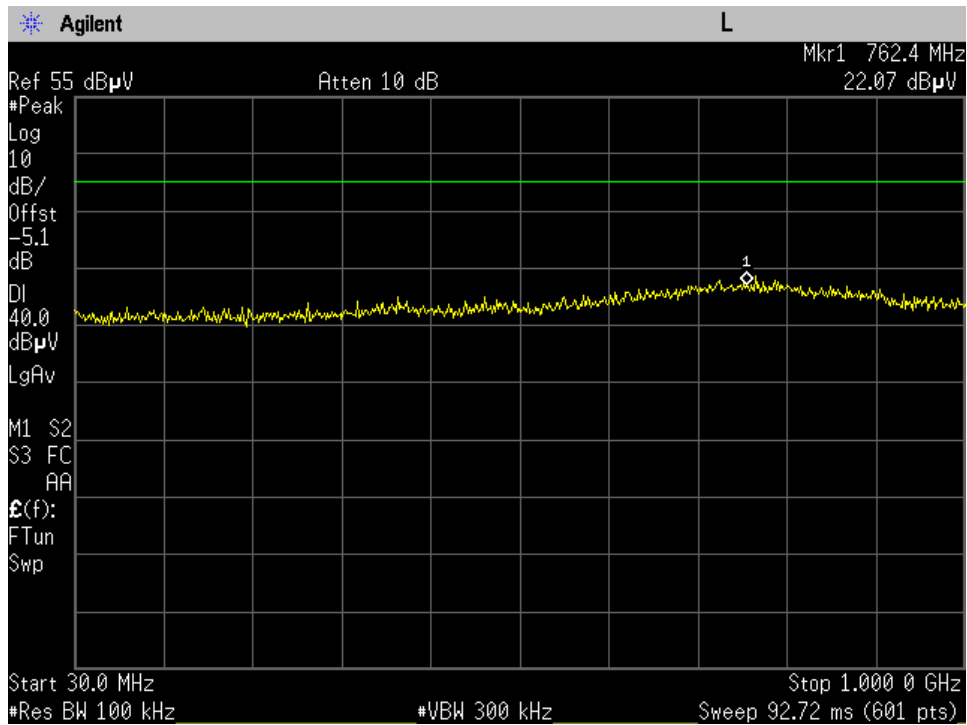


40MHz High Ch 1-26.5GHz Port J7

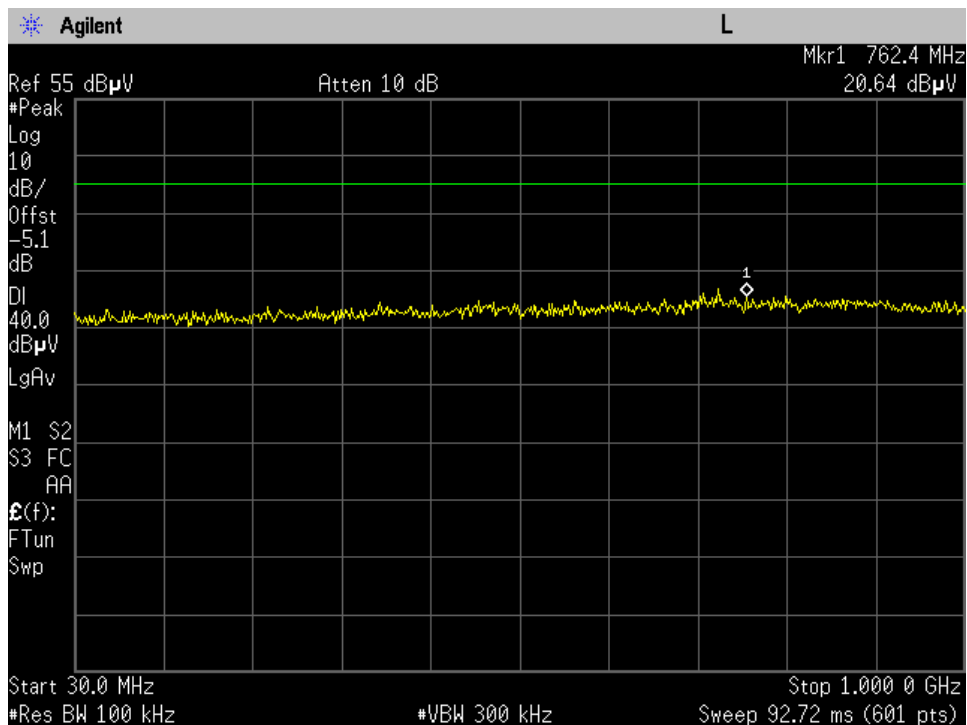




40MHz High Ch 30 - 1000MHz Port J4

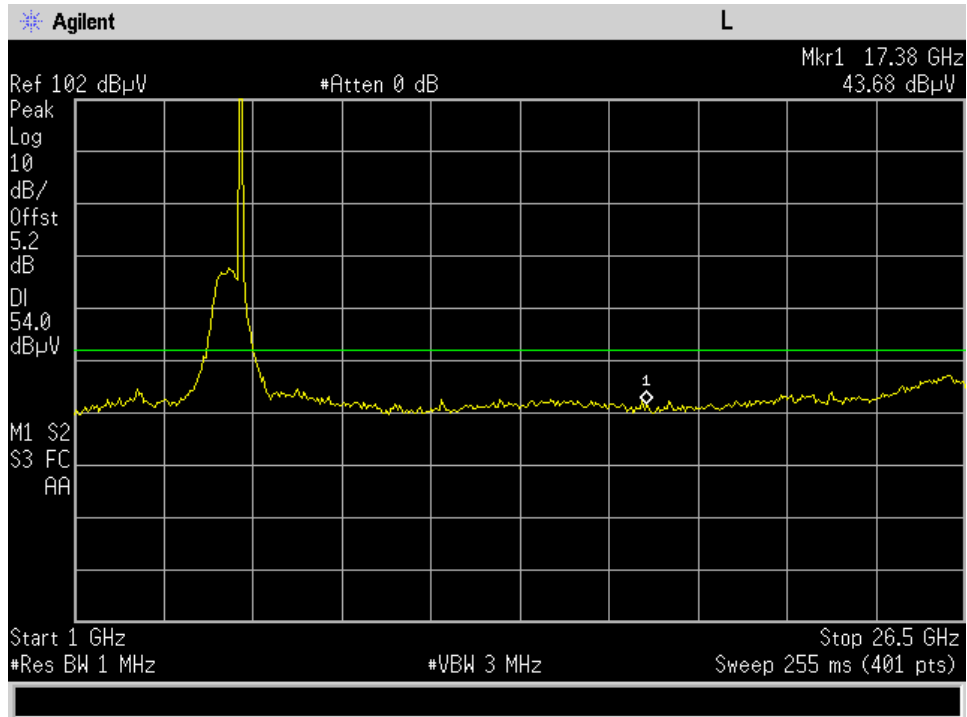


40MHz High Ch 30 - 1000MHz Port J7

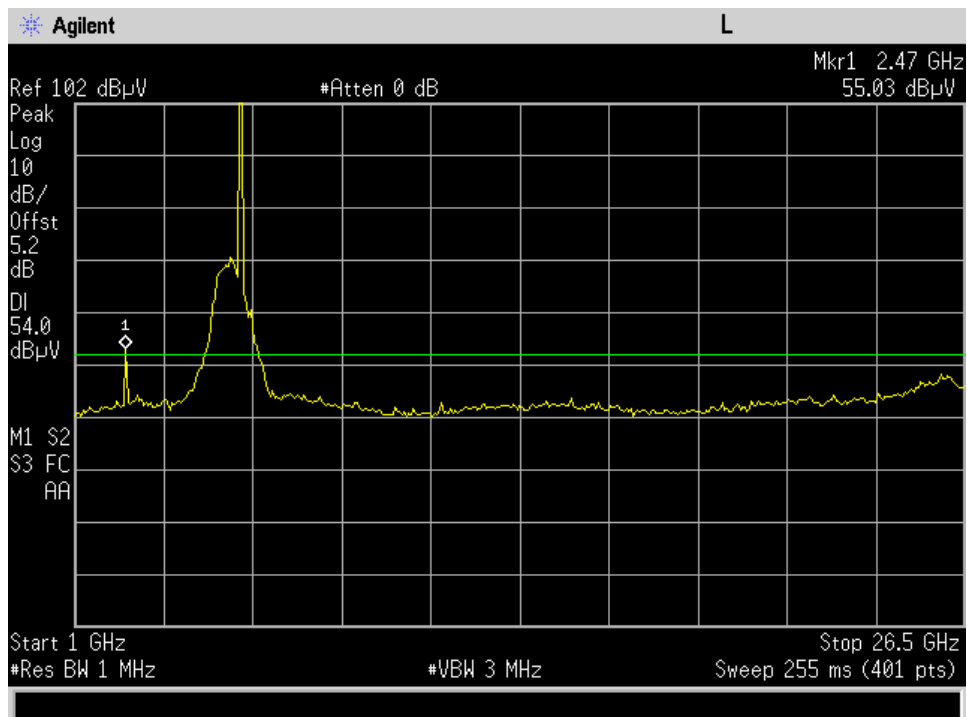




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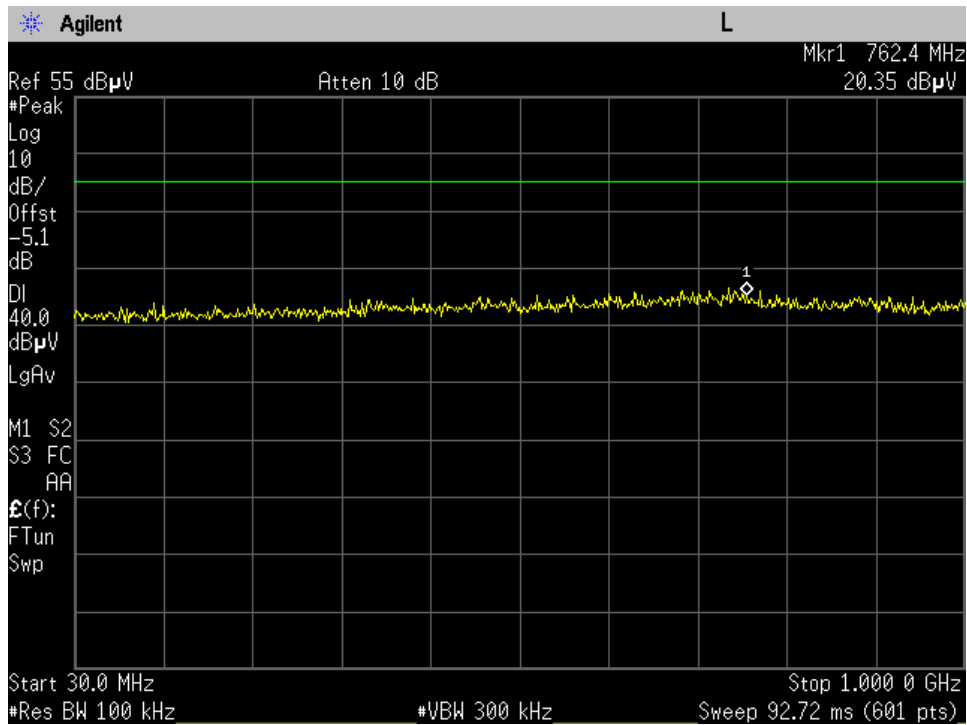


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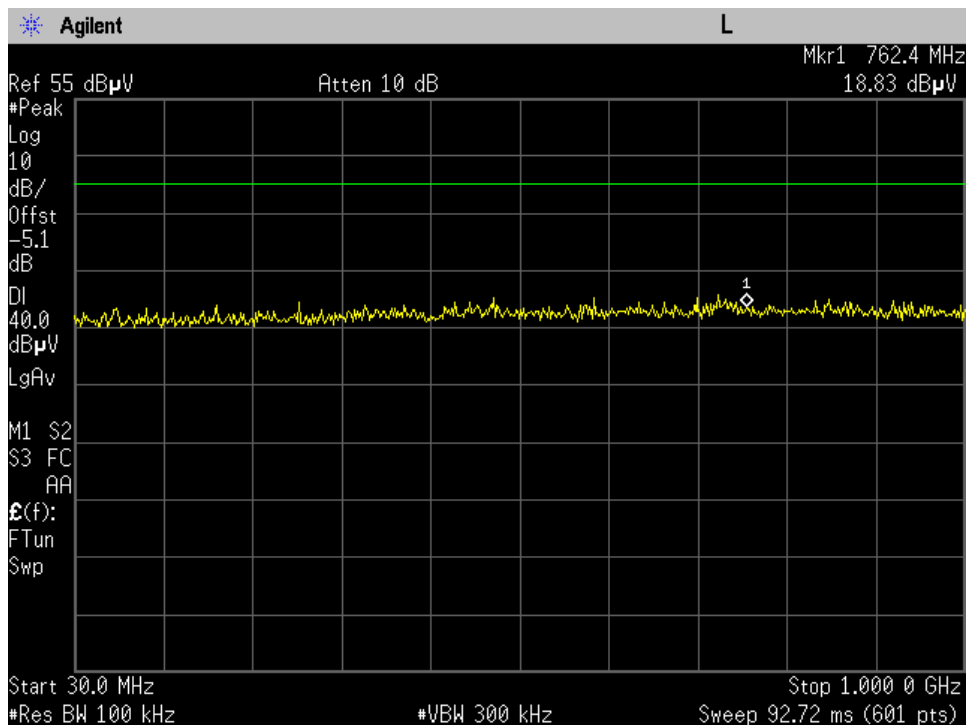




40MHz Low Ch 30 - 1000MHz Port J4

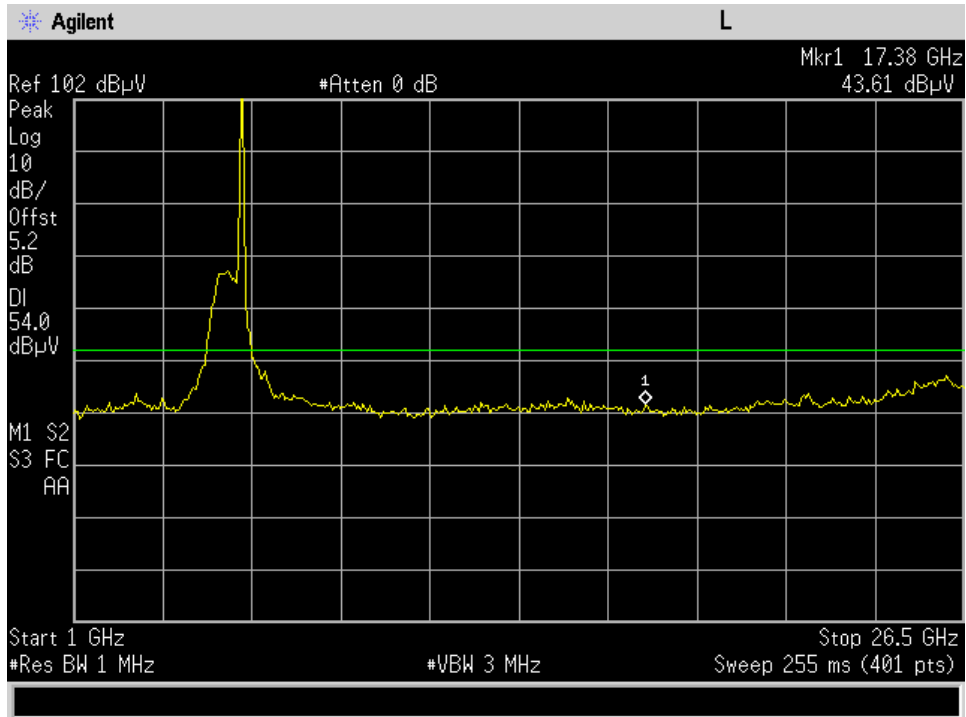


40MHz Low Ch 30 - 1000MHz Port J7

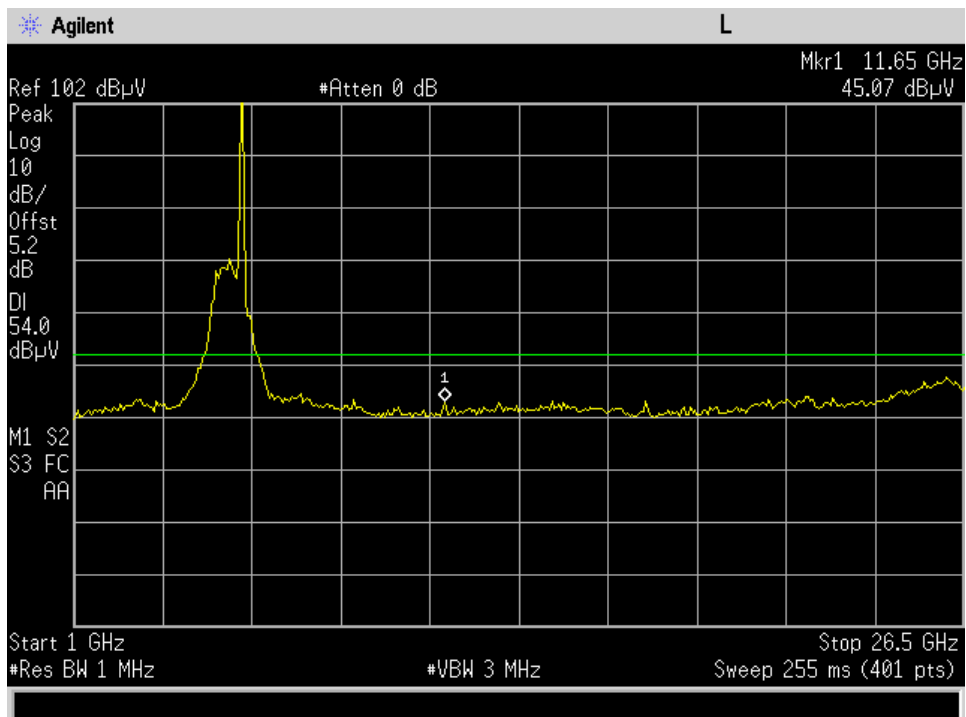




40MHz Mid Ch 1-26.5GHz Port J4

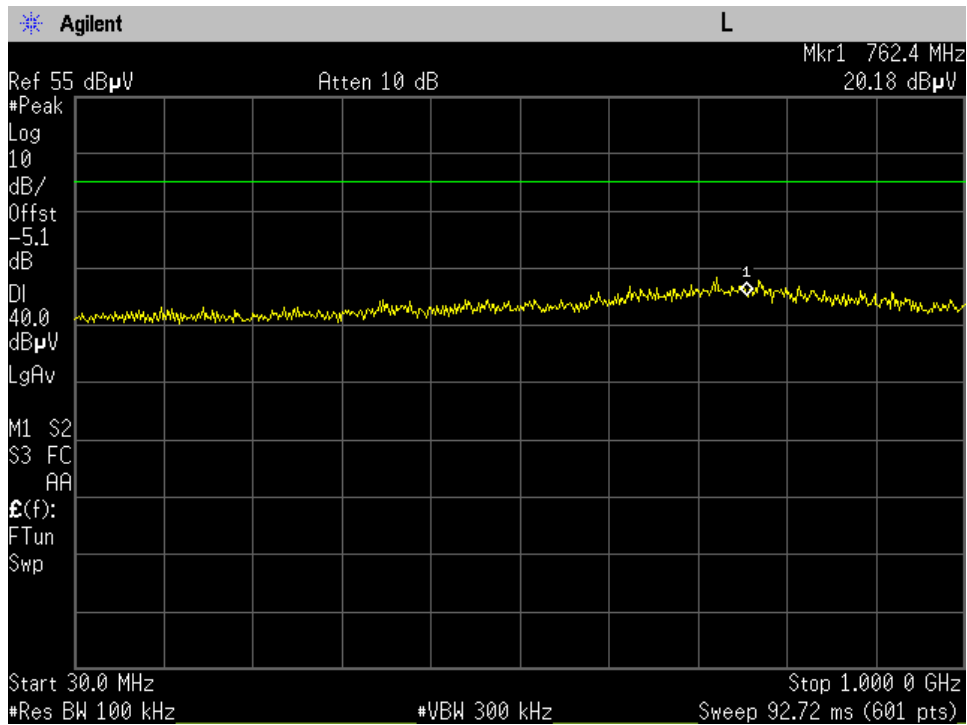


40MHz Mid Ch 1-26.5GHz Port J7

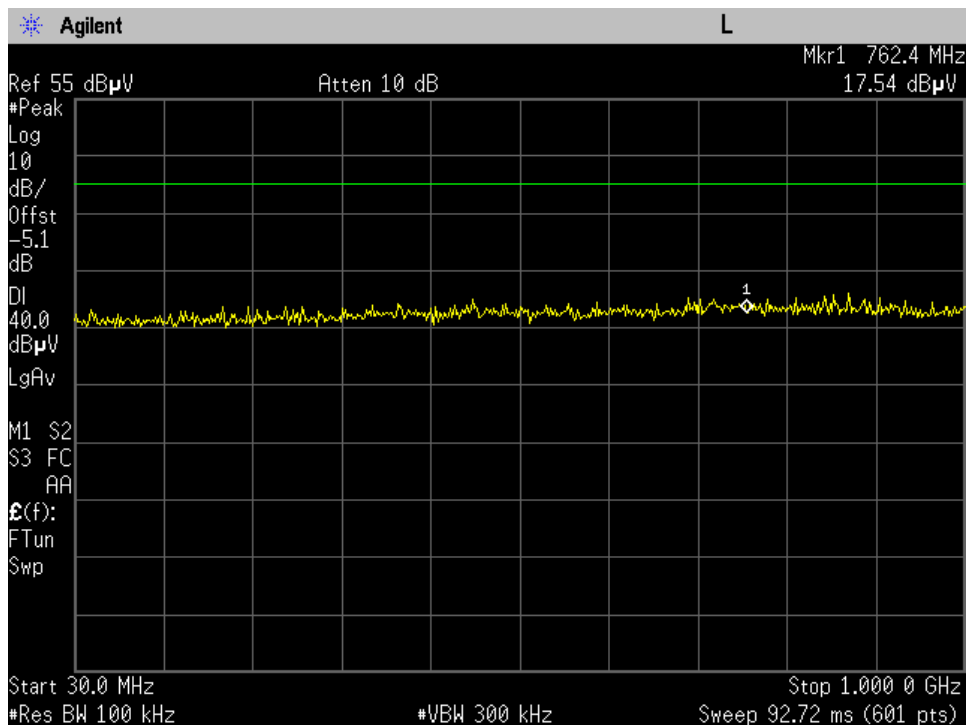




40MHz Mid Ch 30 - 1000MHz Port J4

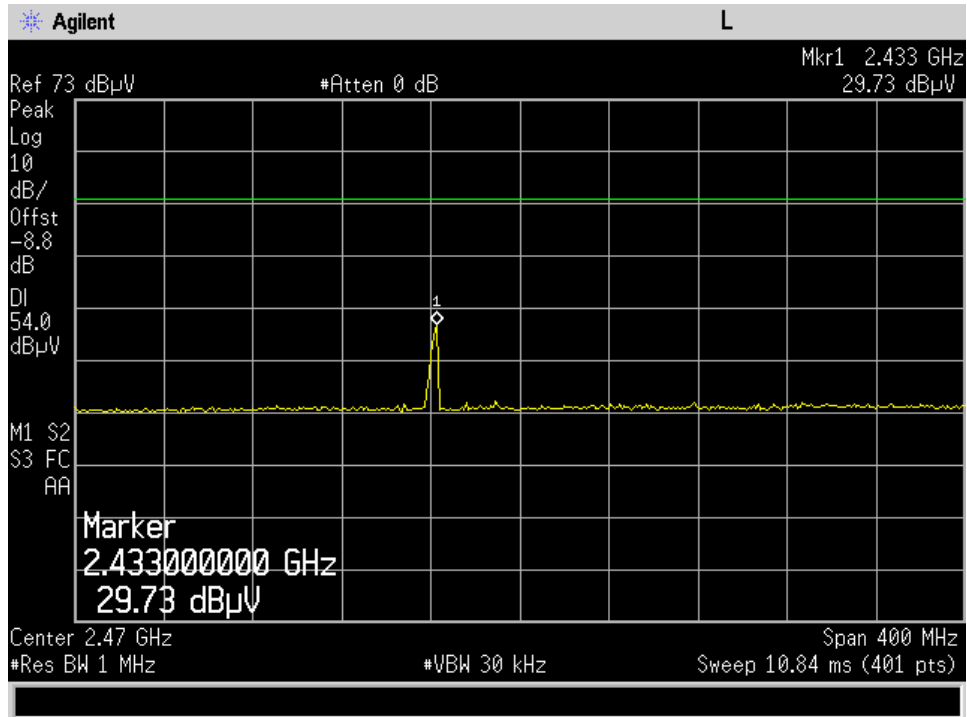


40MHz Mid Ch 30 - 1000MHz Port J7





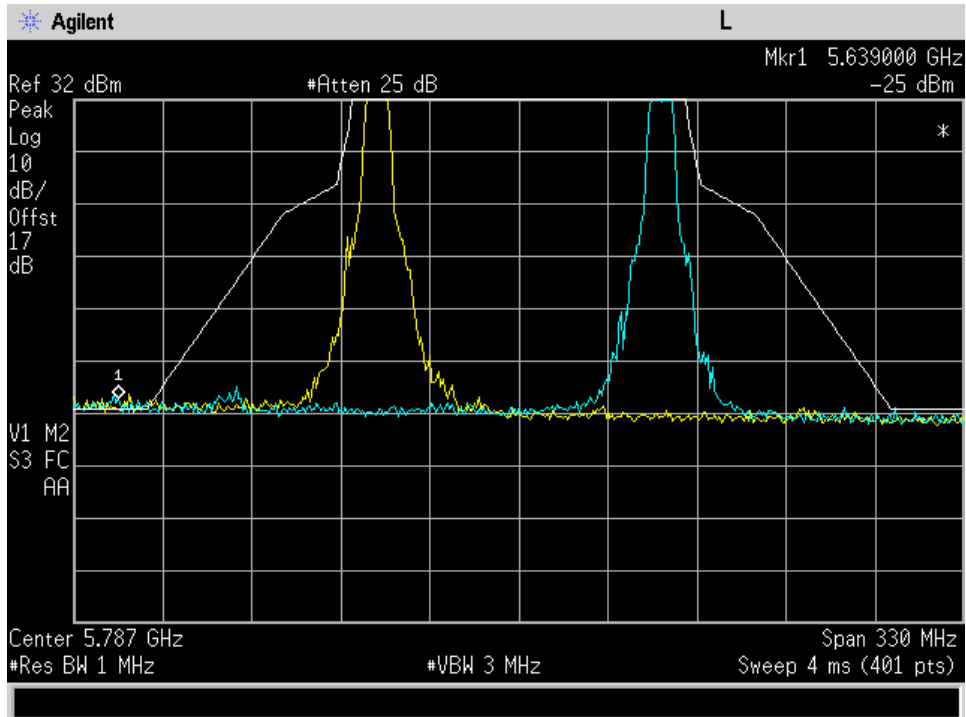
Spur_40MHz Low Ch 2.433GHz Port J7



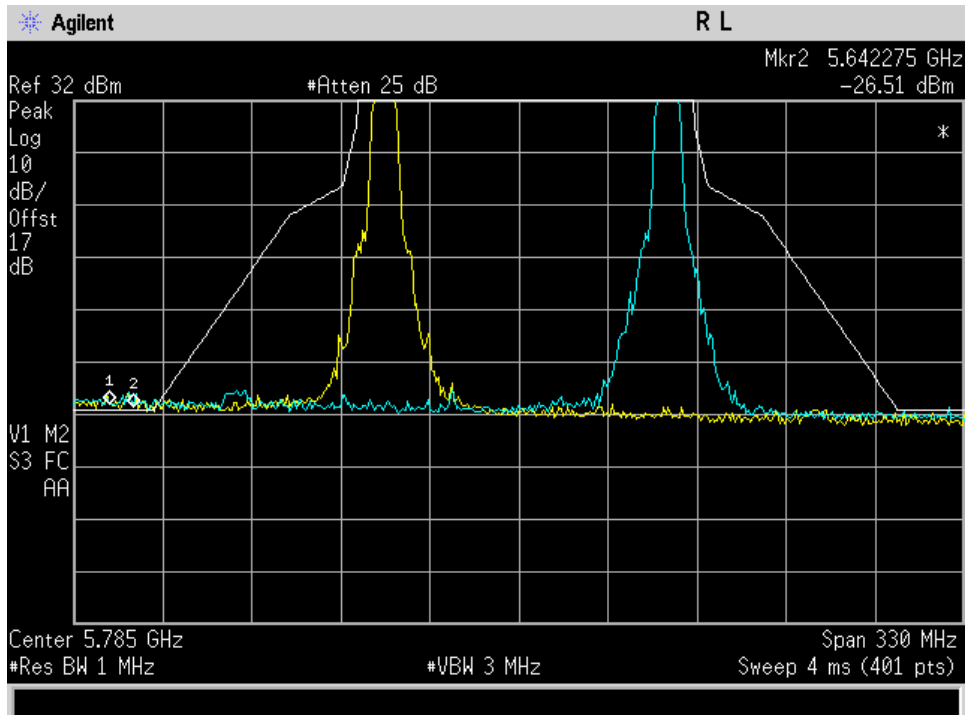


UNII-3 Mask for Fundamental

10 Mhz Mask Low High Ch Port J4

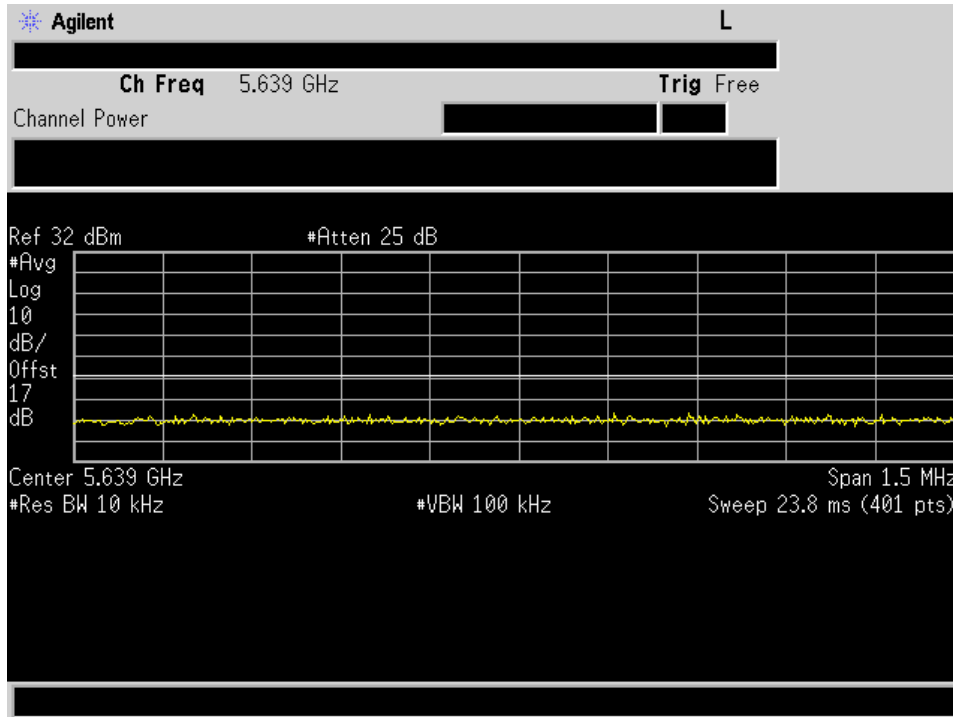


10 Mhz Mask Low High Ch Port J7

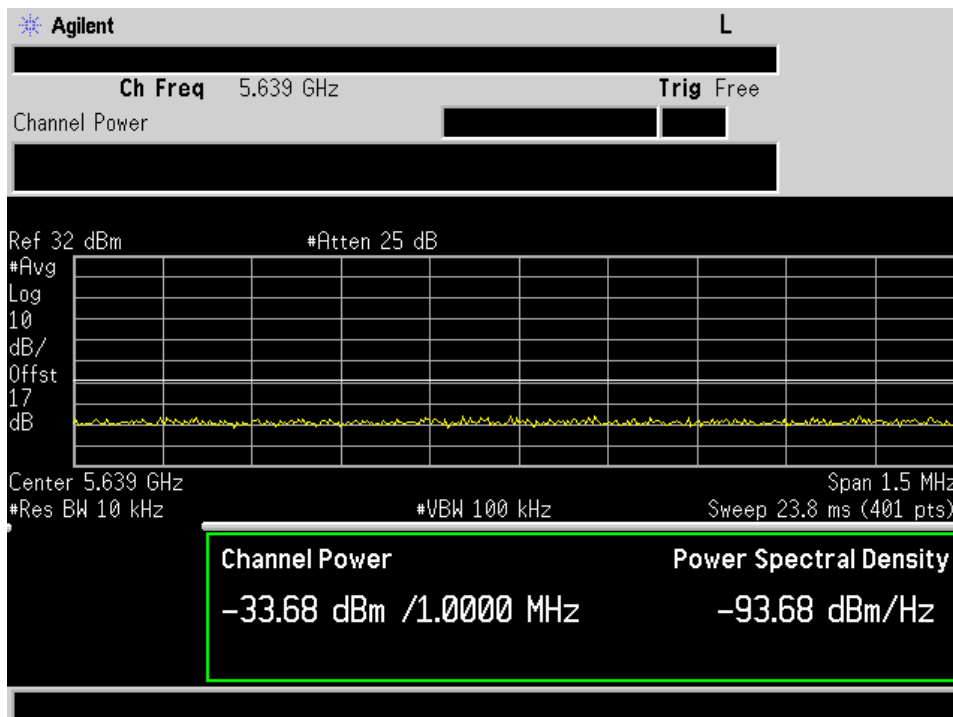




10 Mhz Power integration Low ch Port J4

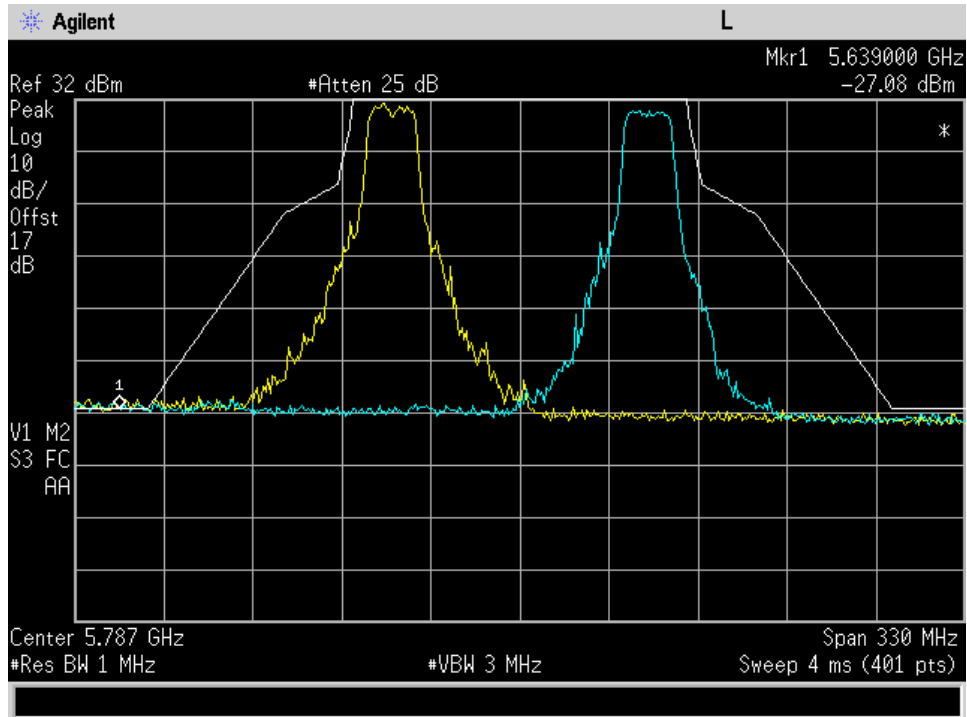


10 Mhz Power integration Low ch Port J7

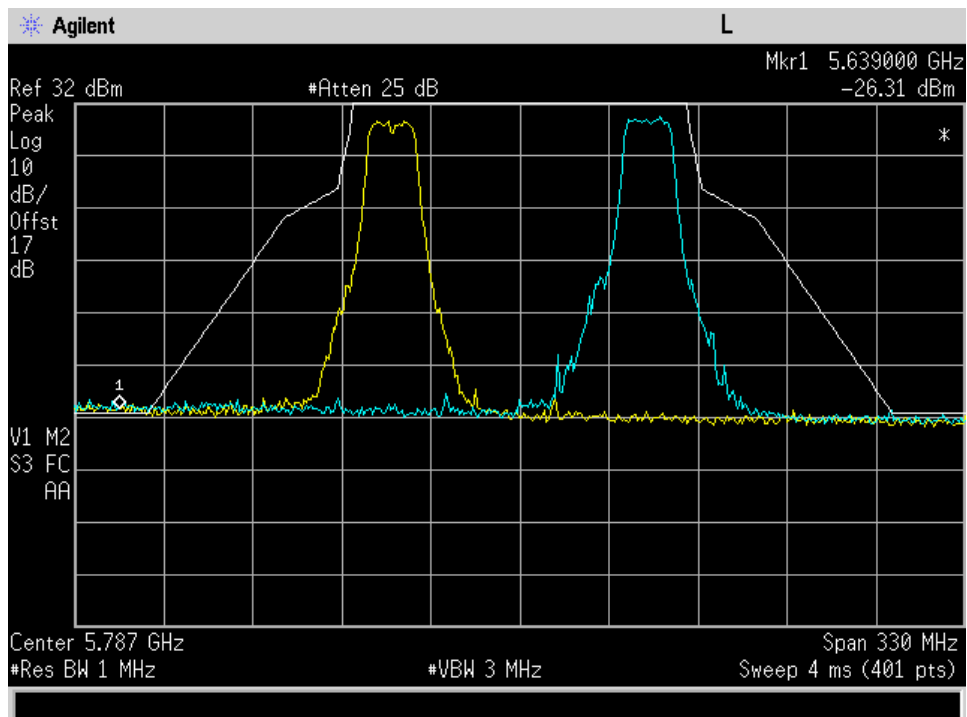




20 Mhz Mask Low High Ch Port J4

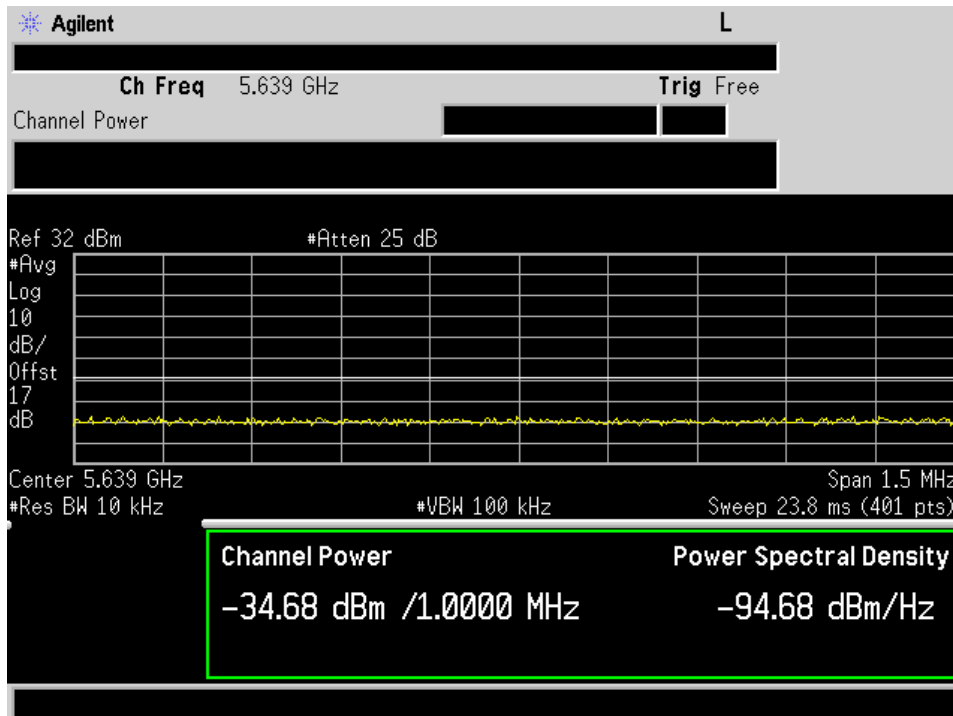


20 Mhz Mask Low High Ch Port J7

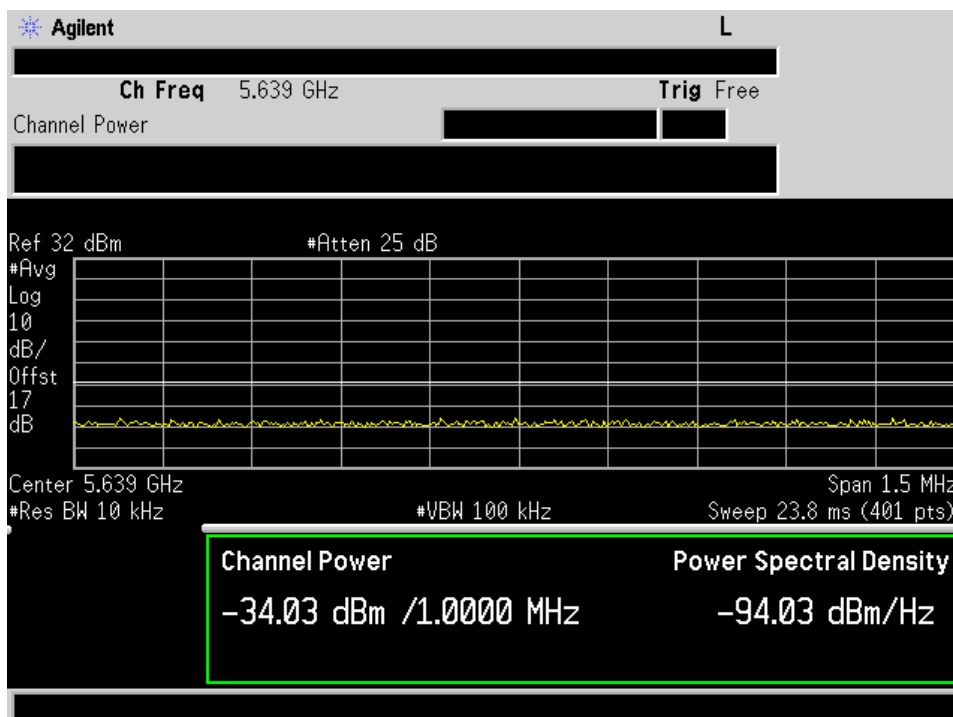




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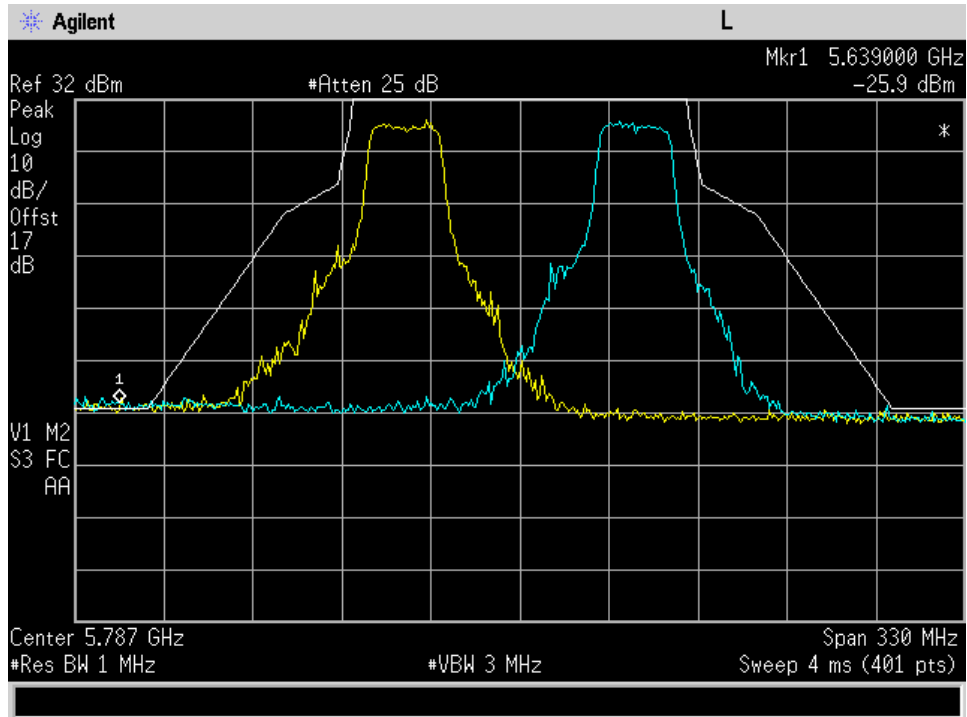


20 Mhz Power integration Low ch Port J7

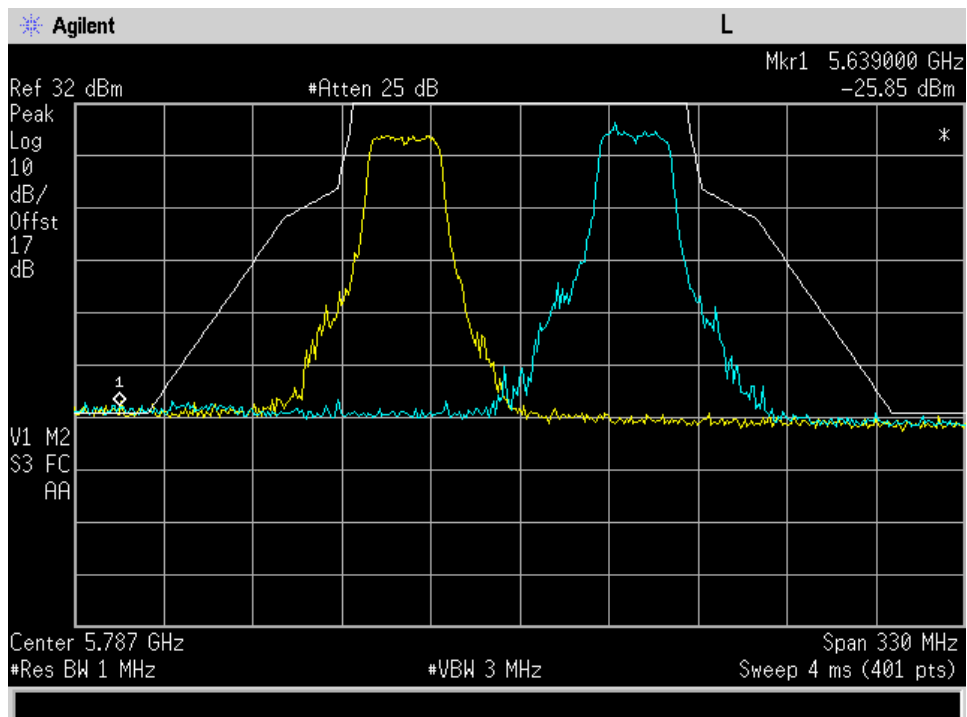




30 Mhz Mask Low High Ch Port J4

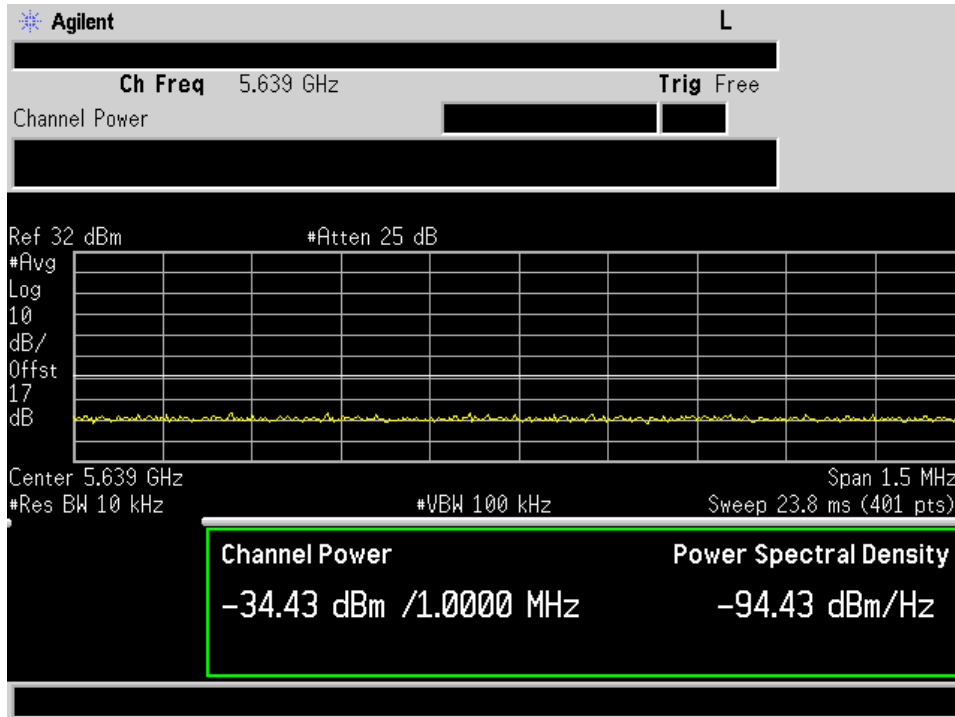


30 Mhz Mask Low High Ch Port J7

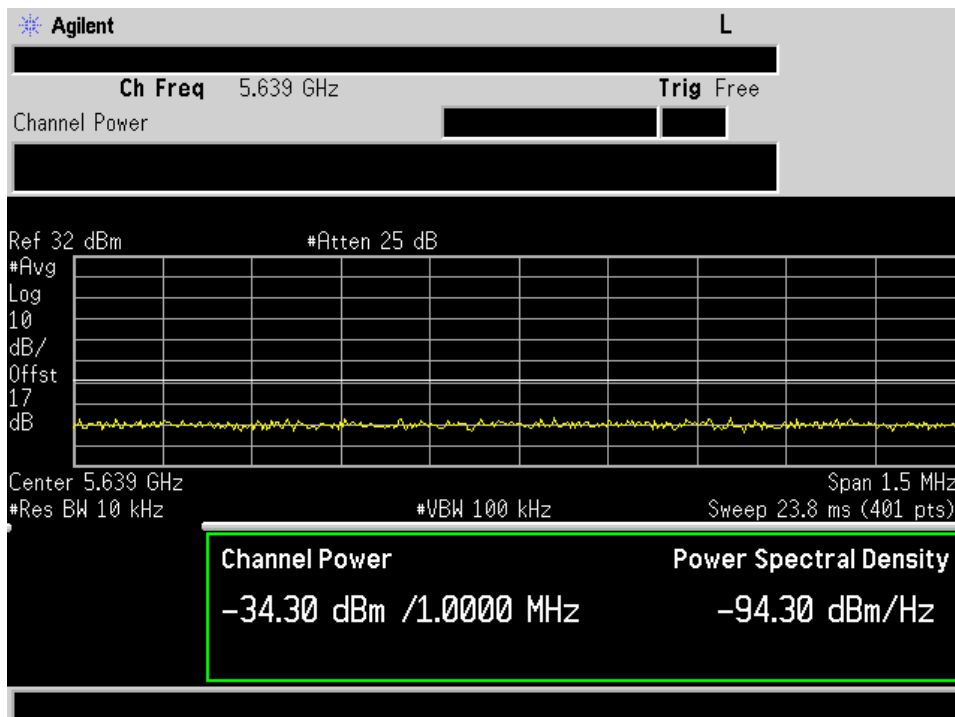




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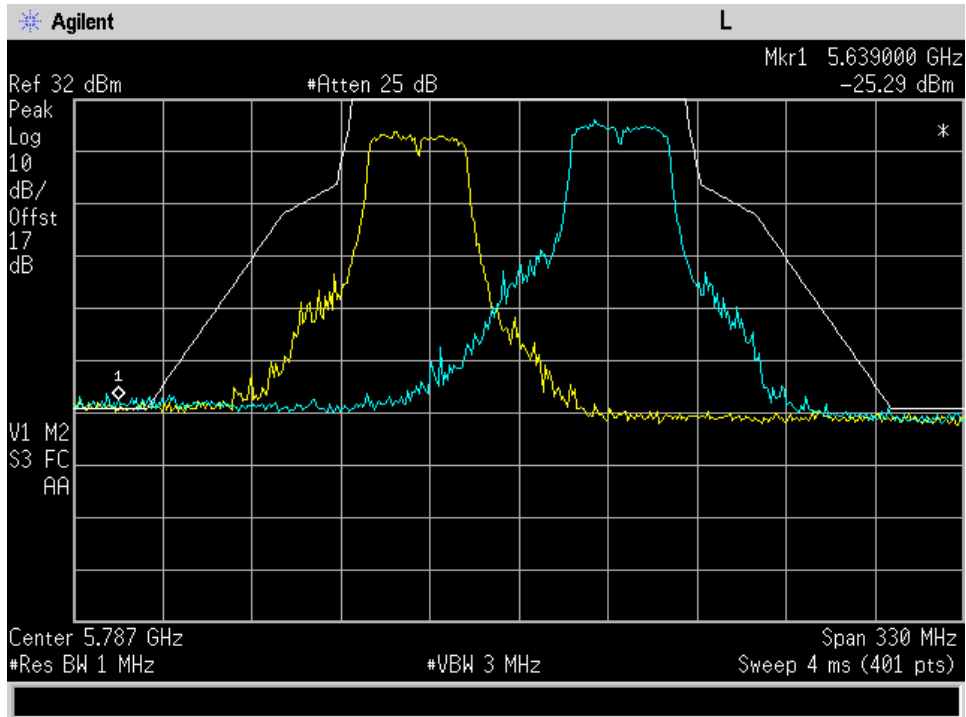


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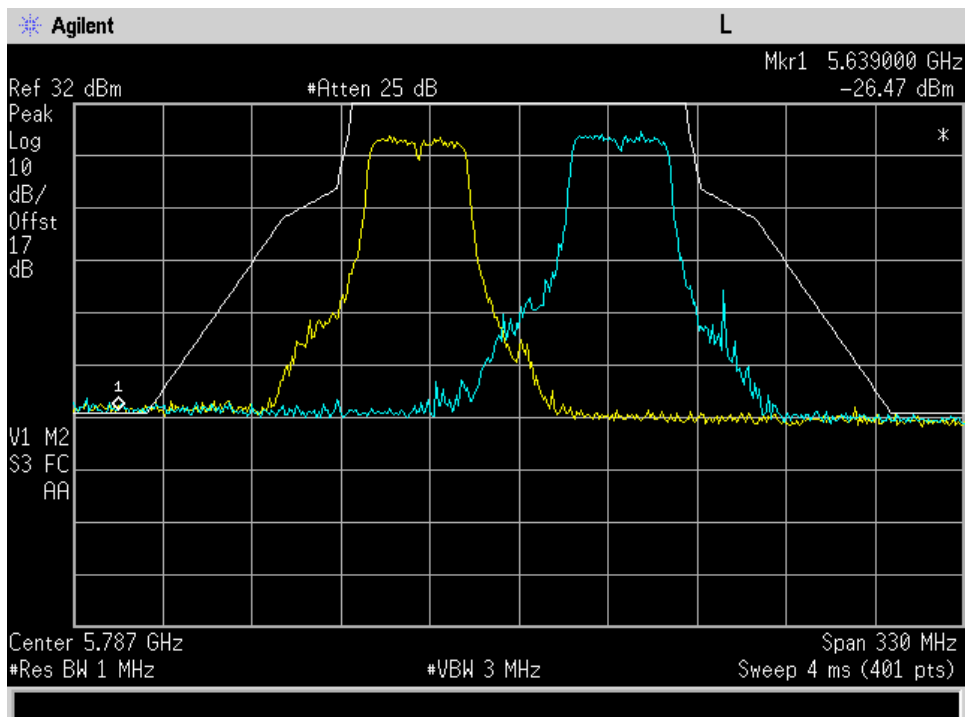




40 Mhz Mask Low High Ch Port J4

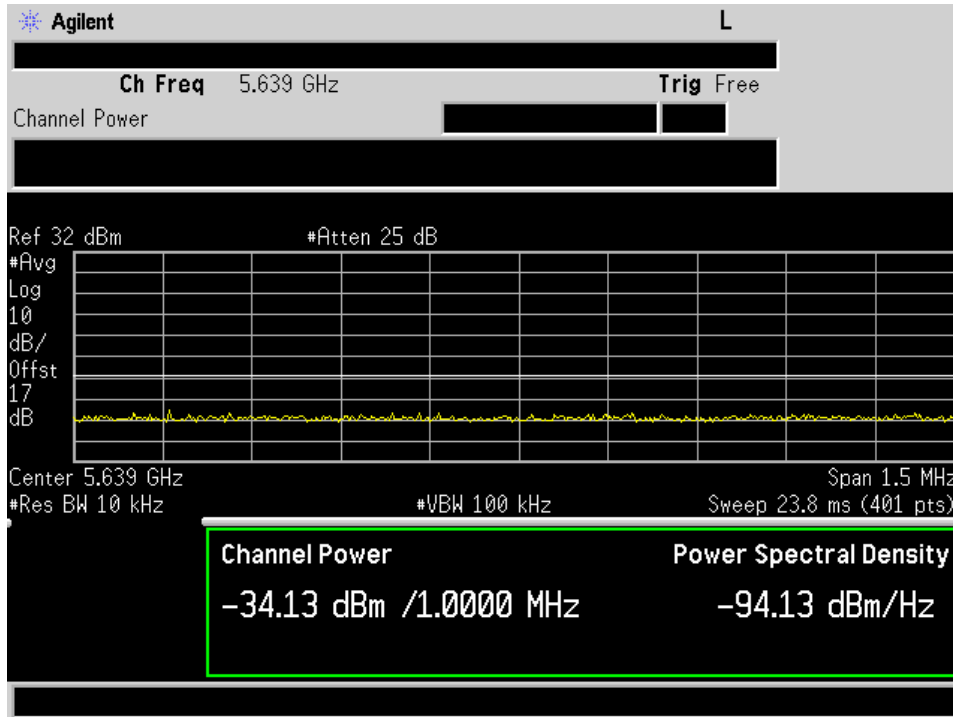


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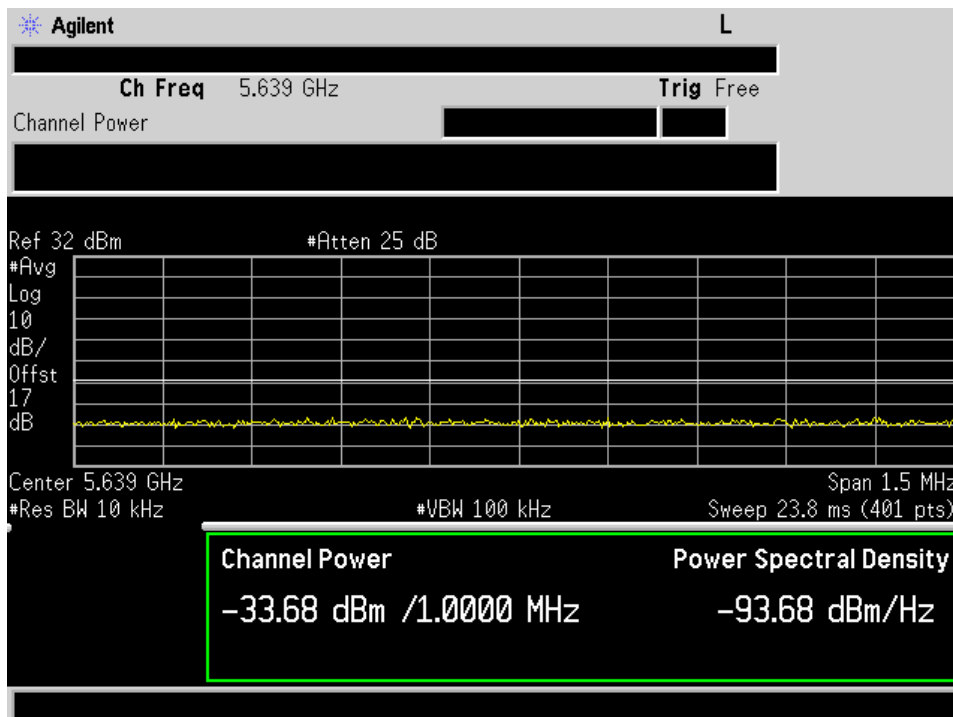




40 Mhz Power integration Low ch Port J4



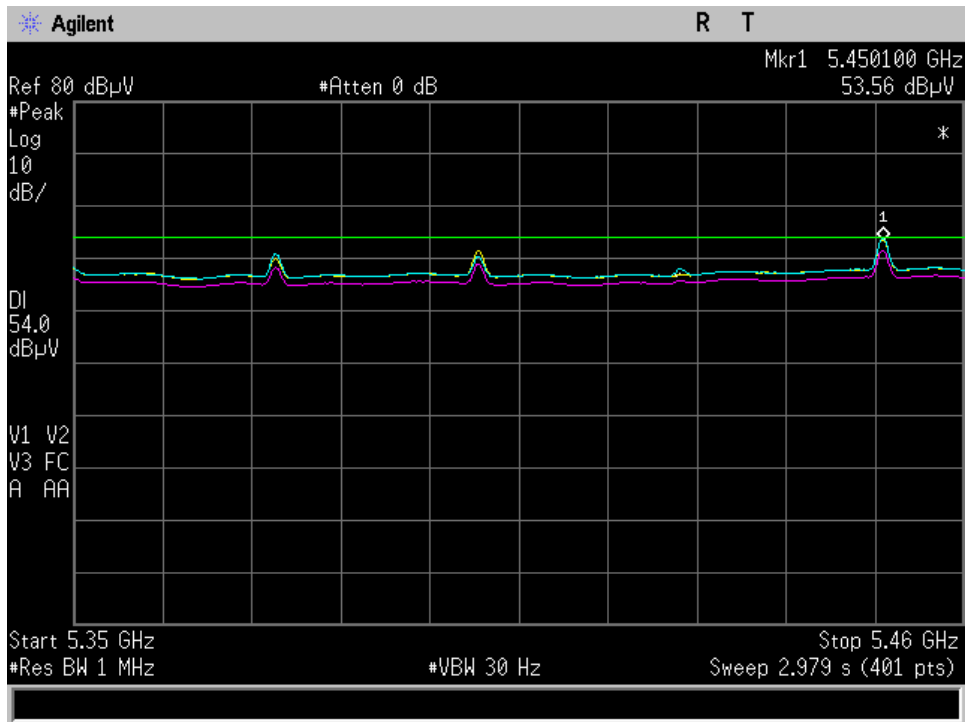
40 Mhz Power integration Low ch Port J7



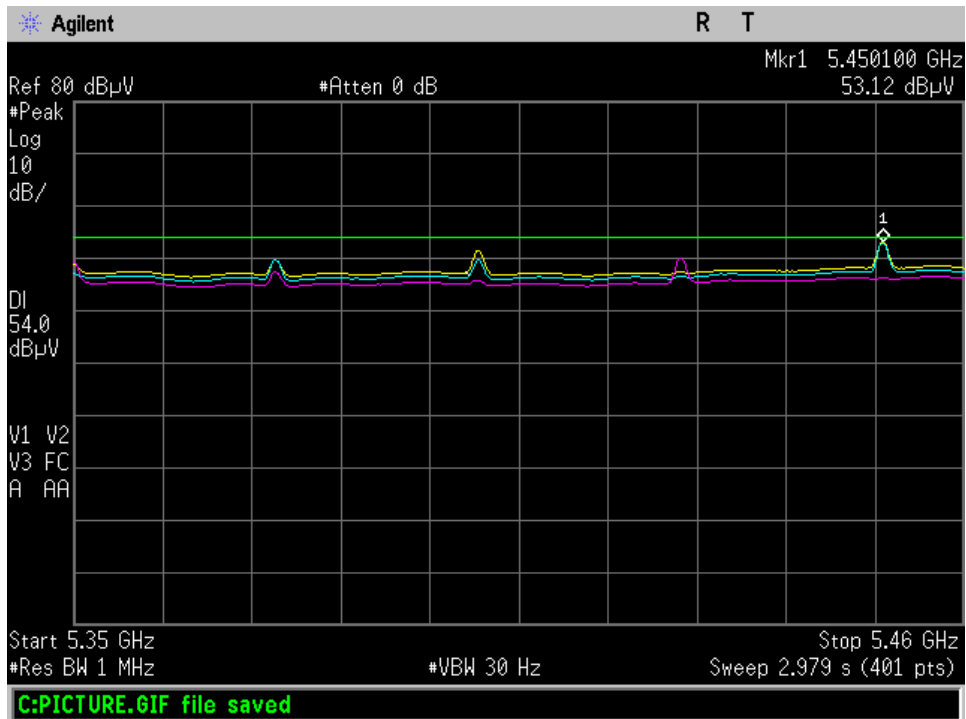


Radiated Restricted Band Measurements

10 Mhz Low Channel

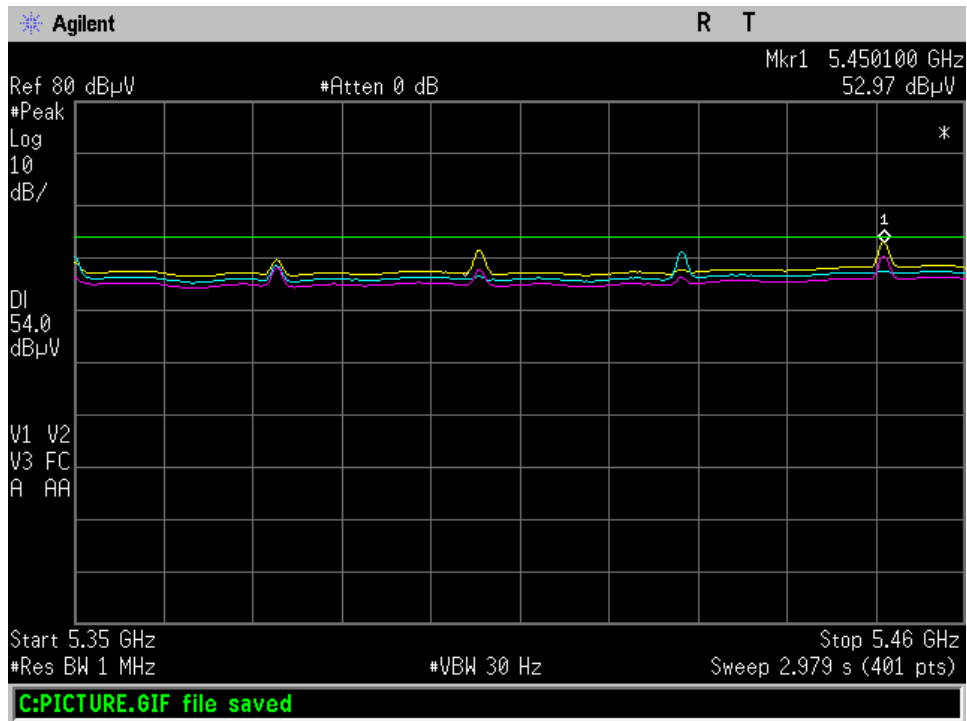


20 Mhz Low Channel





30 Mhz Low Channel



40 Mhz Low Channel

