



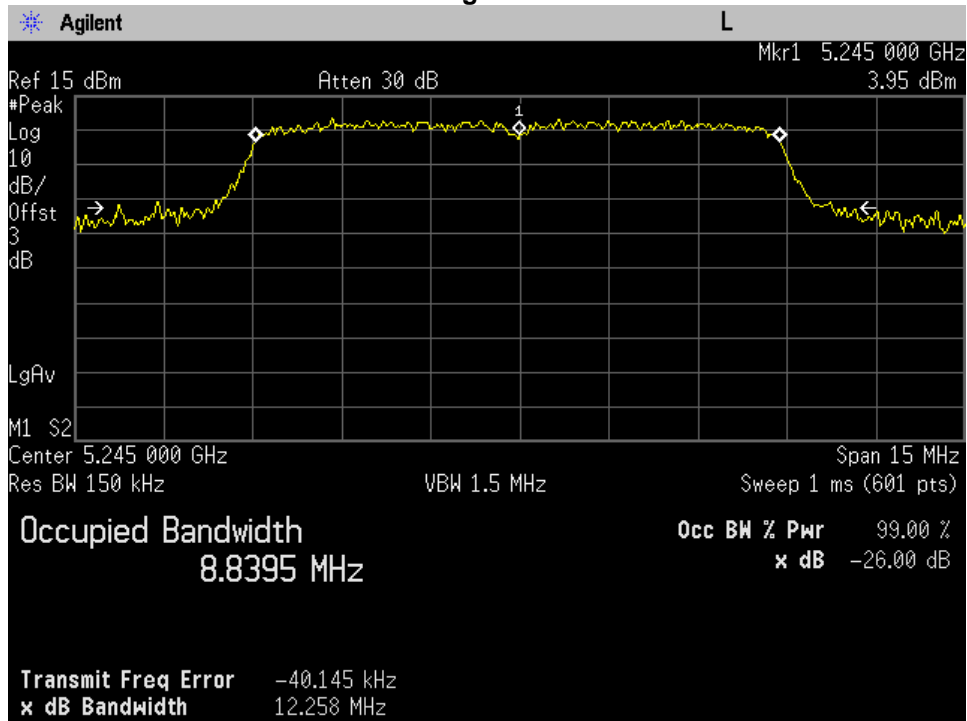
# **Annex C**

## **Occupied Bandwidth**

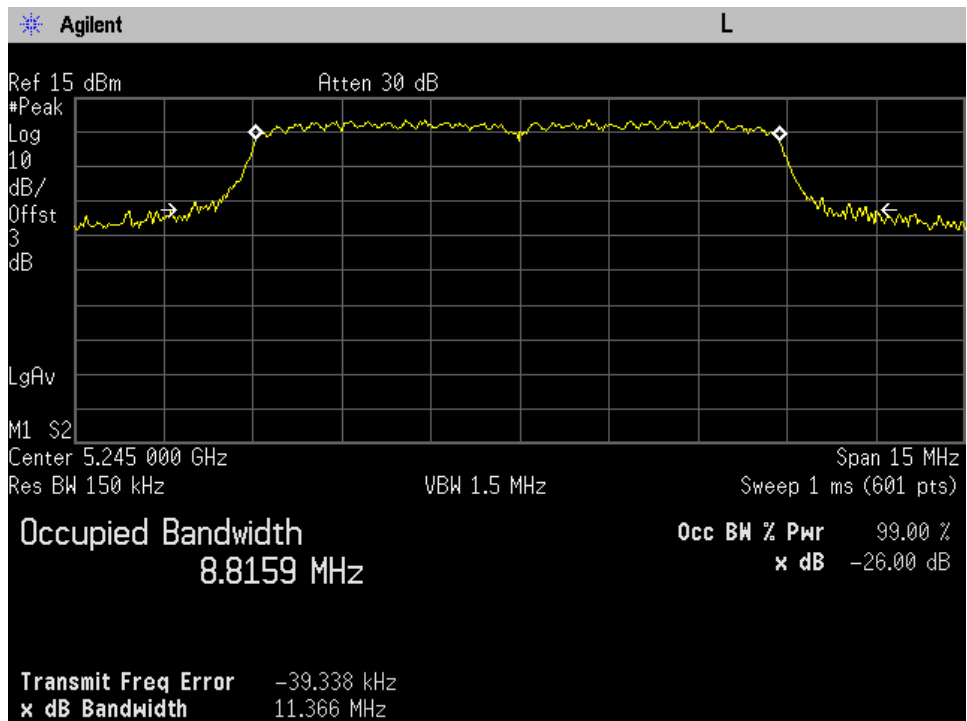
### **UNII-1**



### 10 Mhz High Ch Port J4

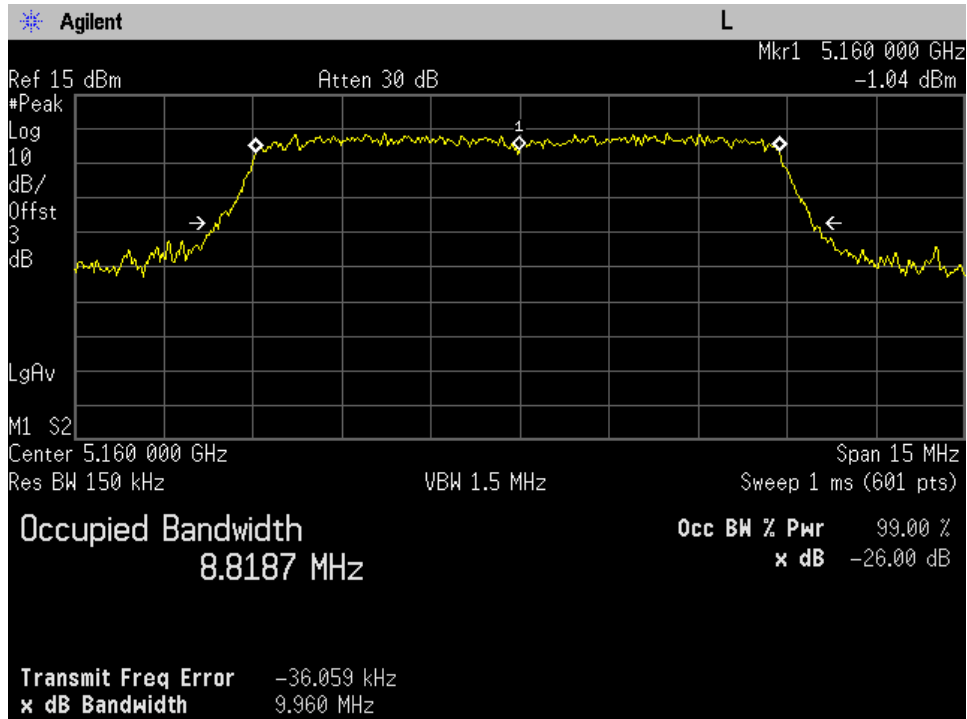


### 10 Mhz High Ch Port J7

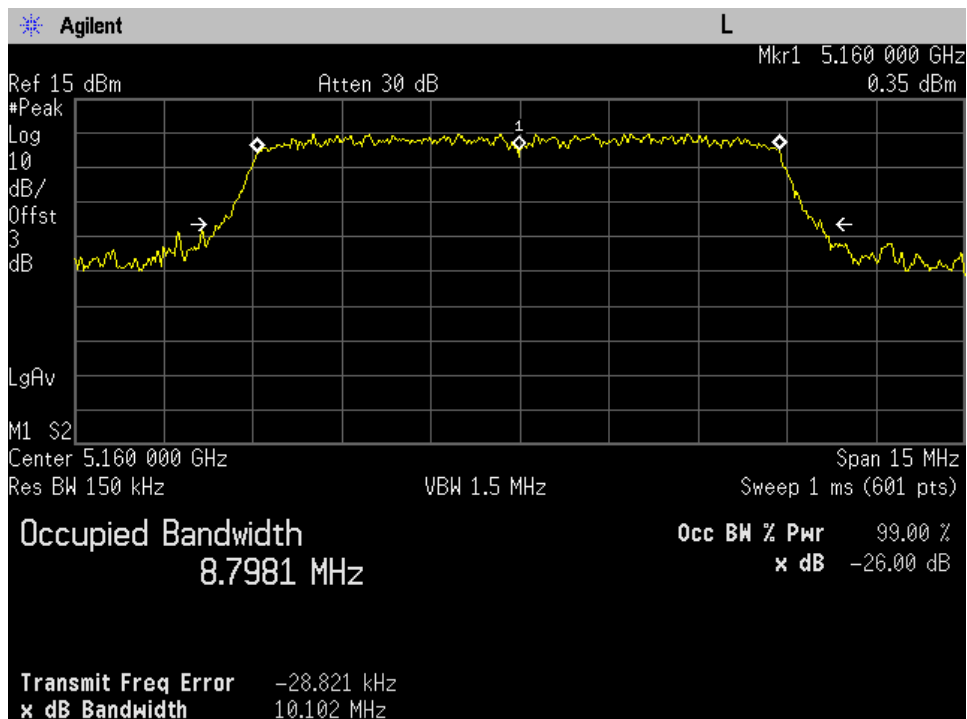




### 10 Mhz Low Ch Port J4

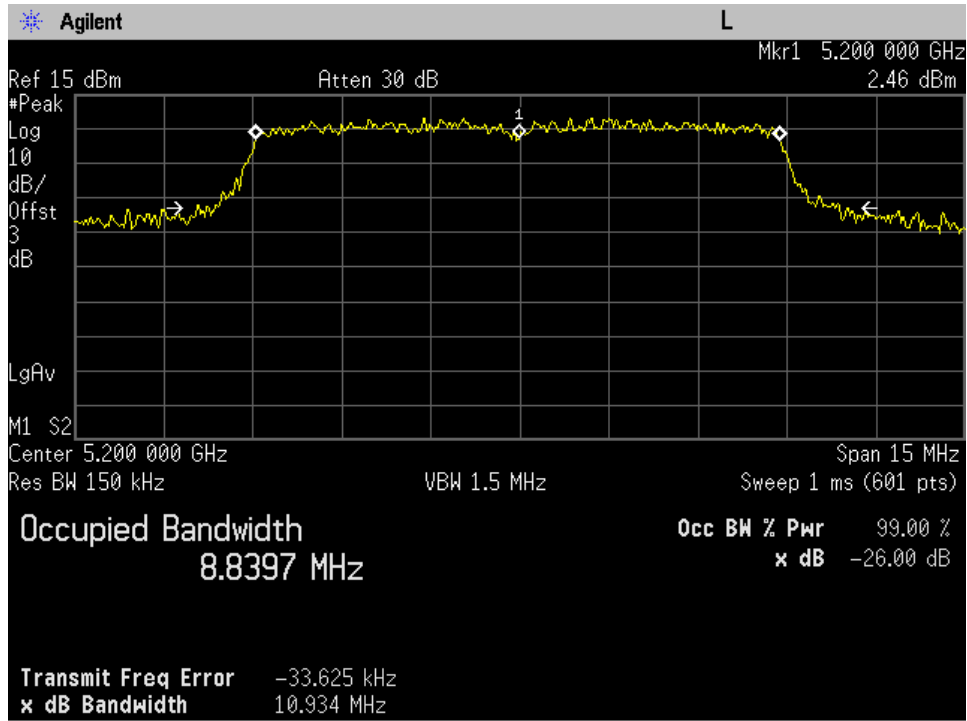


### 10 Mhz Low Ch Port J7

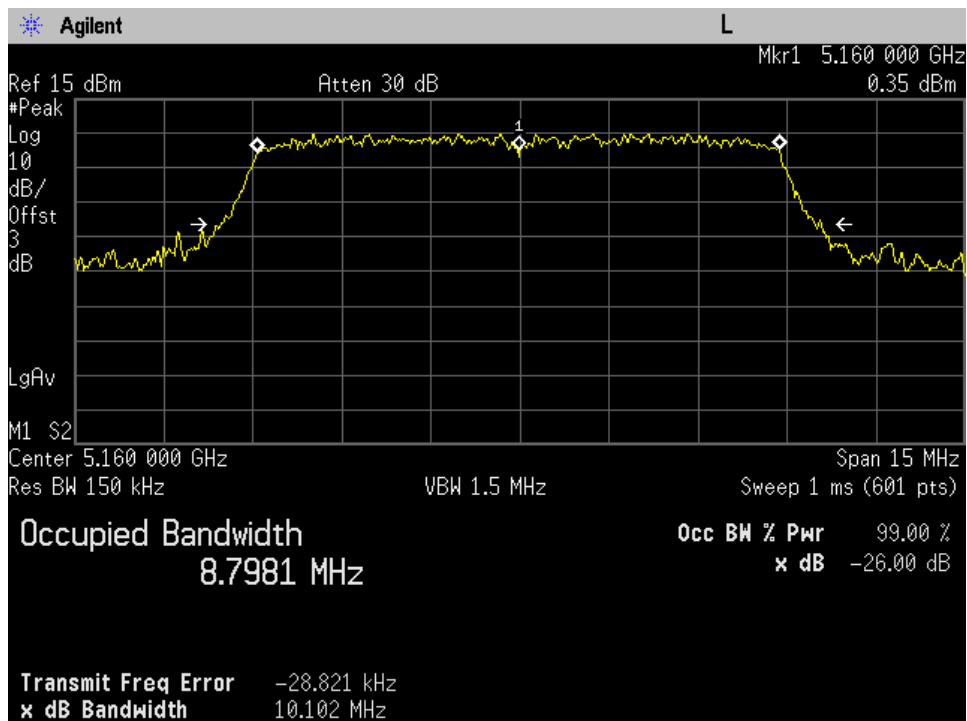




### 10 Mhz Mid Ch Port J4

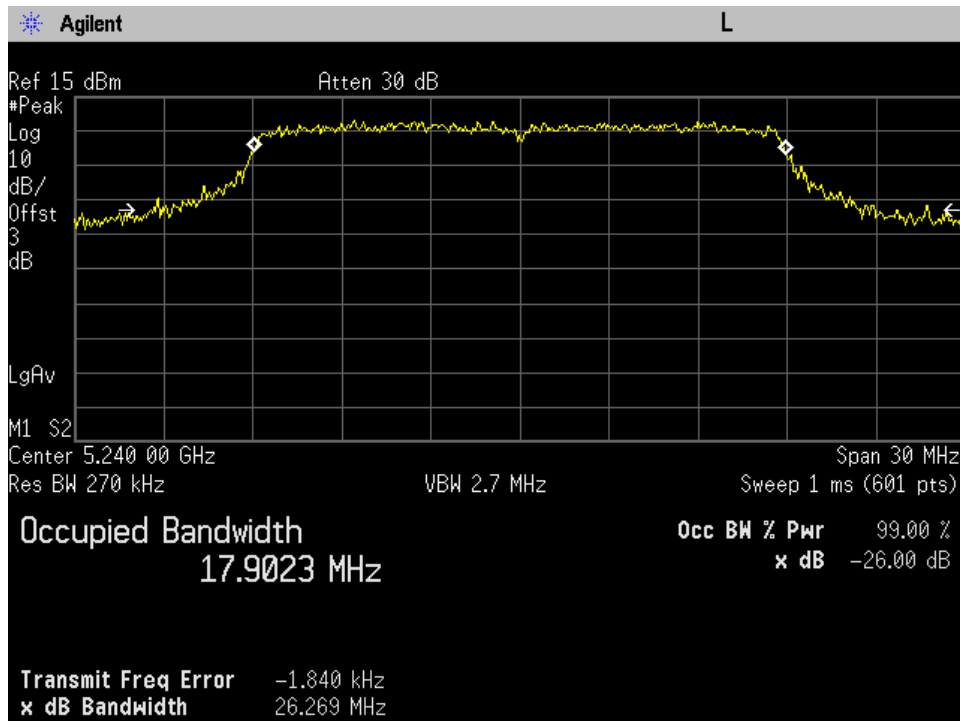


### 10 Mhz Mid Ch Port J7

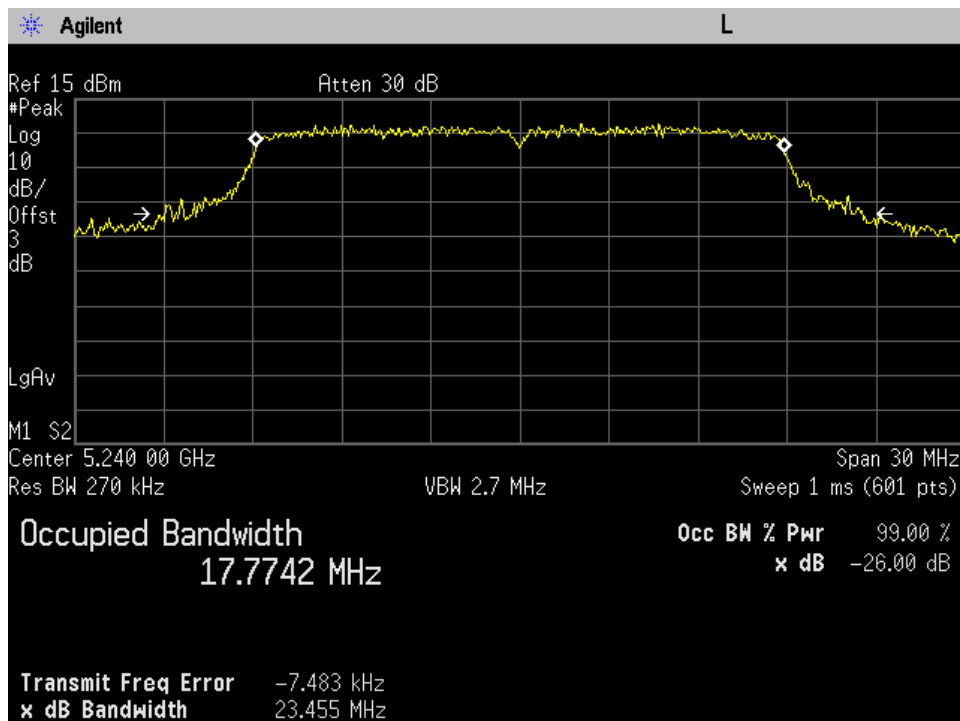




### 20 Mhz High Ch Port J4

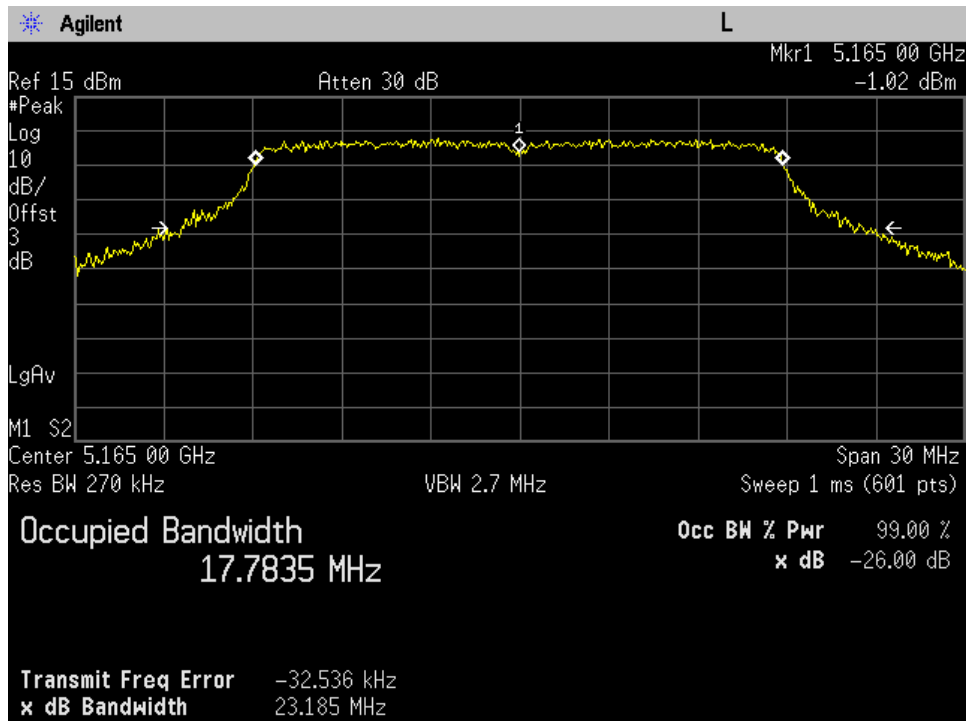


### 20 Mhz High Ch Port J7

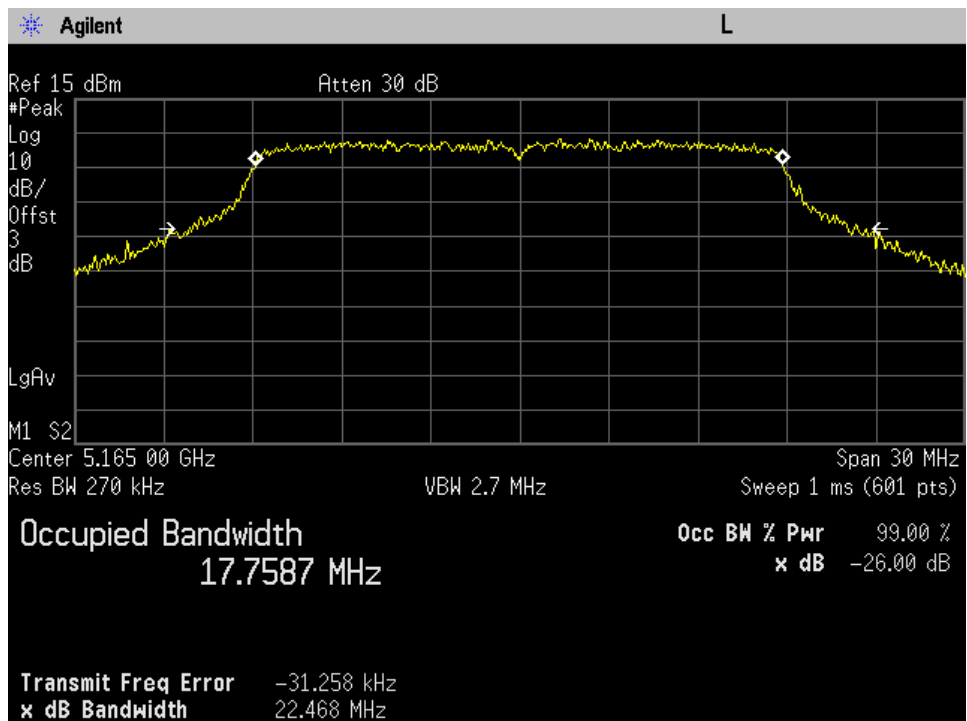




### 20 Mhz Low Ch Port J4

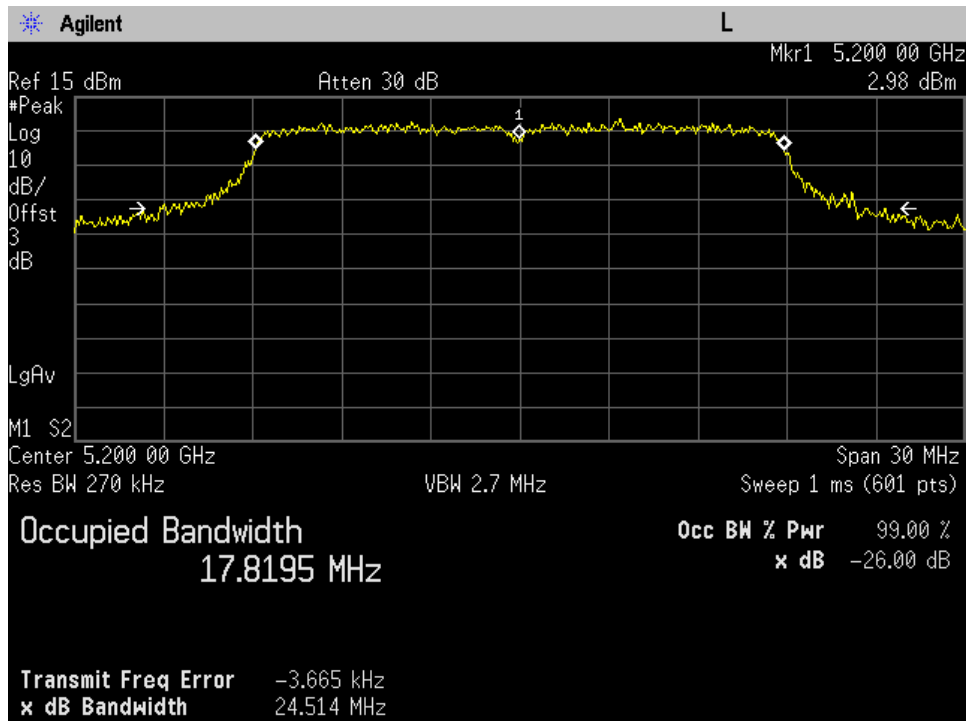


### 20 Mhz Low Ch Port J7

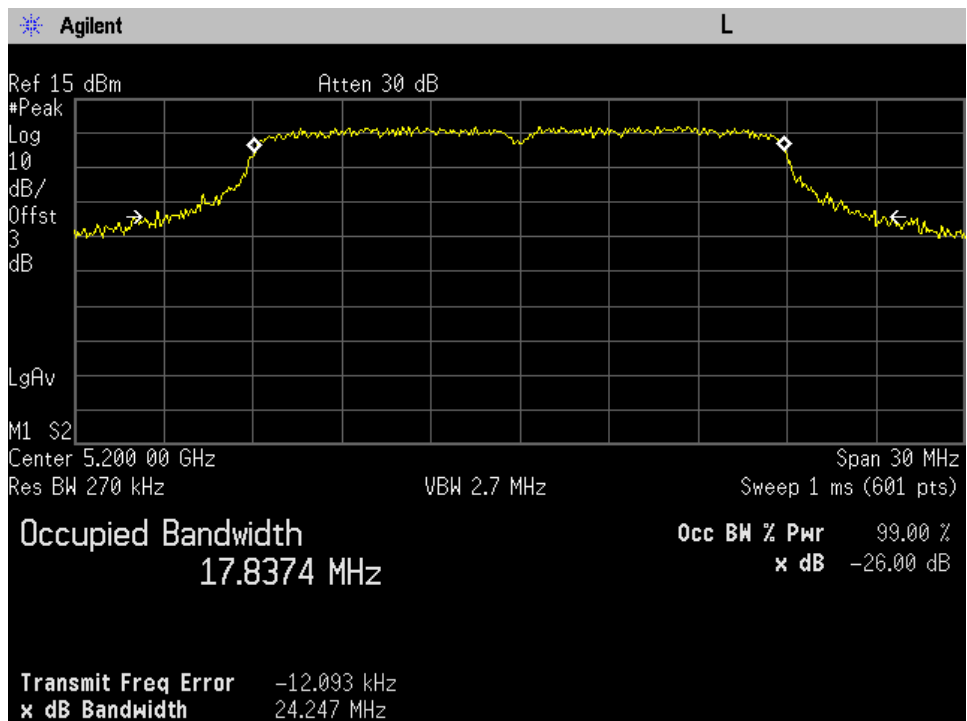




### 20 Mhz Mid Ch Port J4

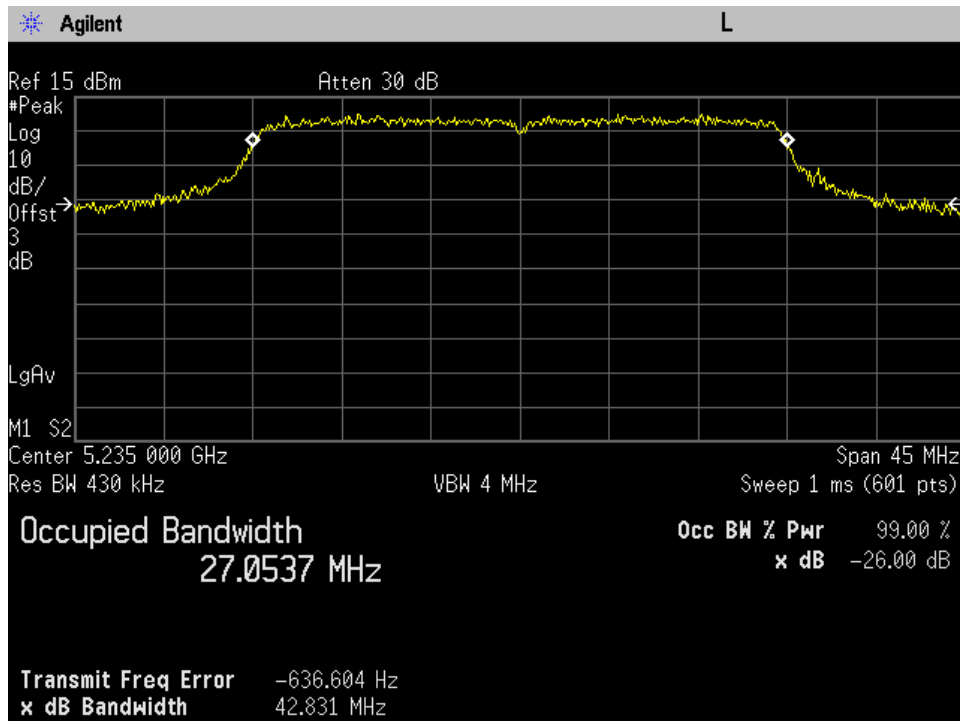


### 20 Mhz Mid Ch Port J7

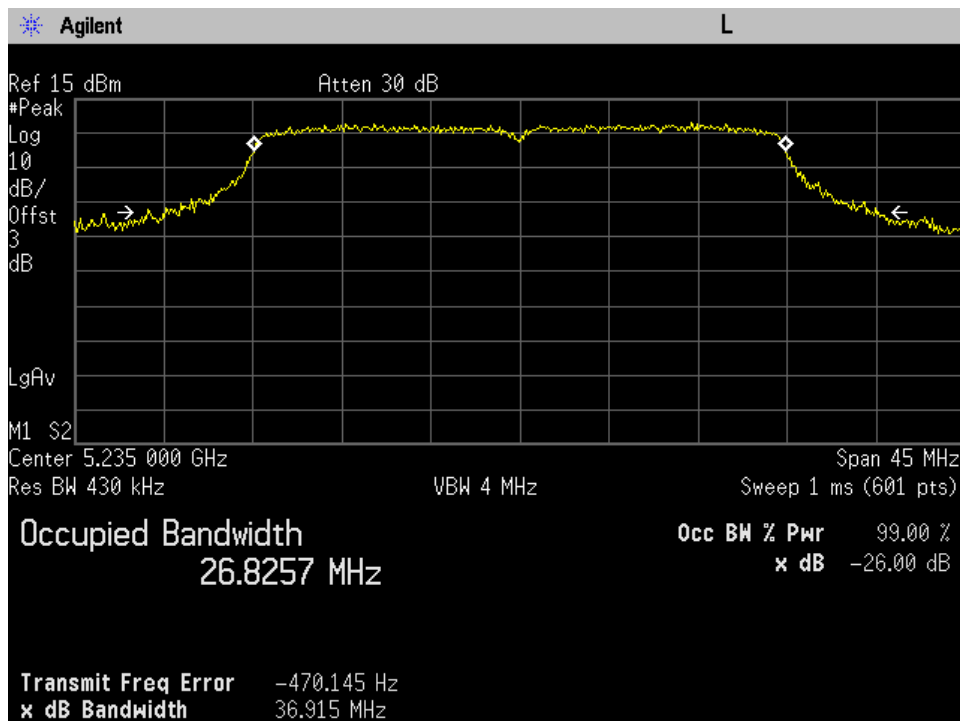




### 30 Mhz High Ch Port J4



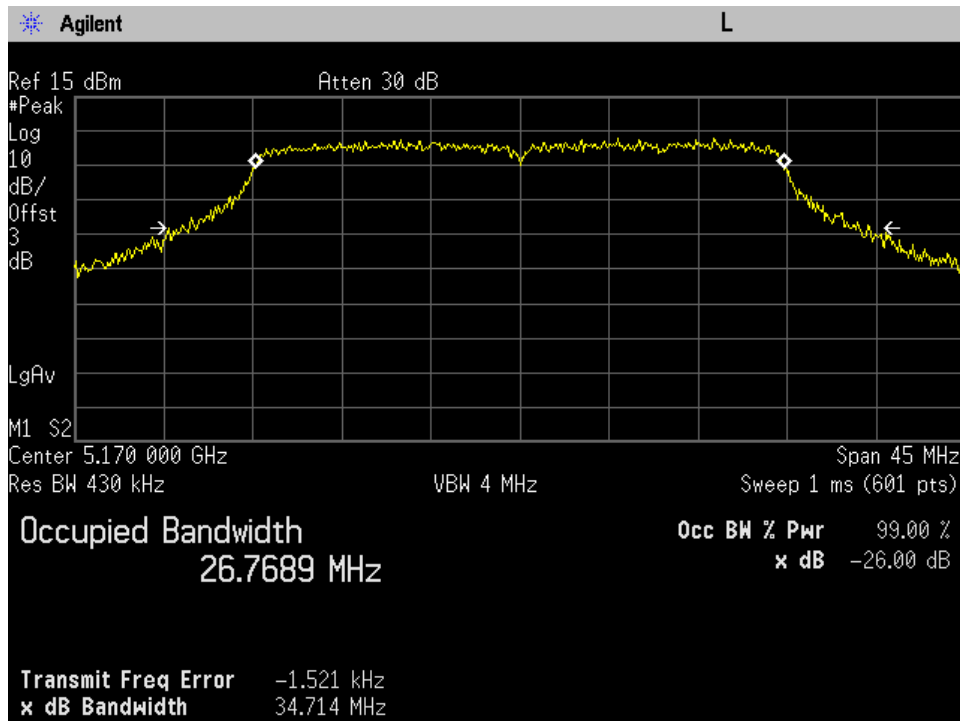
### 30 Mhz High Ch Port J7



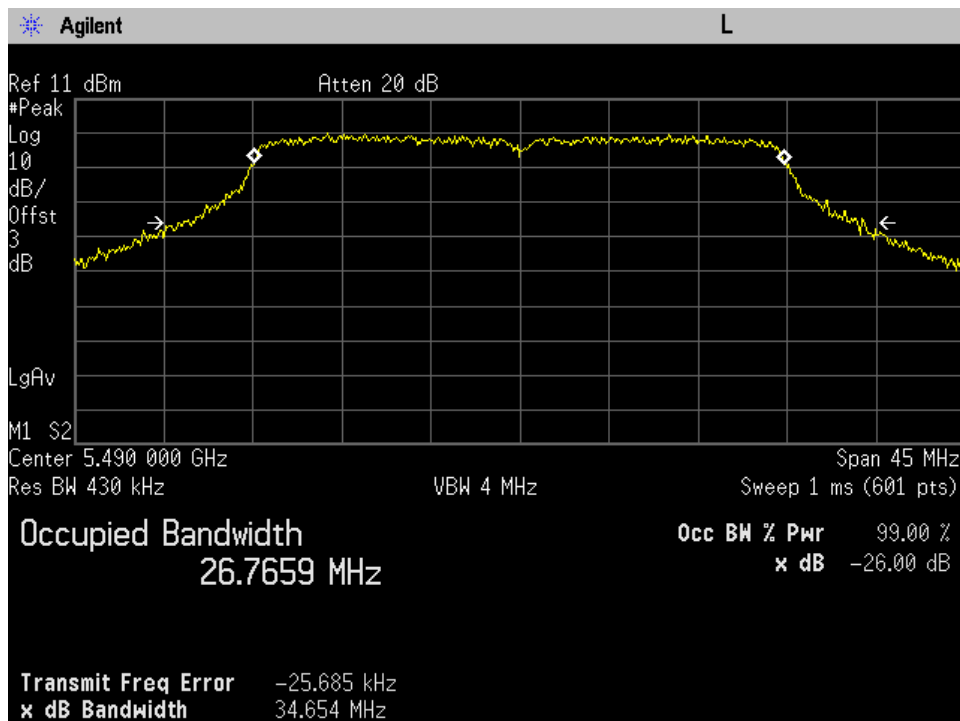




### 30 Mhz Low Ch Port J4

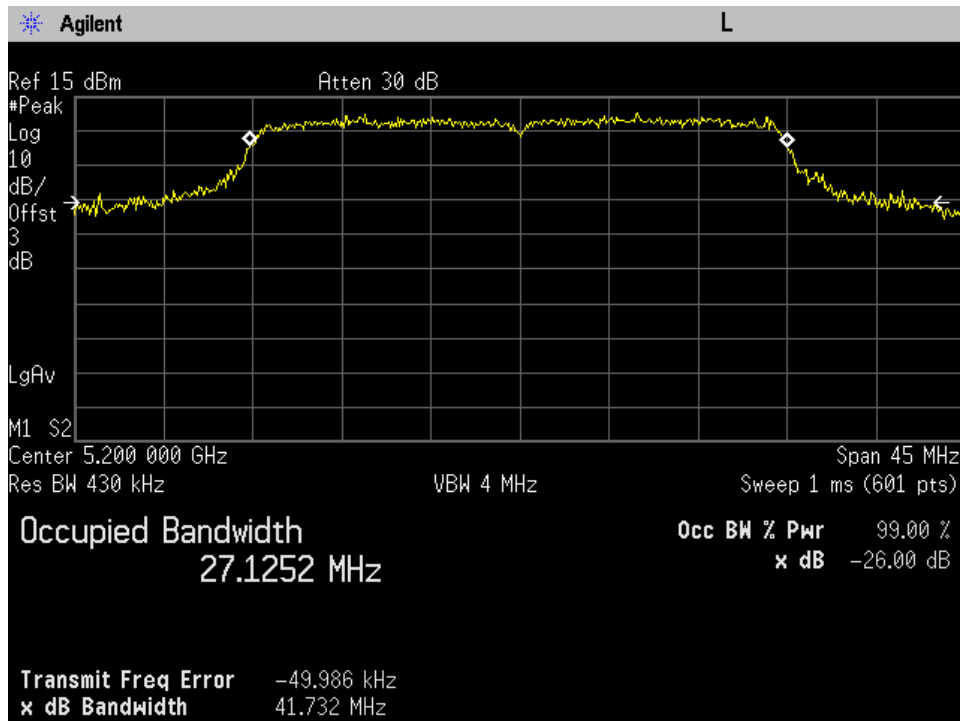


### 30 Mhz Low Ch Port J7

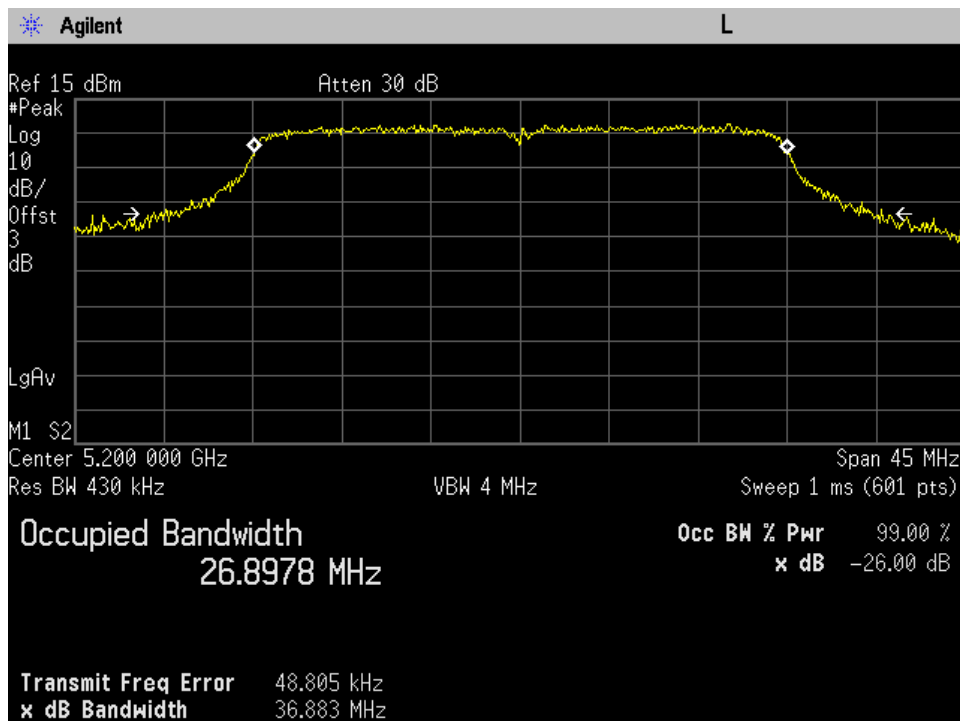




### 30 Mhz Mid Ch Port J4

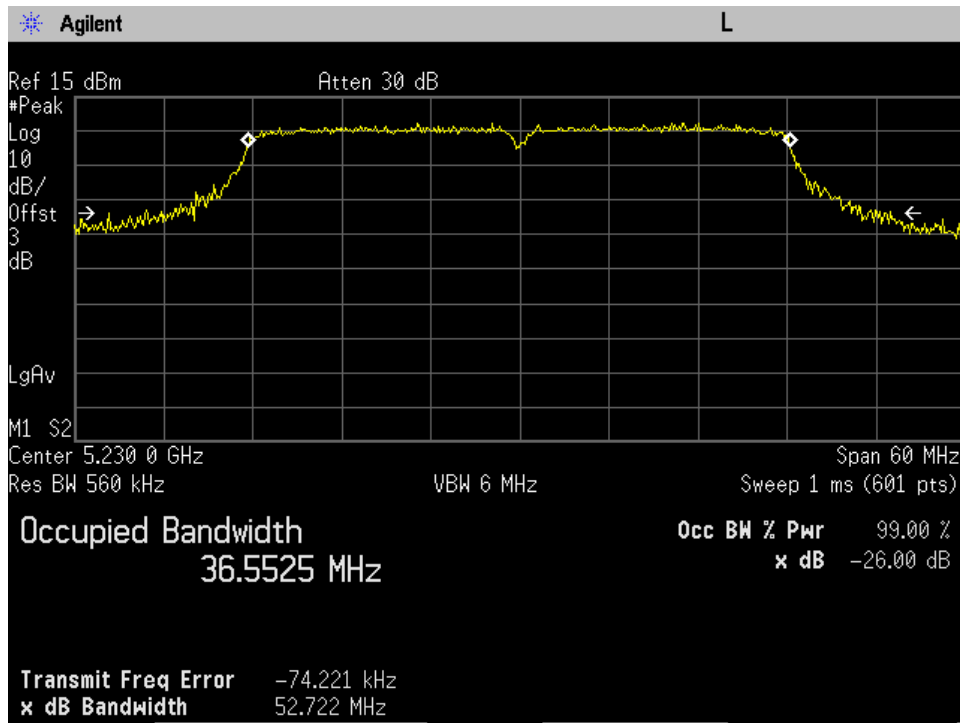


### 30 Mhz Mid Ch Port J7

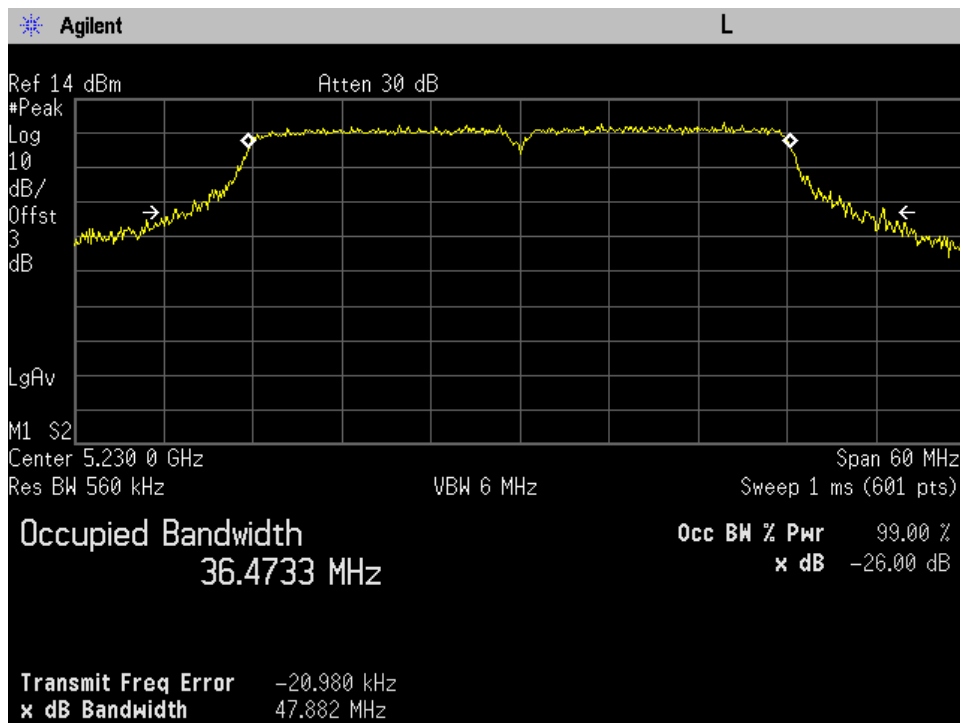




### 40 Mhz High Ch Port J4

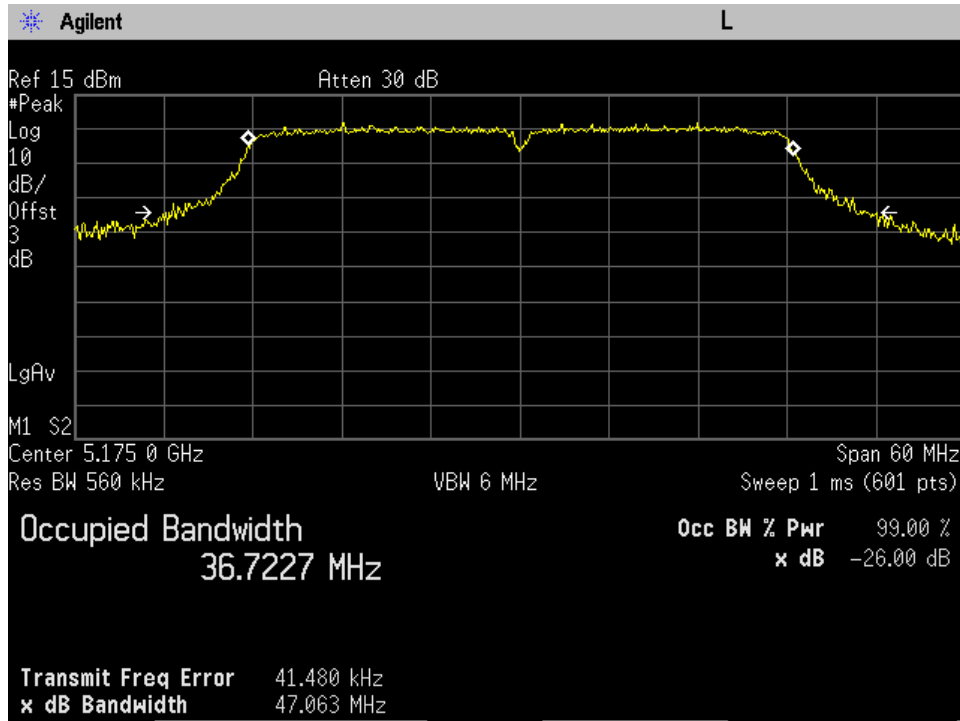


### 40 Mhz High Ch Port J7

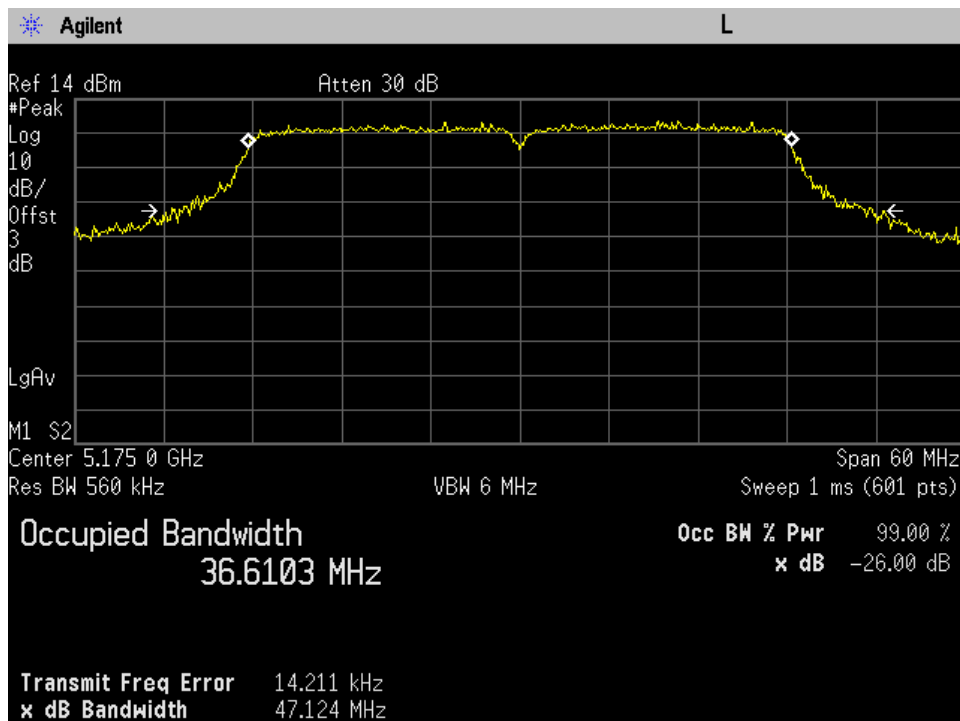




### 40 Mhz Low Ch Port J4

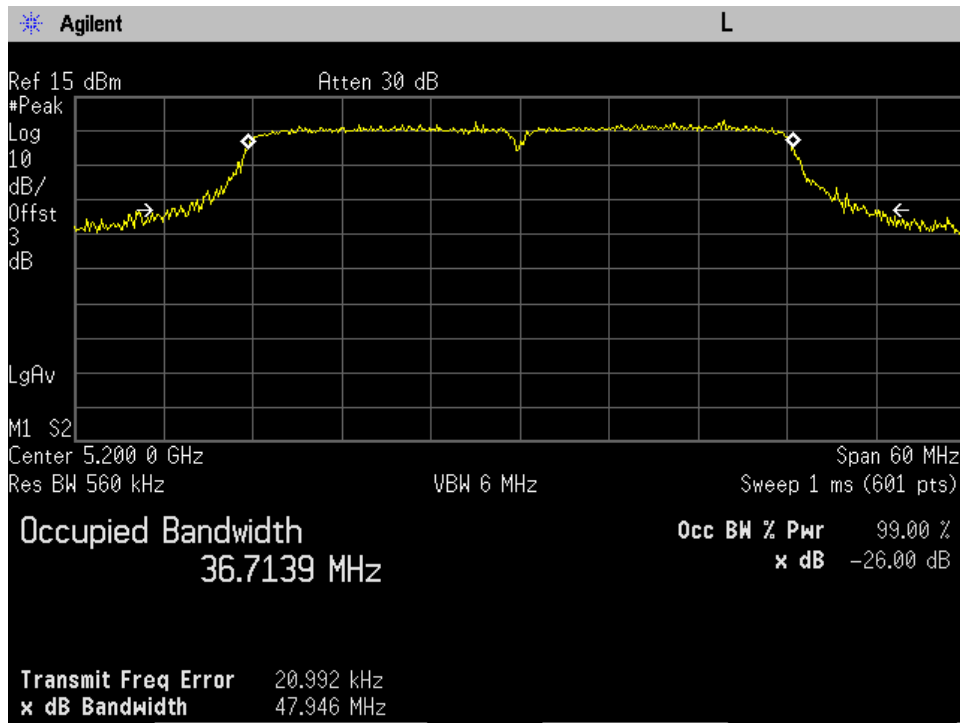


### 40 Mhz Low Ch Port J7





### 40 Mhz Mid Ch Port J4



### 40 Mhz Mid Ch Port J7

