



Annex C

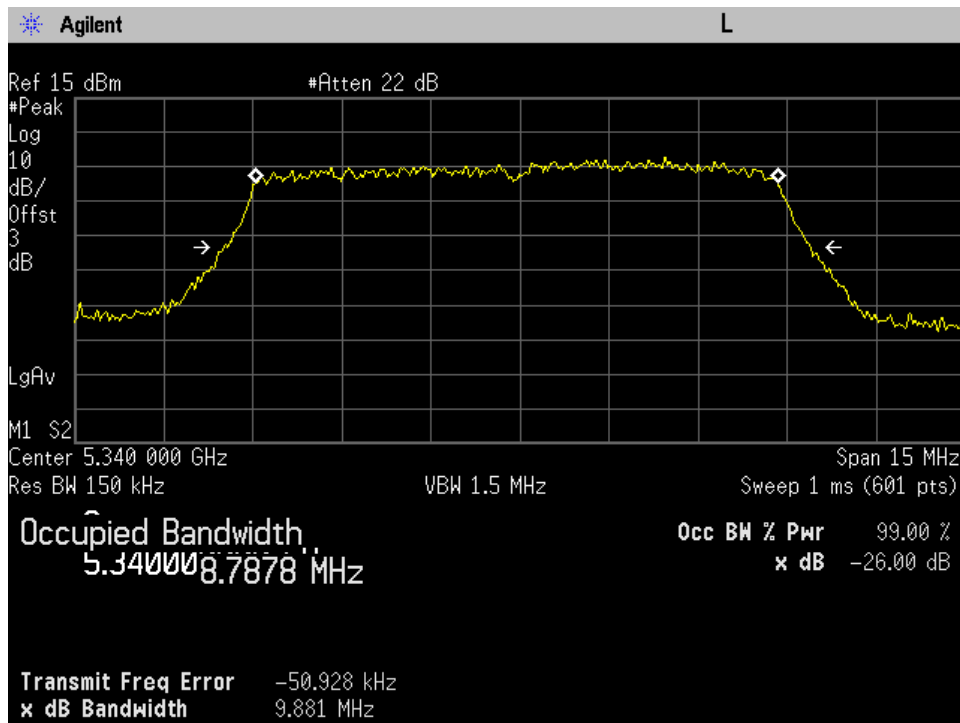
Occupied Bandwidth

UNII-2A and UNII-2C



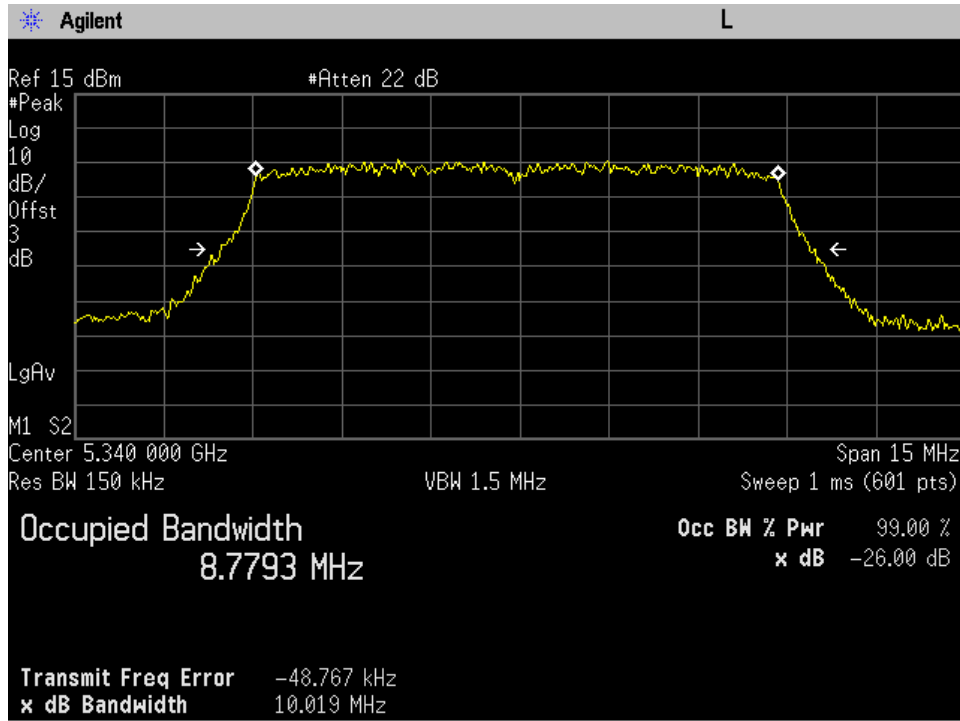
UNII-2A

10 Mhz High Ch Port 1

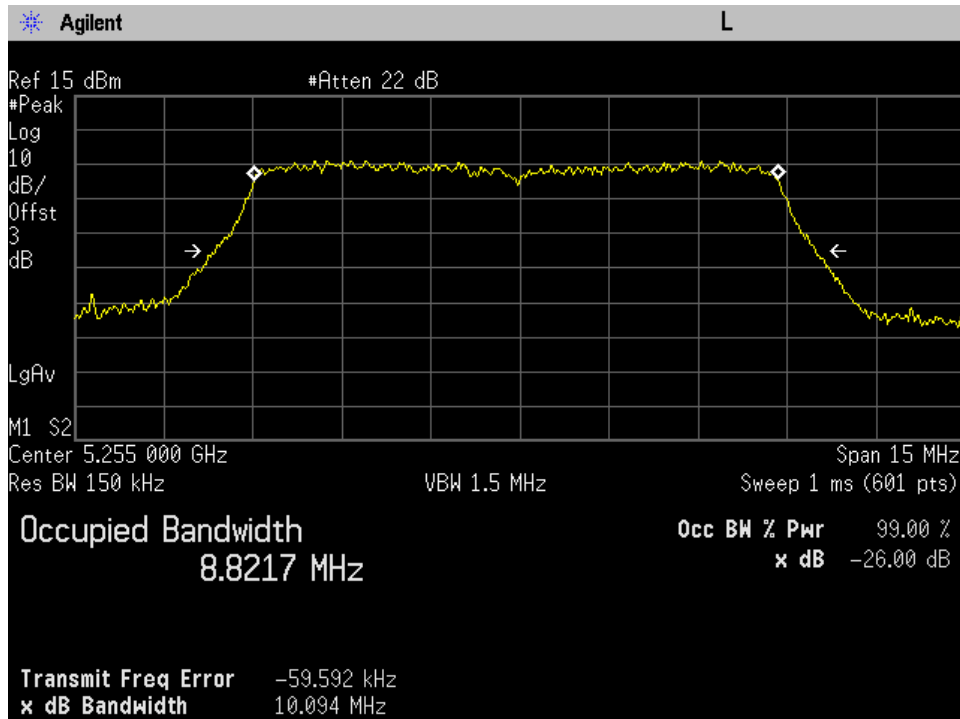




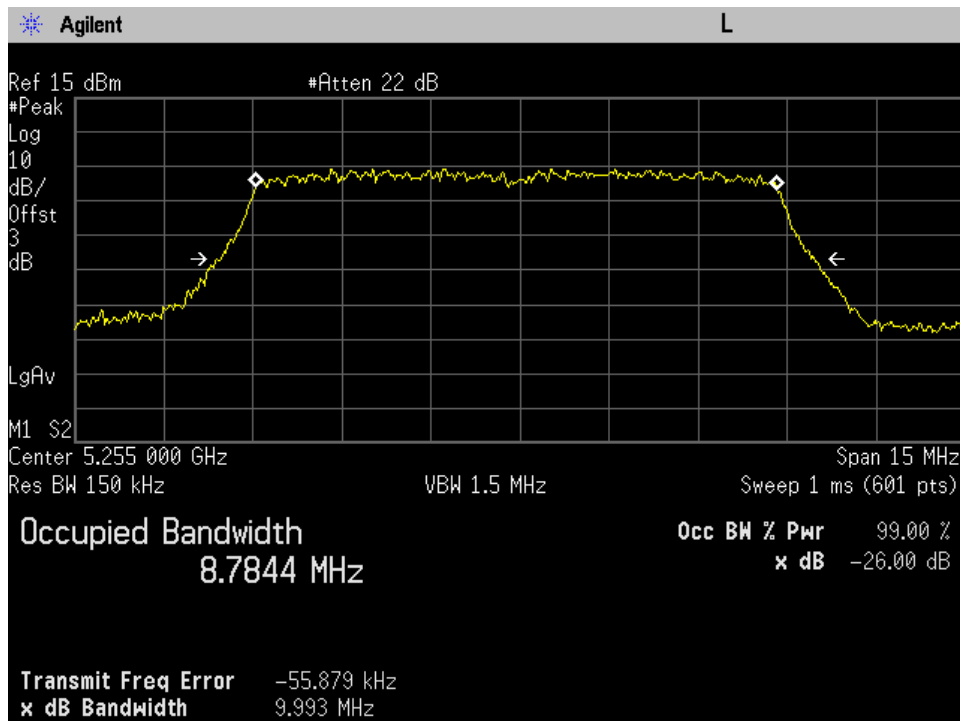
10 Mhz High Ch Port 2



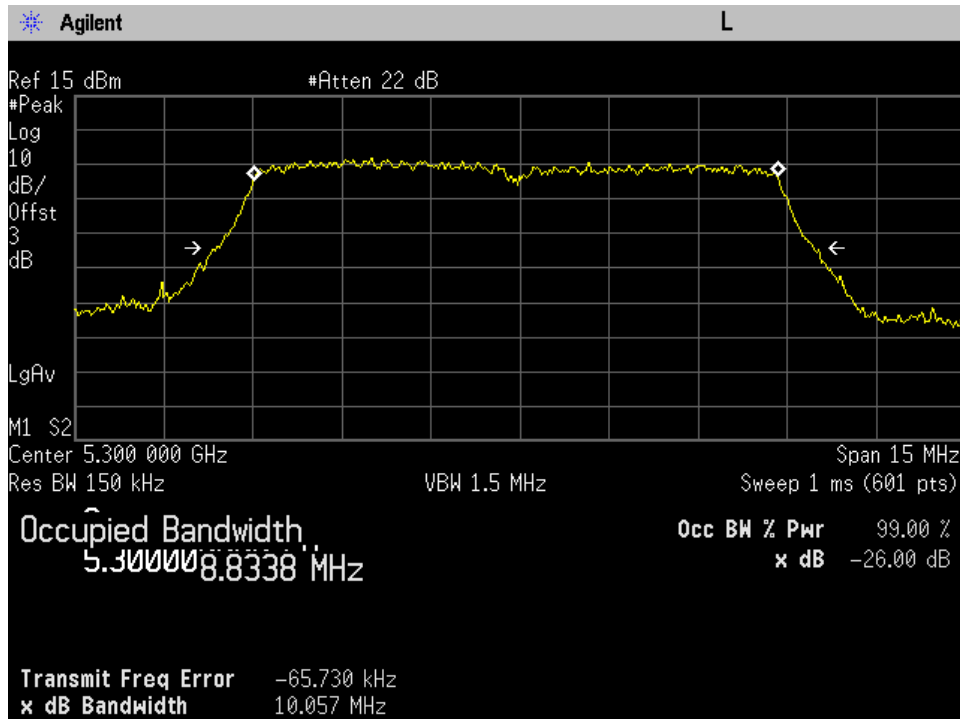
10 Mhz Low Ch Port 1



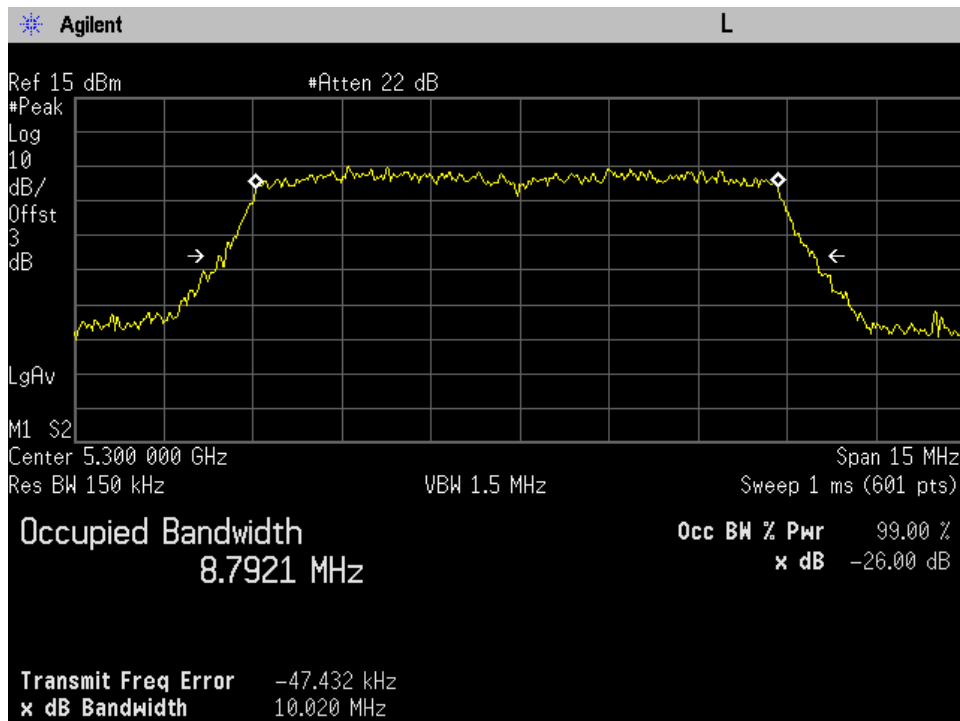
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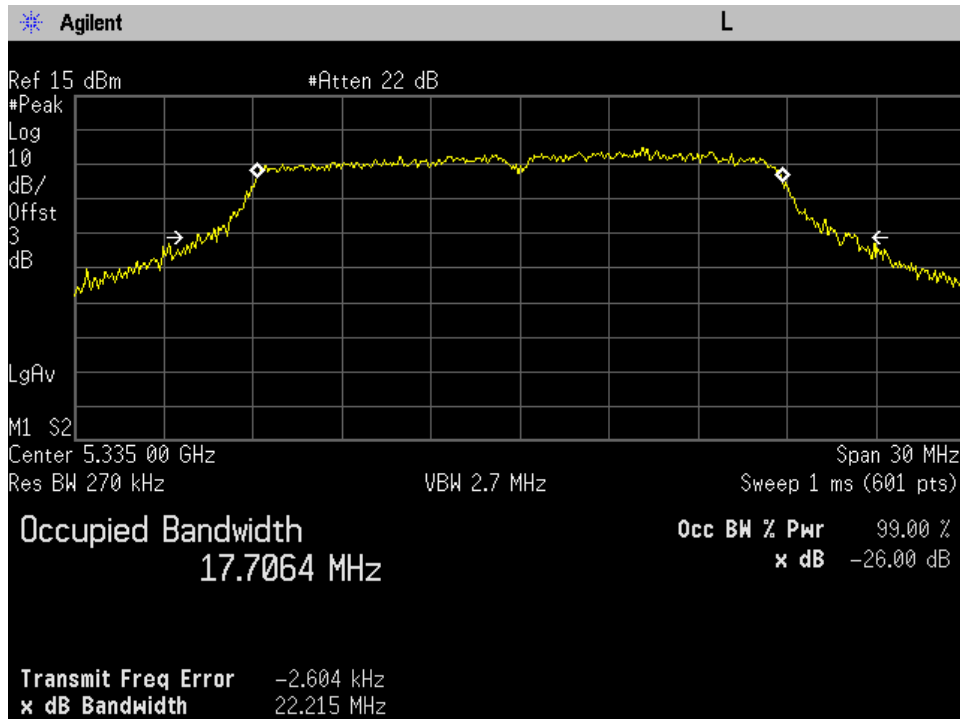
10 Mhz Mid Ch Port 1



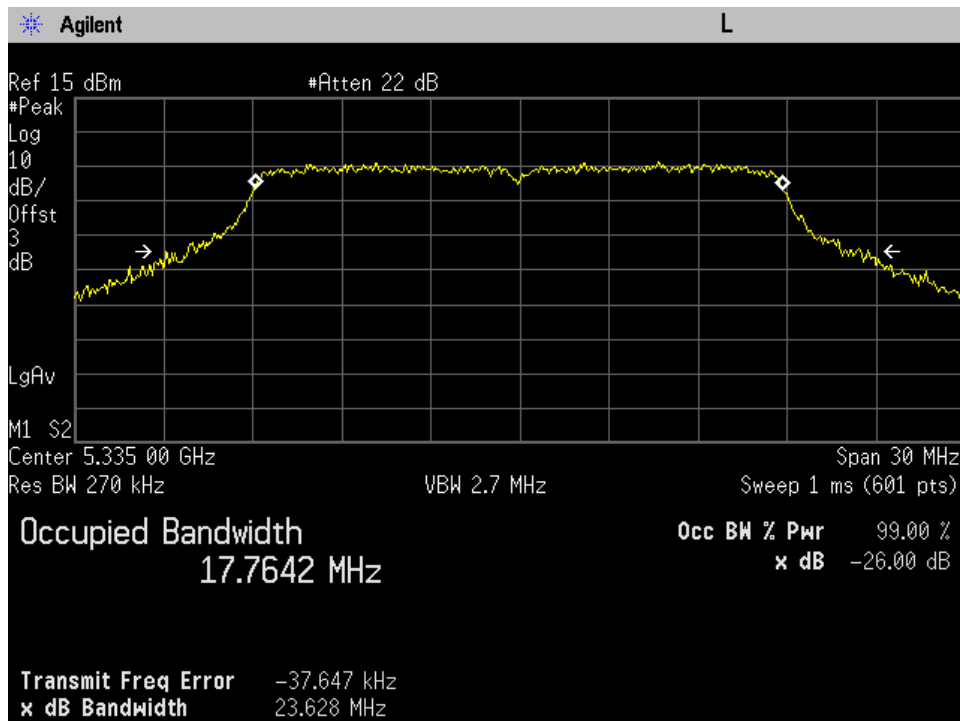
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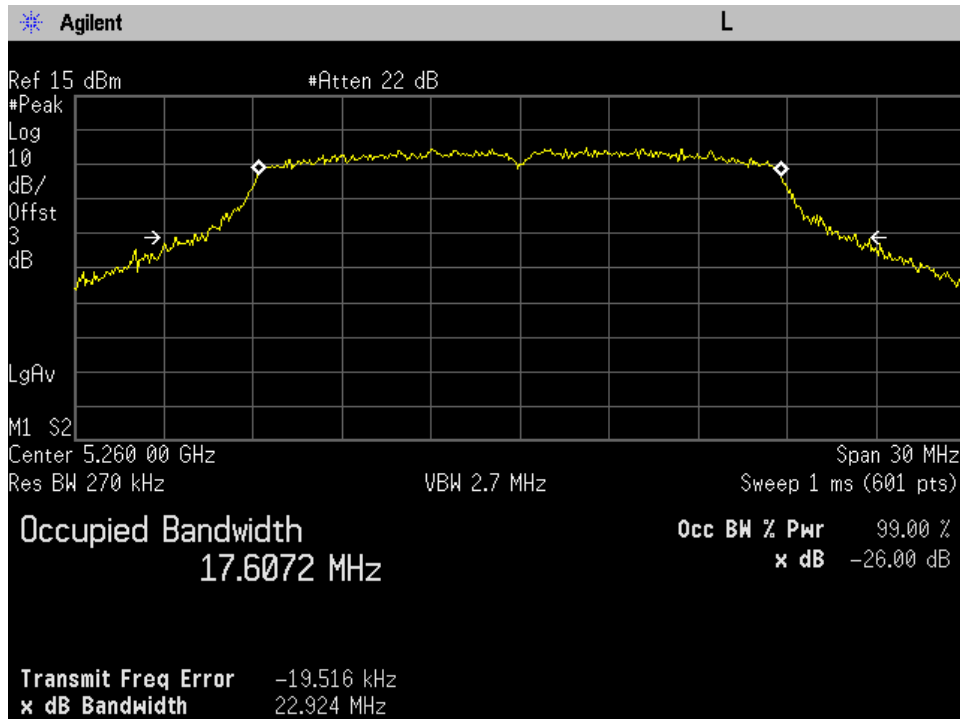
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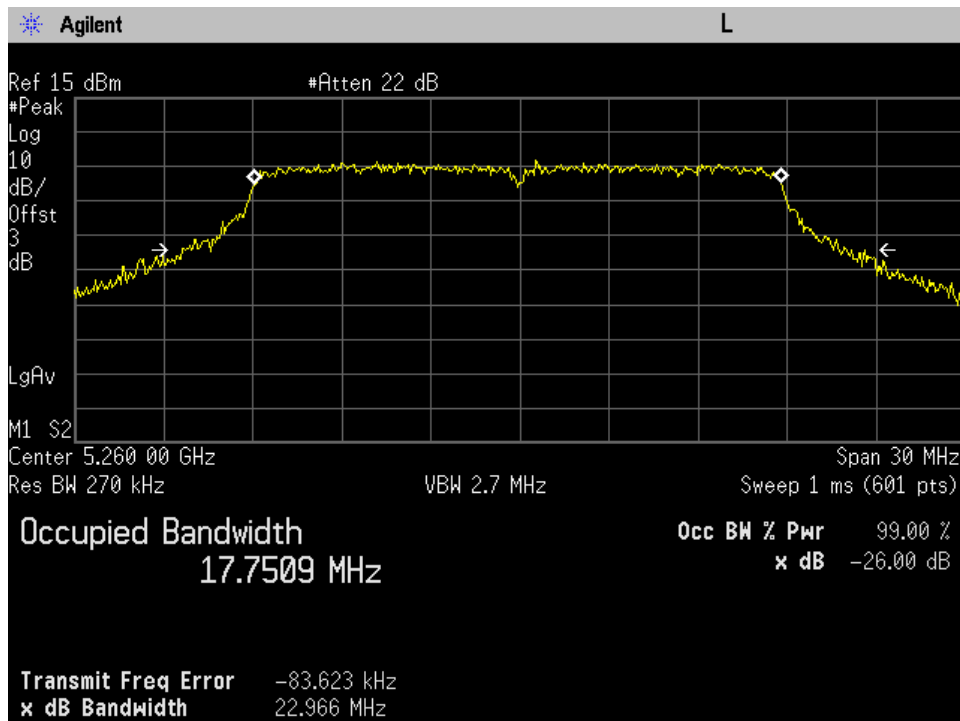
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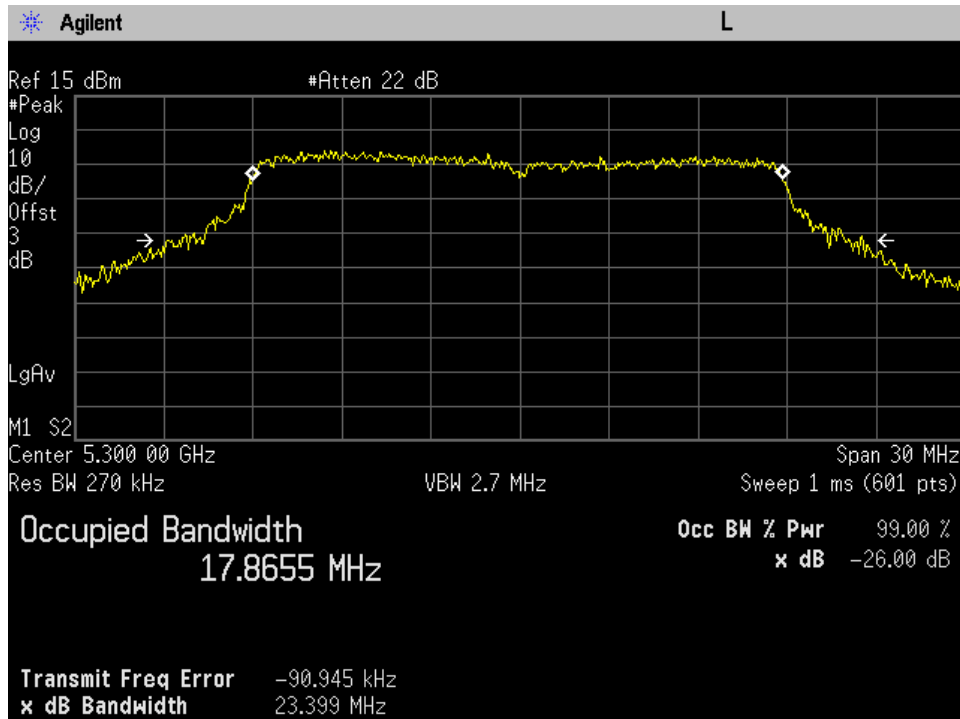
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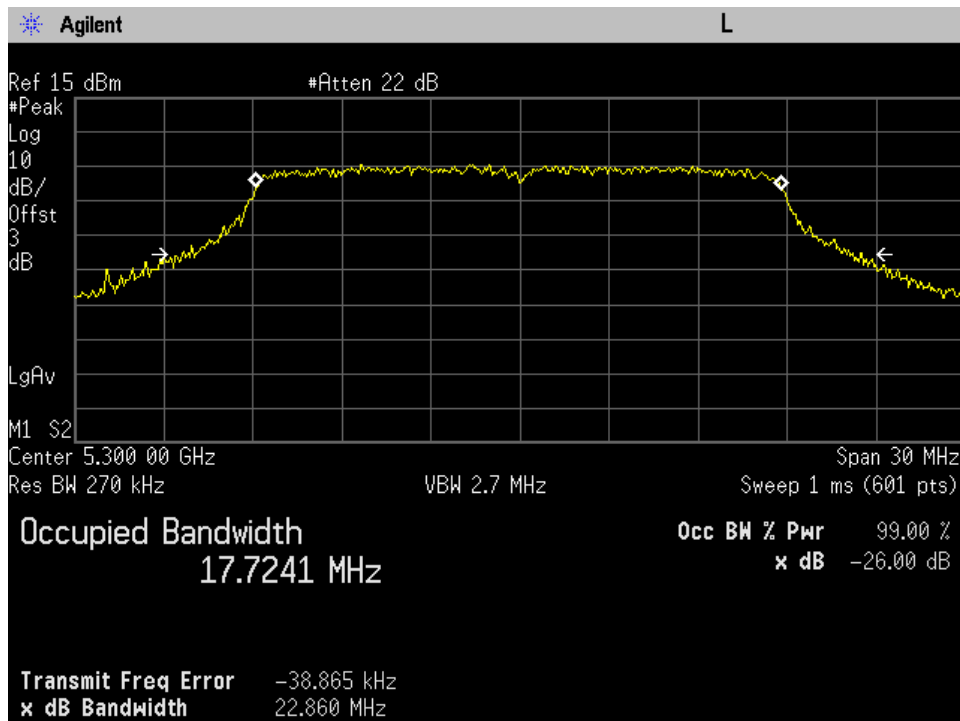
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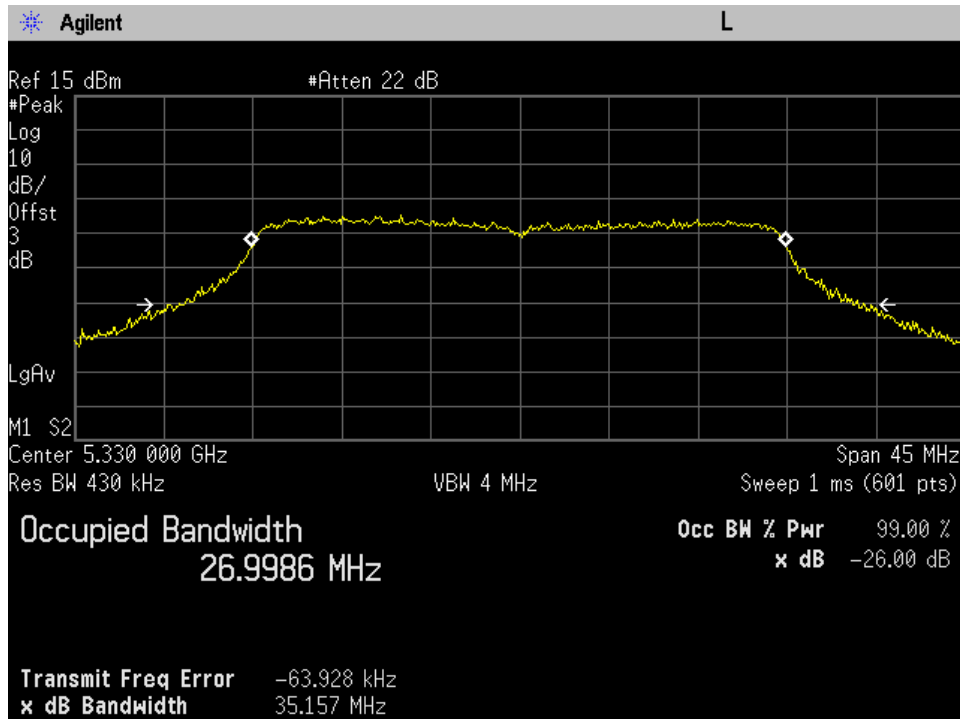
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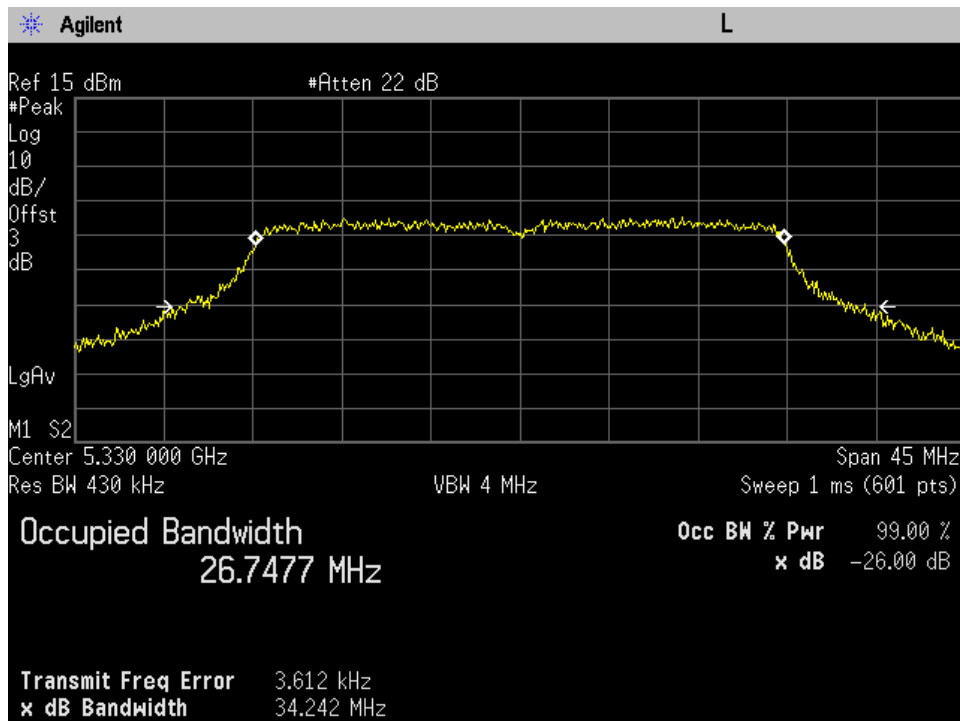
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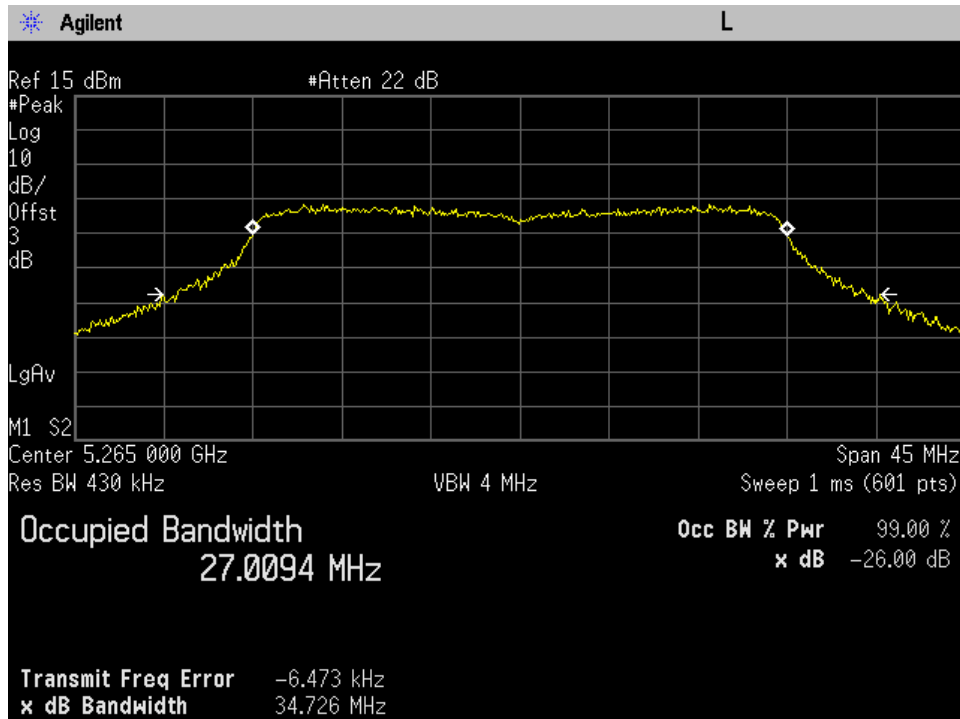
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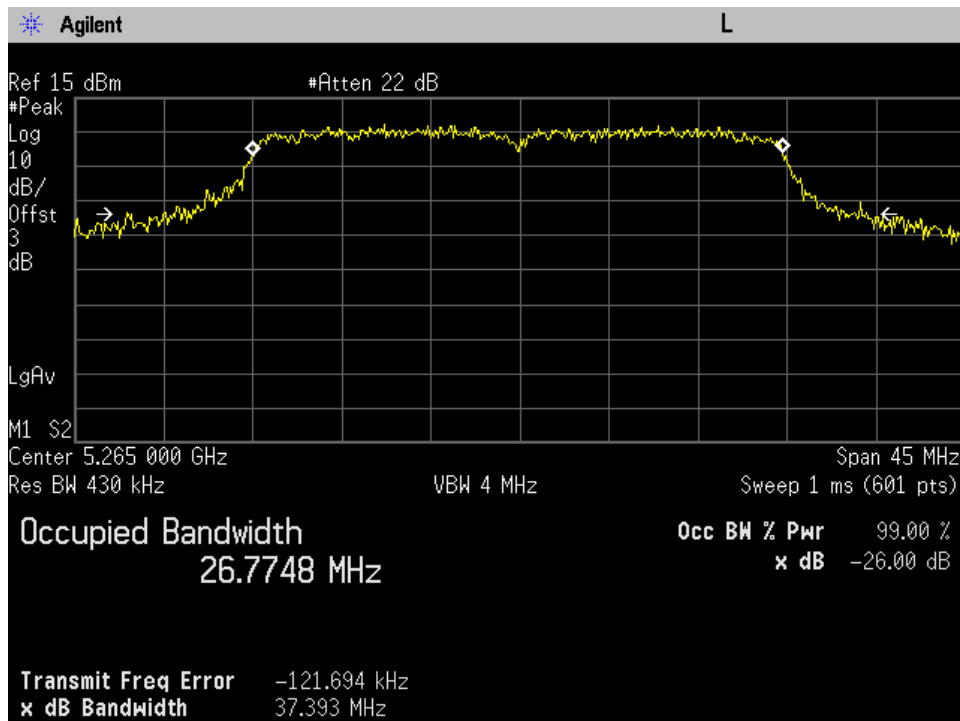
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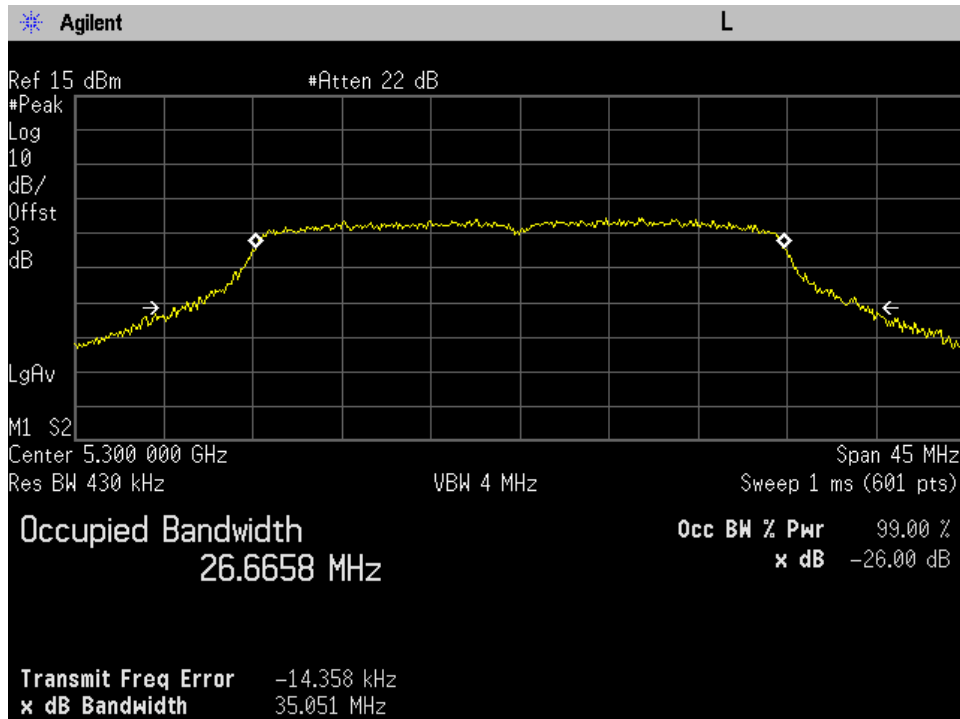
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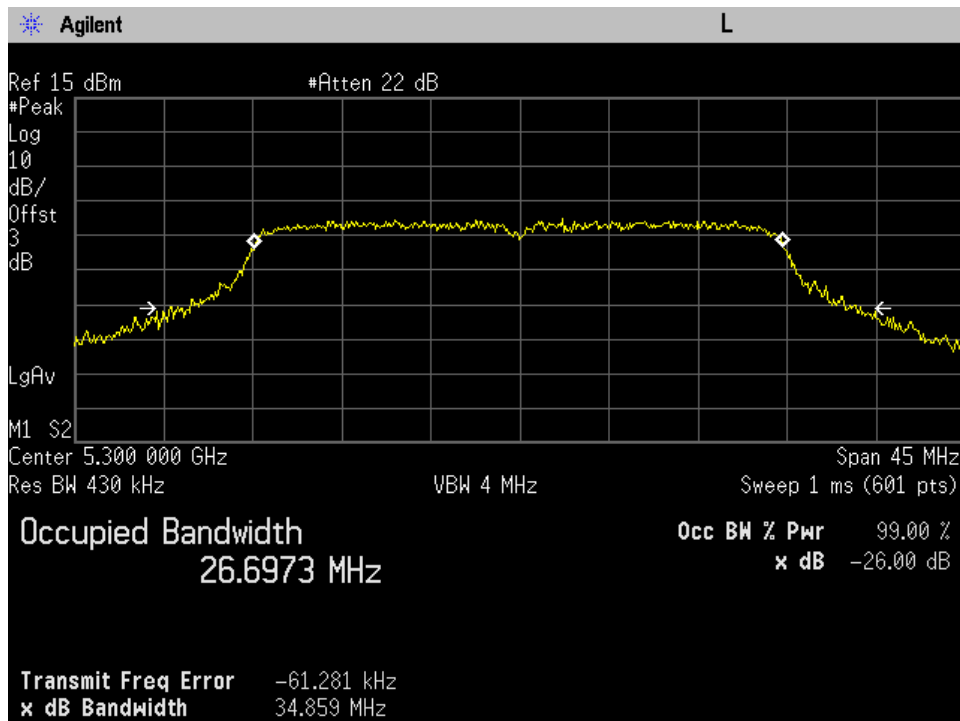
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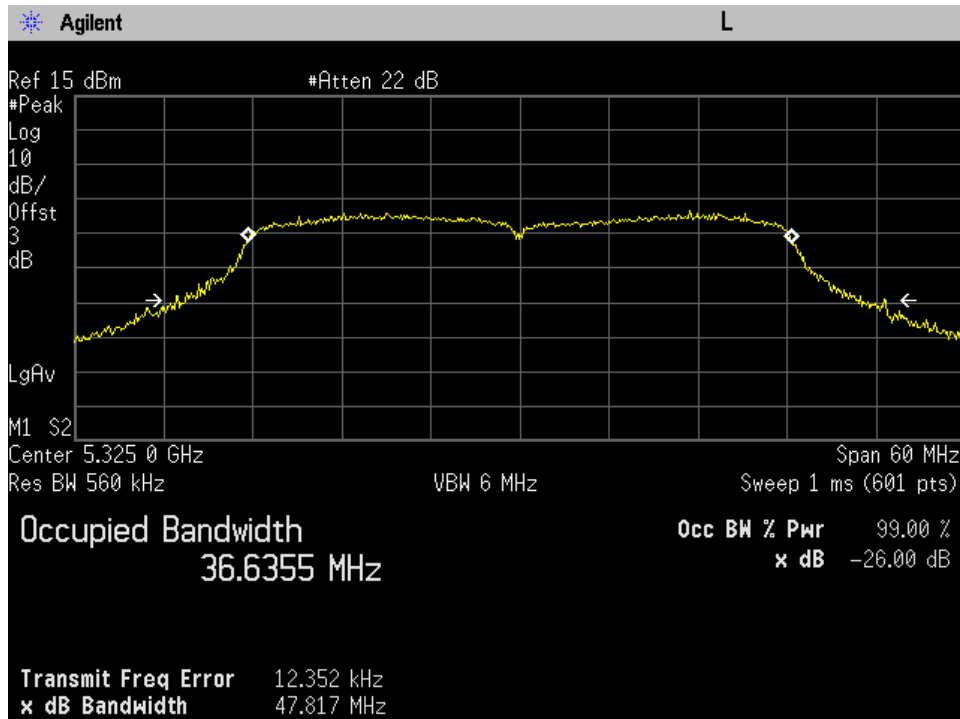
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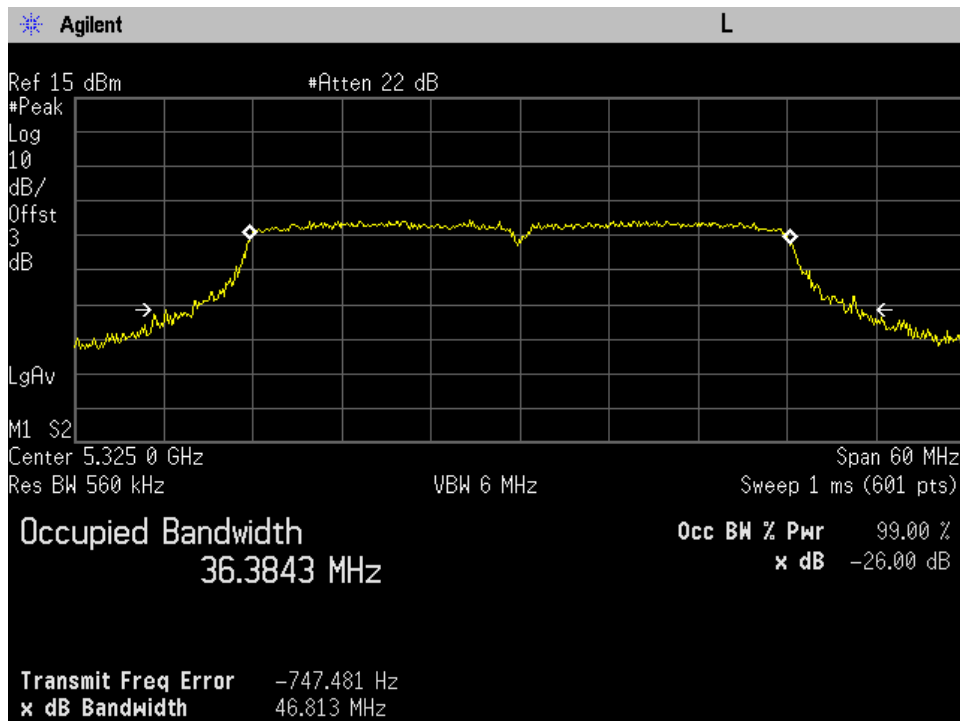
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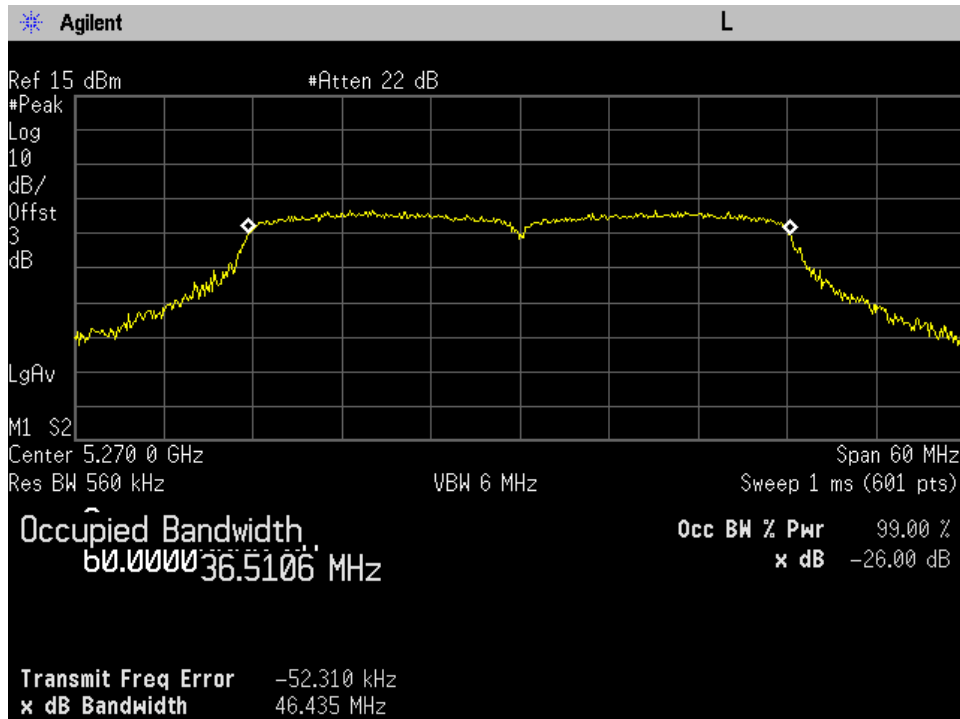
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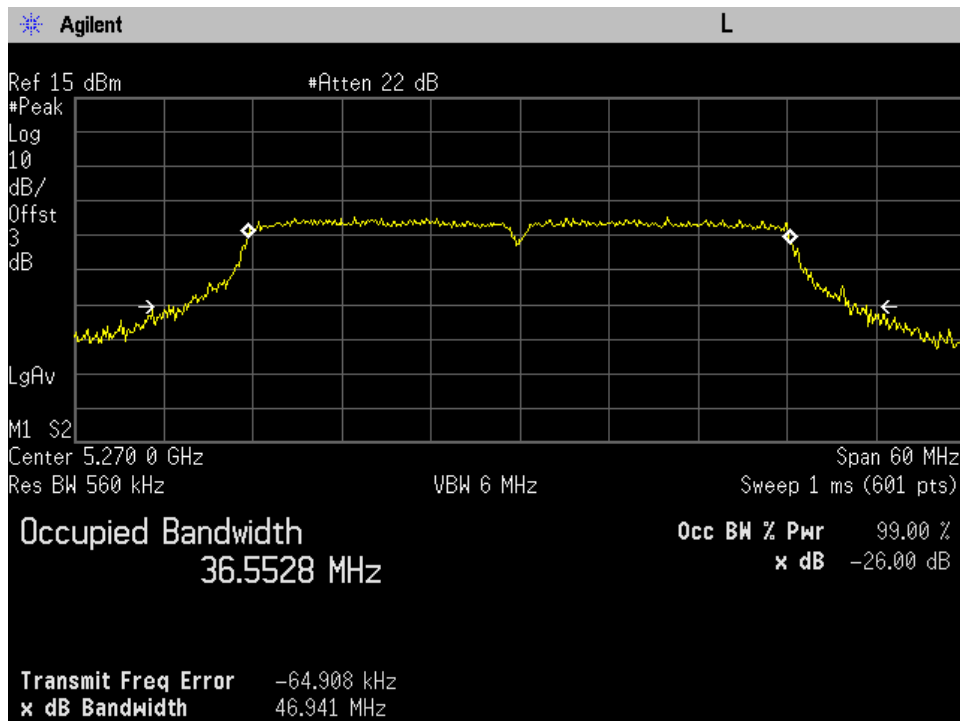
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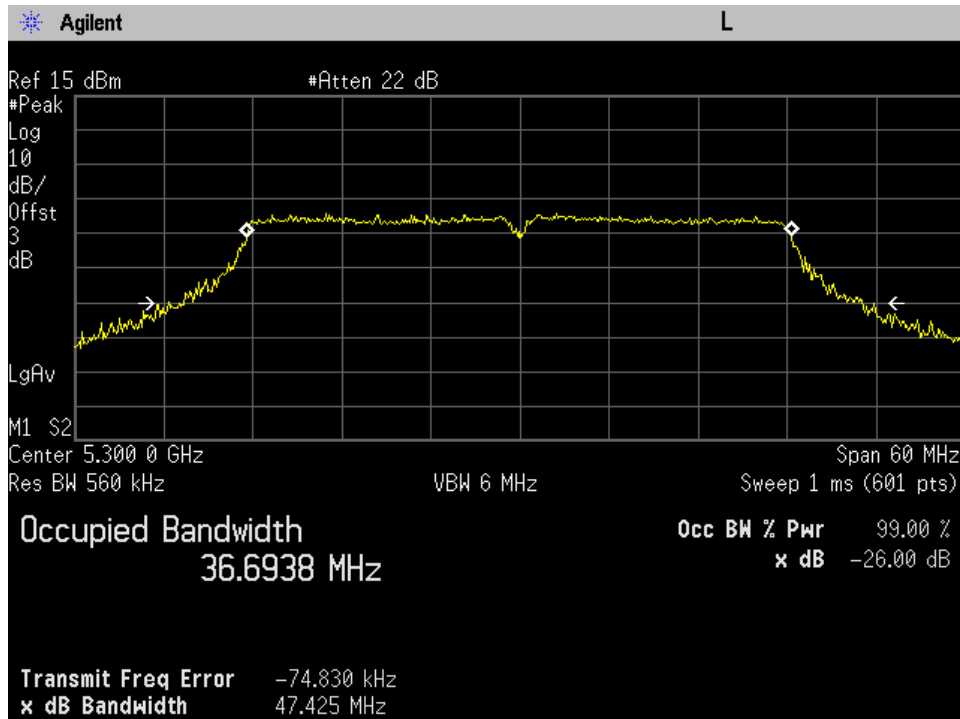
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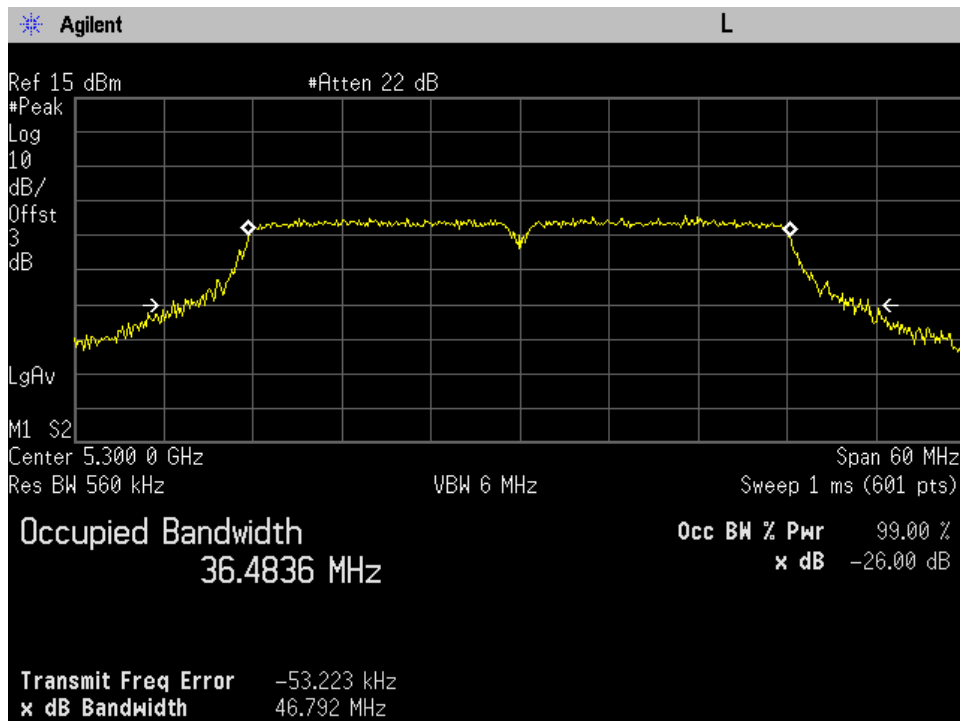
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40 Mhz Mid Ch Port 1



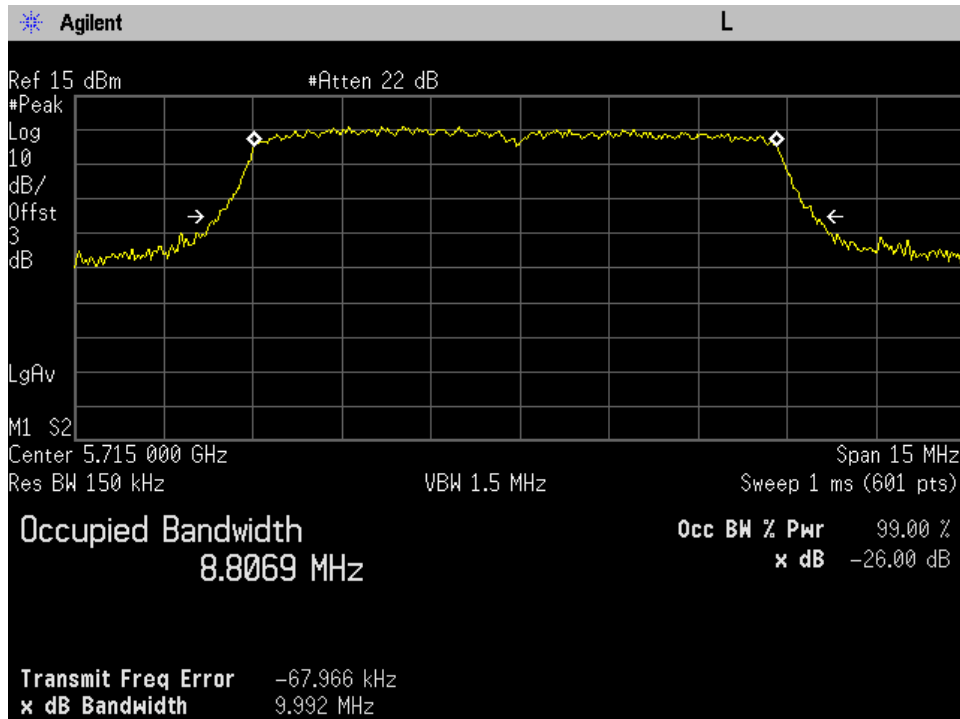
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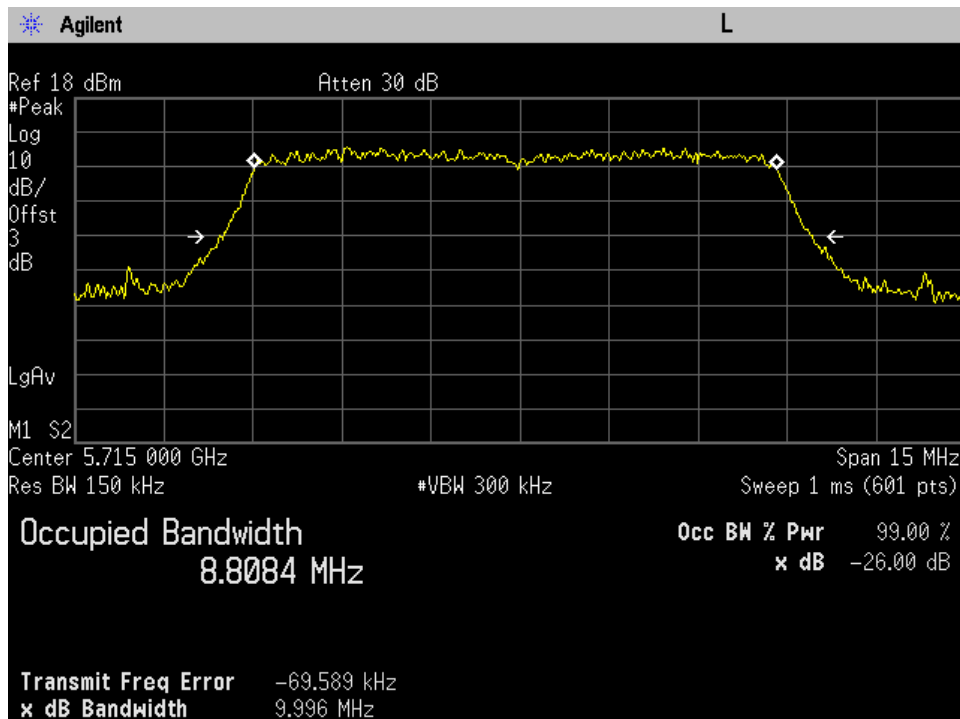


UNII-2C

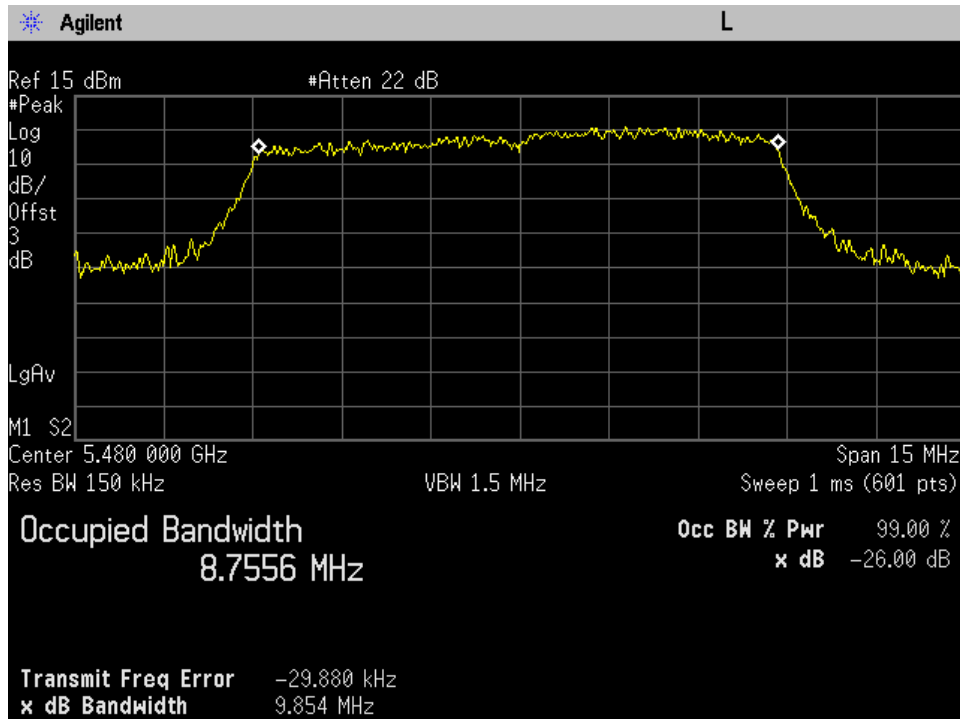
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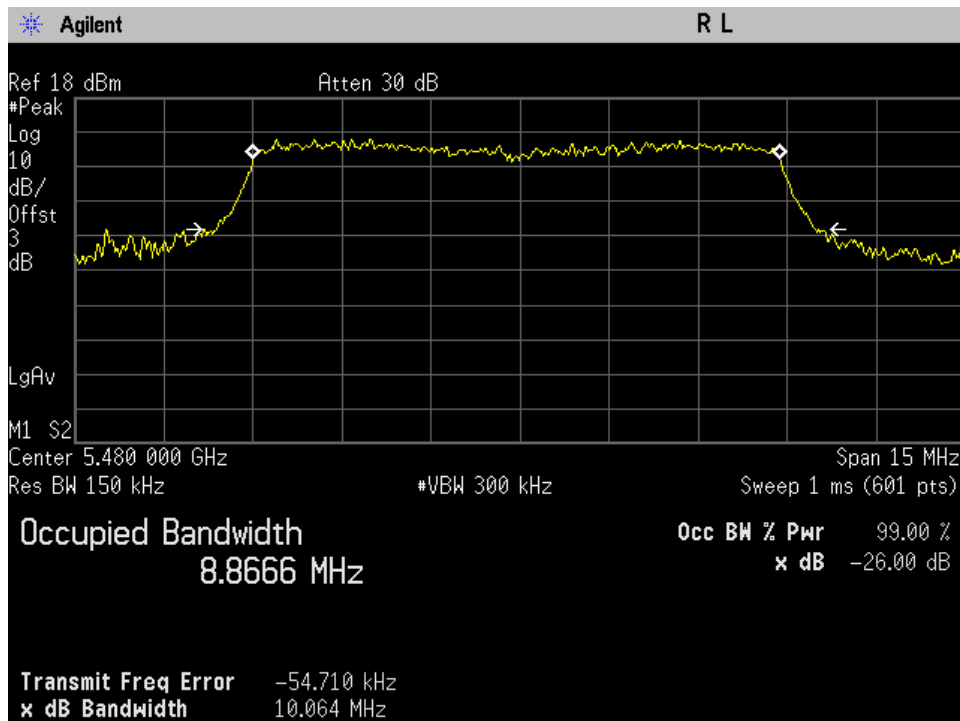
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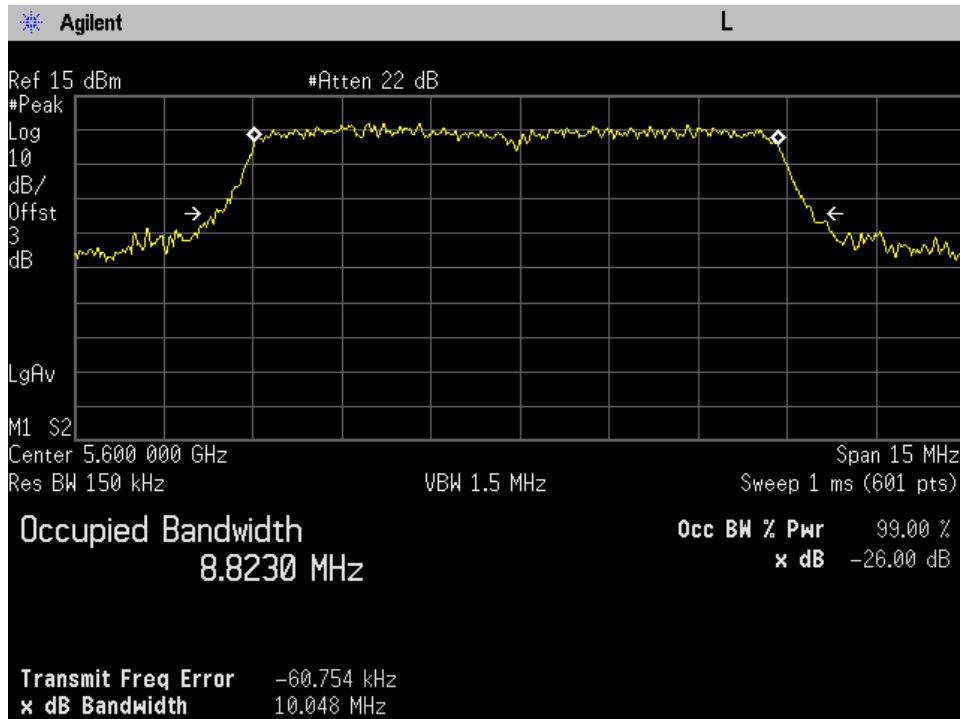
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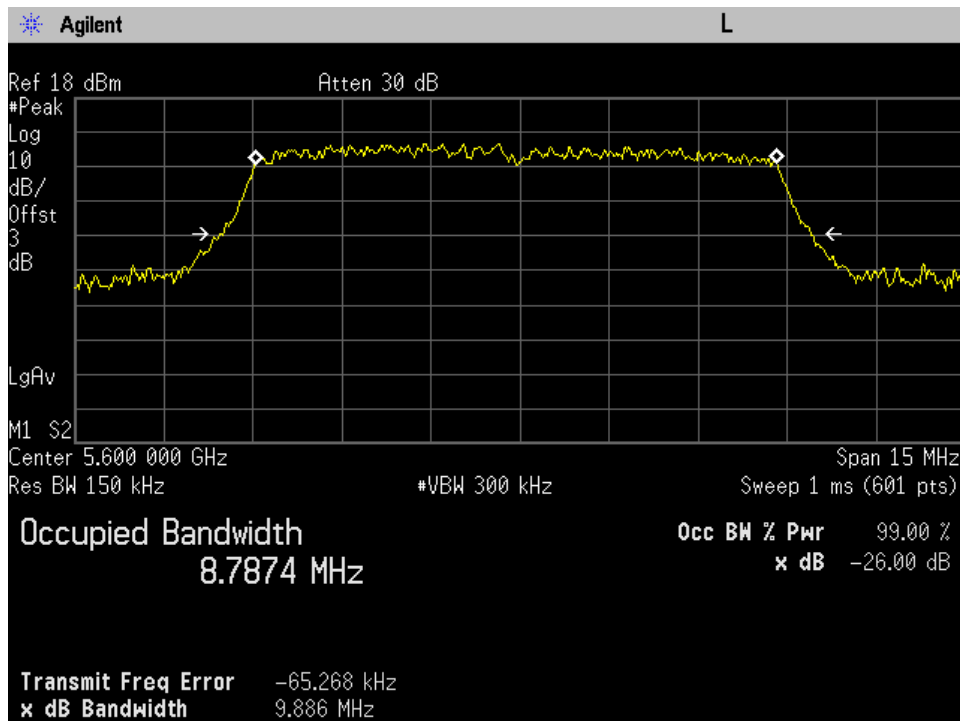
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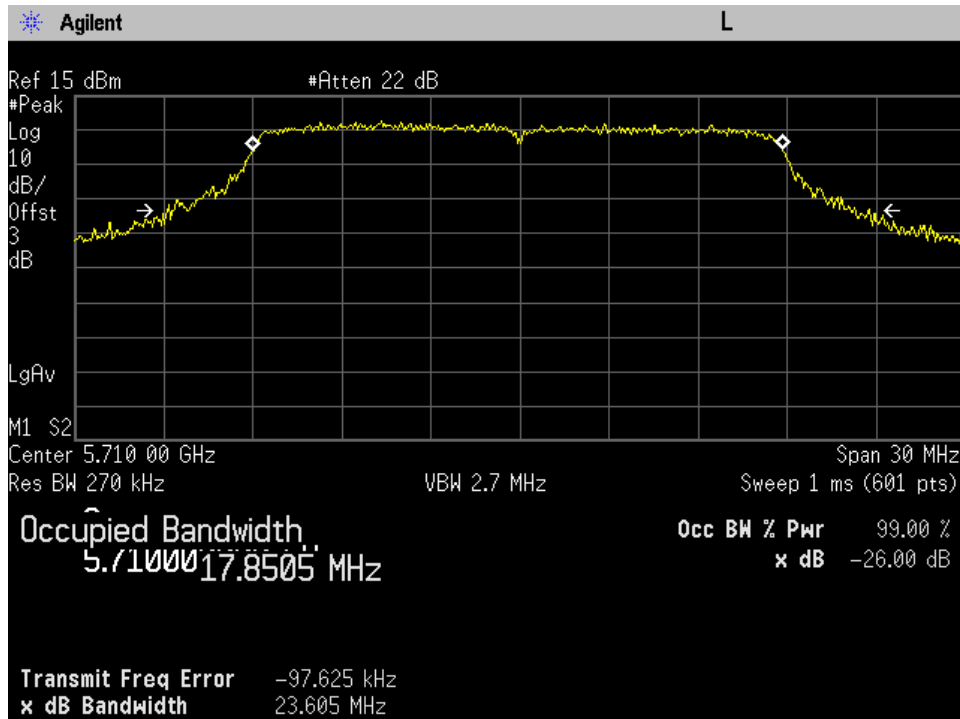
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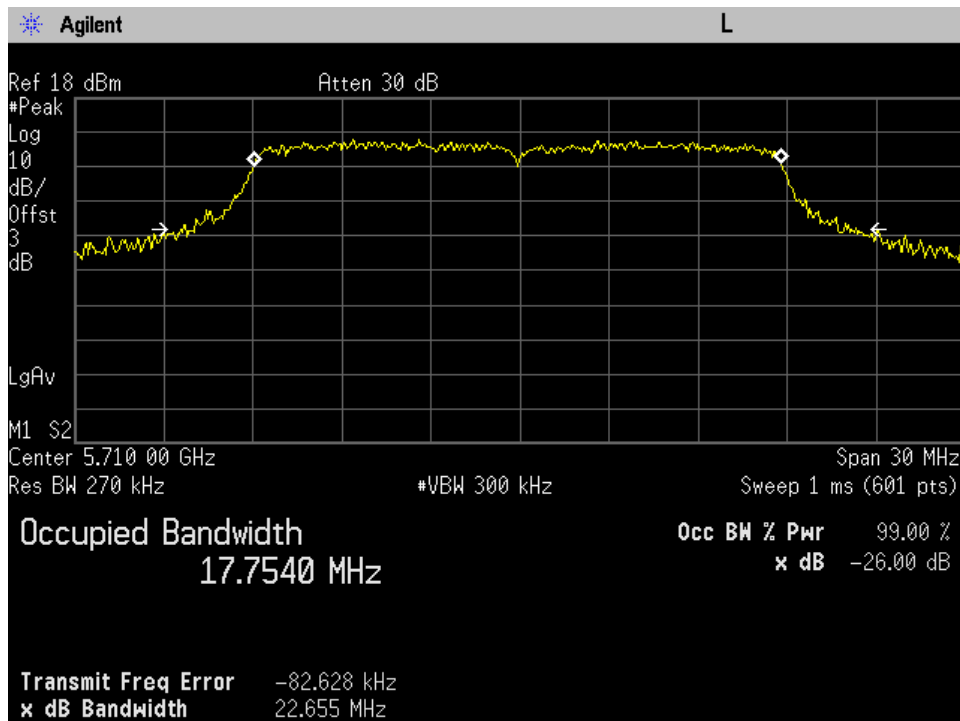
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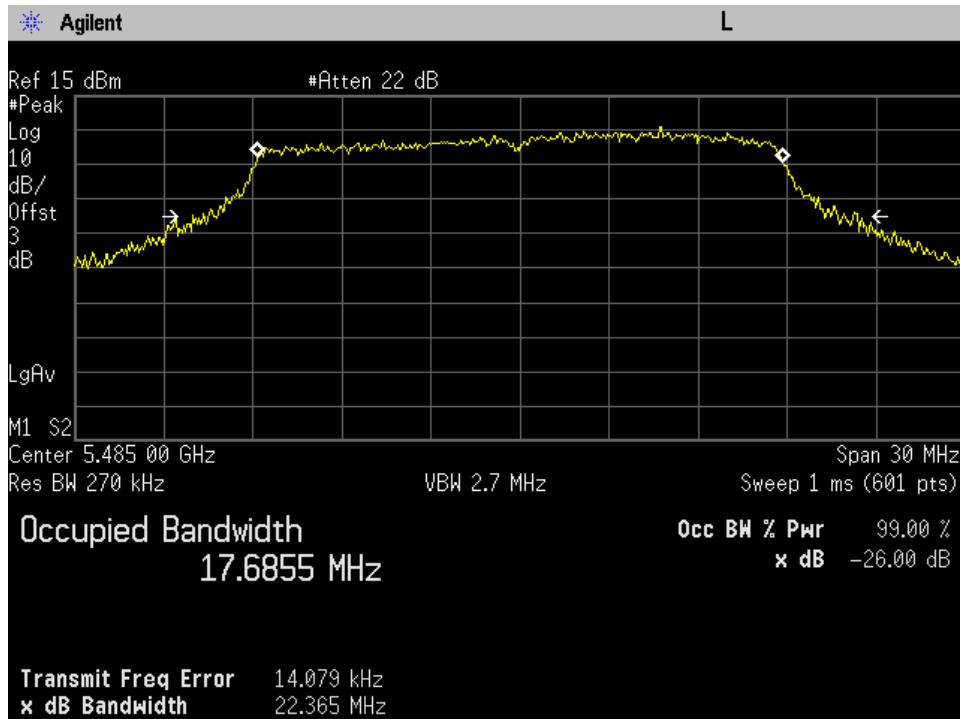
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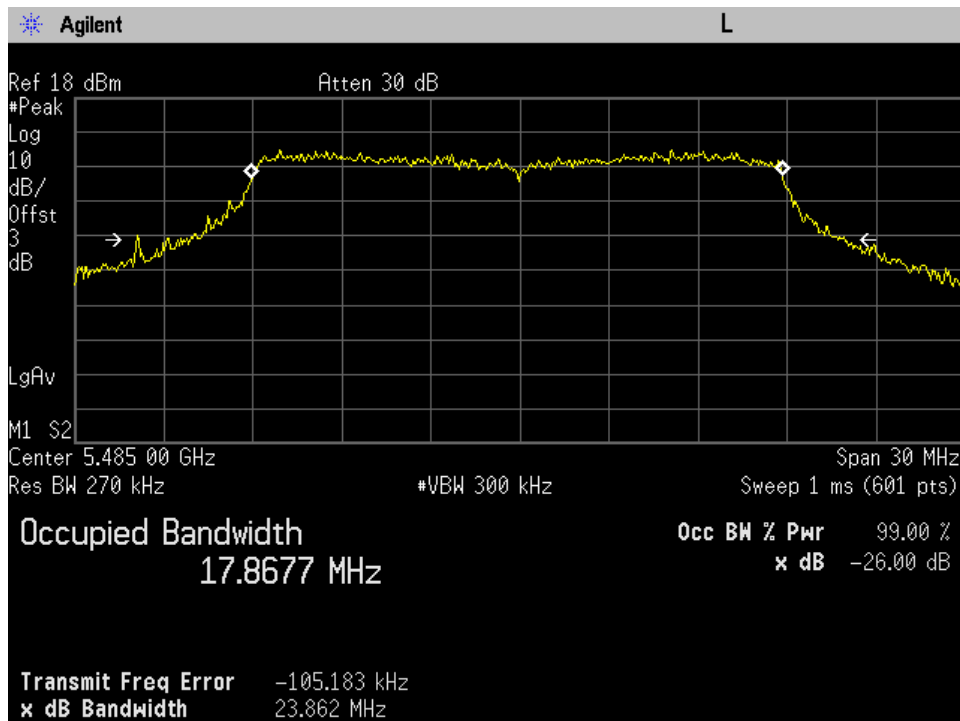
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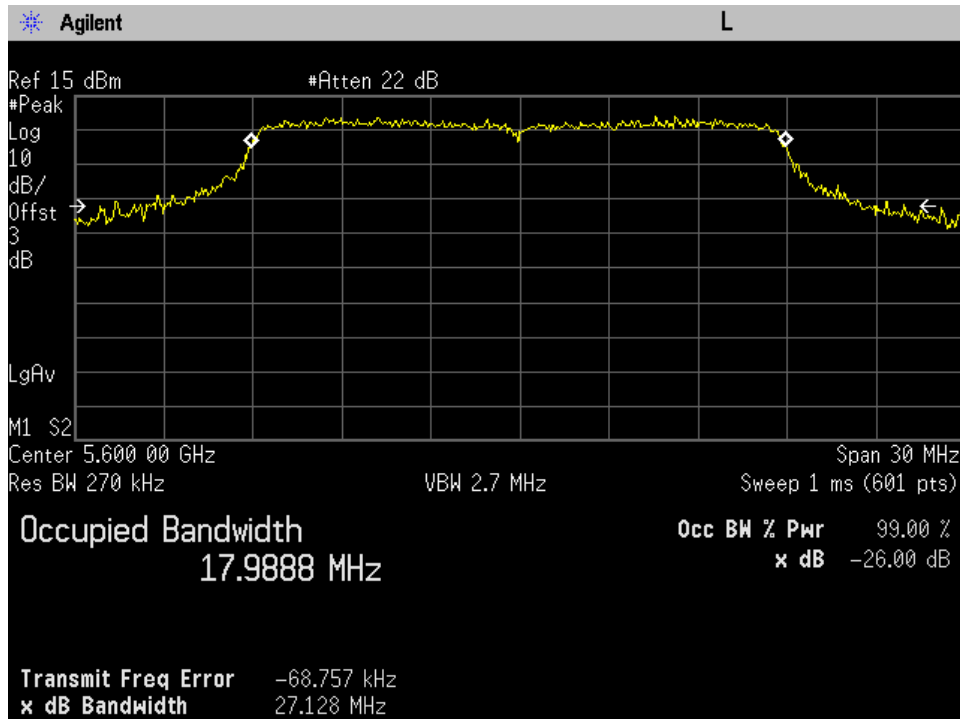
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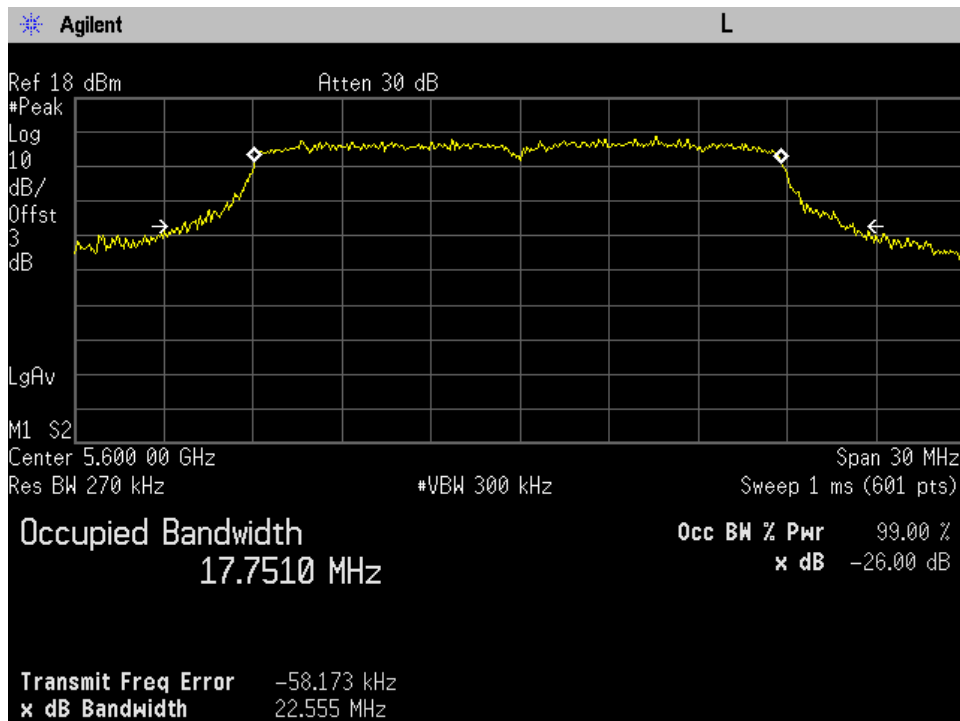
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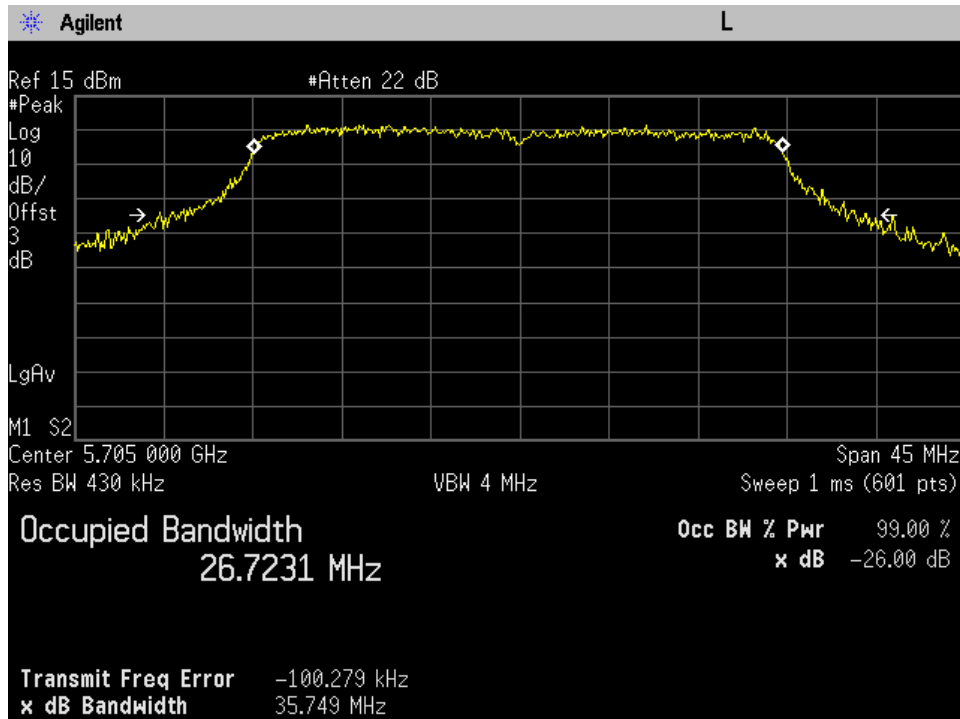
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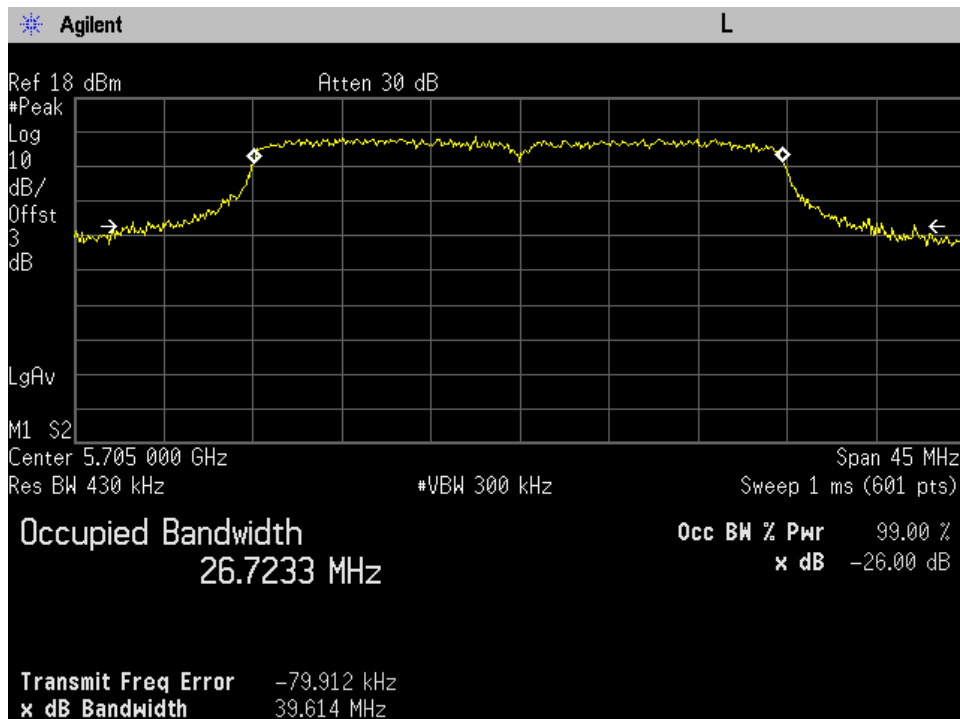
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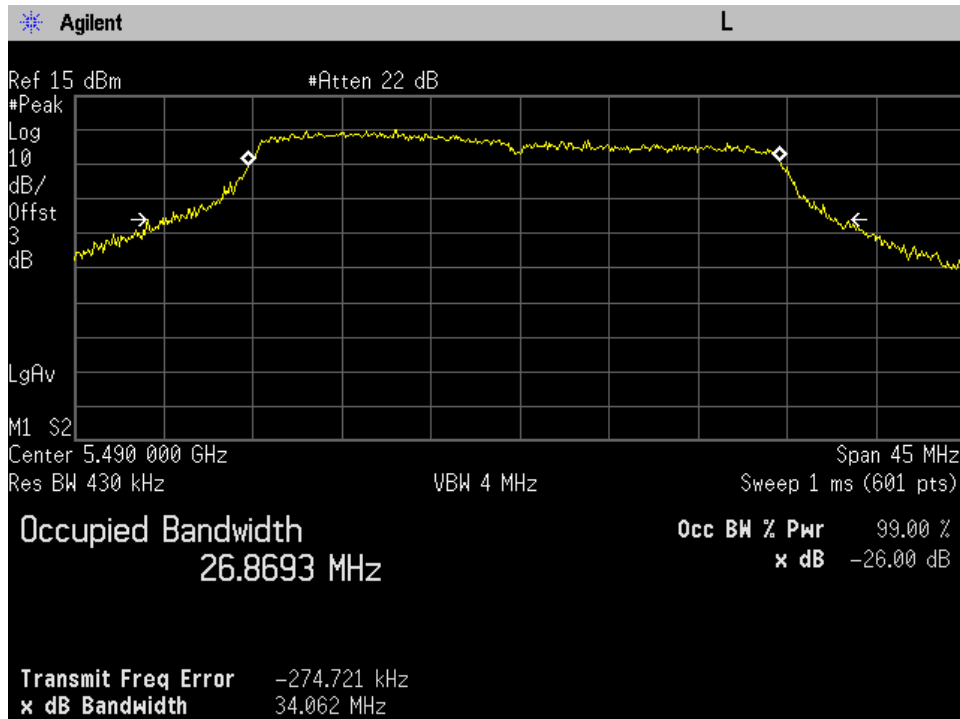
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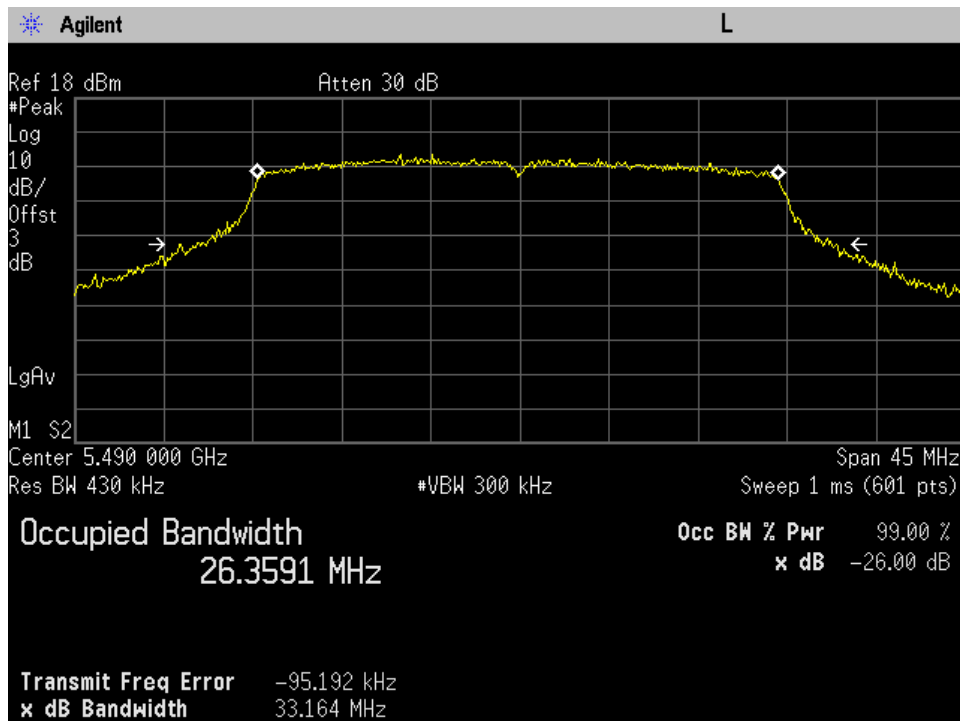
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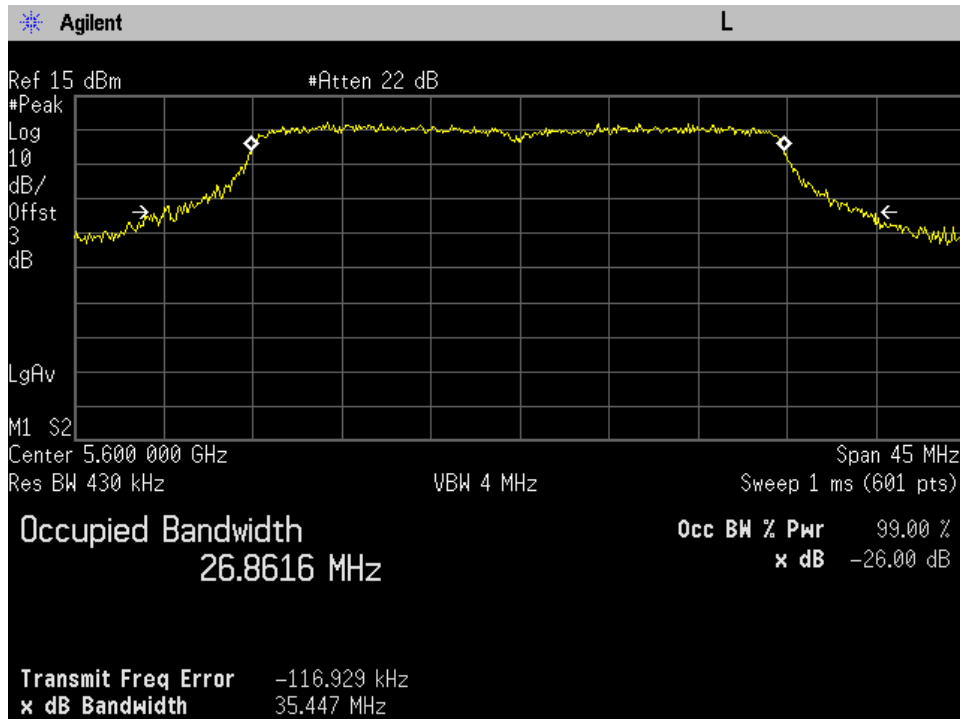
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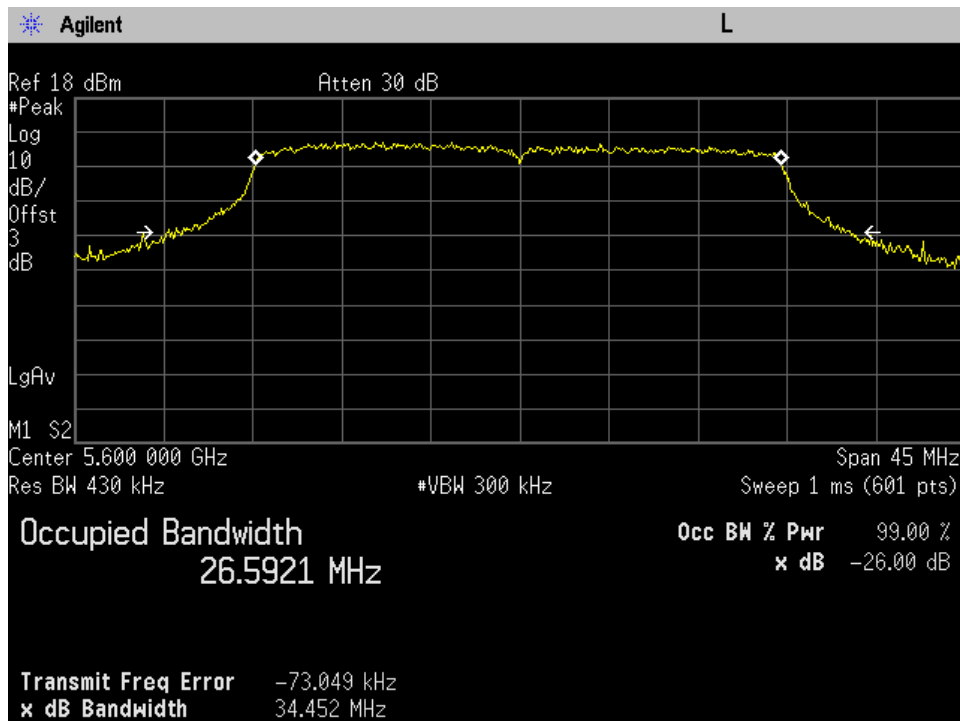
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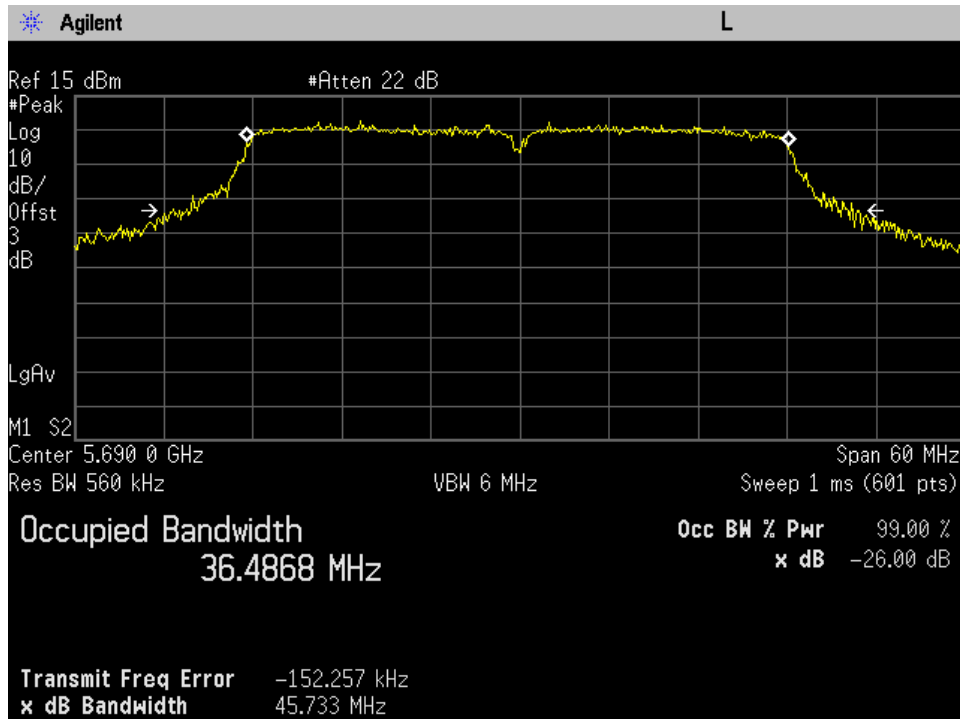
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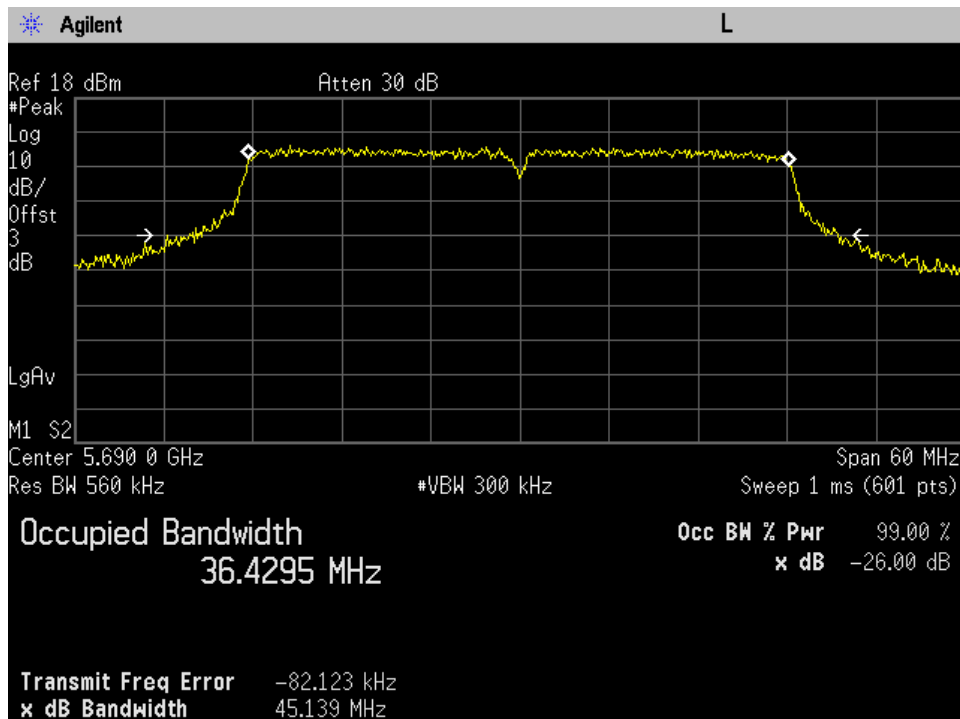
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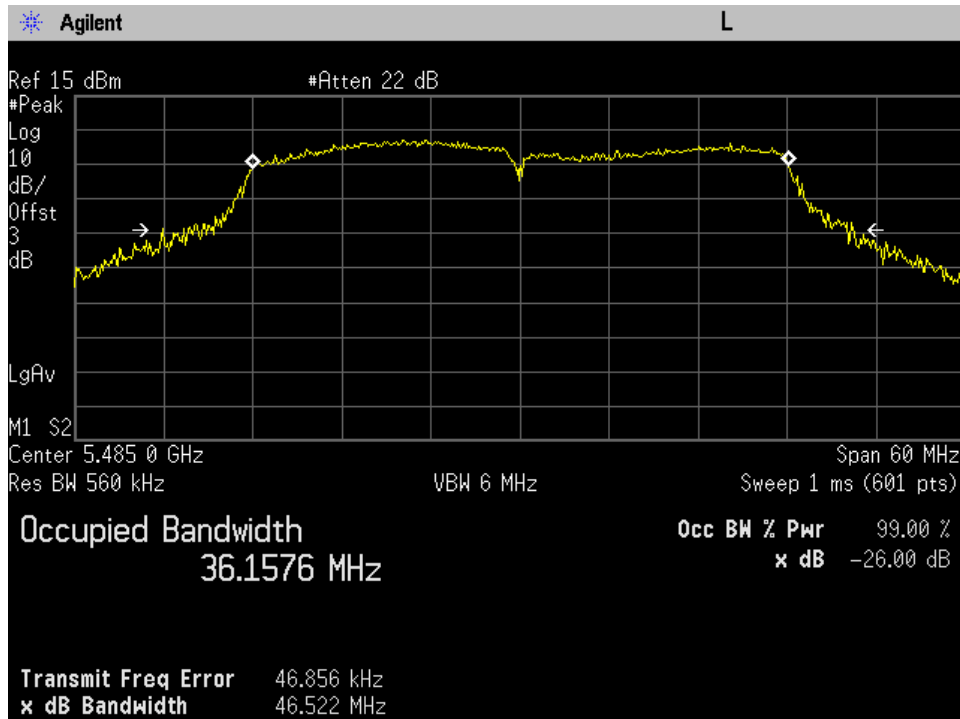
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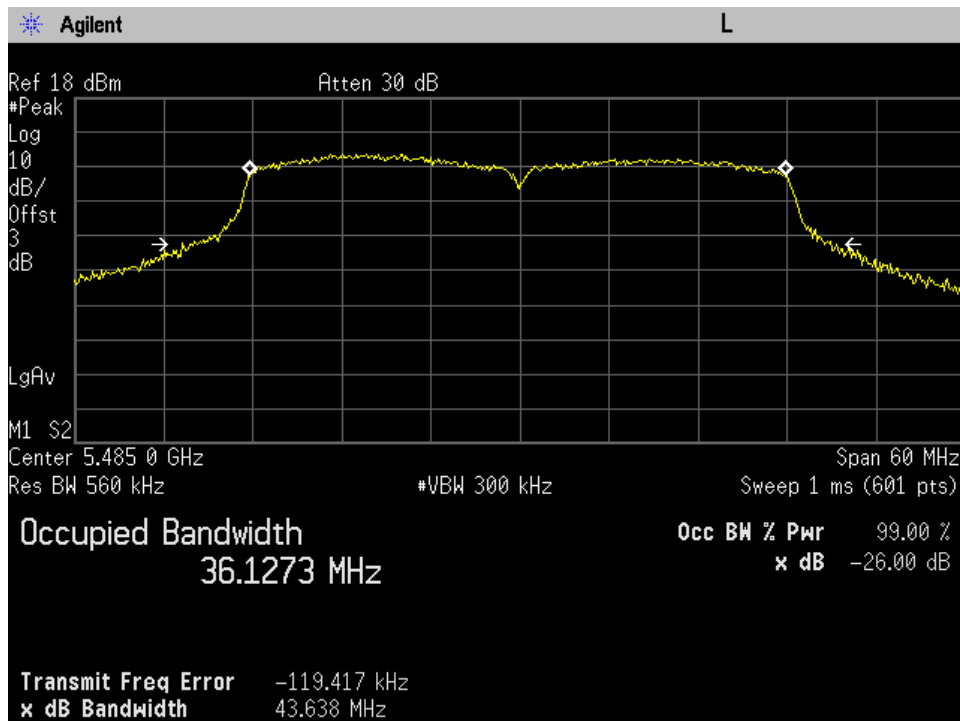
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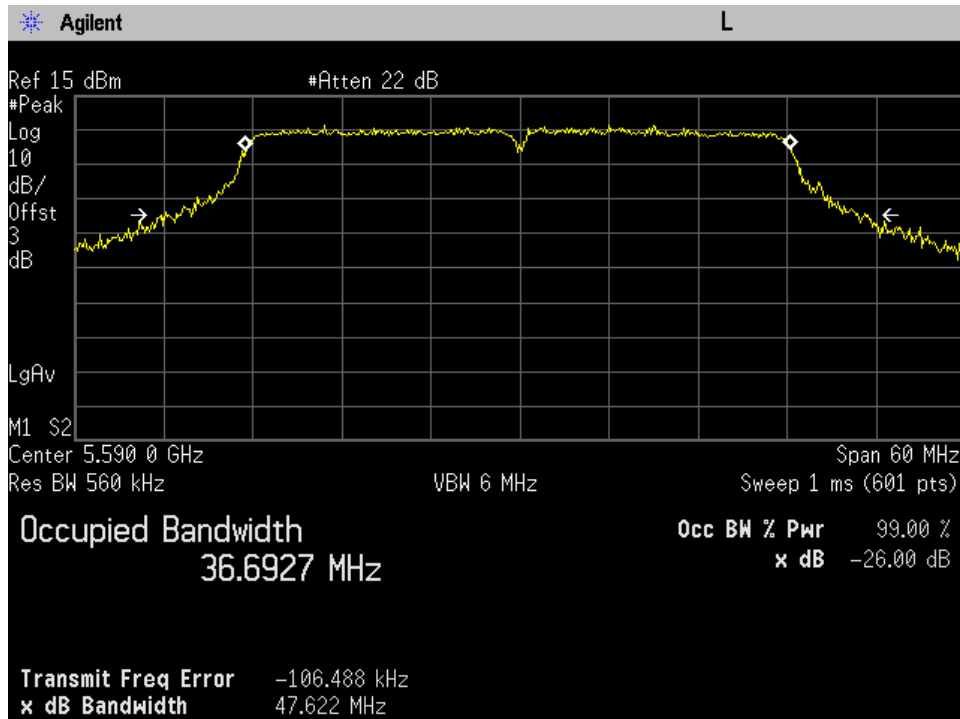
40 Mhz Low Ch Port 1



40 Mhz Low Ch Port 2



40 Mhz Mid Ch Port 1



40 Mhz Mid Ch Port 2

