



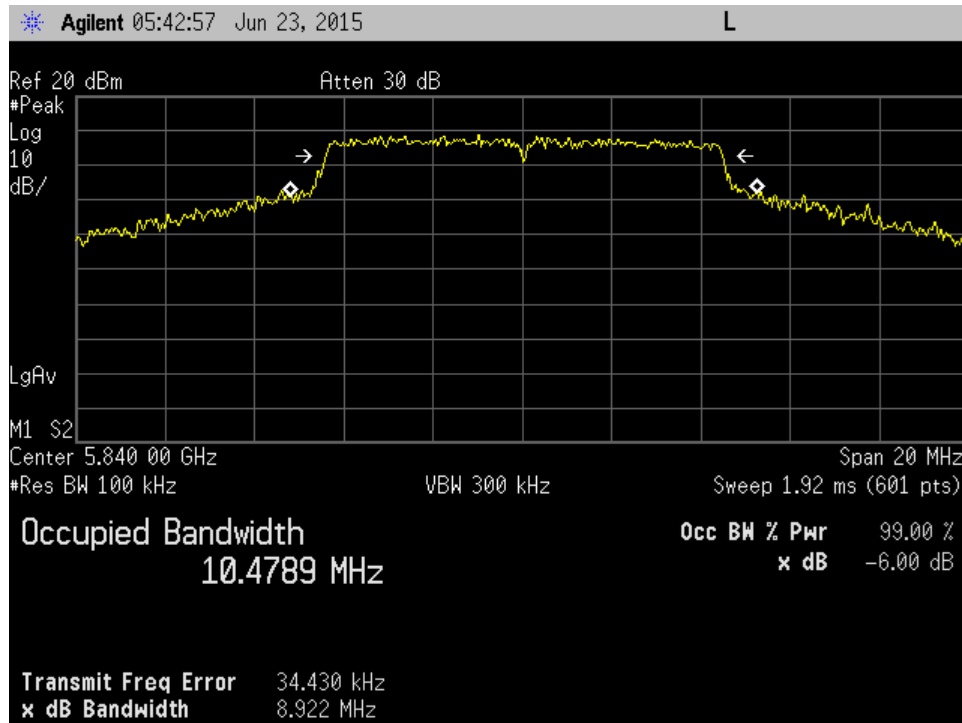
# **Annex C**

## **Occupied Bandwidth**

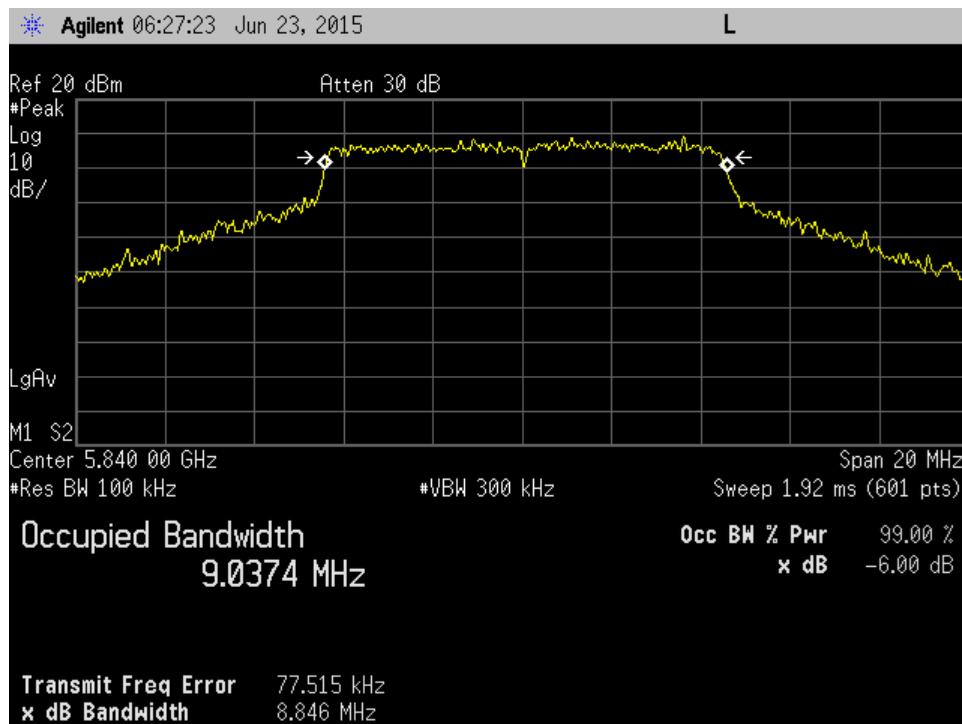
### **Ports J7 and J13**



### 10MHz high ch J13

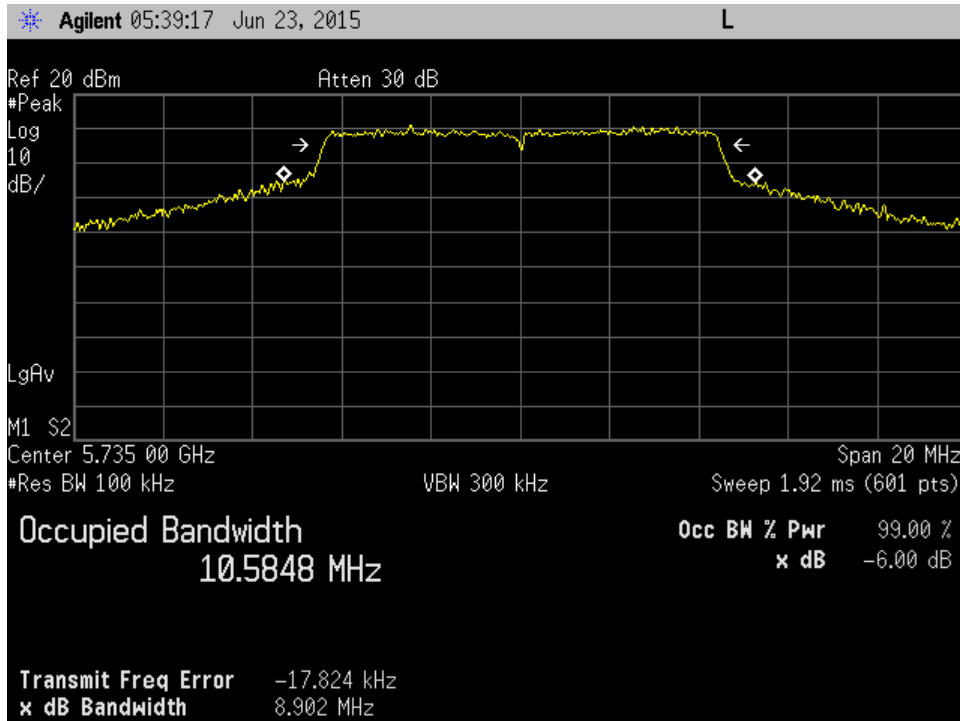


### 10MHz high ch J7

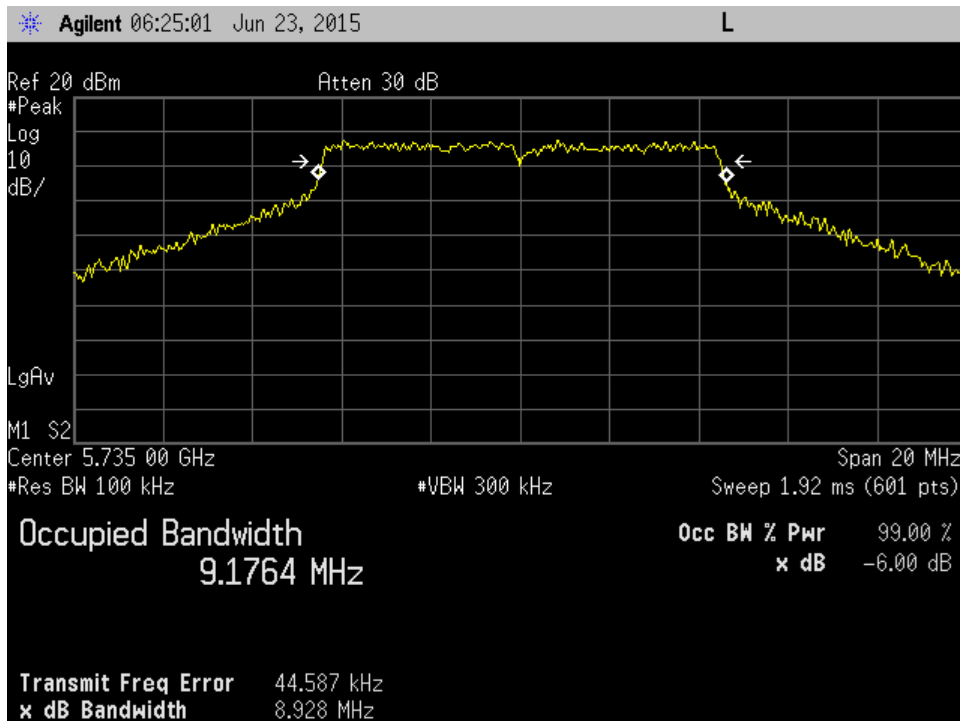




### 10MHz low ch J13

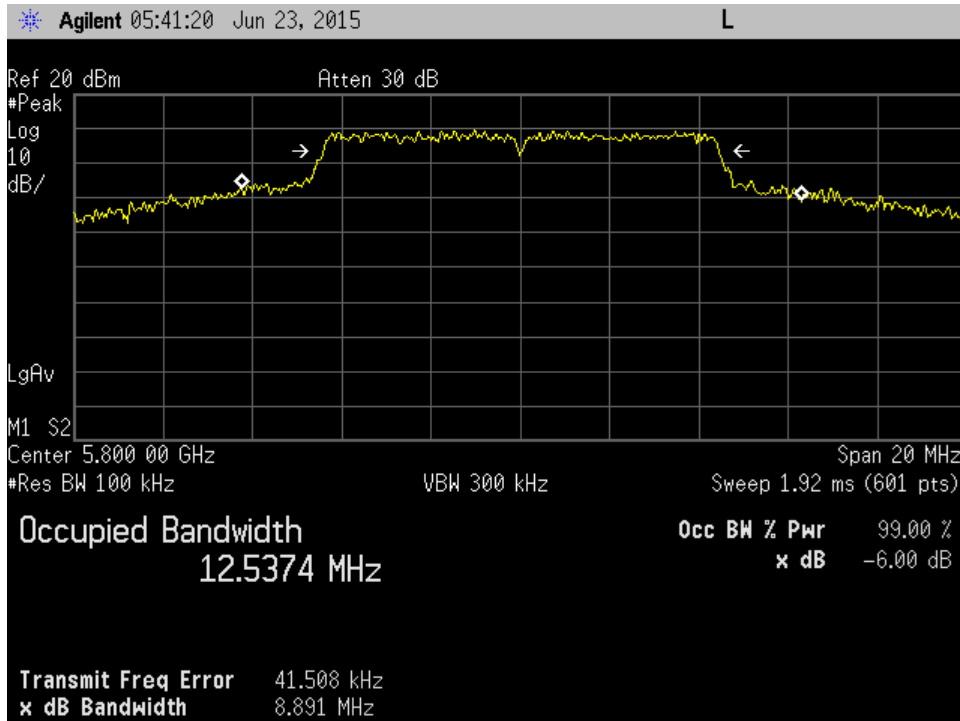


### 10MHz low ch J7

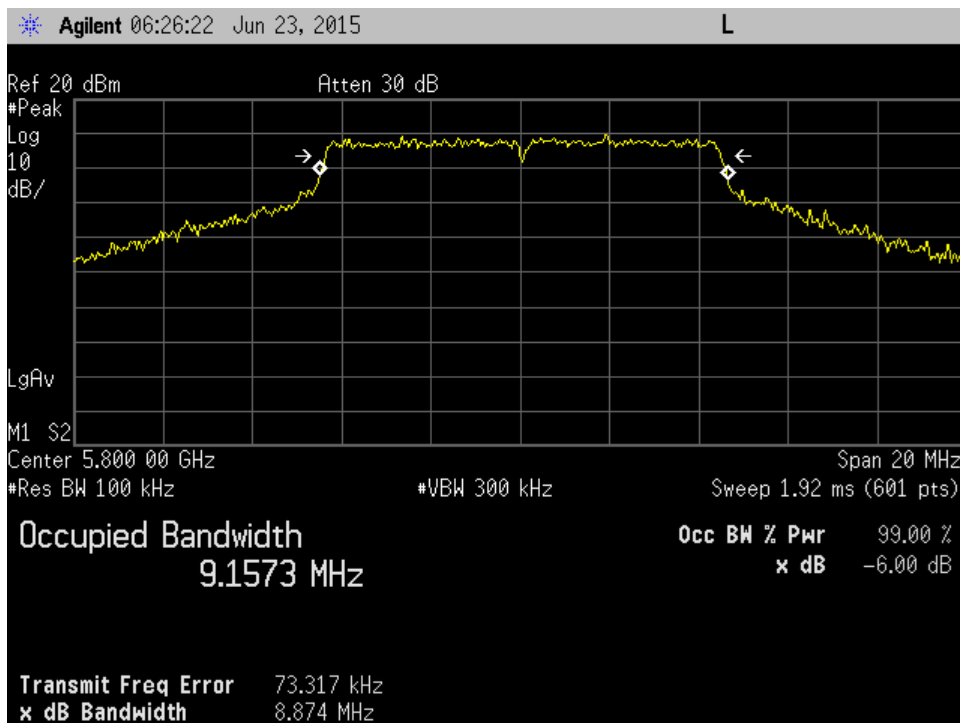




### 10MHz mid ch J13

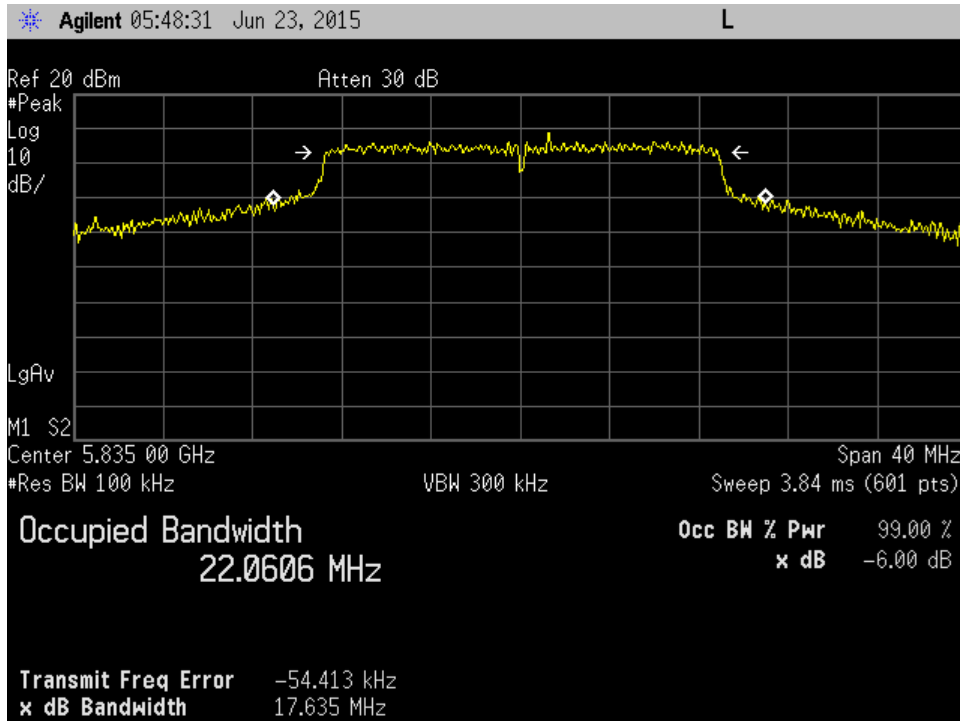


### 10MHz mid ch J7

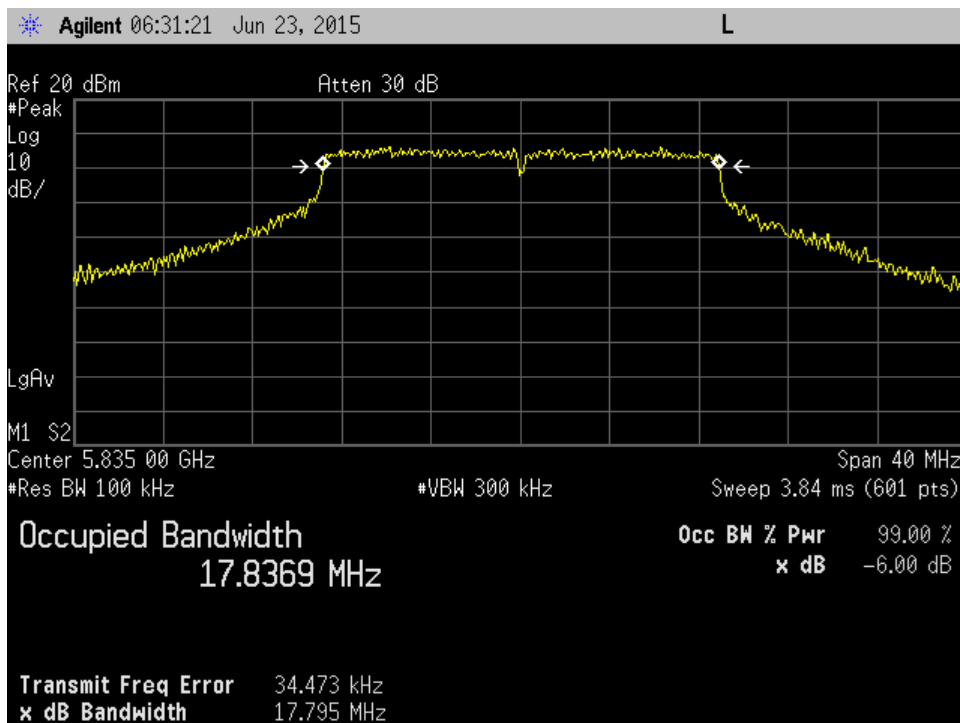




### 20MHz high ch J13

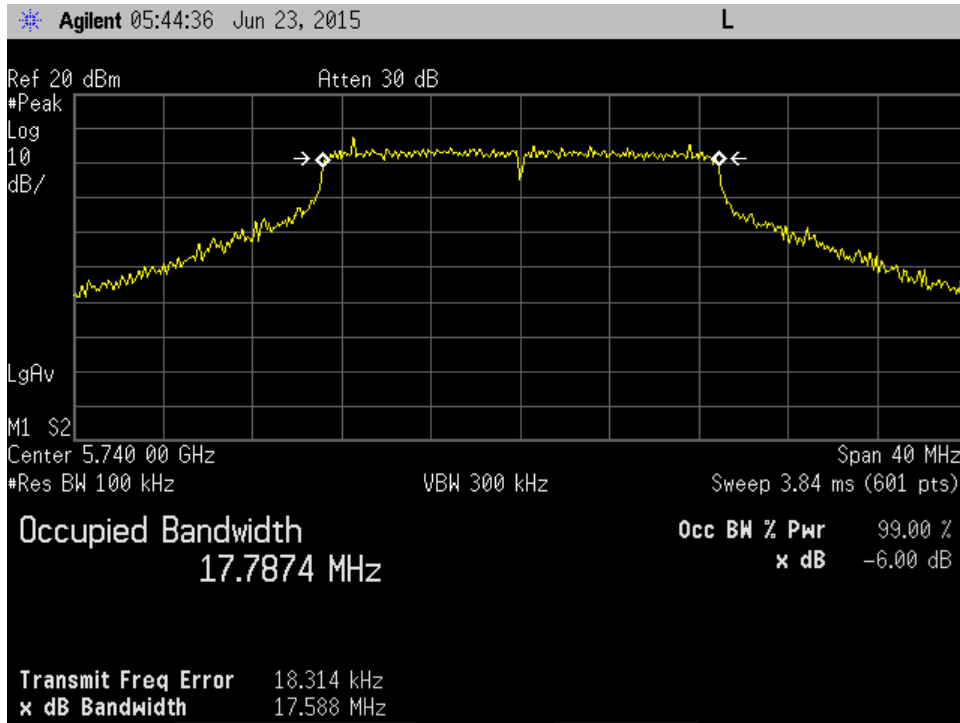


### 20MHz high ch J7

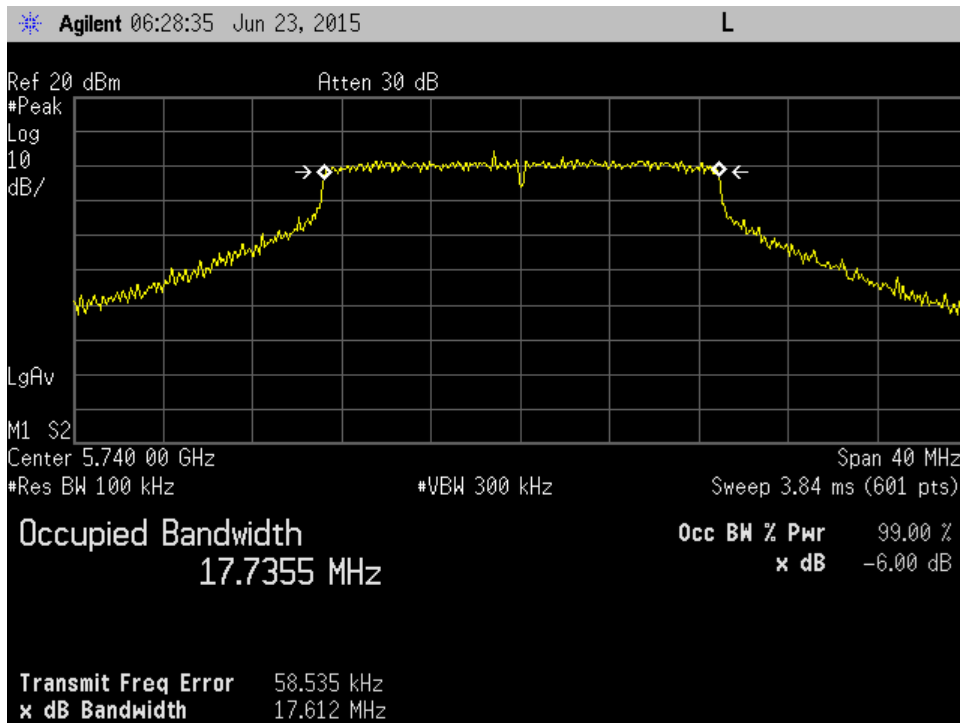




### 20MHz low ch J13

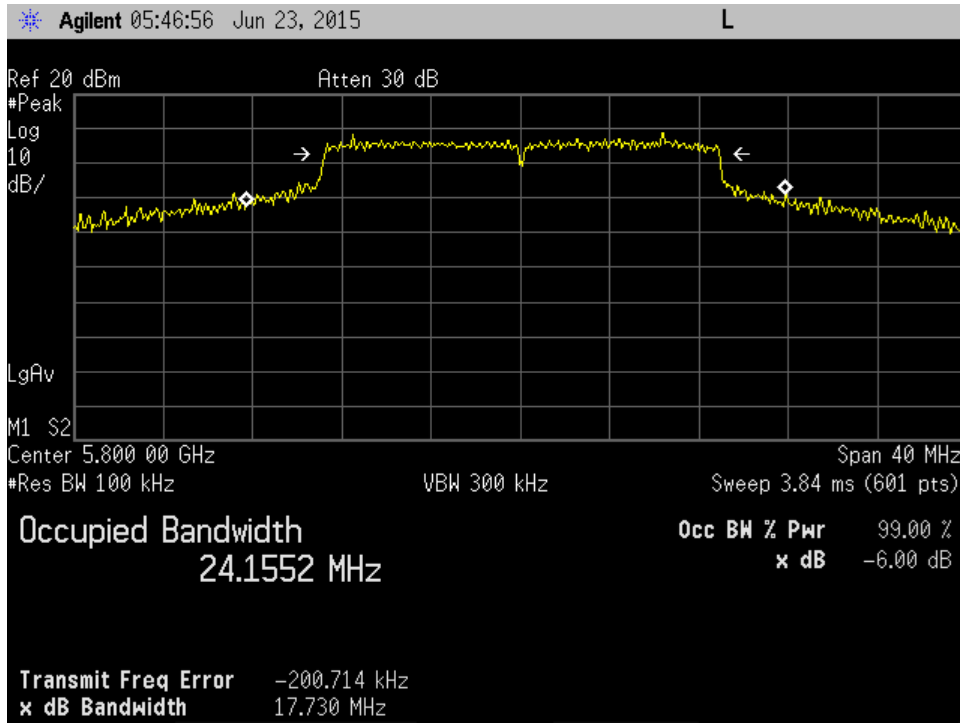


### 20MHz low ch J7

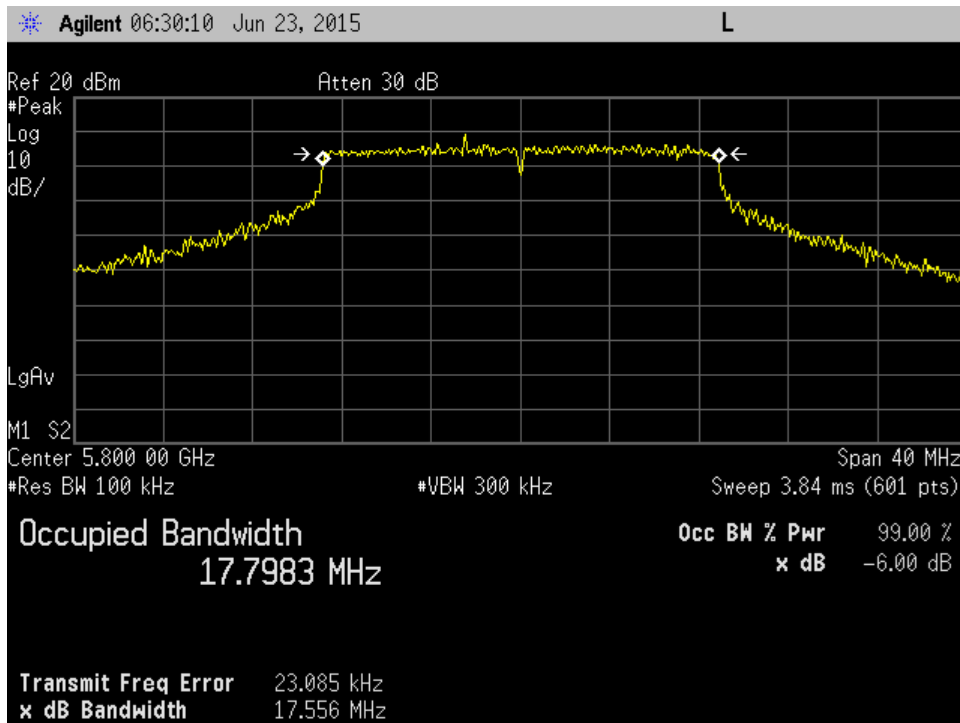




### 20MHz mid ch J13

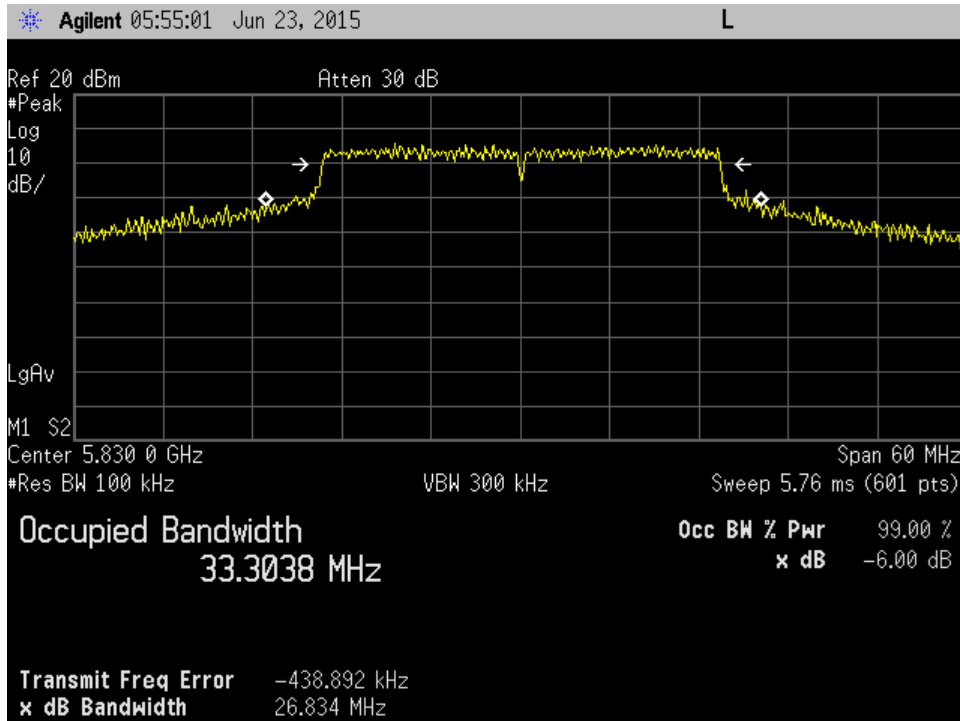


### 20MHz mid ch J7

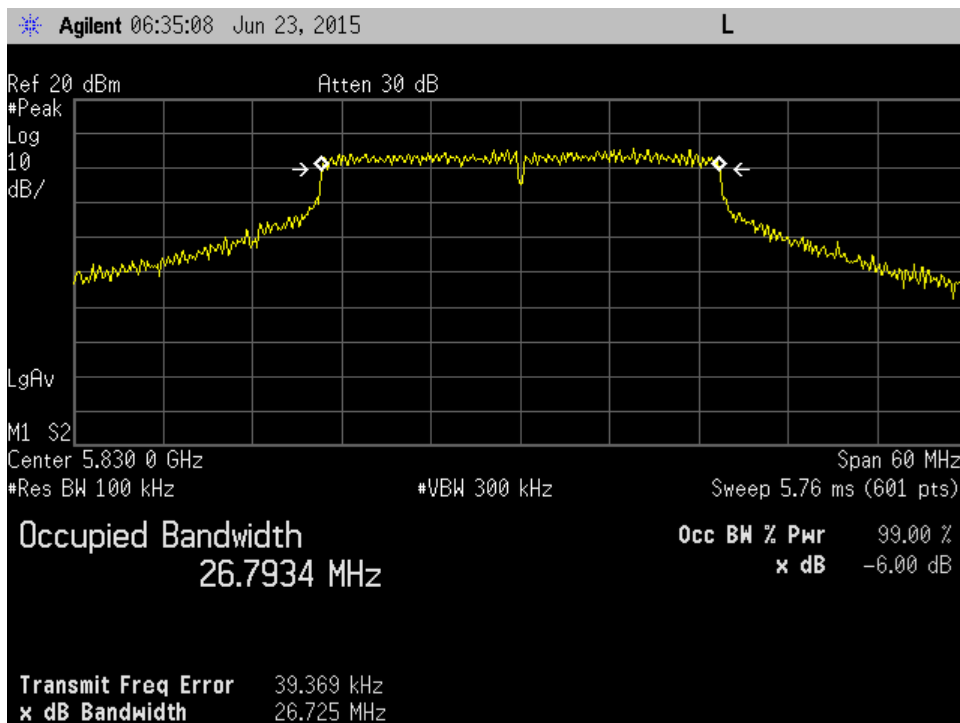




### 30MHz high ch J13



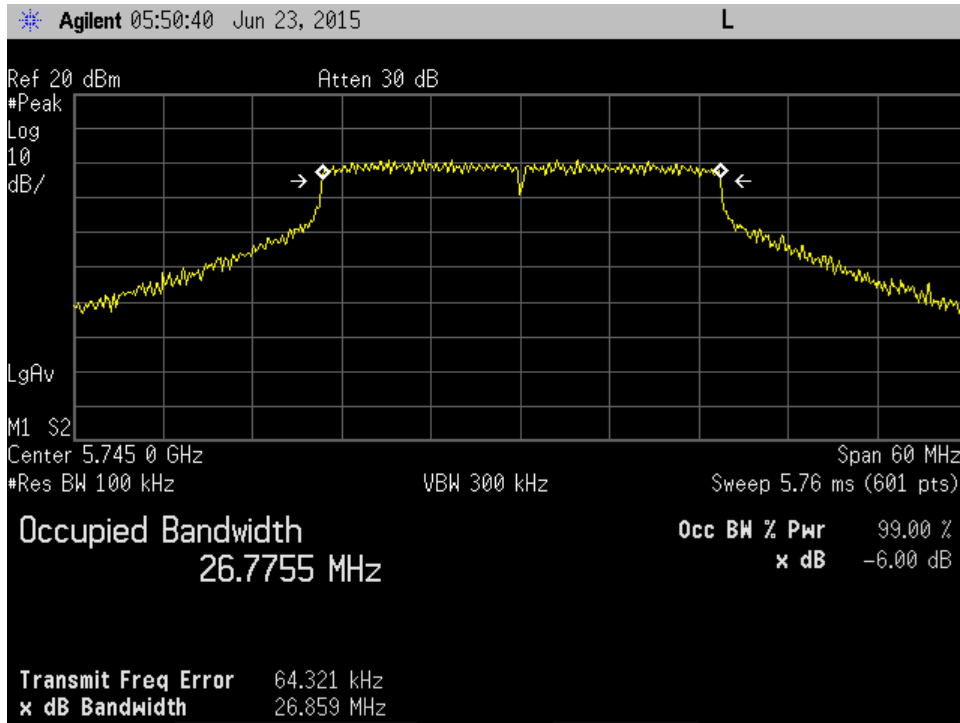
### 30MHz high ch J7



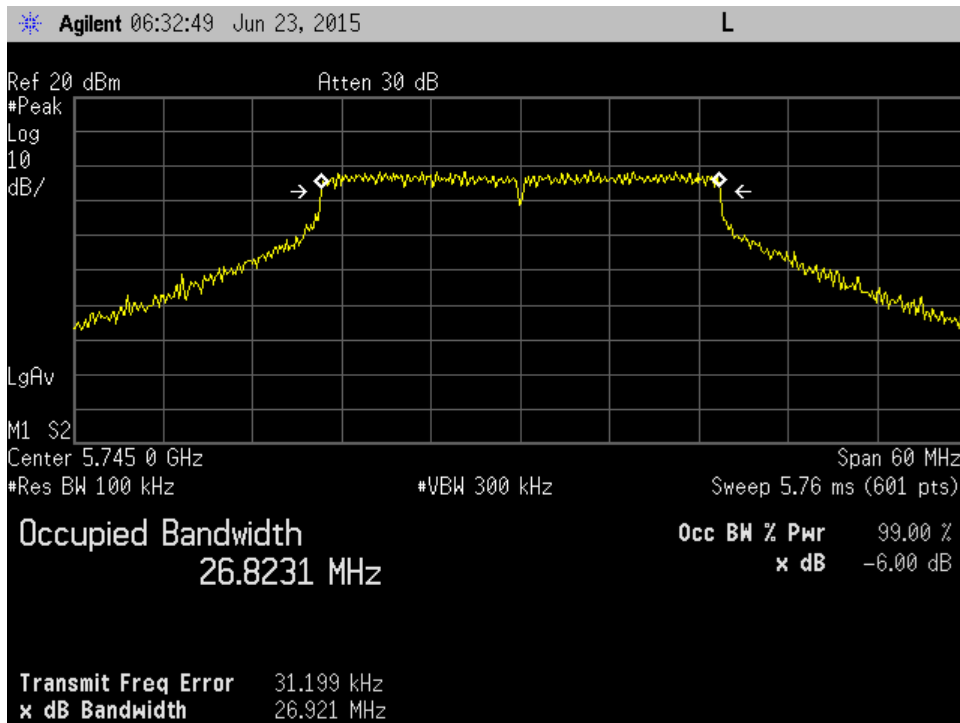




### 30MHz low ch J13

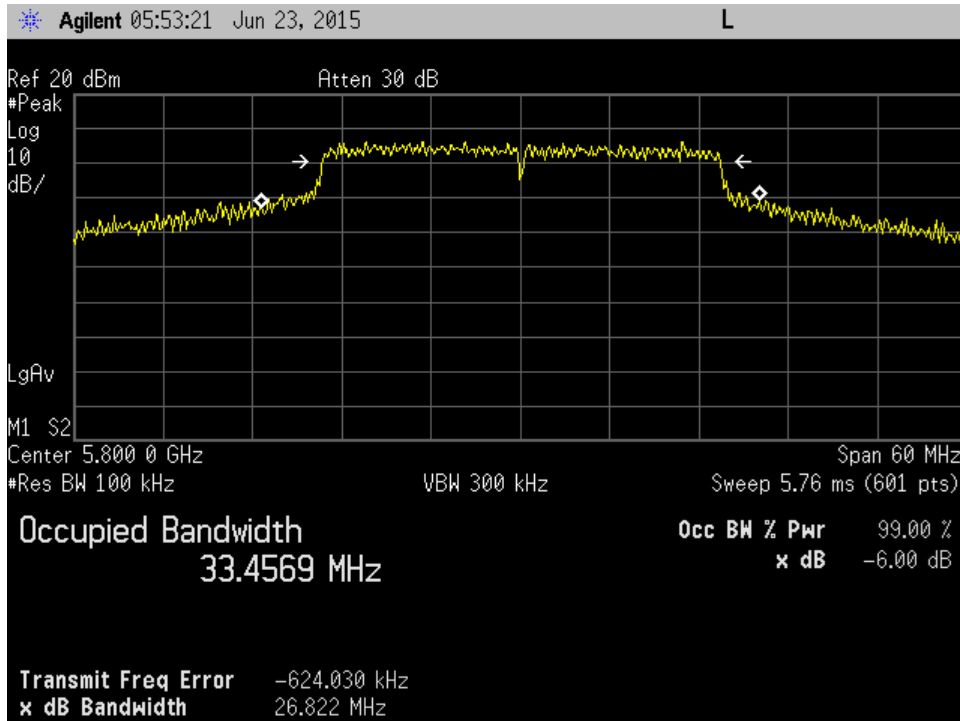


### 30MHz low ch J7

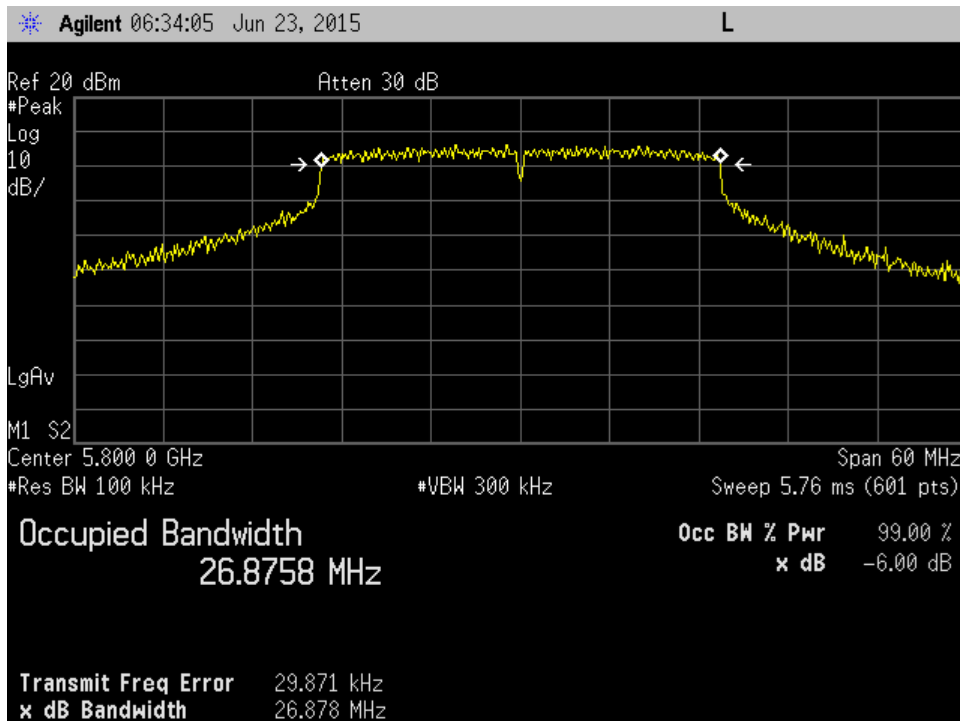




### 30MHz mid ch J13

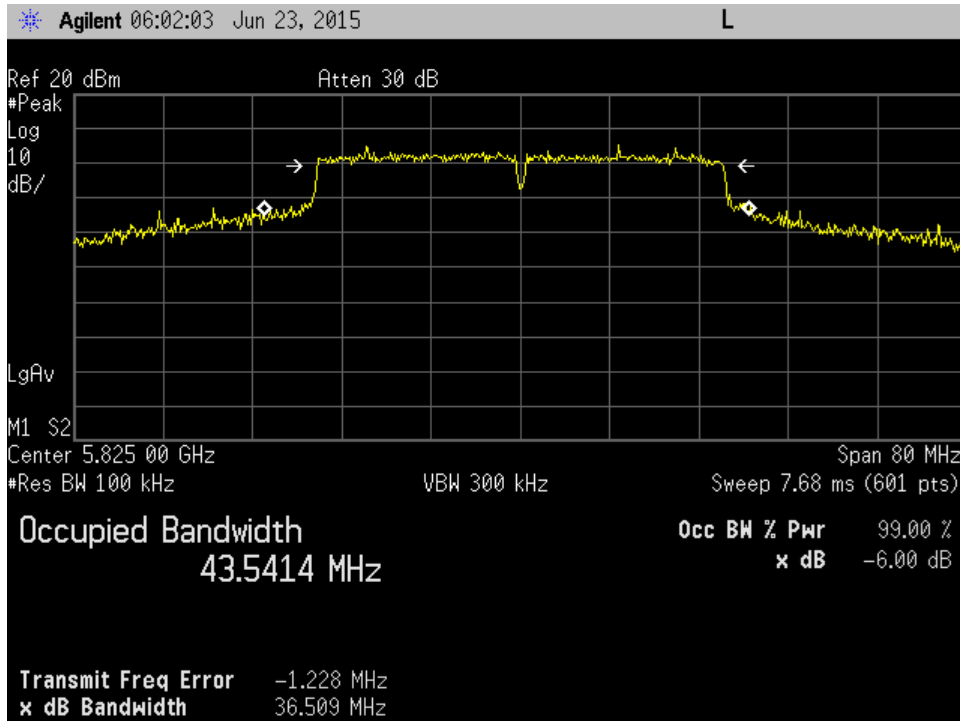


### 30MHz mid ch J7

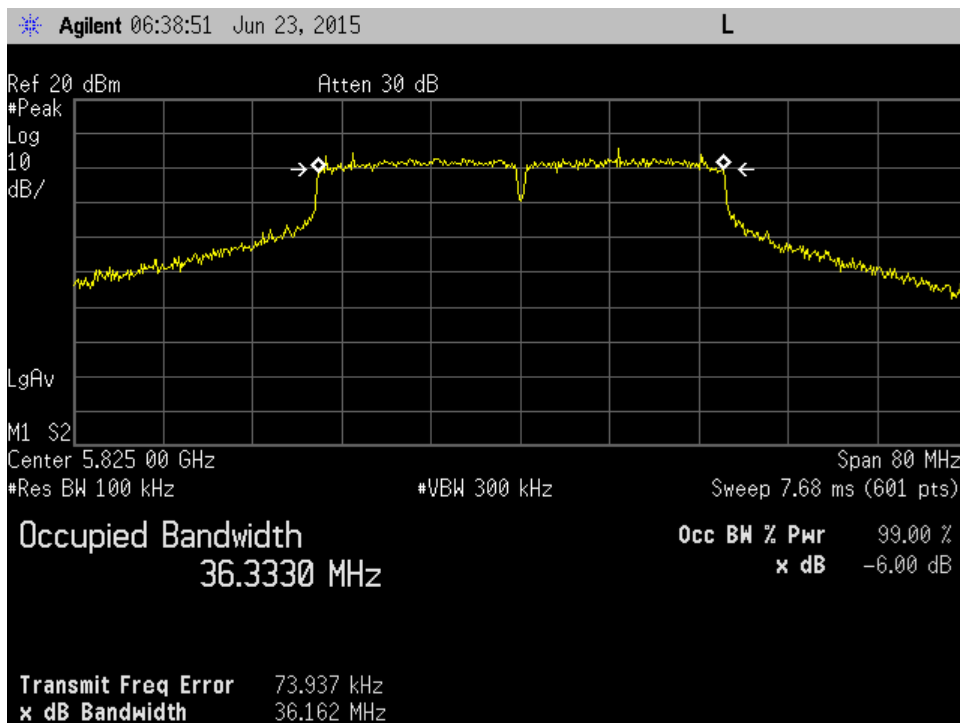




### 40MHz high ch J13

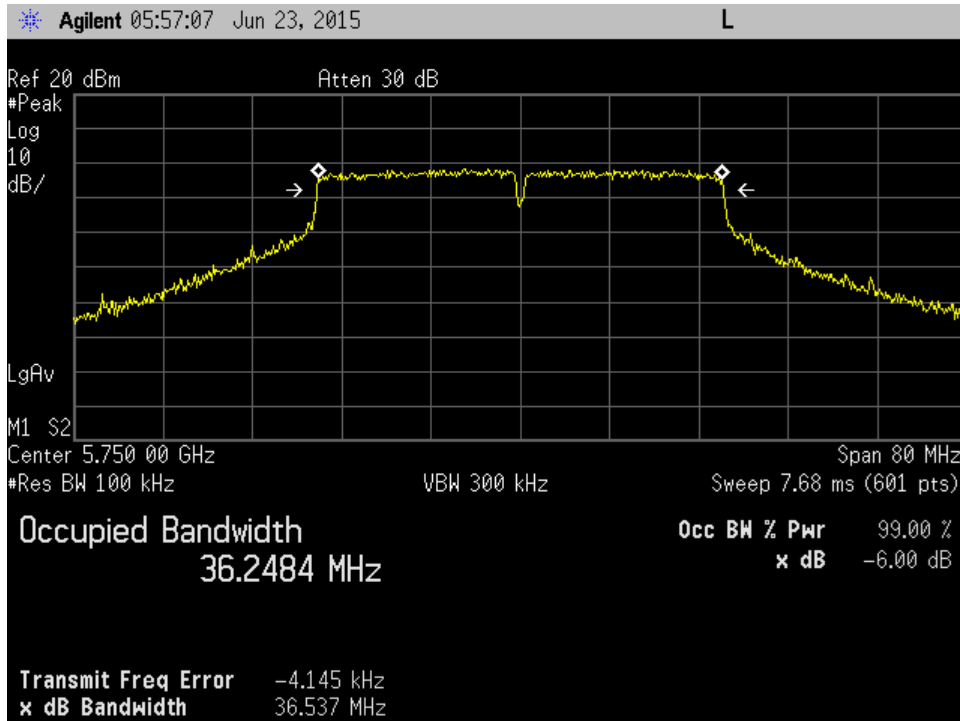


### 40MHz high ch J7

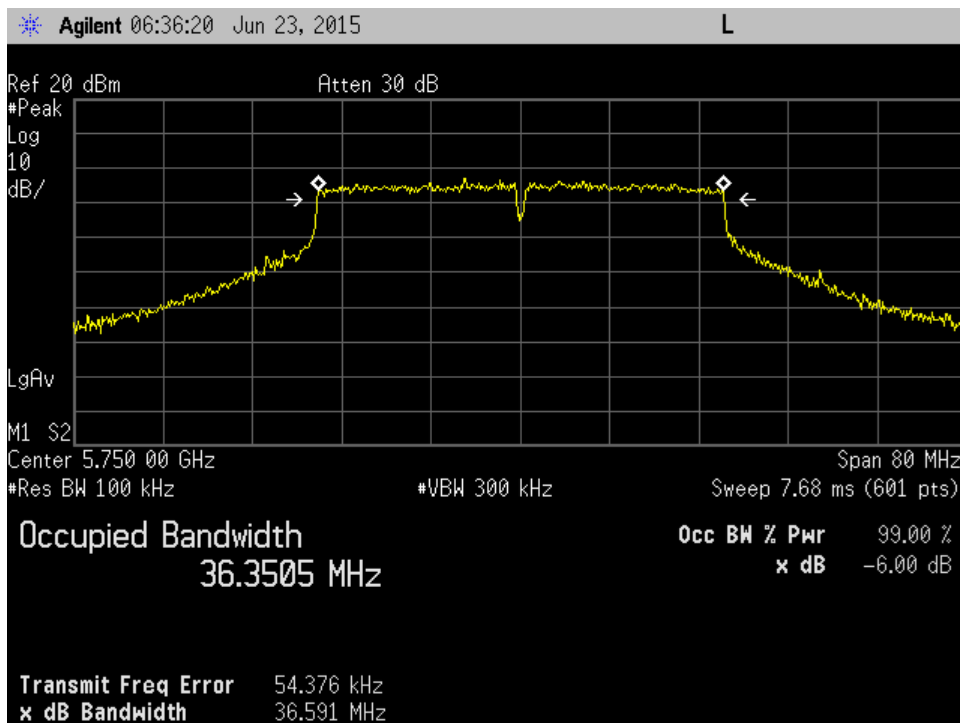




### 40MHz low ch J13

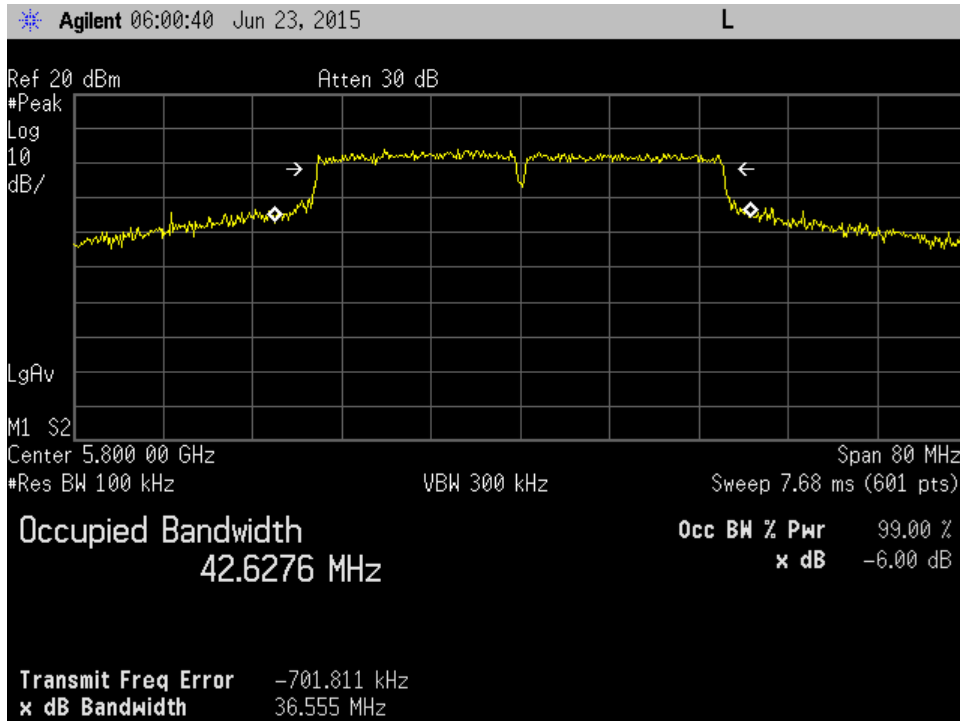


### 40MHz low ch J7

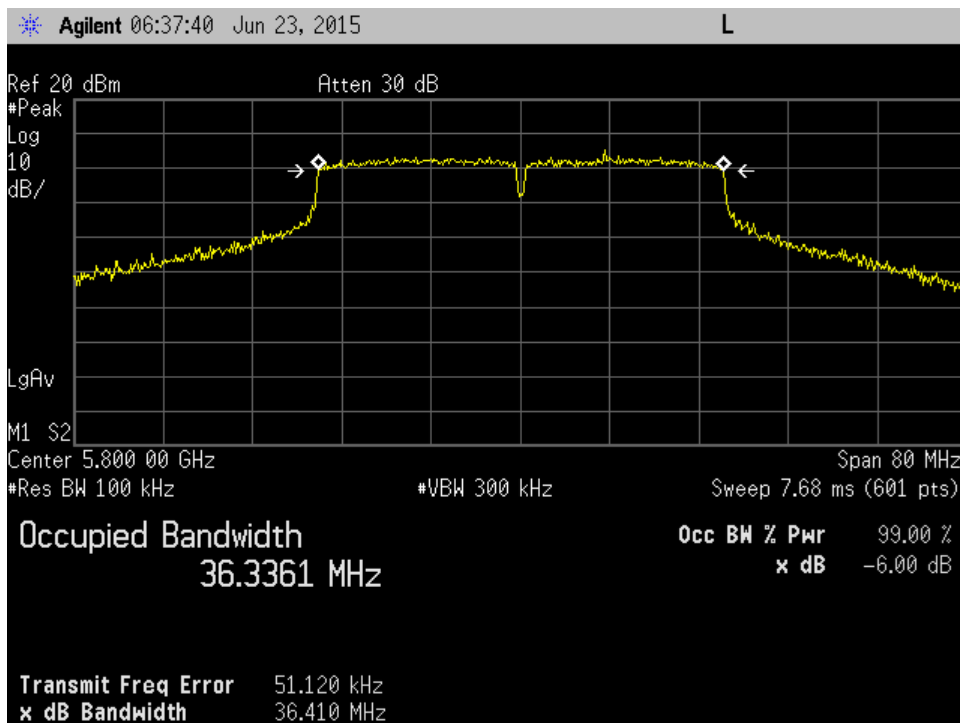




### 40MHz mid ch J13

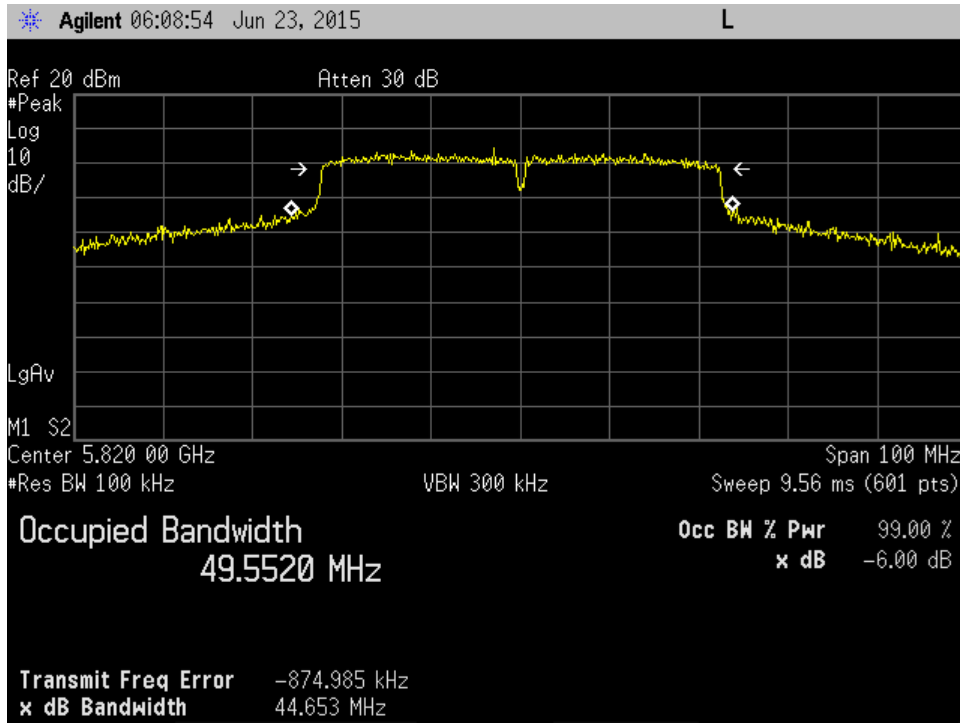


### 40MHz mid ch J7

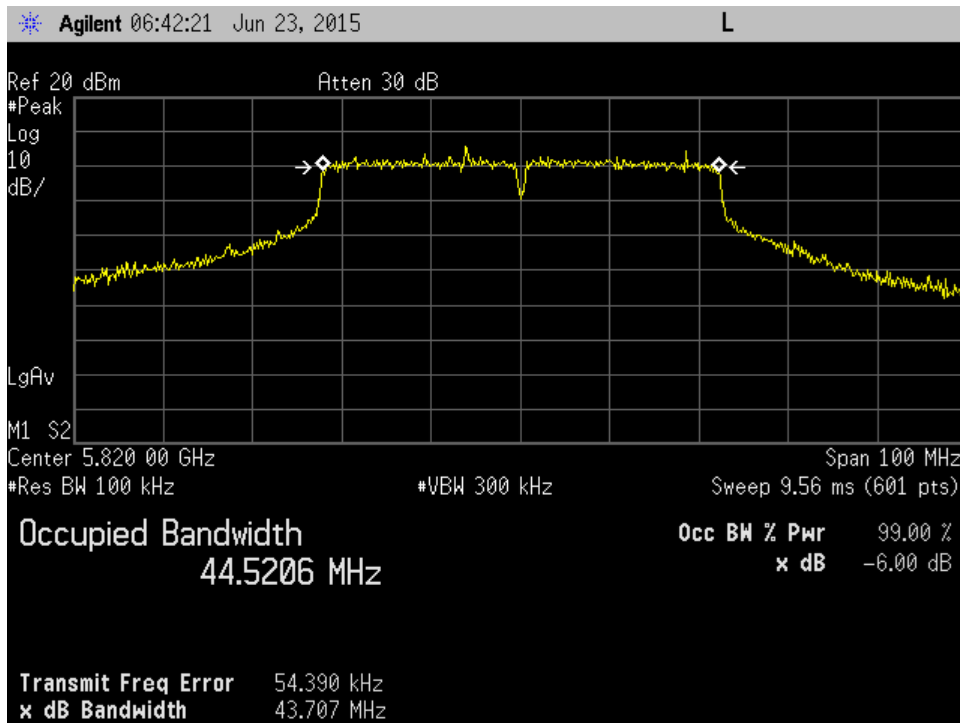




### 50MHz high ch J13

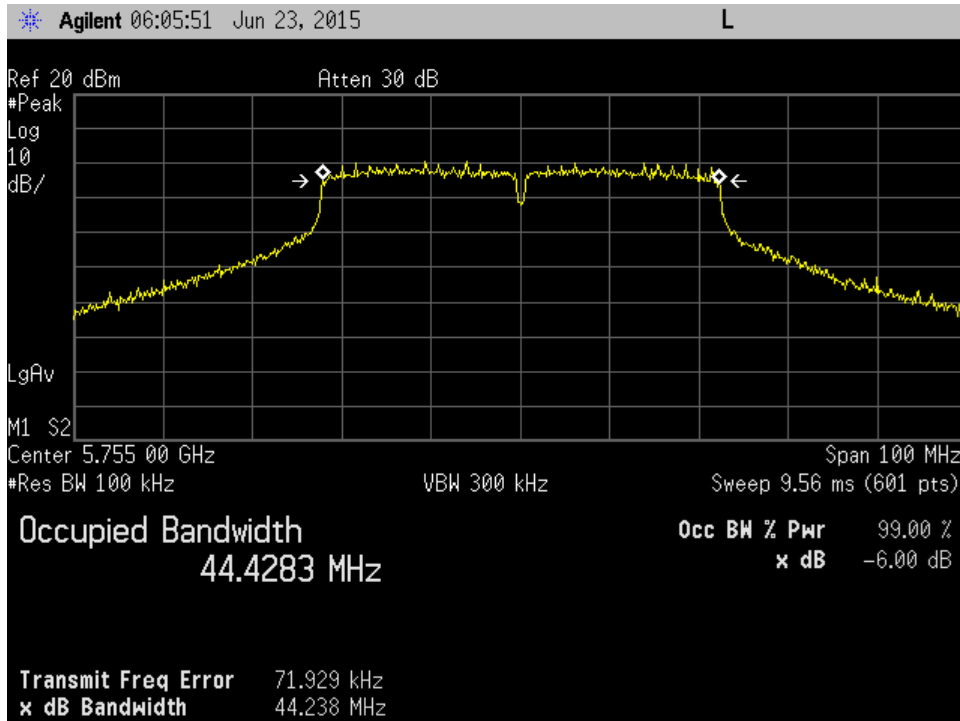


### 50MHz high ch J7

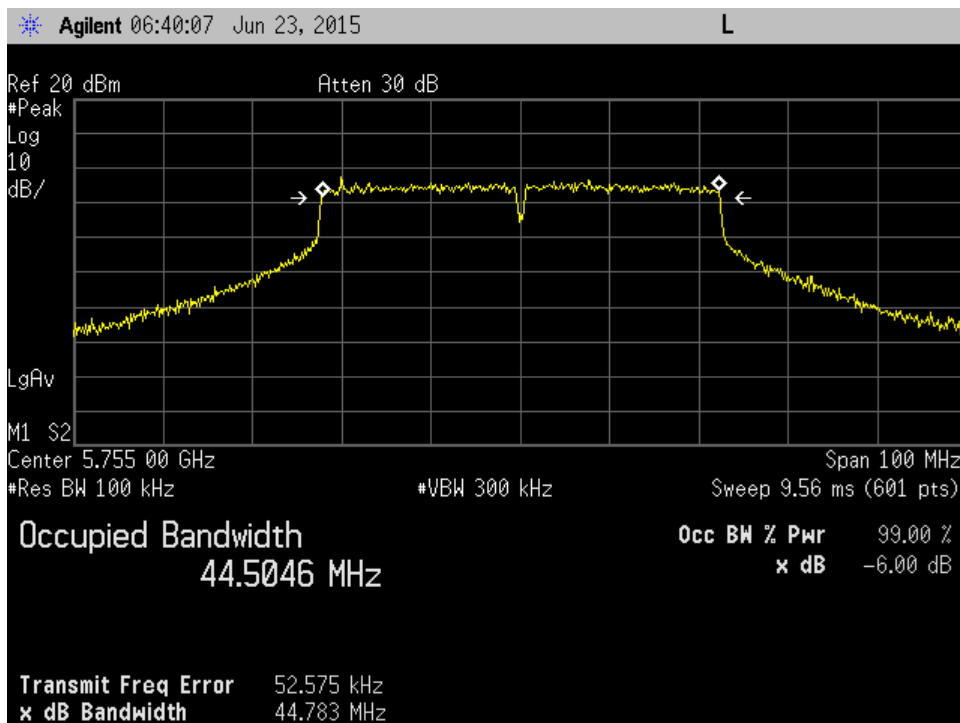




### 50MHz low ch J13

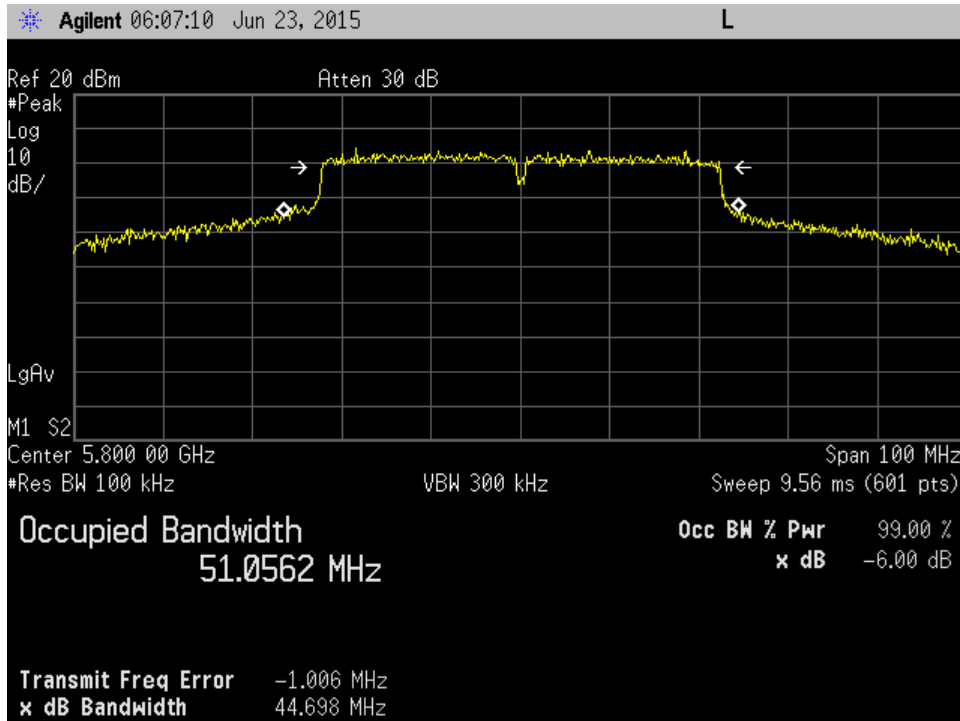


### 50MHz low ch J7

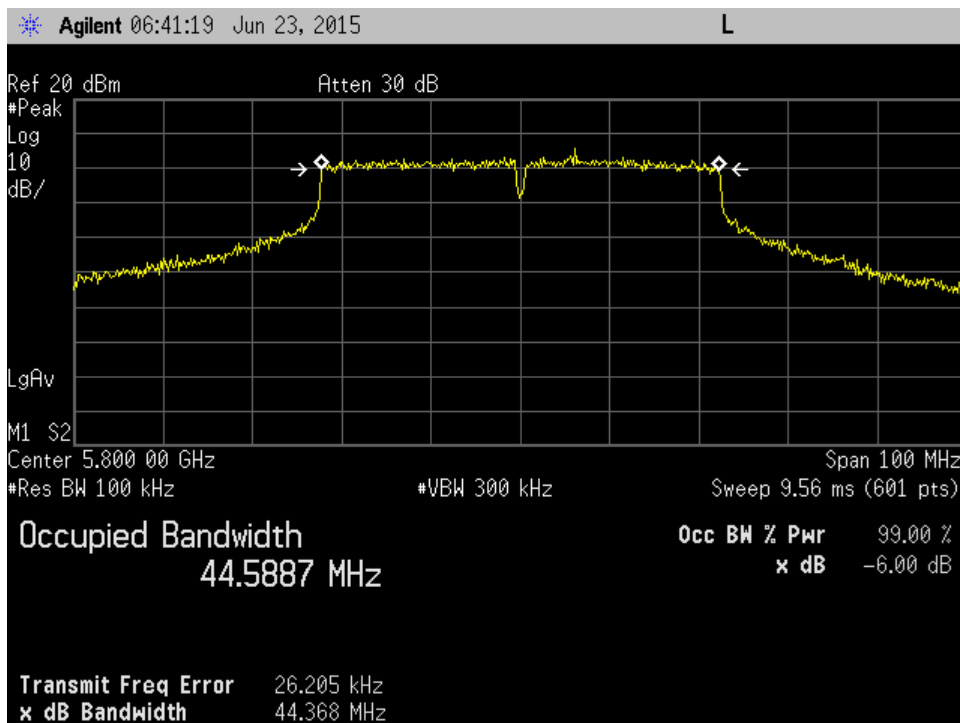




### 50MHz mid ch J13



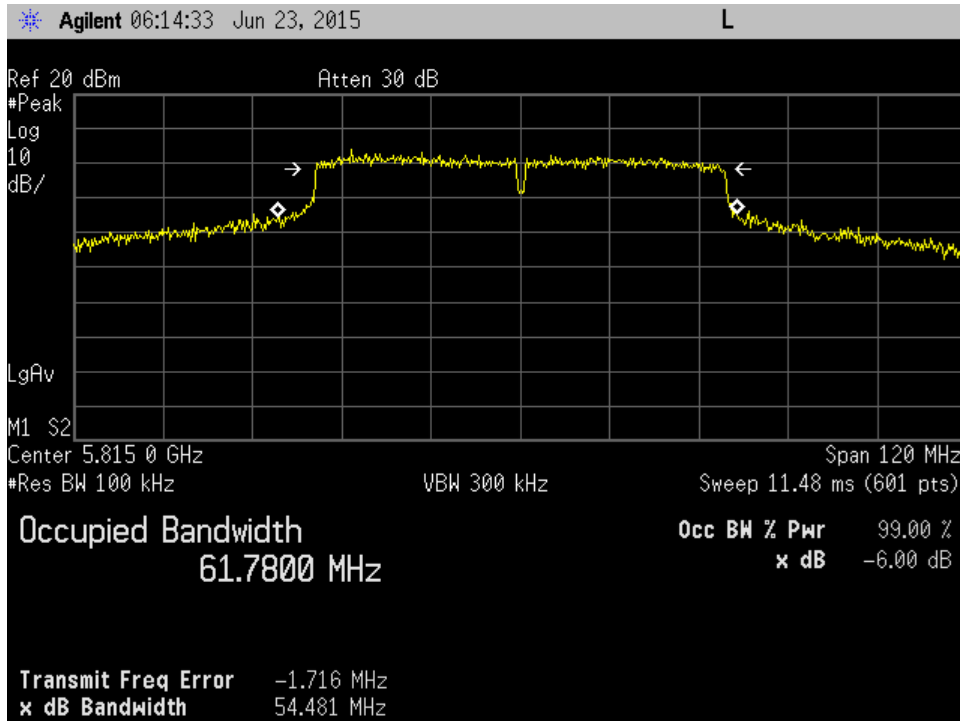
### 50MHz mid ch J7



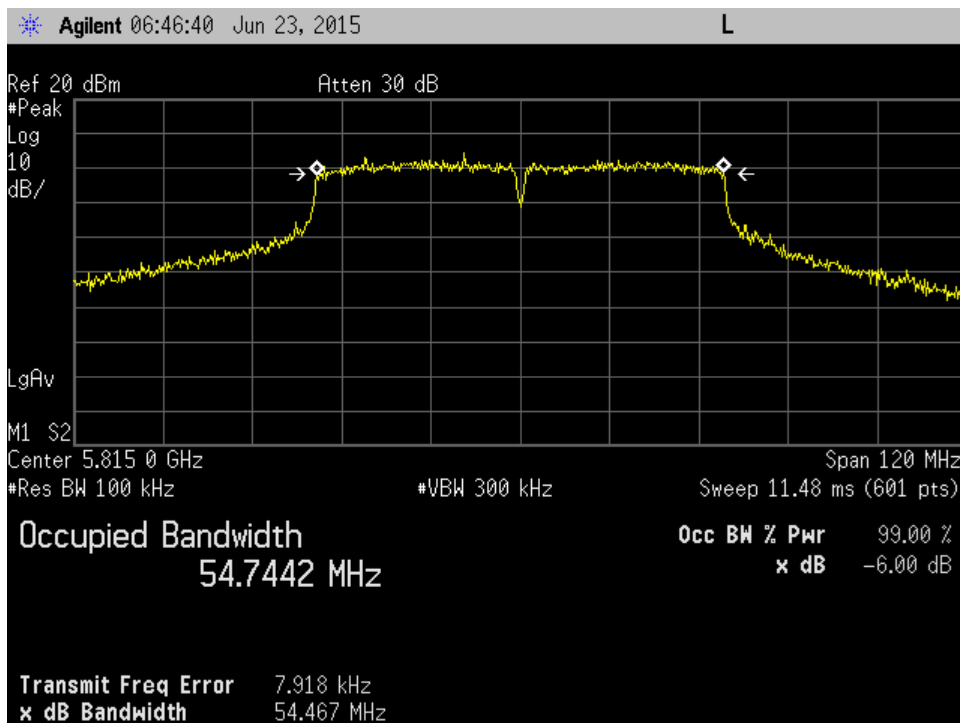




### 60MHz high ch J13

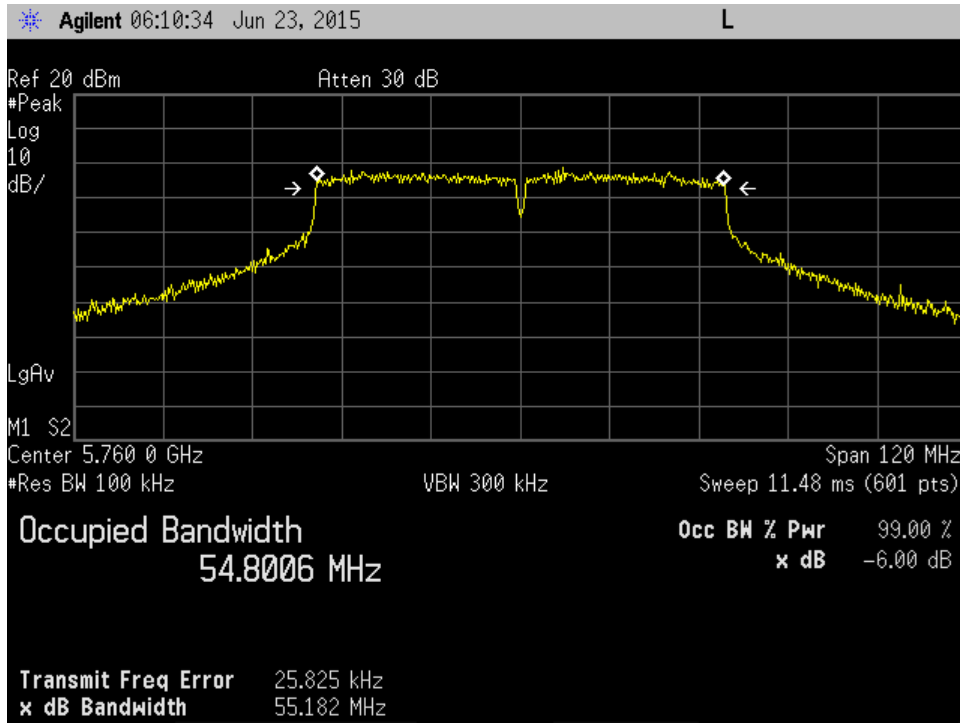


### 60MHz high ch J7

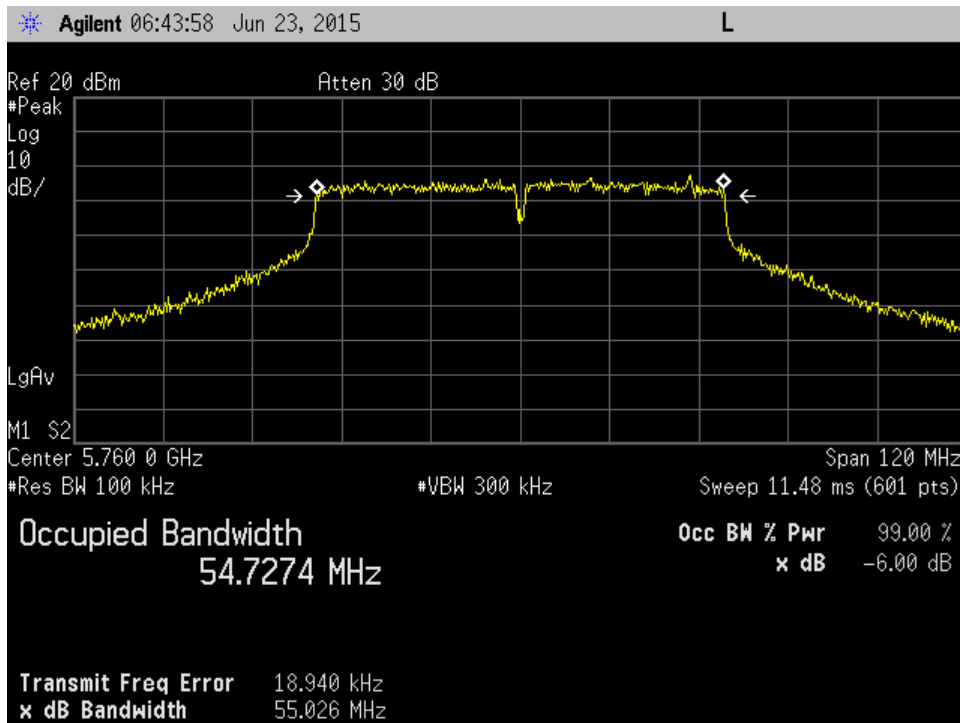




### 60MHz low ch J13

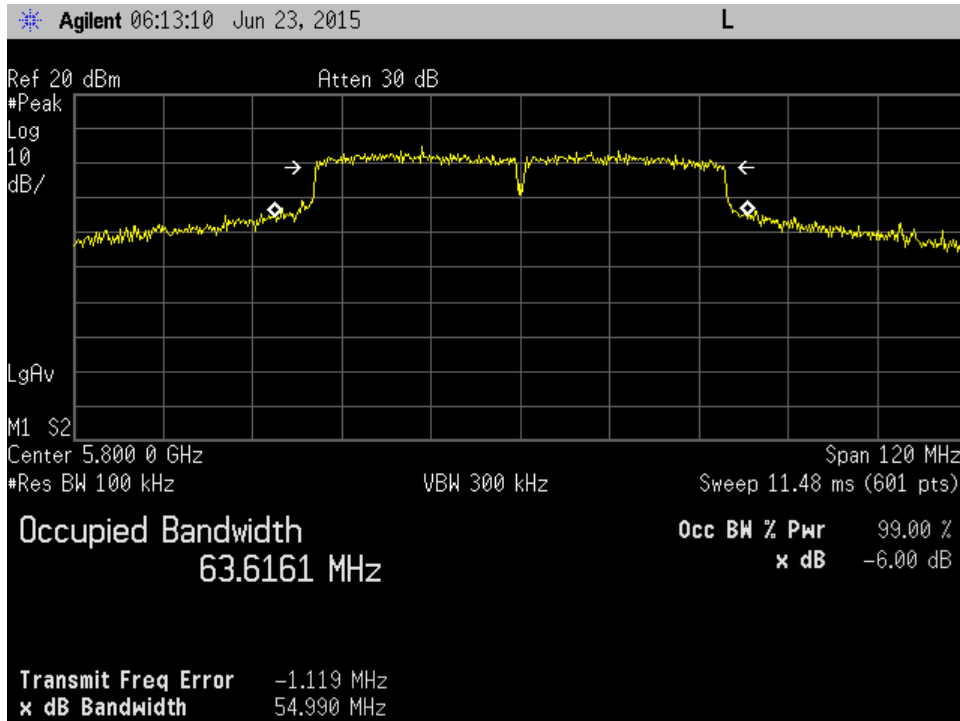


### 60MHz low ch J7

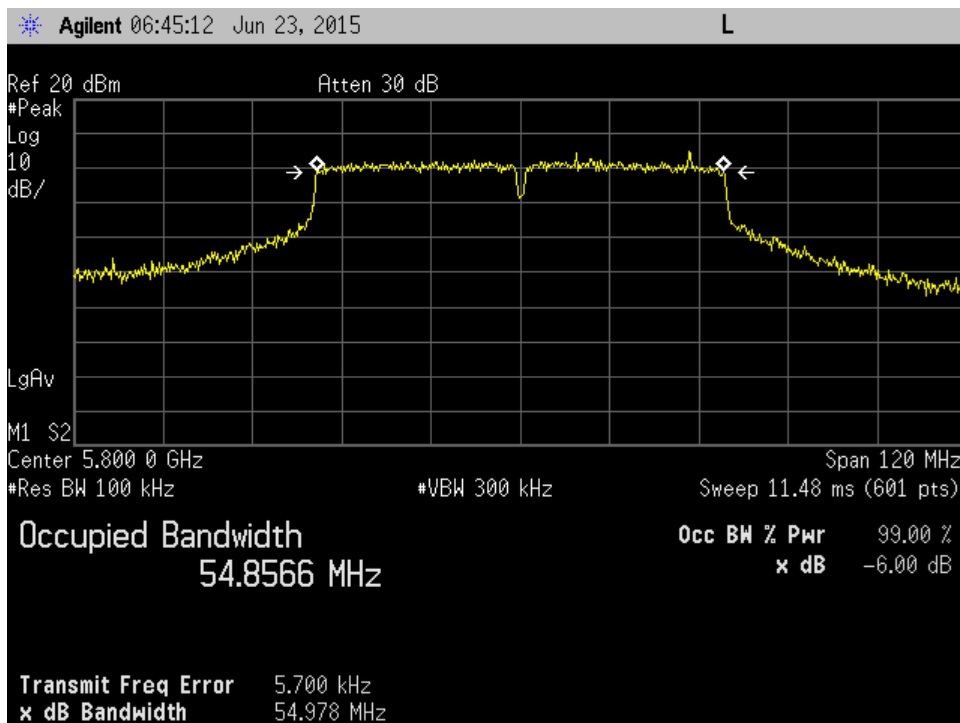




**60MHz mid ch J13**

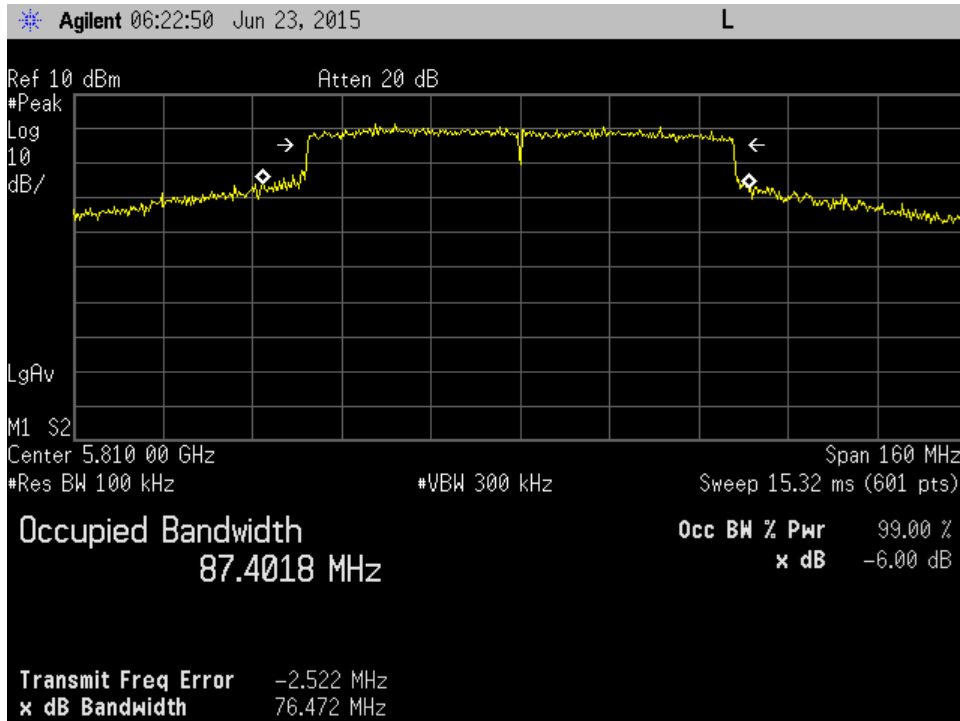


**60MHz mid ch J7**

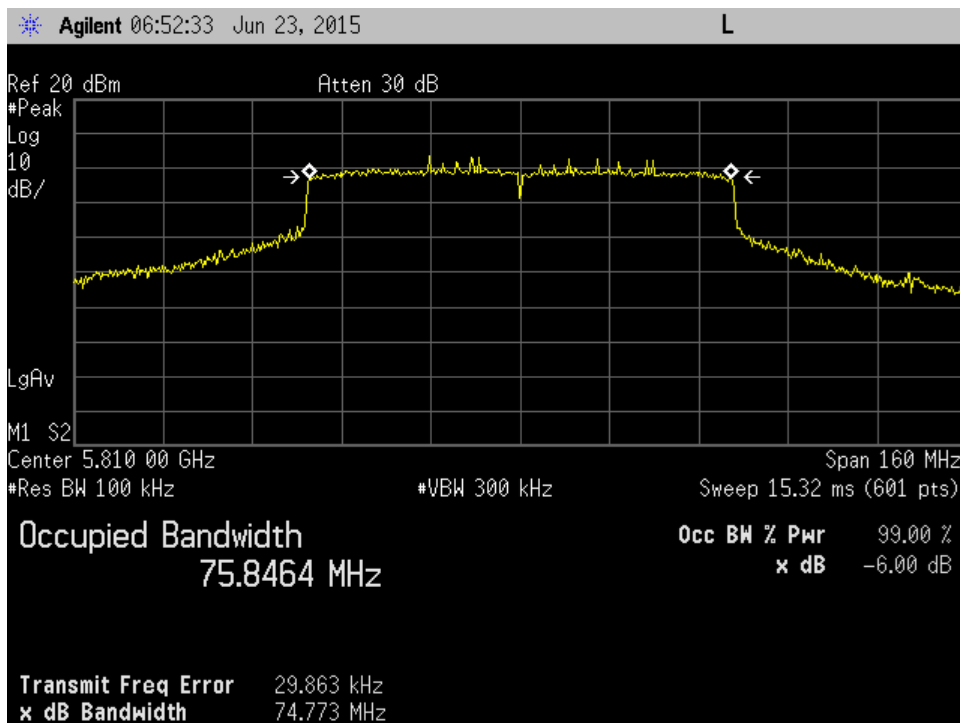




### 80MHz high ch J13

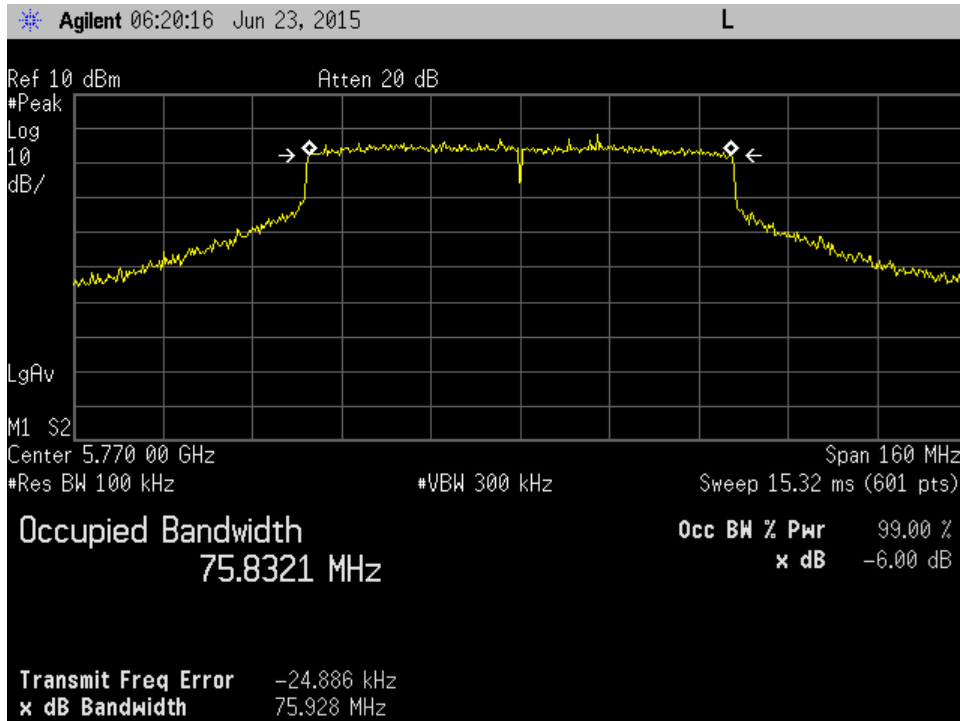


### 80MHz high ch J7

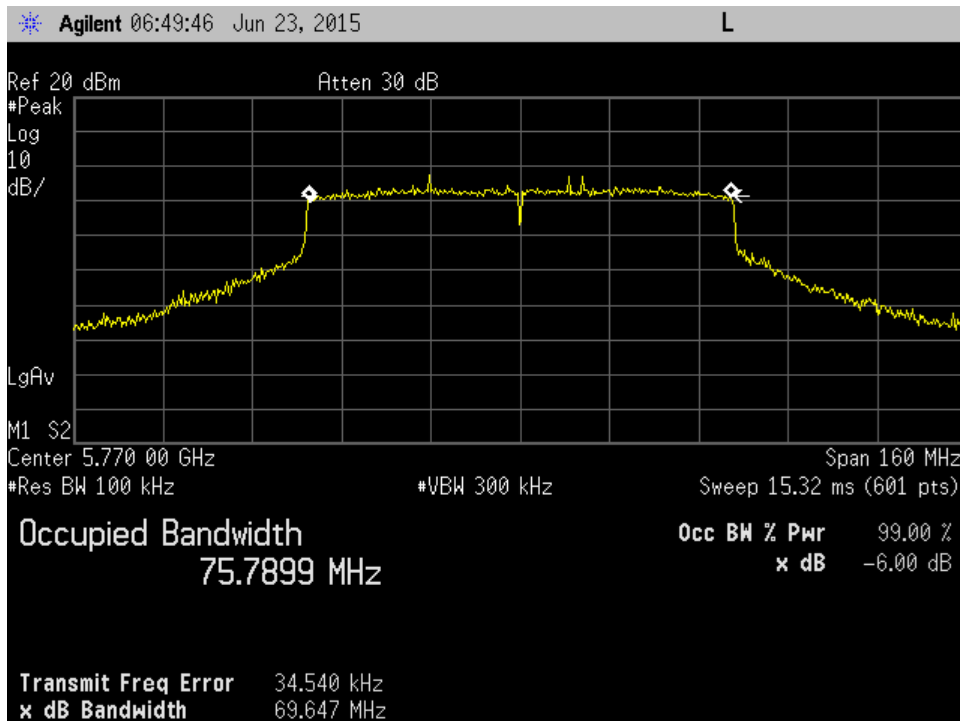




### 80MHz low ch J13

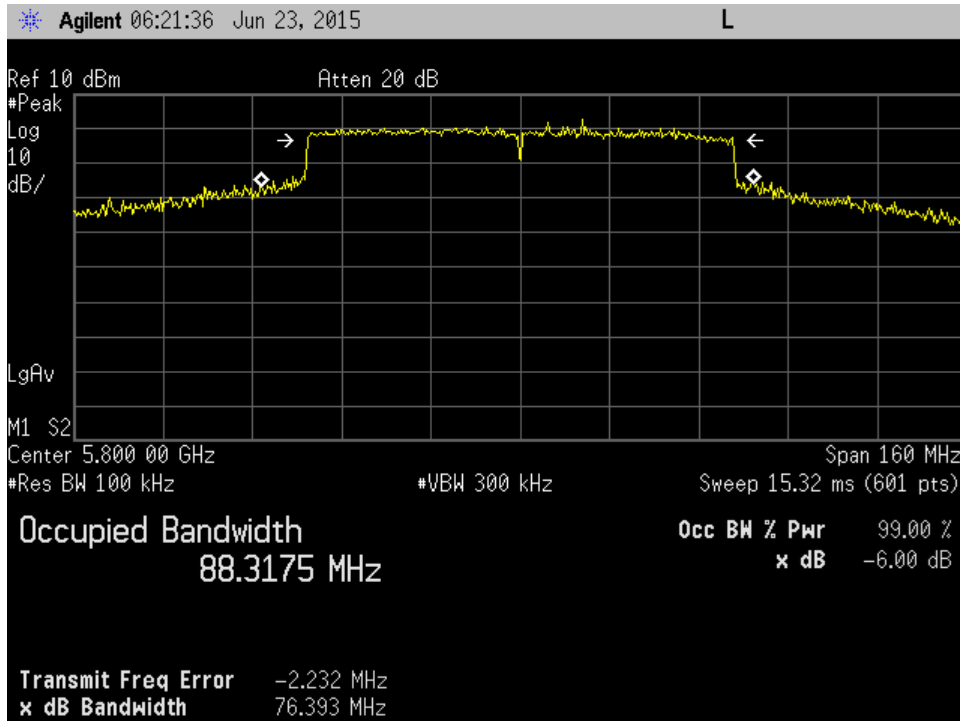


### 80MHz low ch J7





### 80MHz mid ch J13



### 80MHz mid ch J7

