

REVISIONS

REV	DESCRIPTION	CHECKED	DATE	CONF MGMT	DATE
1					

# 1. Block Diagram of C4L5200-ETH

A block level architecture of the C4L5200-ETH Ethernet-to-Powerline Bridge is shown in Figure 1-1.

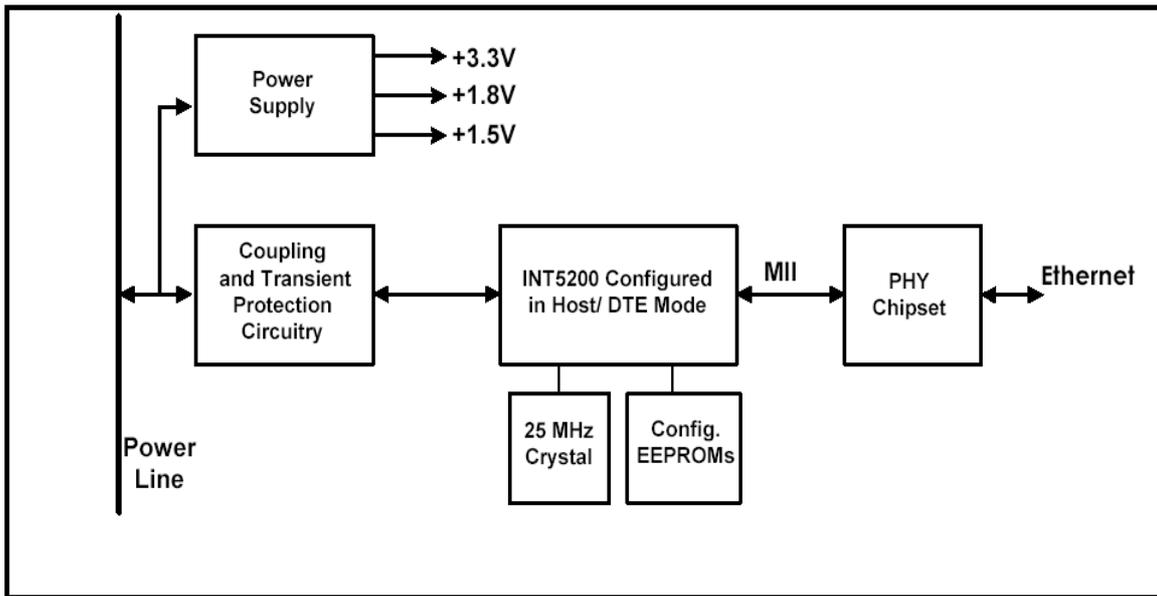


Figure 1-1. Block diagram of the C4L5200-ETH Ethernet-to-Powerline Bridge

		ORIGINAL ISSUE APPROVAL			C4Line		
		APPROVALS	DATE		209 Mok-Dong GardenSweet, 907 Mok5-Dong, Yangchun-Gu, Seoul		
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		CHECKED:					
		APPROVED:		S.H. Kim			
		CONF MGMT:					
		UNLESS OTHERWISE SPECIFIED:				Block Diagram of C4L5200-ETH	
NEXT ASSY	USED ON	DIMENSIONS ARE IN INCHES[mm]					
APPLICATION		SIZE:A	FSCM NO:	DWG NO:	26002083	REV 1	
				SCALE: NONE		SHEET: 1 OF 1	