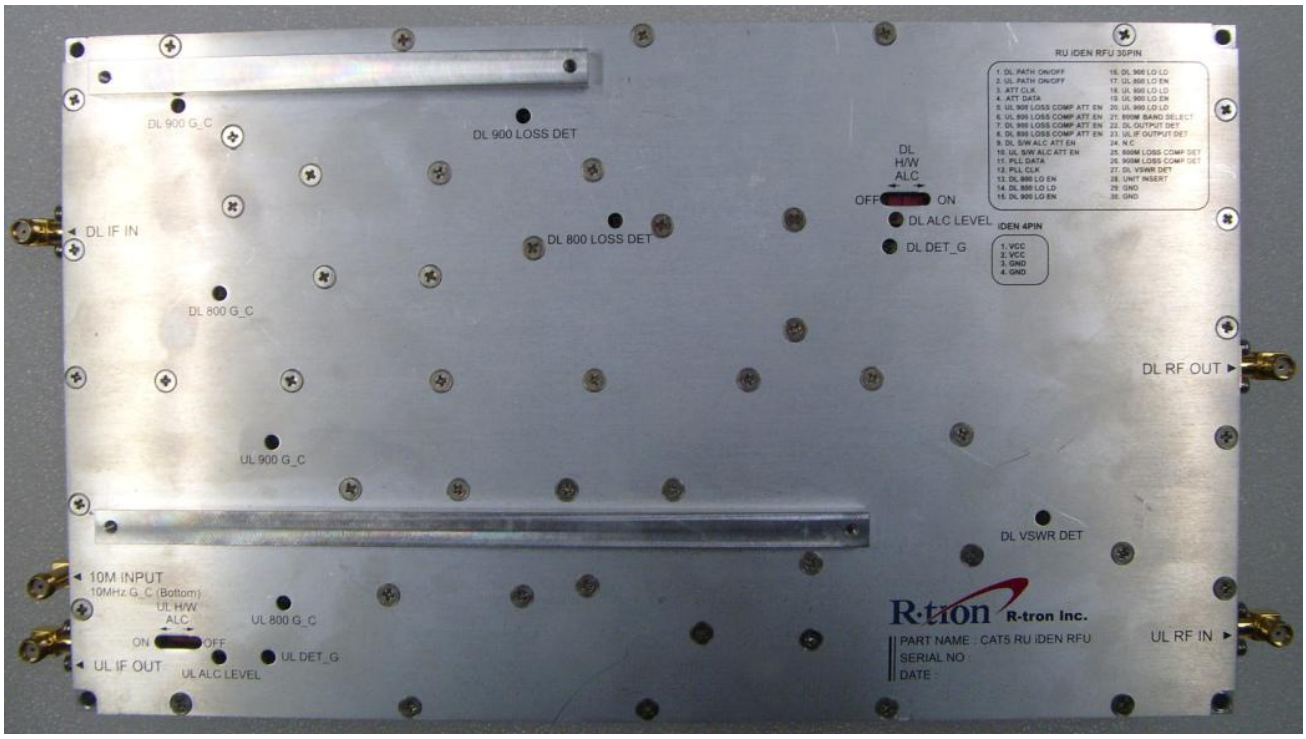
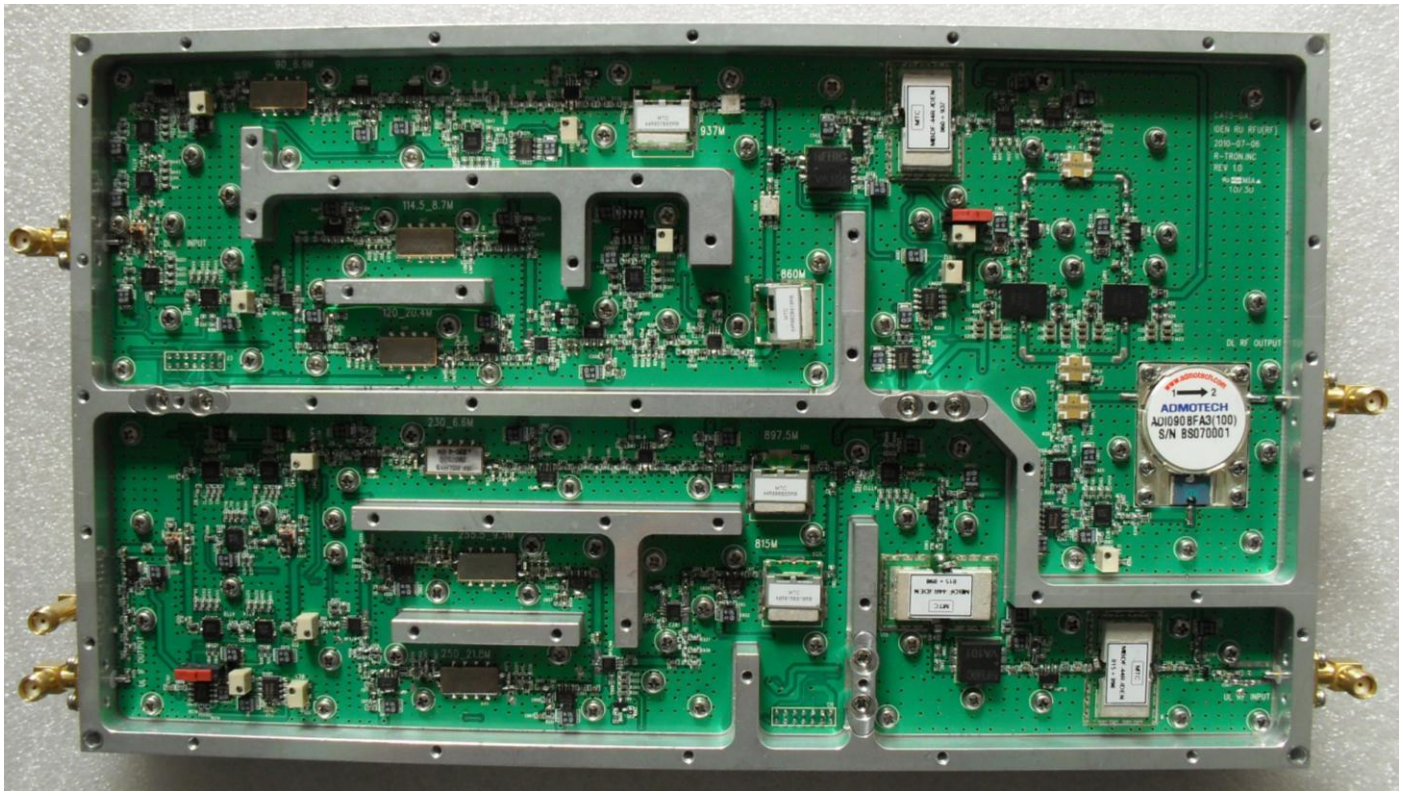
* IDEN RFU *





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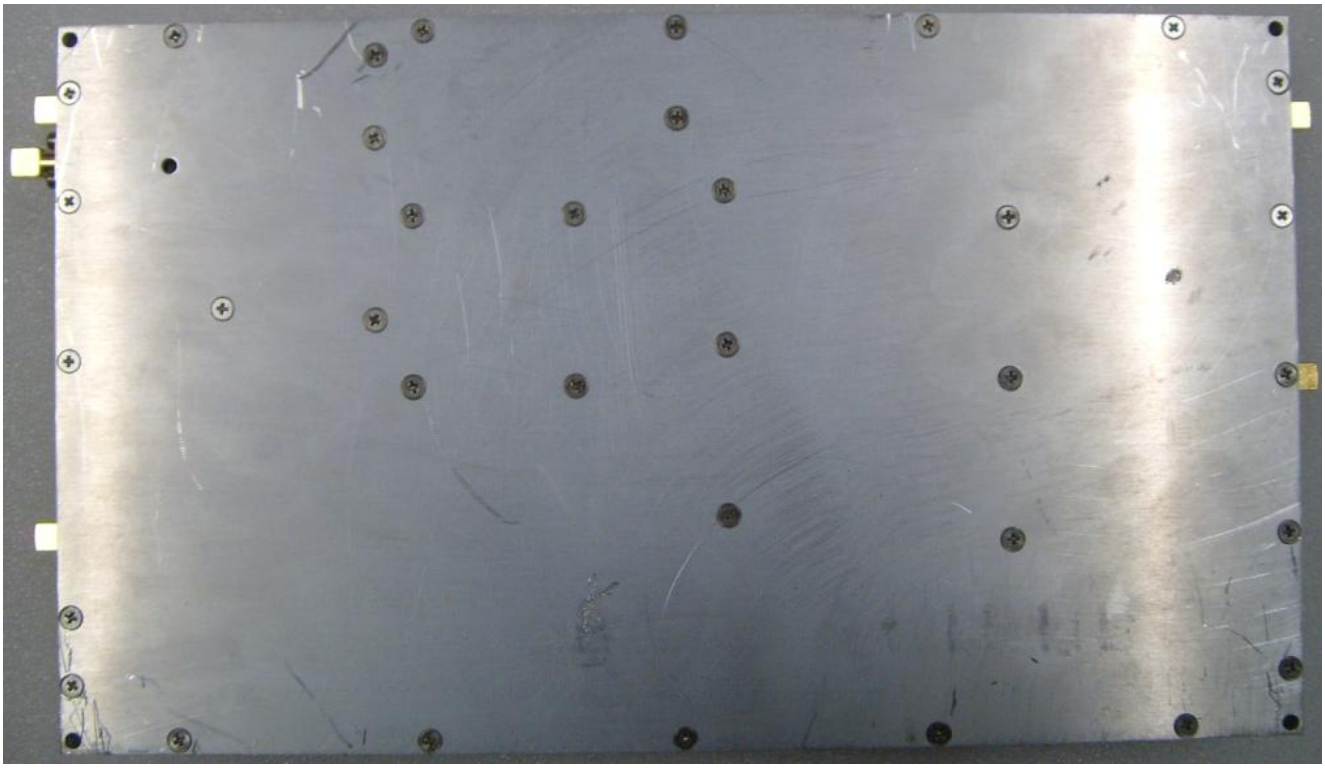

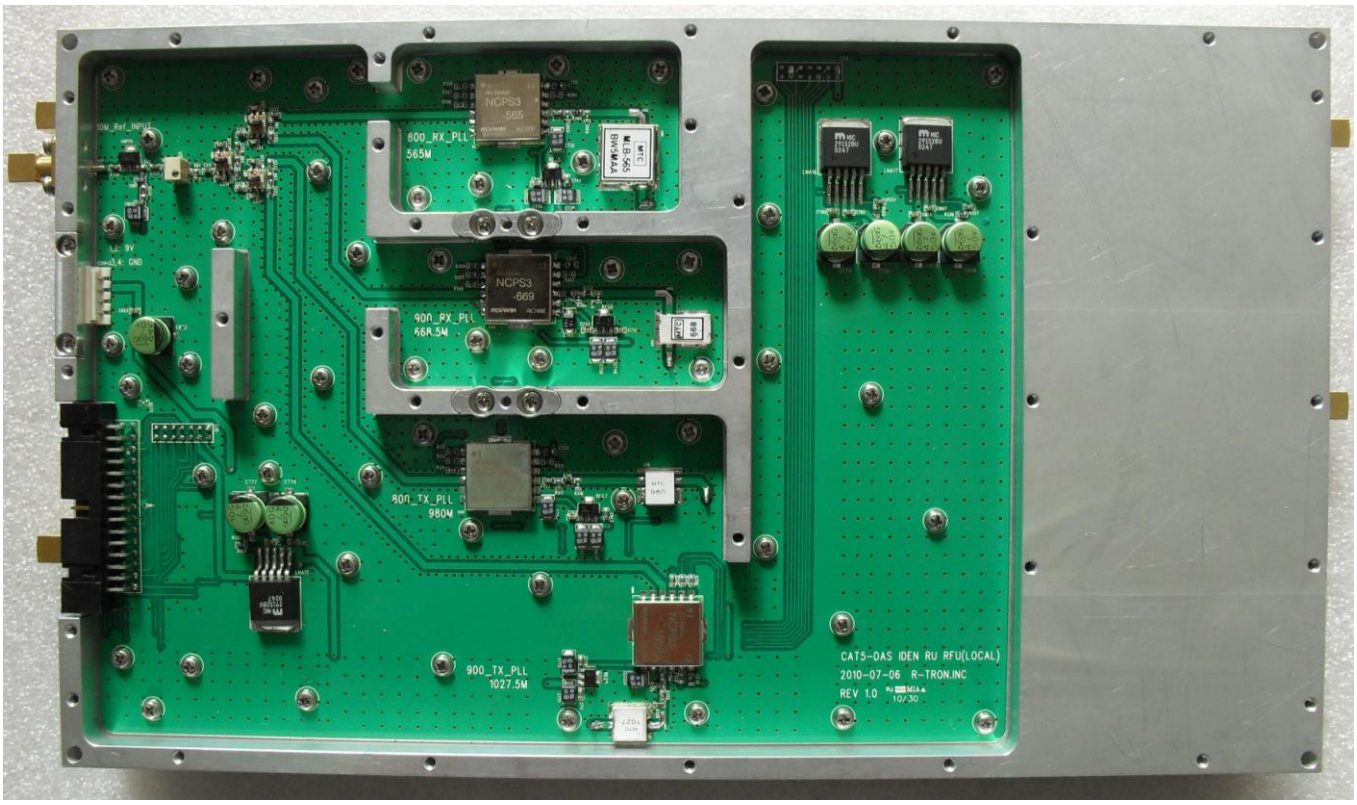

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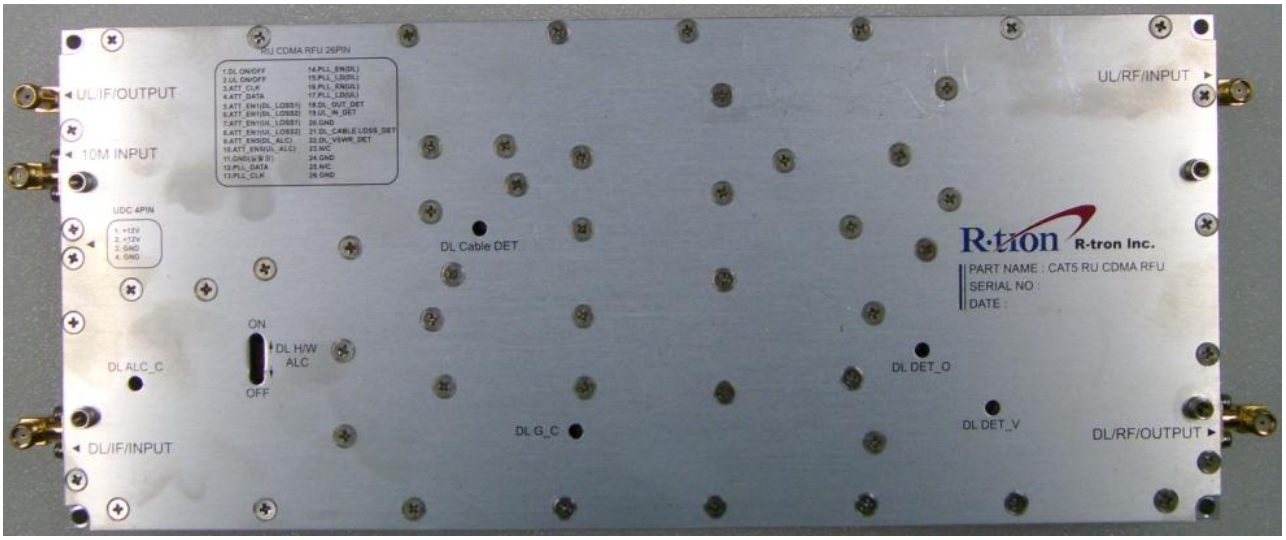

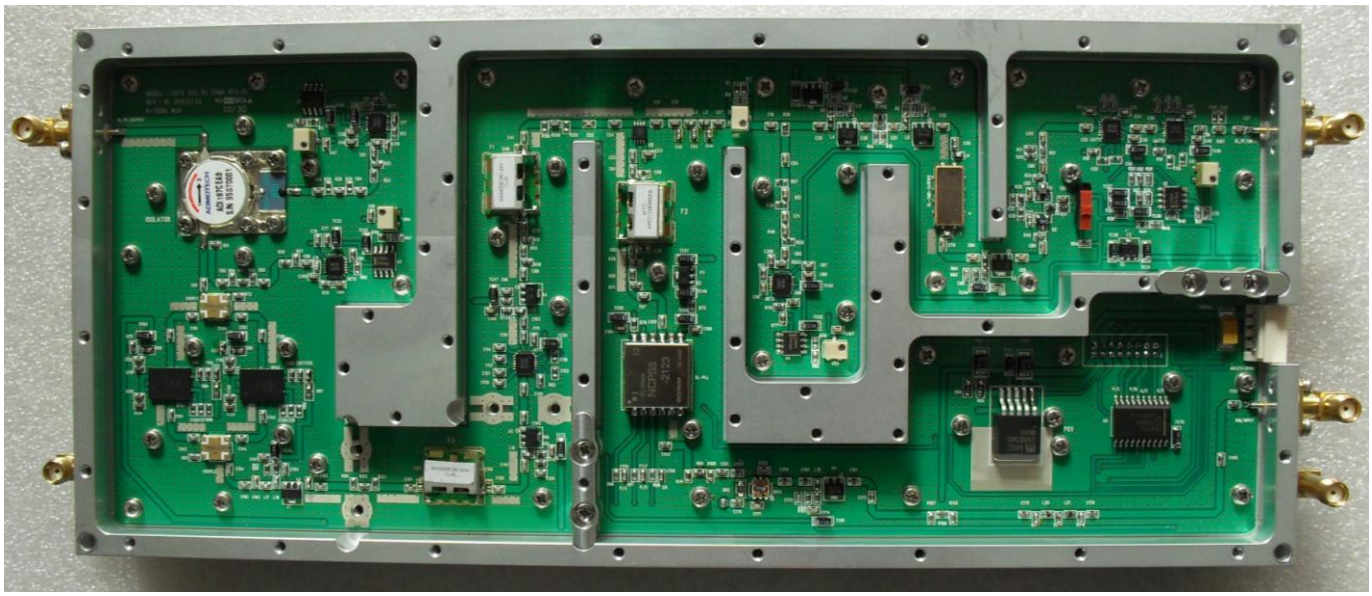

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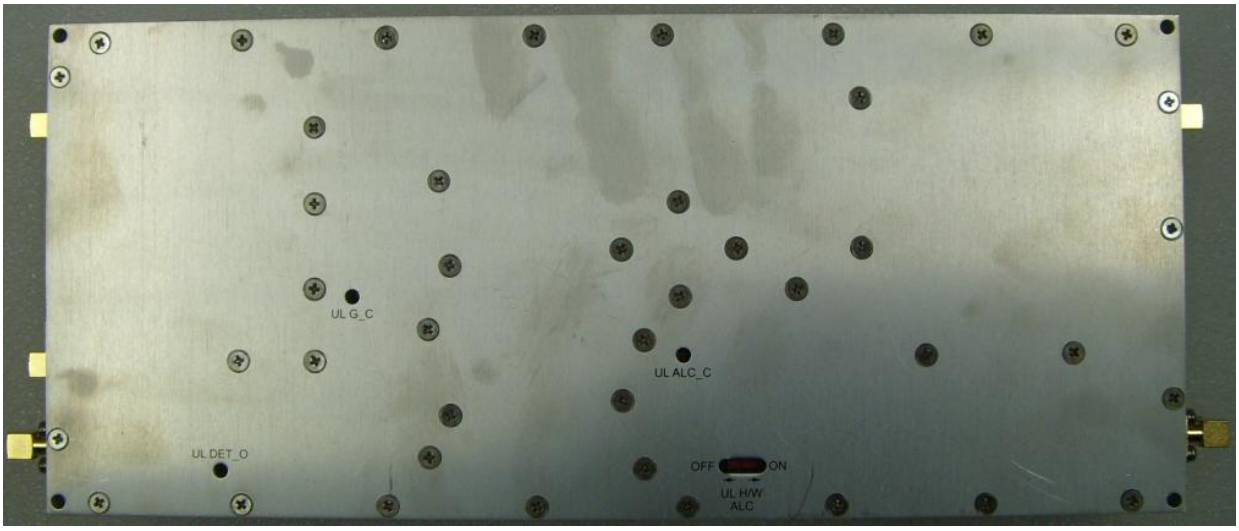

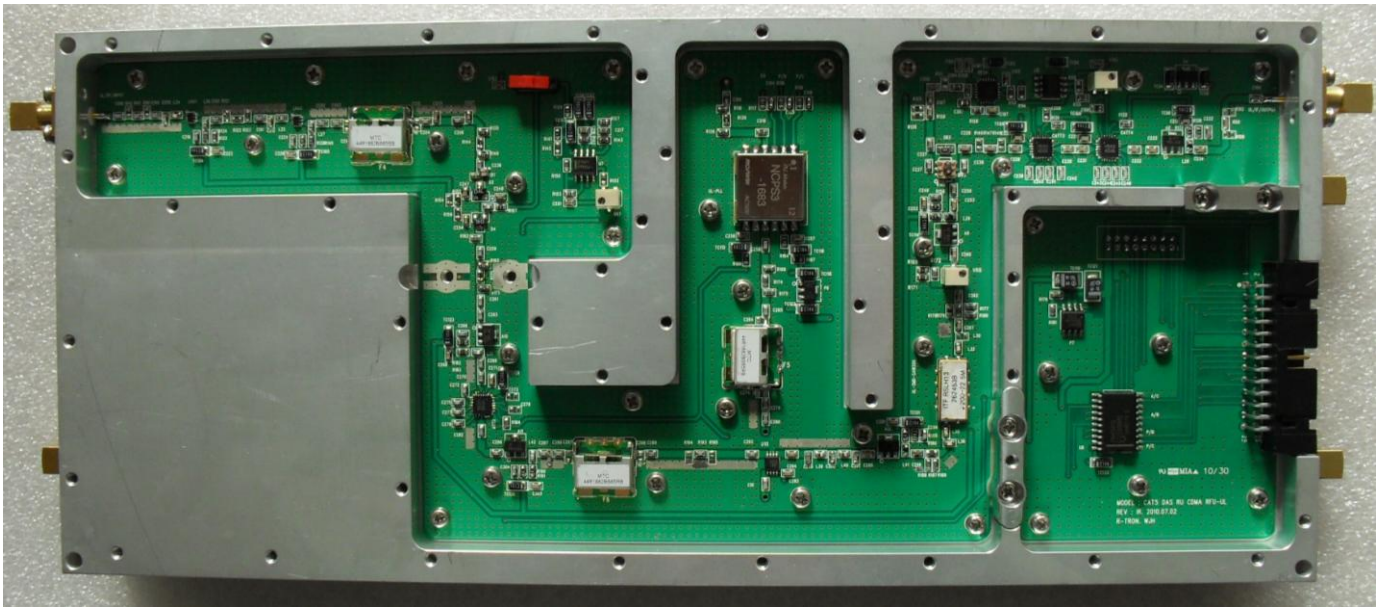

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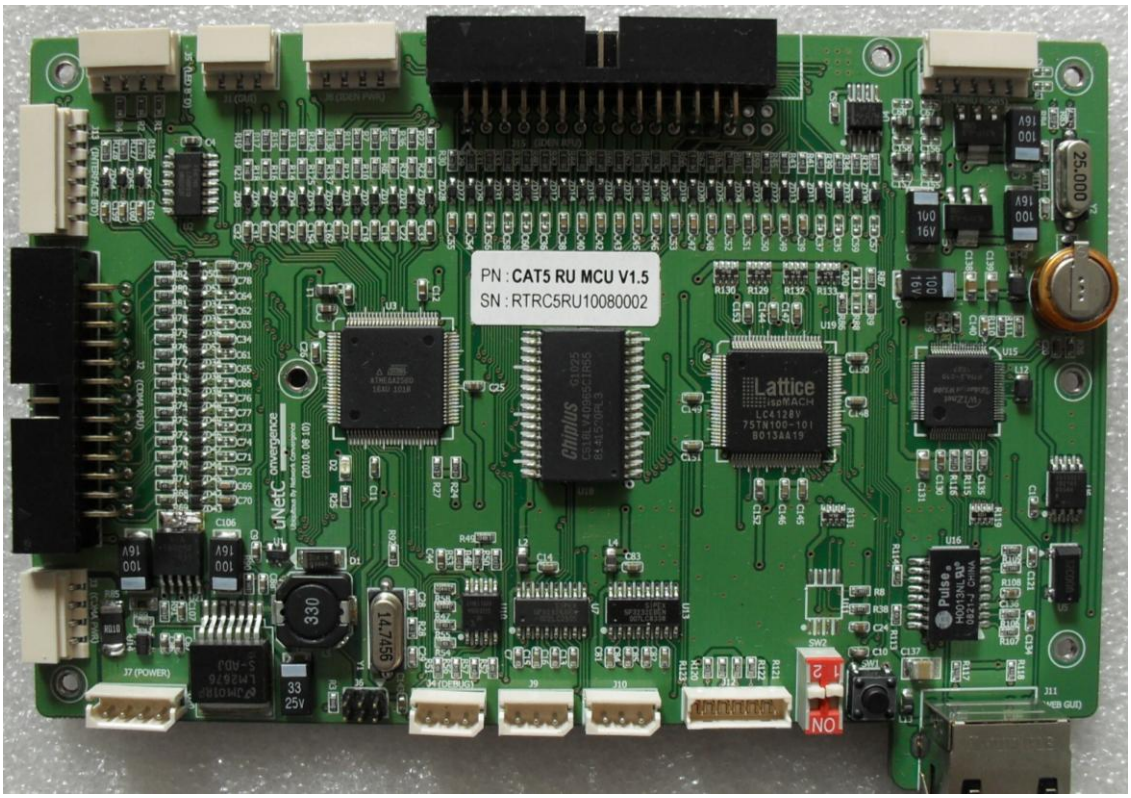

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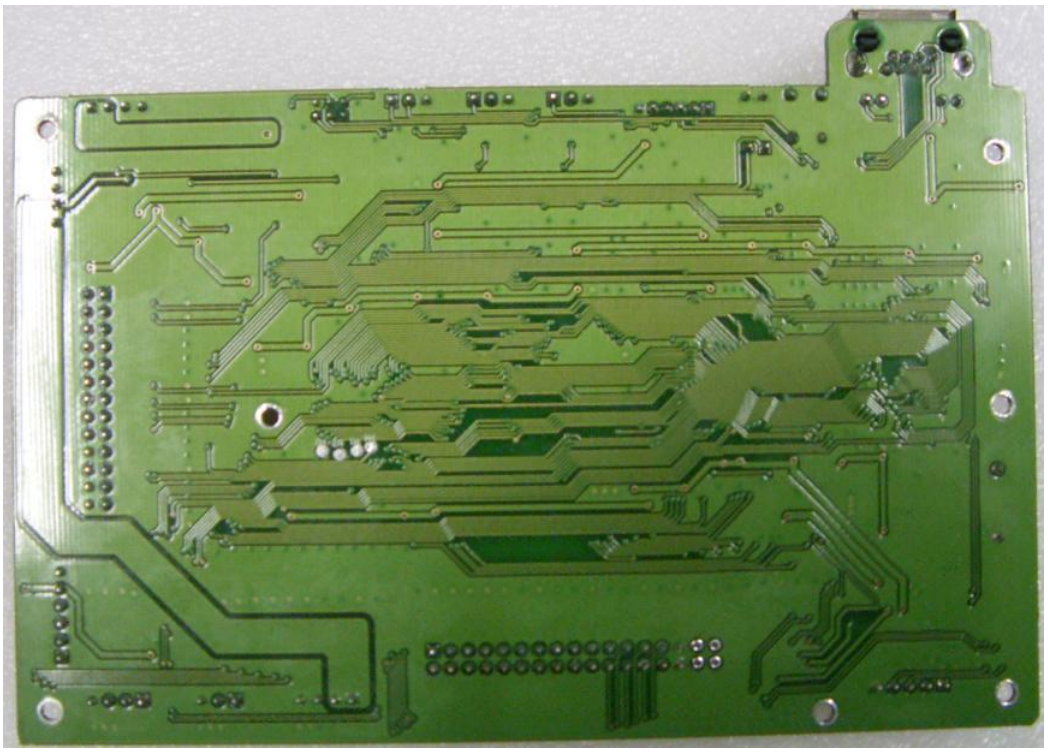

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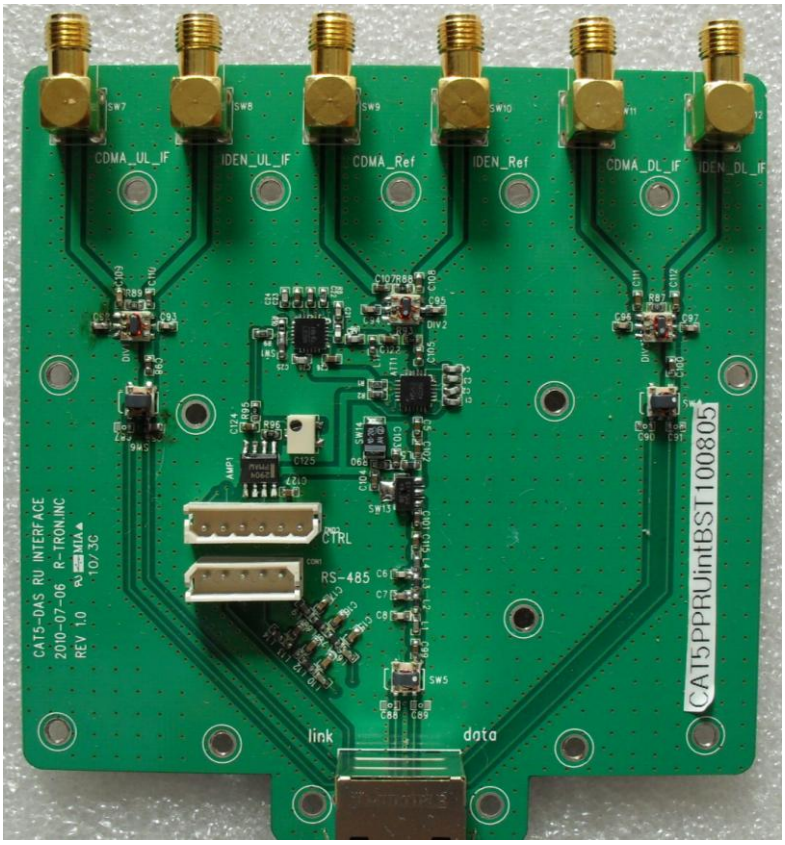
Interface Board



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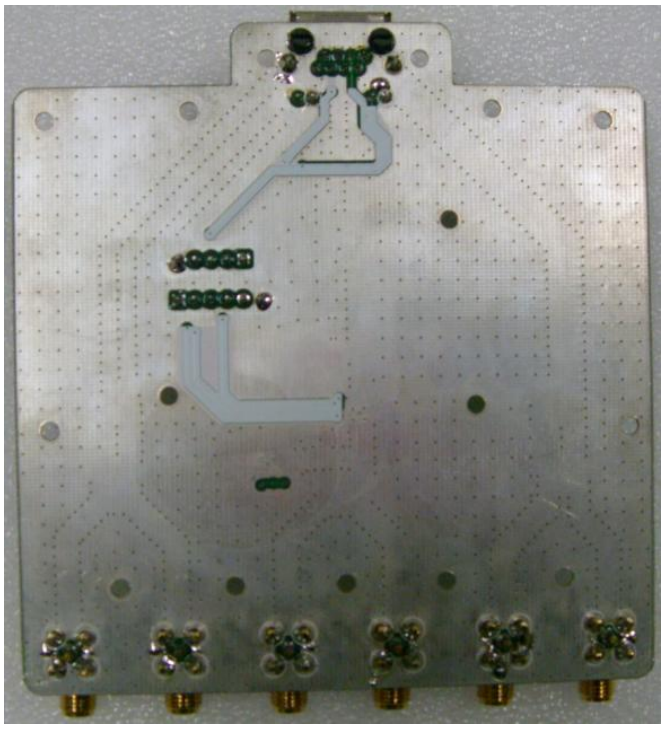

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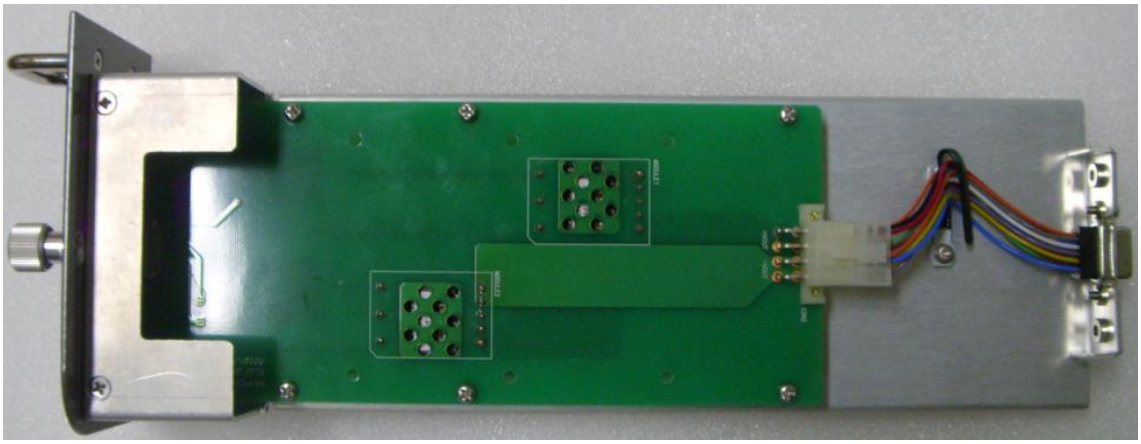
PD

< PD Front view>



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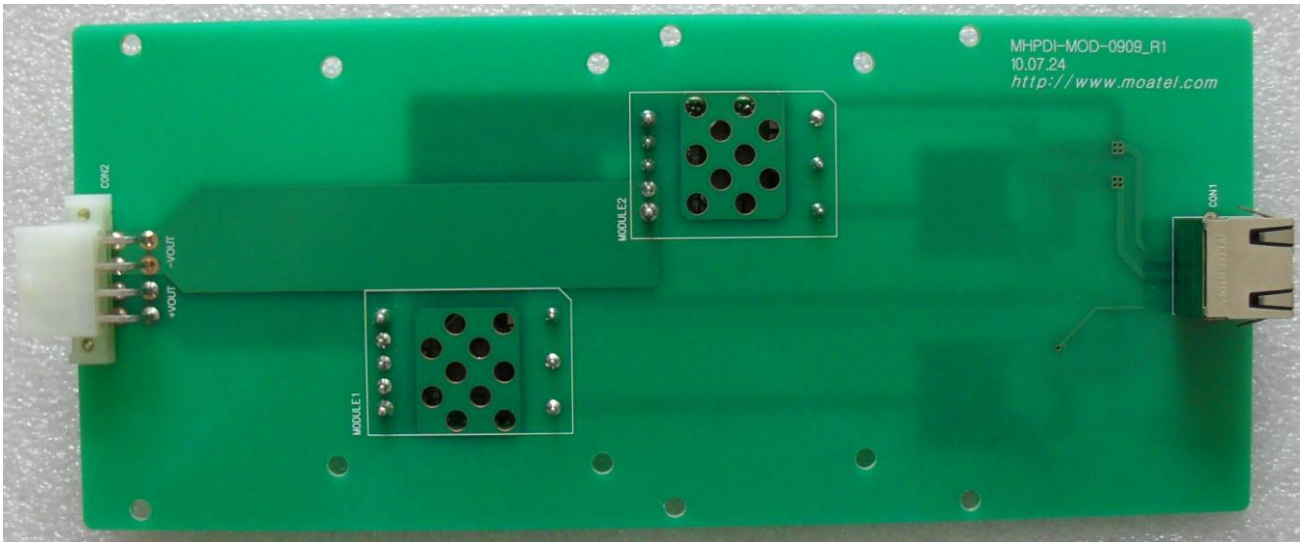

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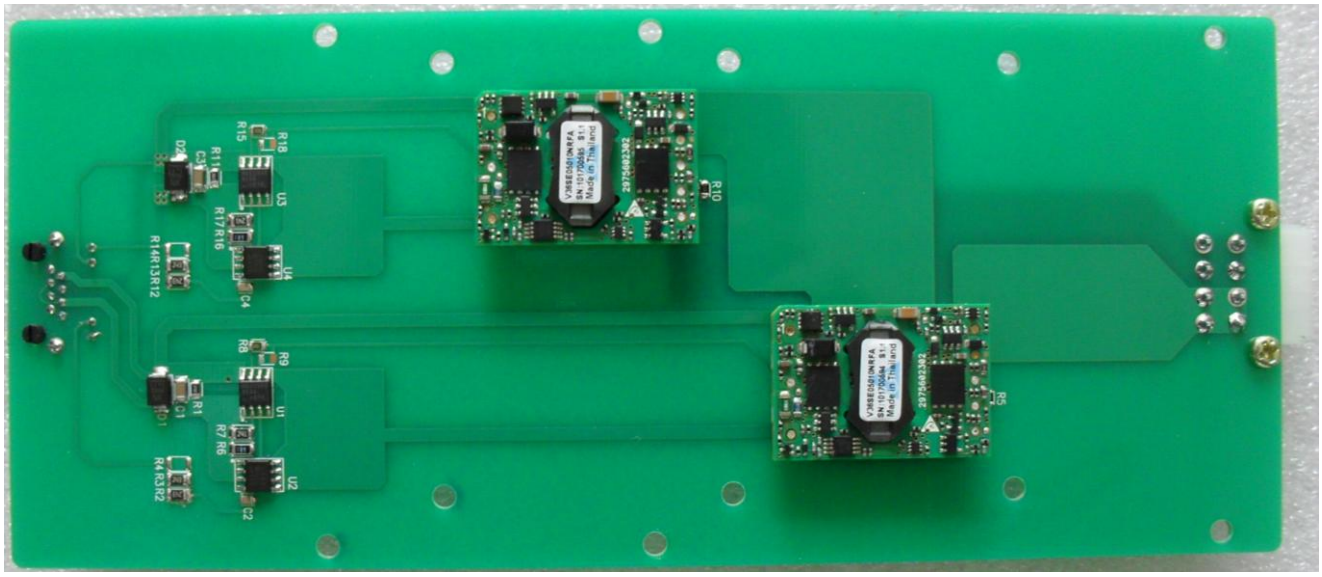

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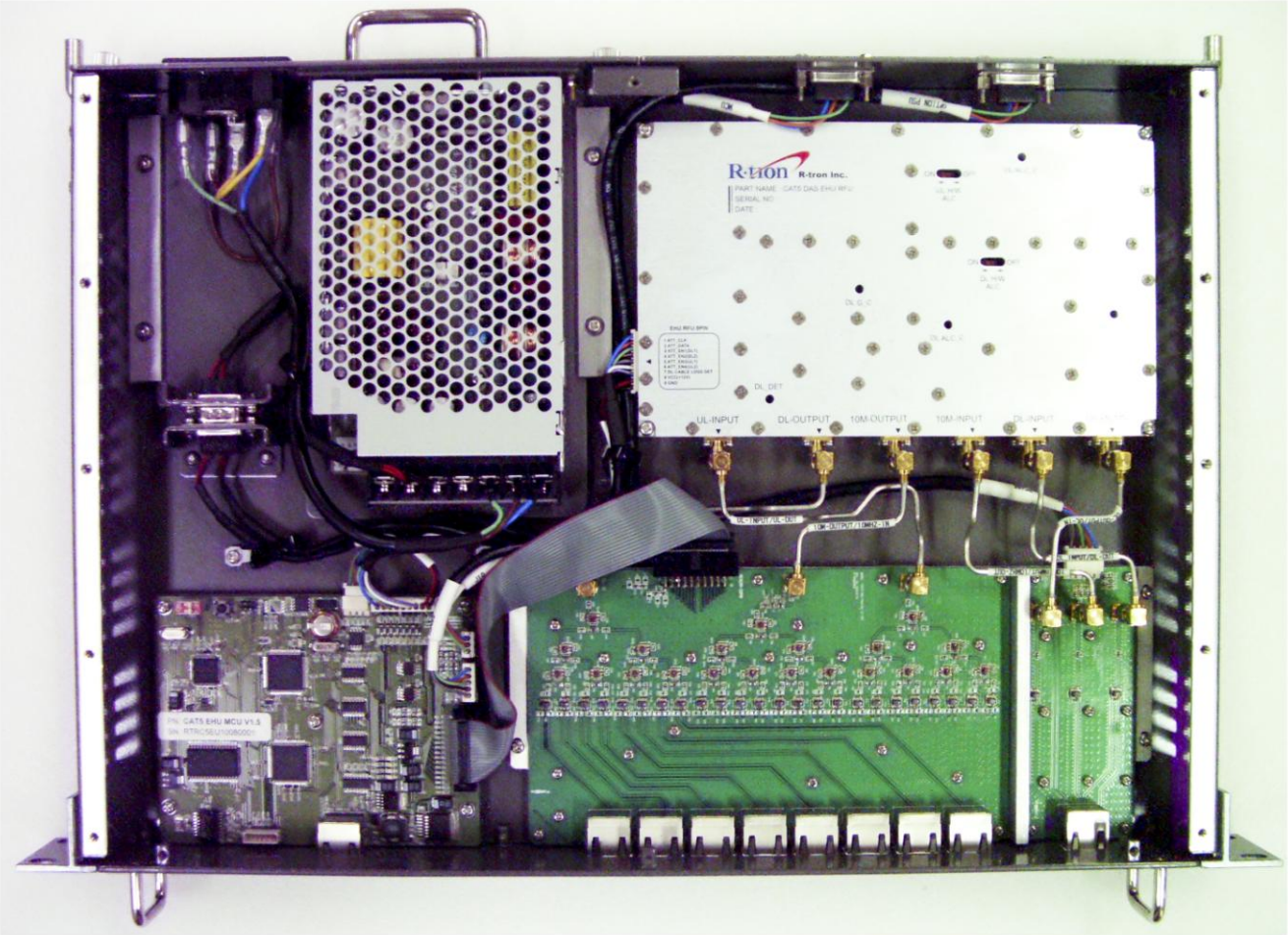
[PD PCB]

3. EHU

<EHU Front view>

<EHU Rear view>

<EHU Top view>



<The inside of EHU>

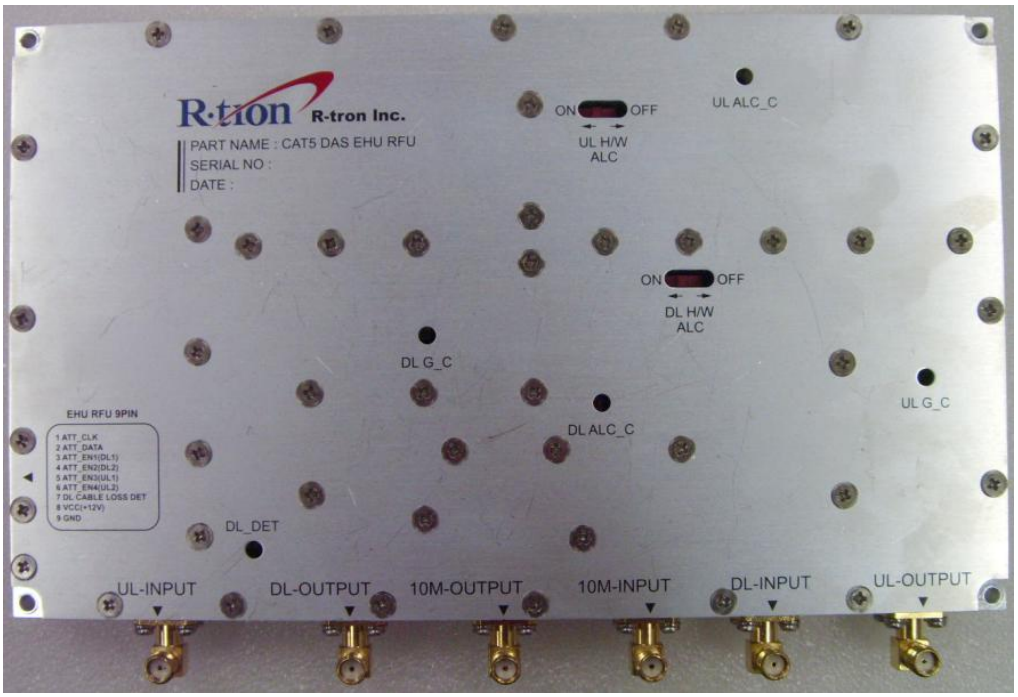
- EHU RFU

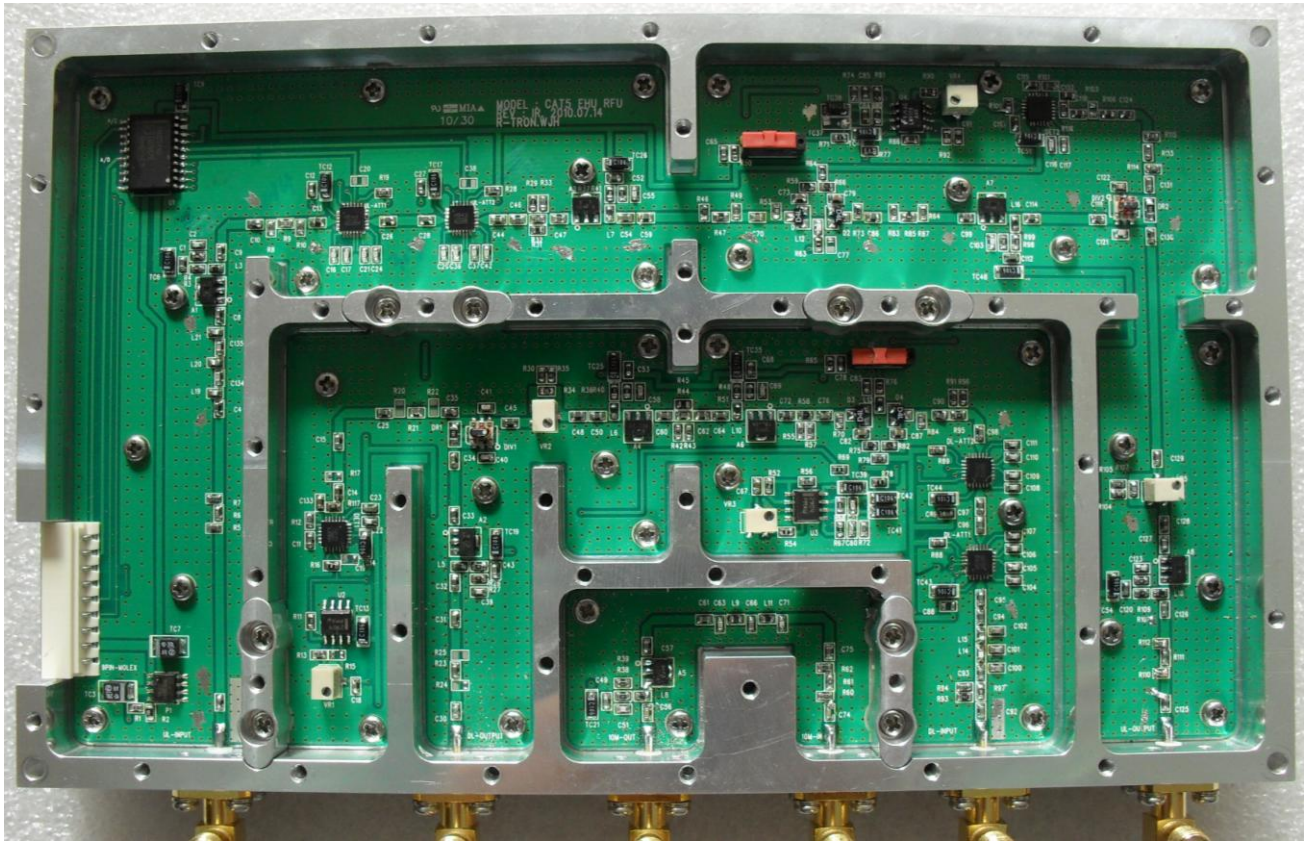
- MCU

- Interface Board

- PSU

*** EHU RFU ***





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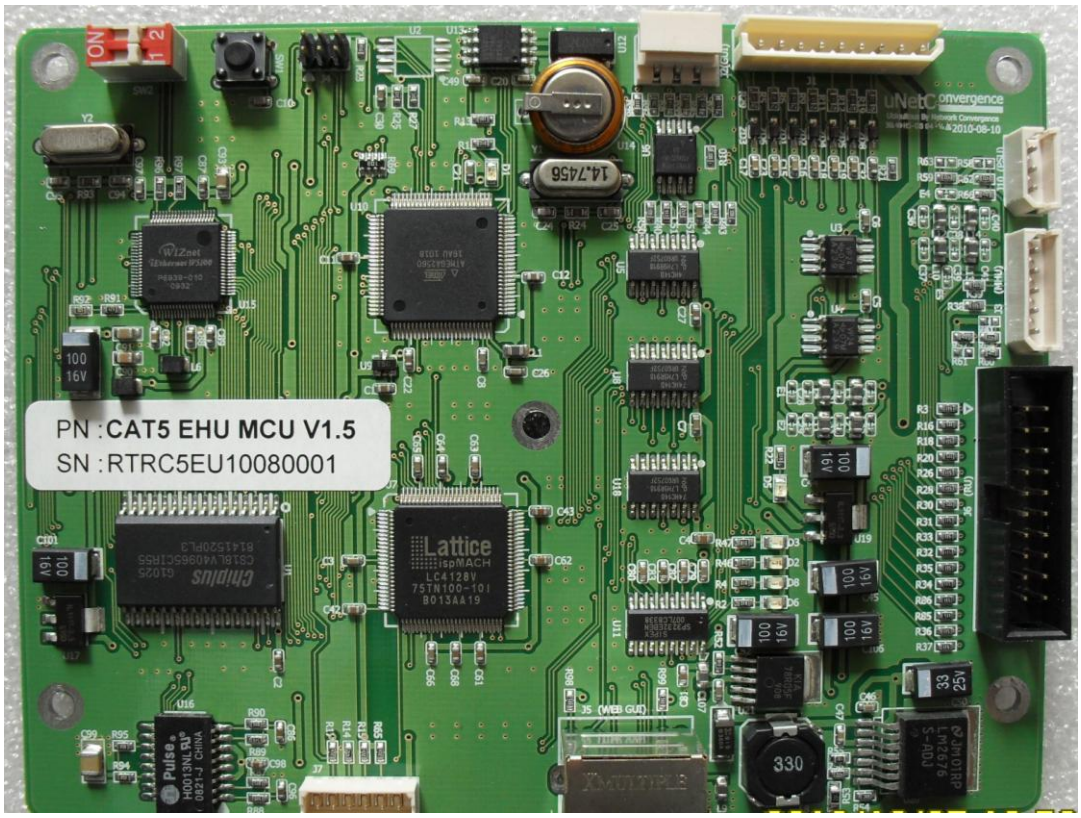
*** MCU ***



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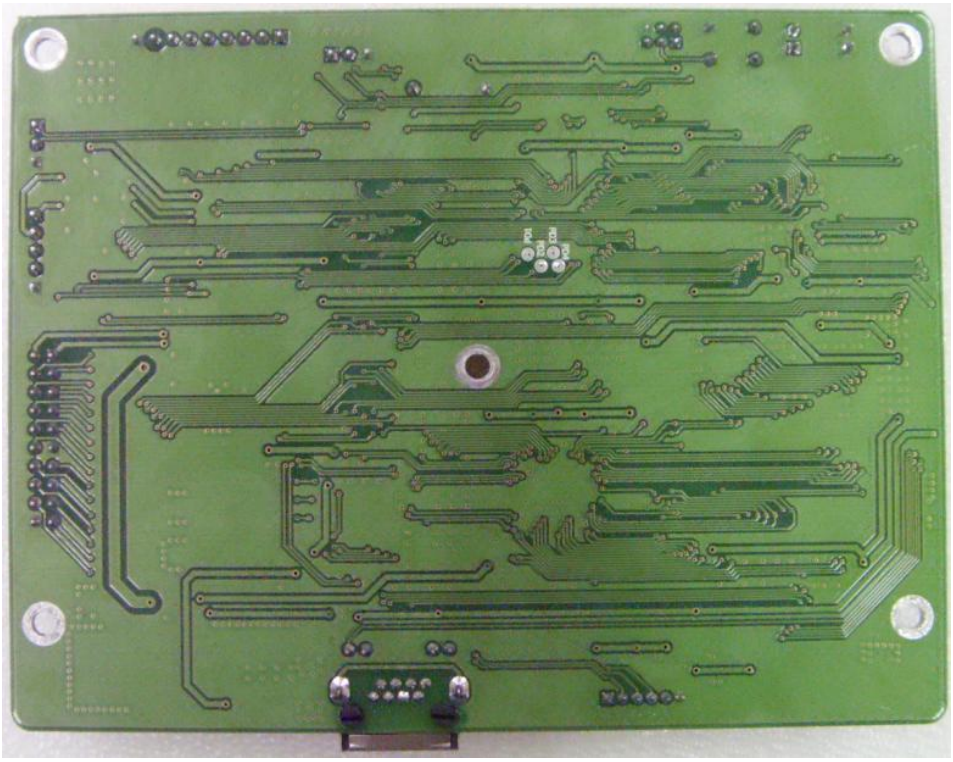

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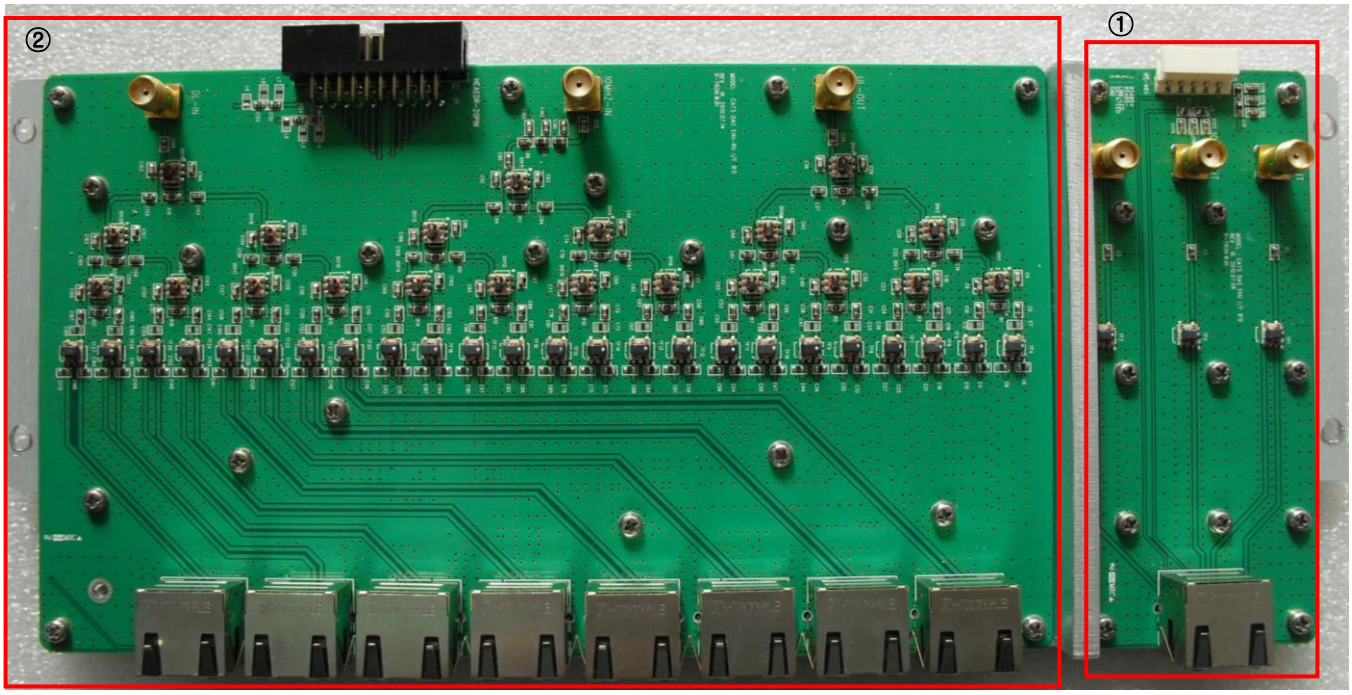
* Interface Board *

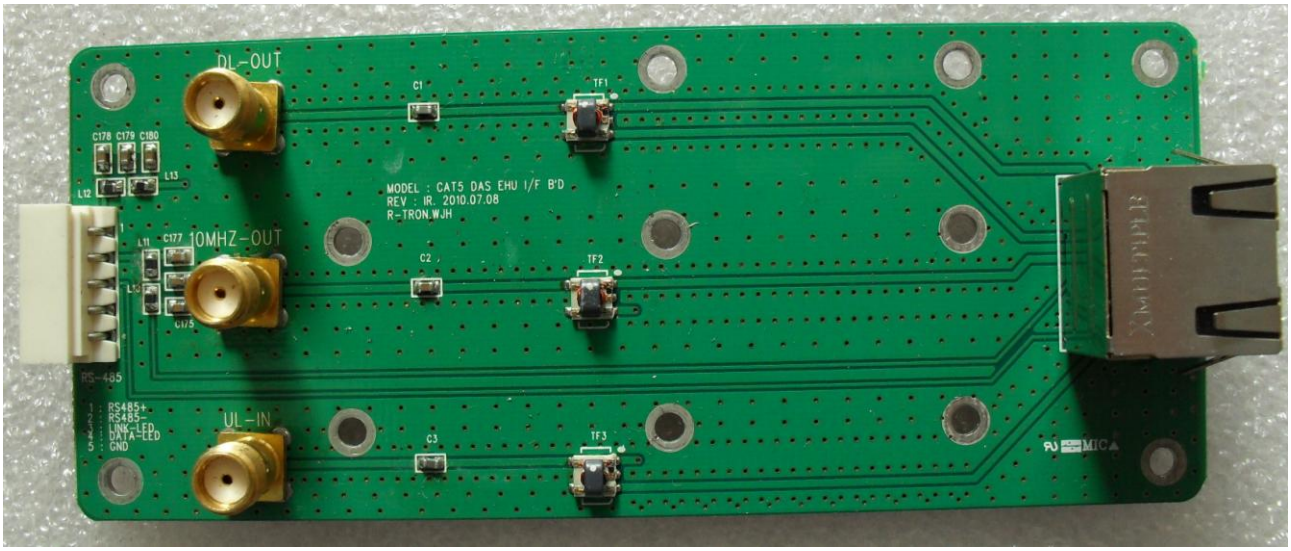


<Interface Top view>

① **Interface Board of the communication EHU with MHU**

② **Interface Board of the communication EHU With RU**

① Interface Board of the communication EHU with MHU



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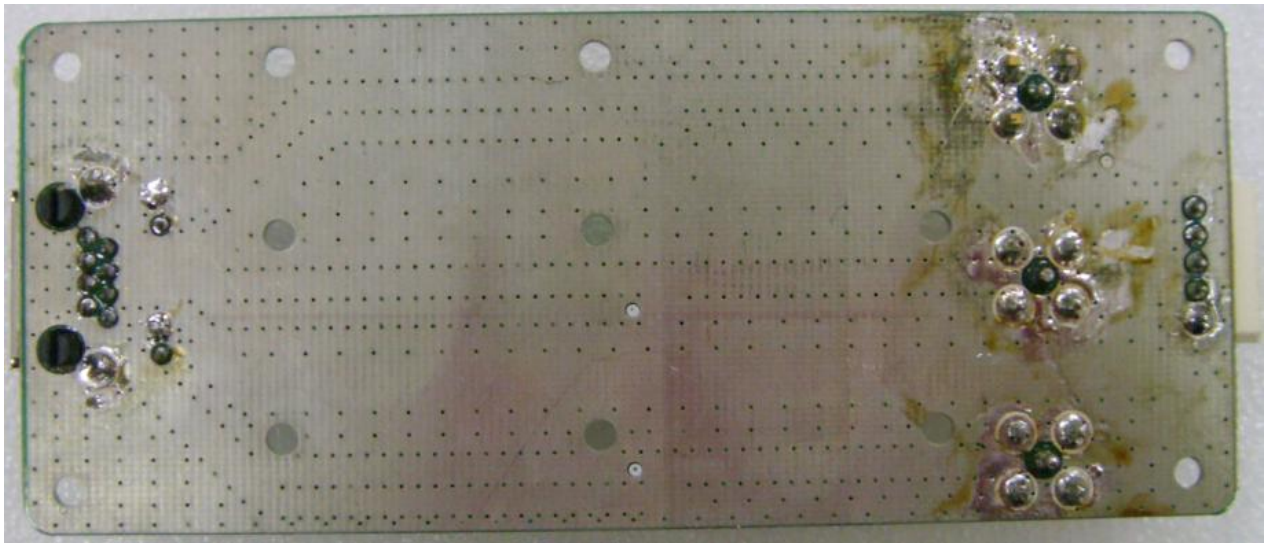

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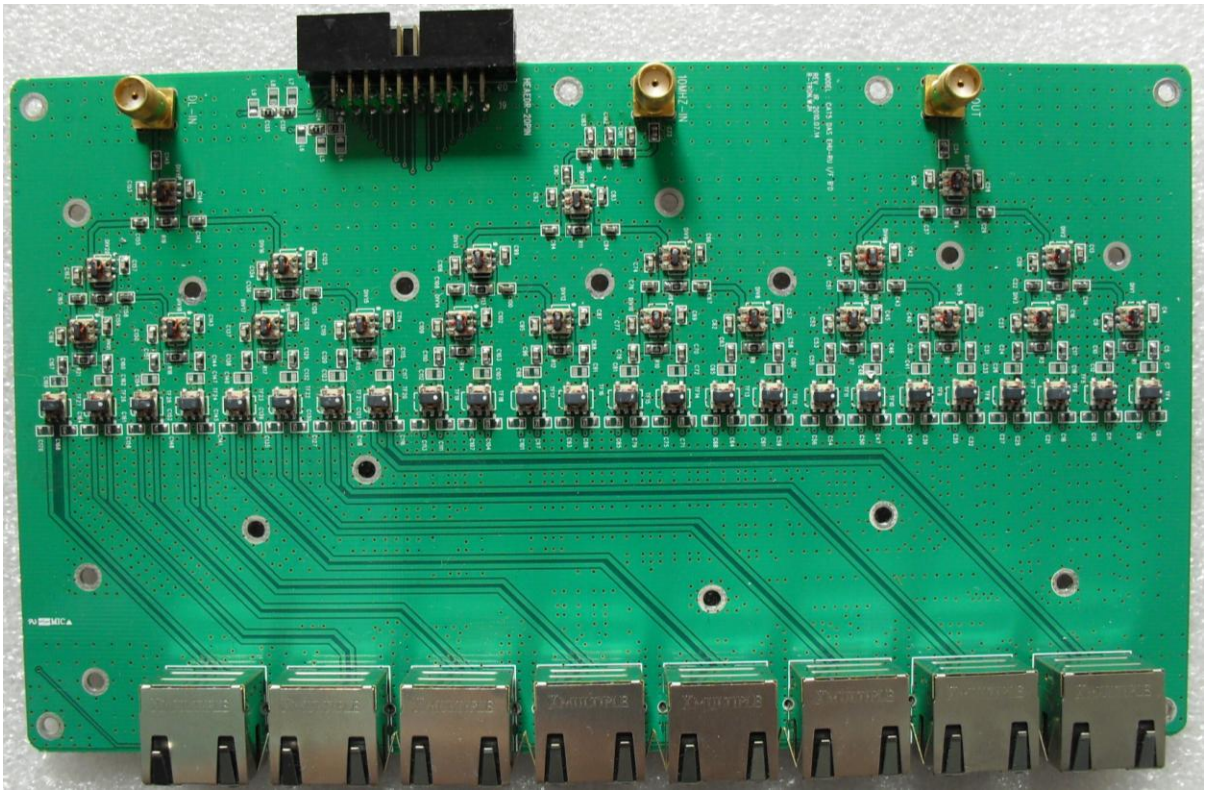
② Interface Board of the communication EHU with RU



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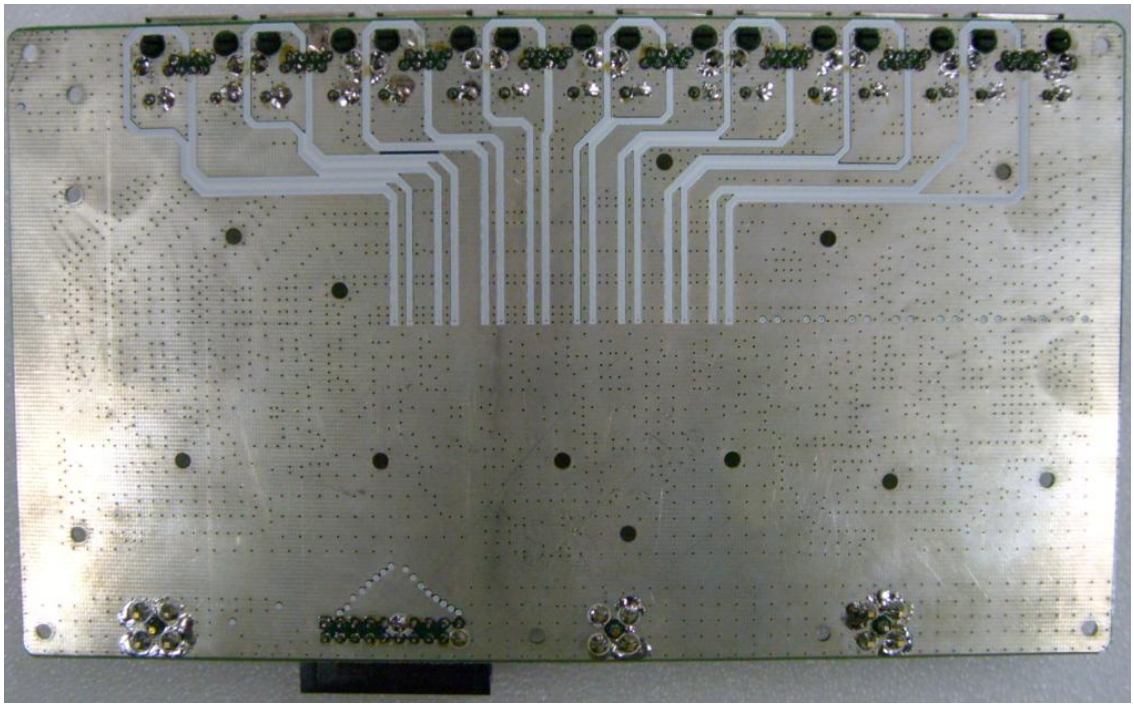

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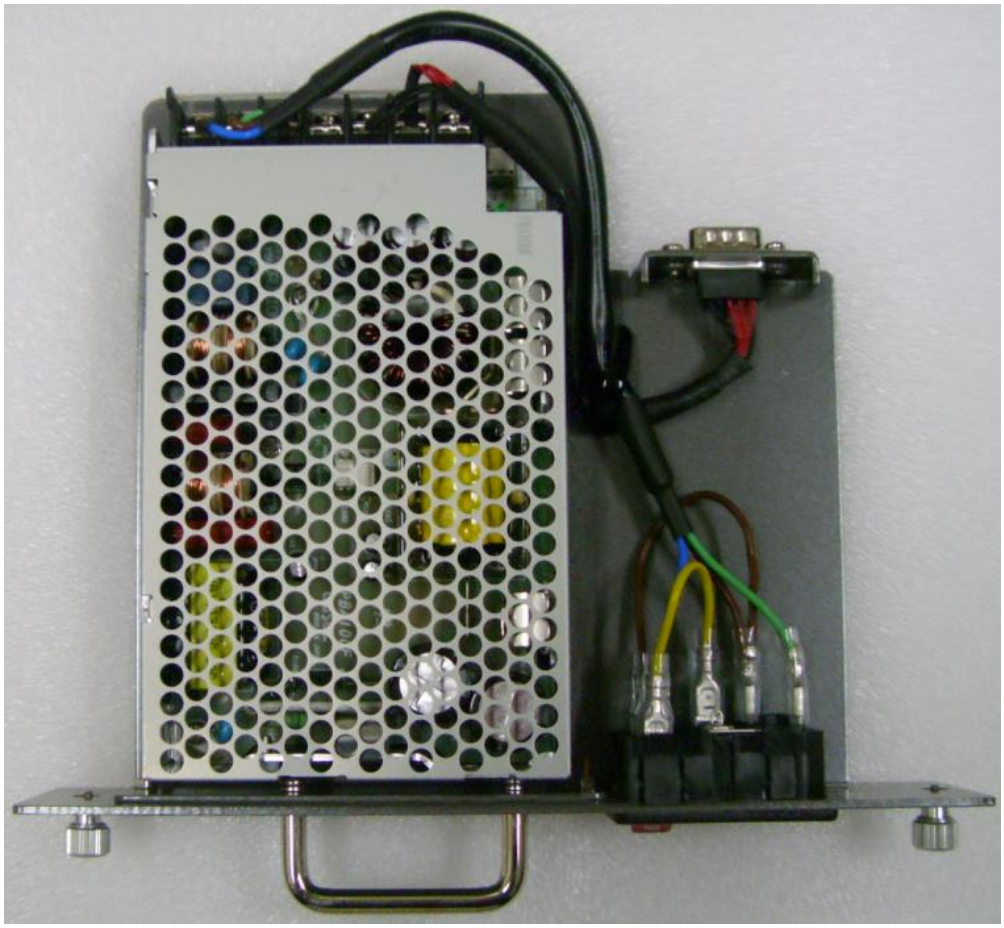
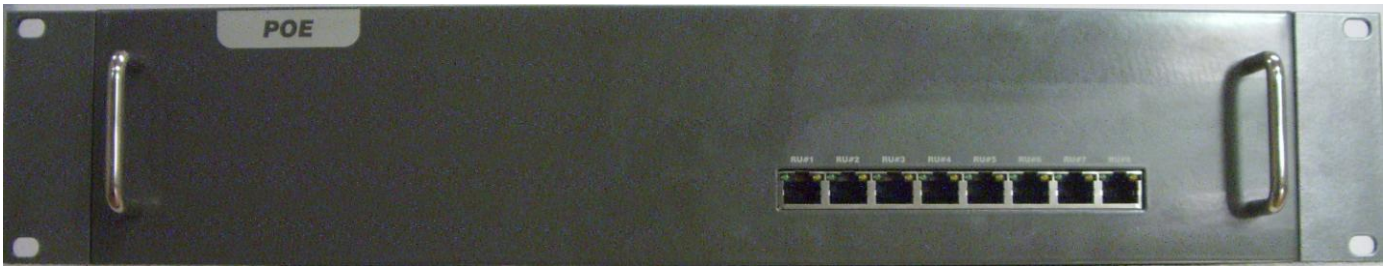
PSU



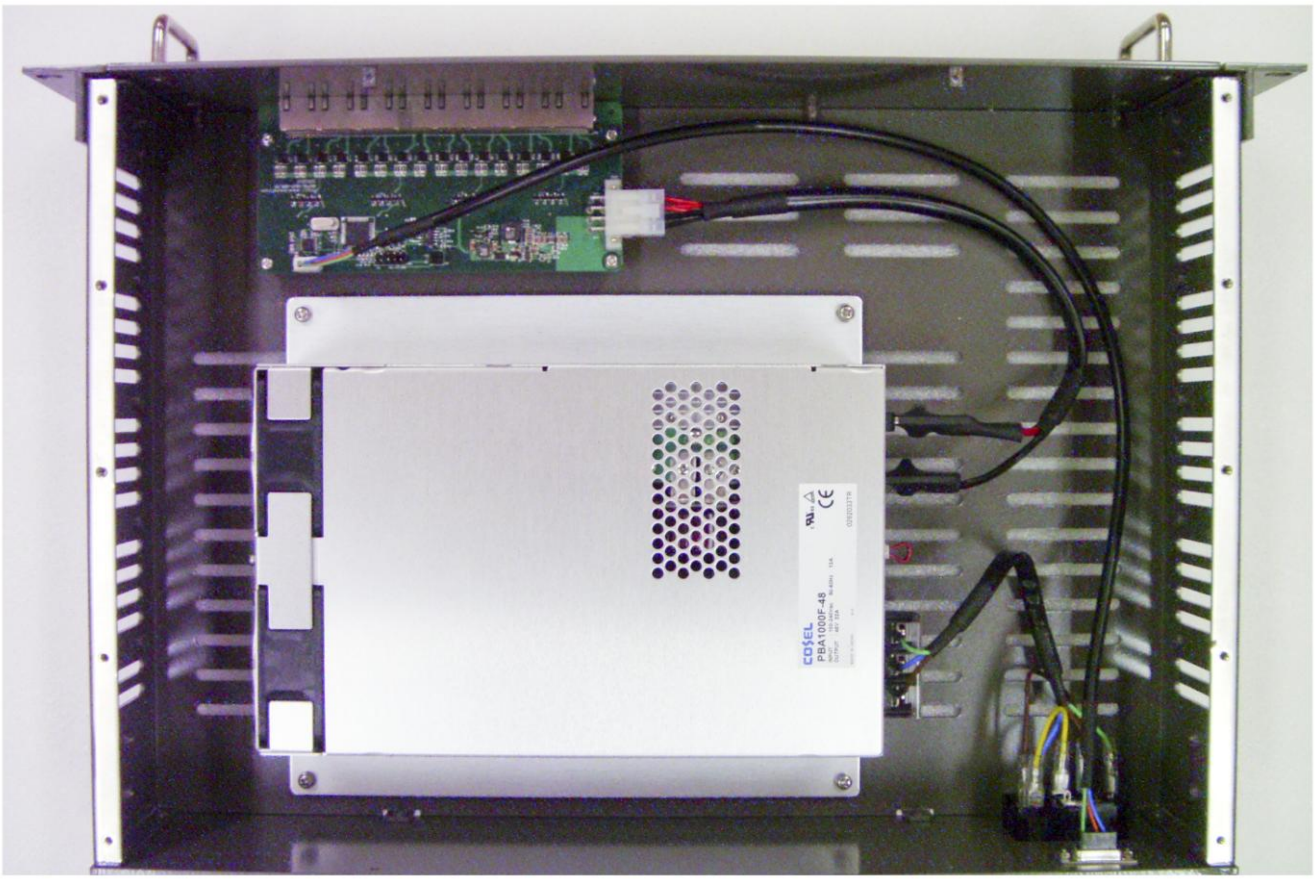
4. POE



<Front view>

<Rear view>

<Top view>

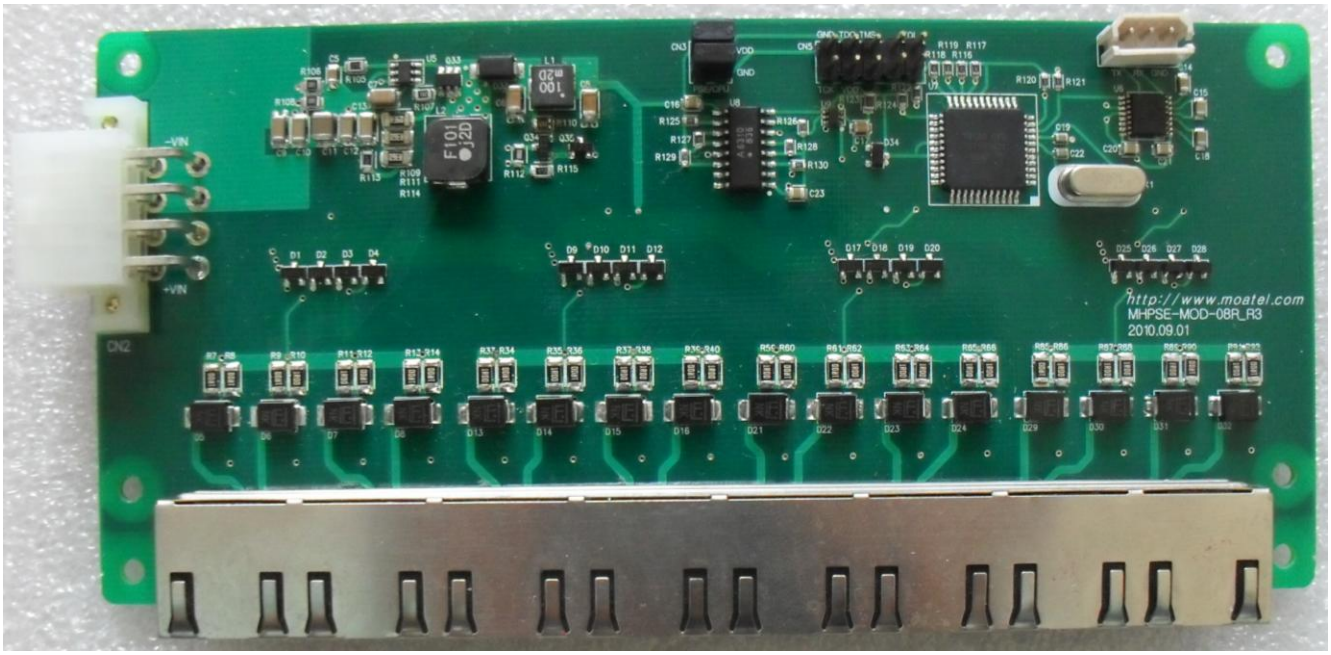


<Inside of the POE>

PSE

PSU

PSE



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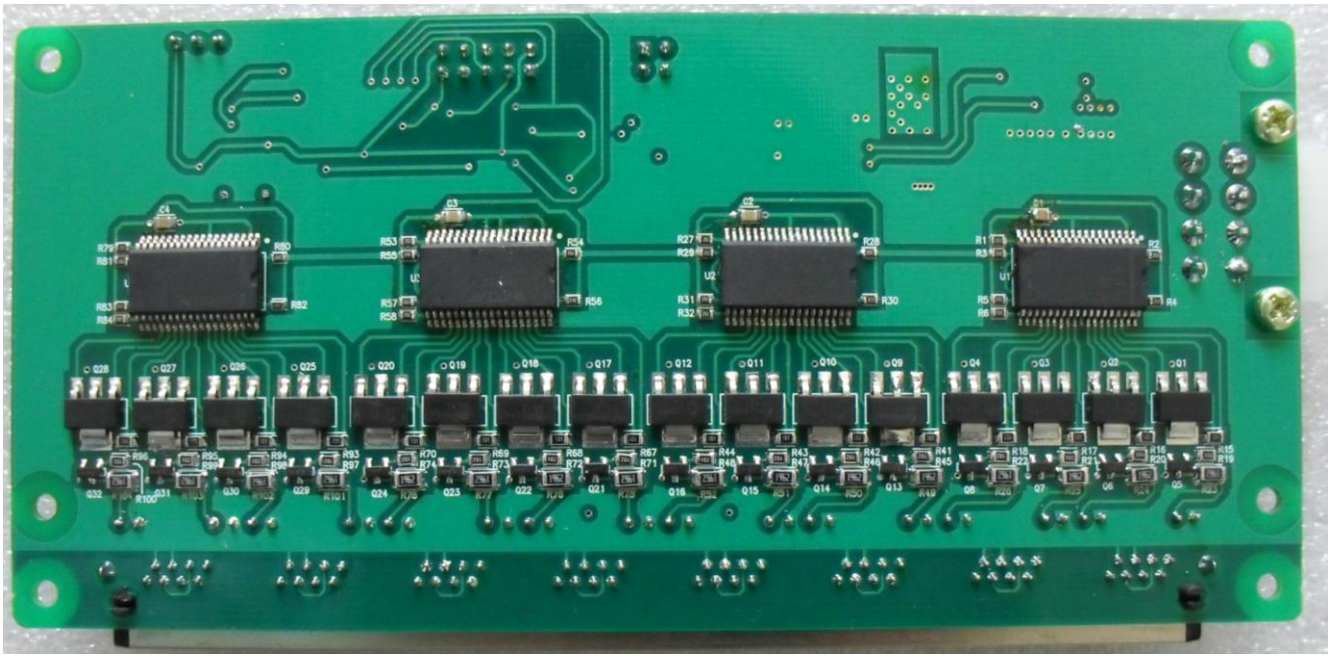

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PSU