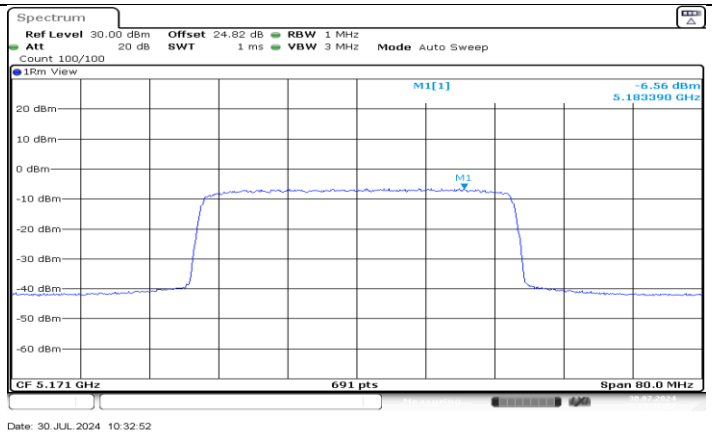
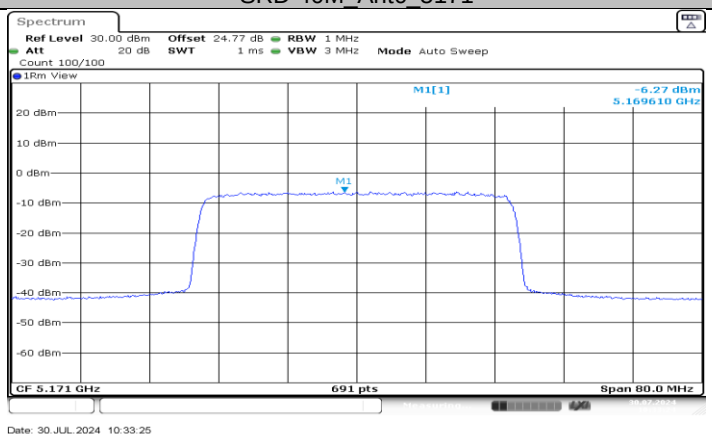
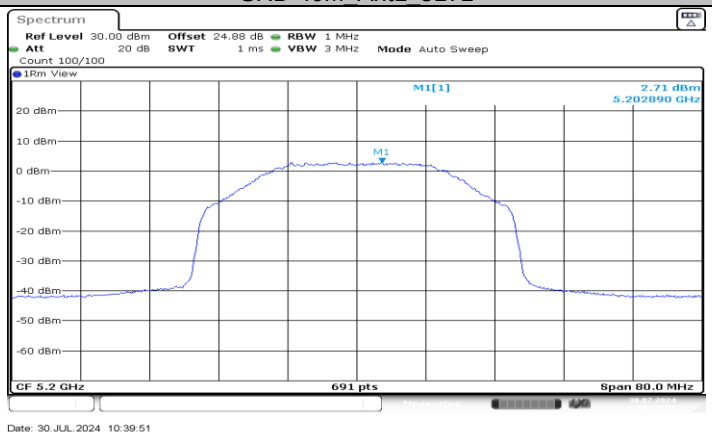


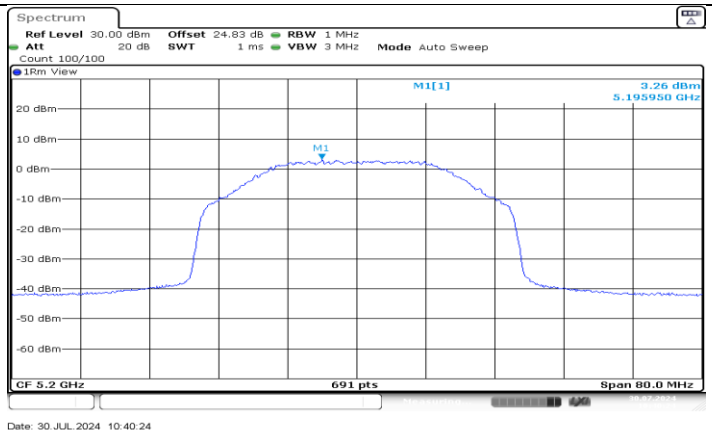
SRD 40M_Ant0_5171



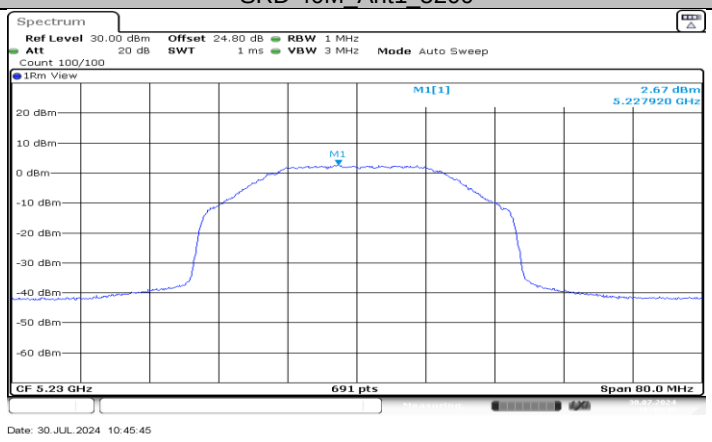
SRD 40M_Ant1_5171



SRD 40M_Ant0_5200



SRD 40M_Ant1_5200



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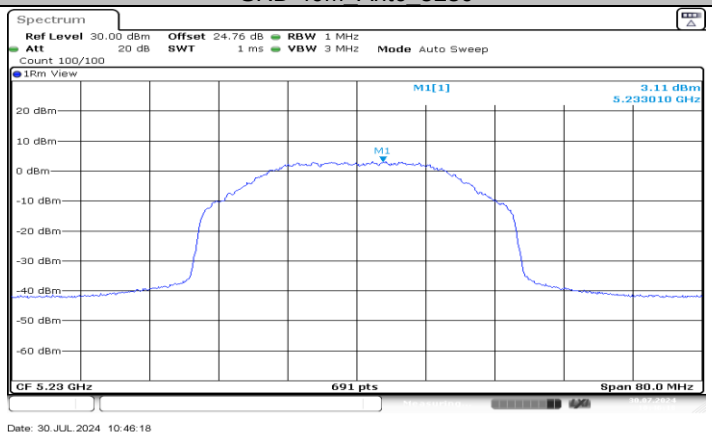
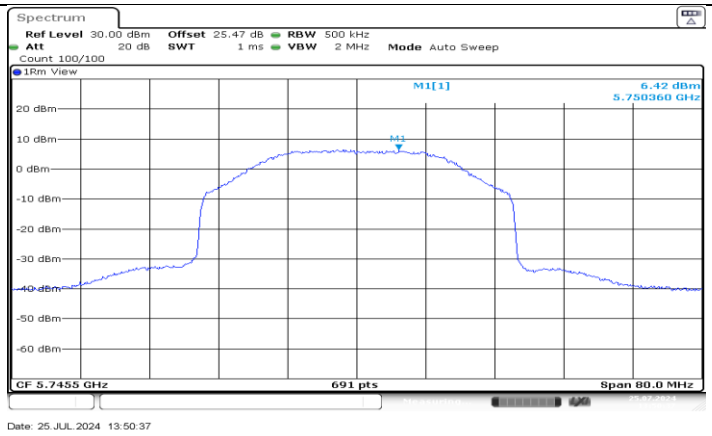
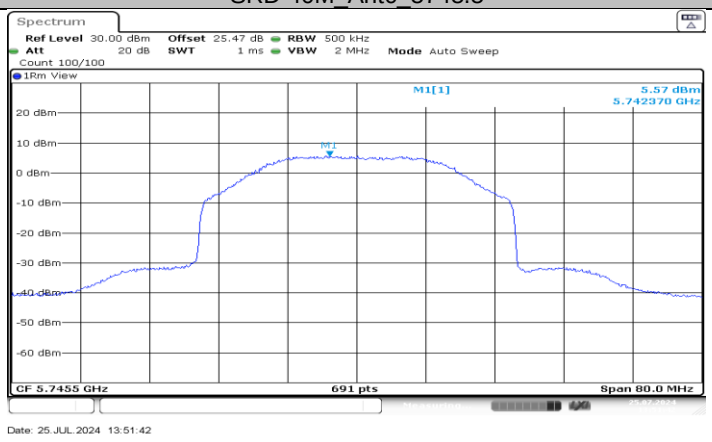


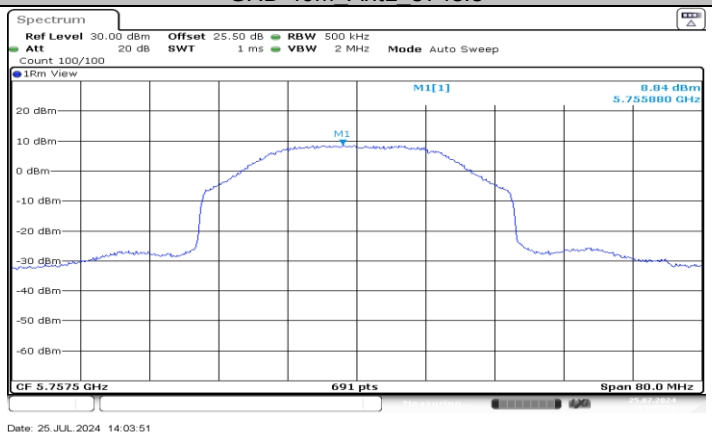
SRD 40M_Ant1_5230



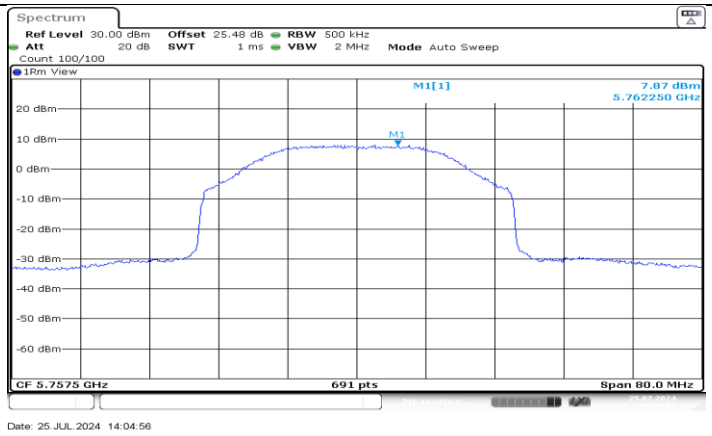
SRD 40M_Ant0_5745.5



SRD 40M_Ant1_5745.5



SRD 40M_Ant0_5757.5



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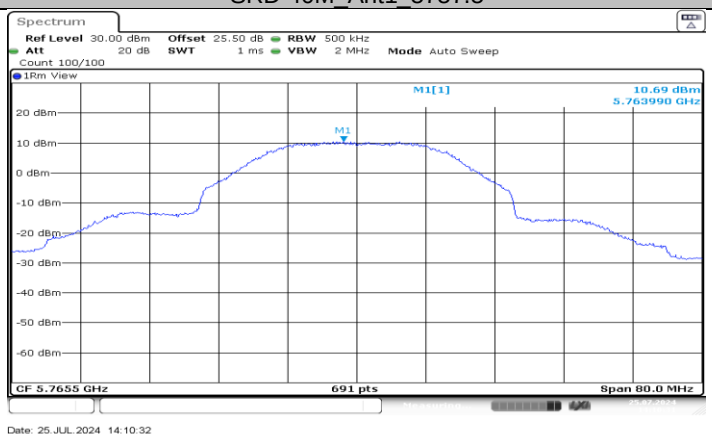
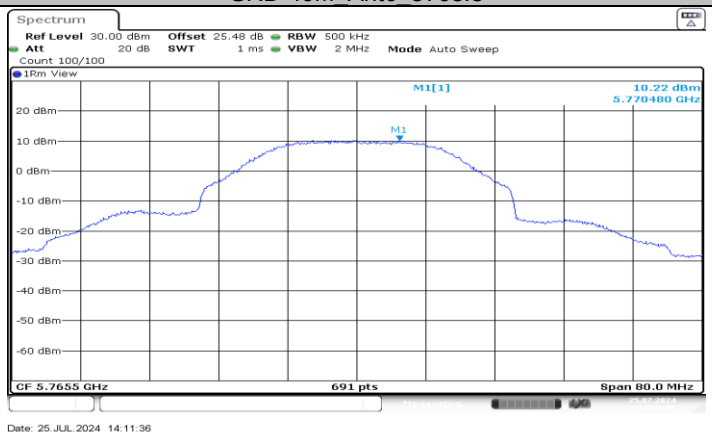
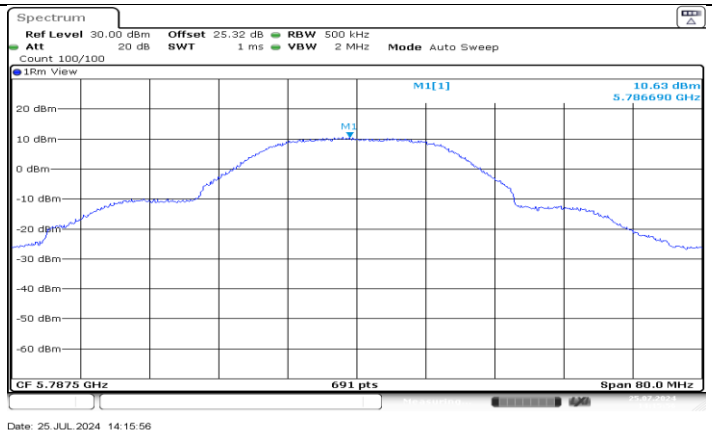


SRD 40M_Ant0_5765.5



SRD 40M_Ant1_5765.5



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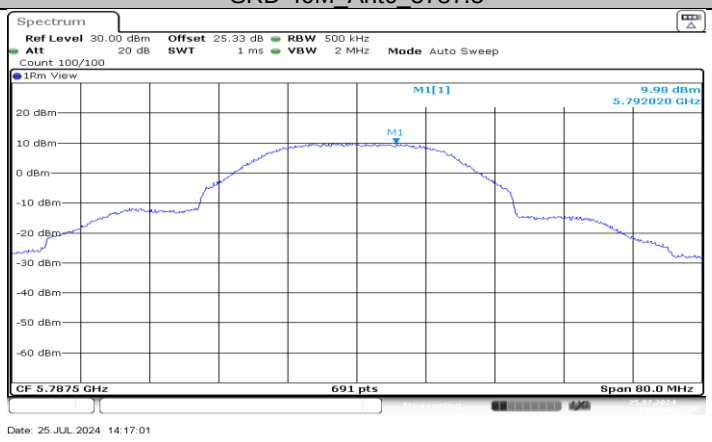
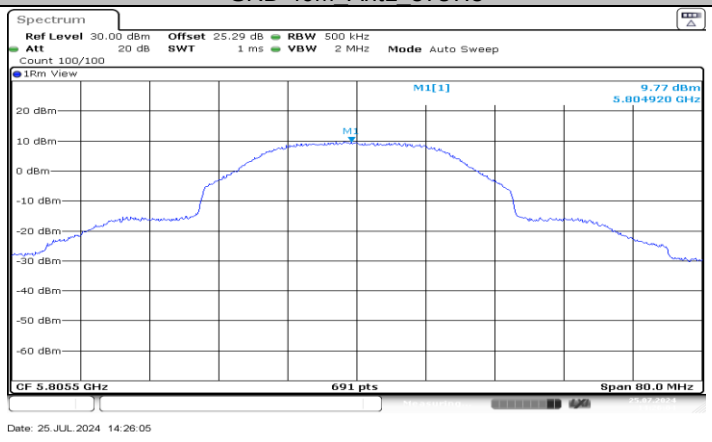
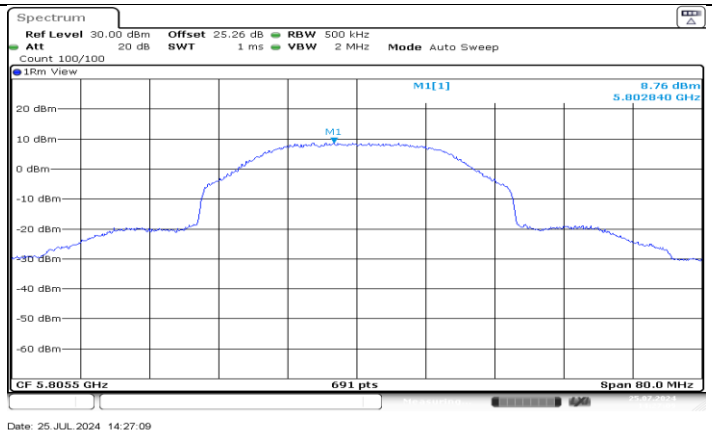


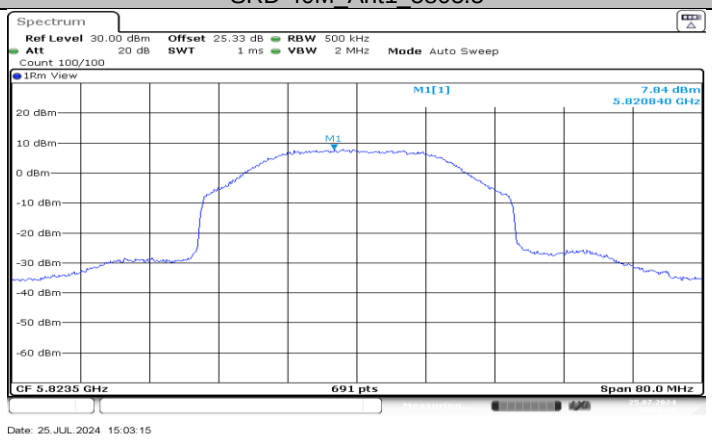
SRD 40M_Ant1_5787.5



SRD 40M_Ant0_5805.5



SRD 40M_Ant1_5805.5



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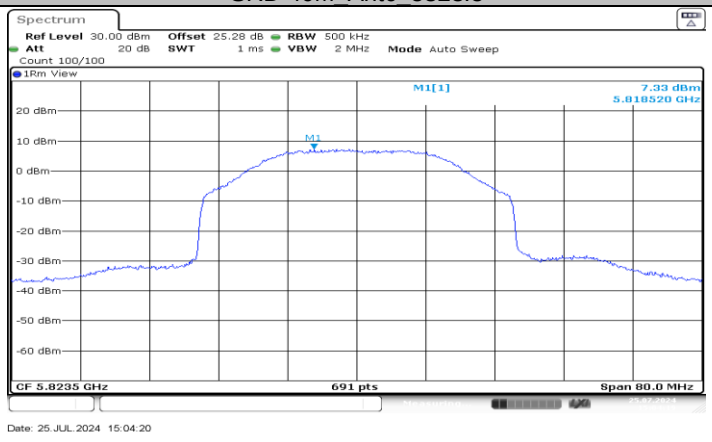

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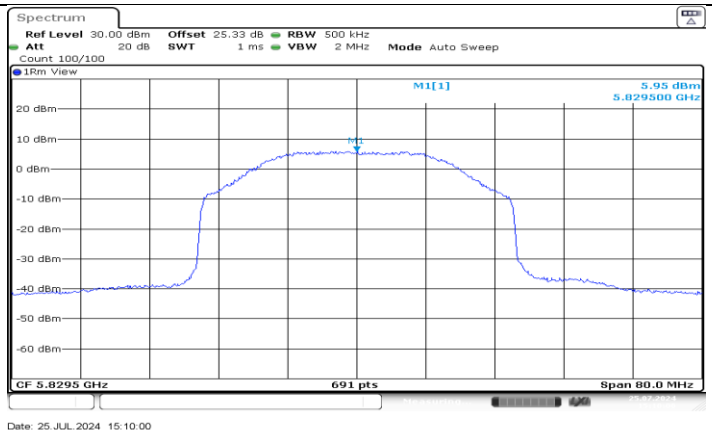
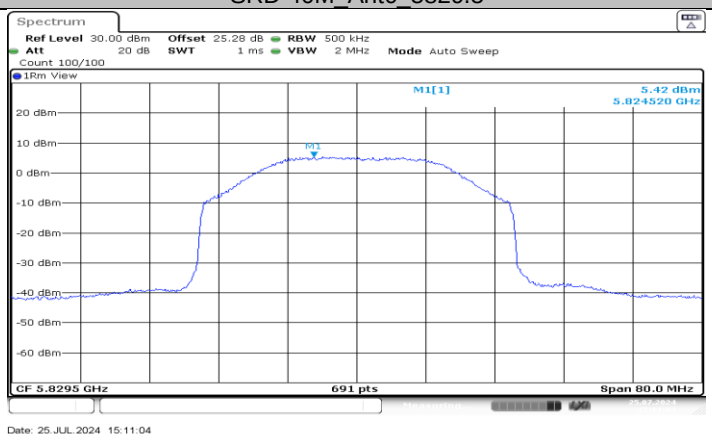
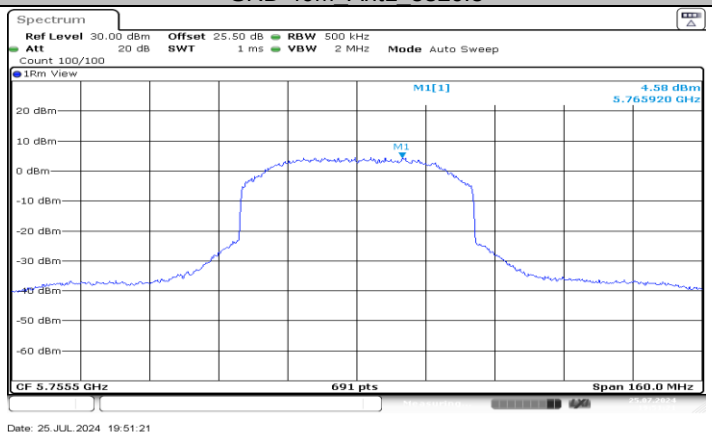


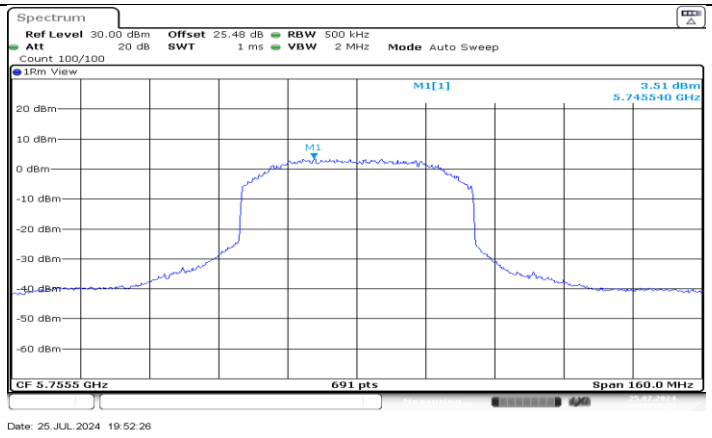
SRD 40M_Ant0_5829.5



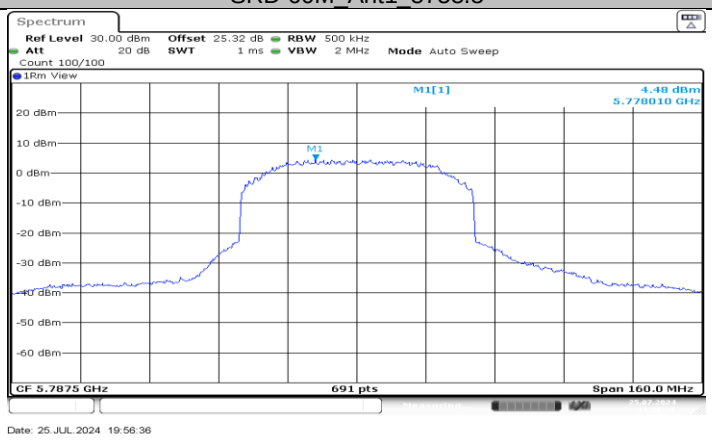
SRD 40M_Ant1_5829.5



SRD 60M_Ant0_5755.5



SRD 60M_Ant1_5755.5



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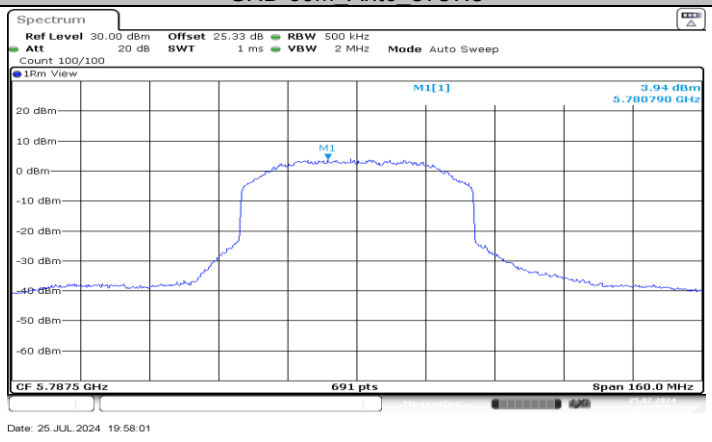
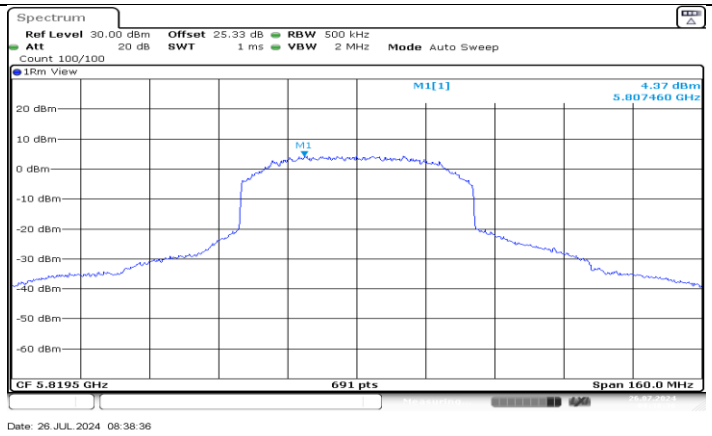
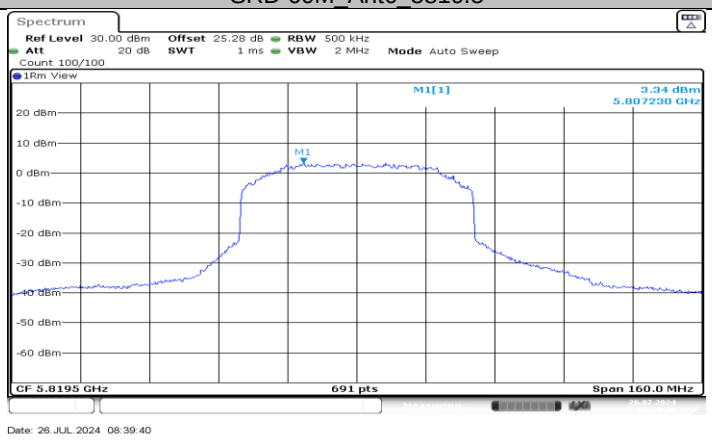


SRD 60M_Ant1_5787.5



SRD 60M_Ant0_5819.5



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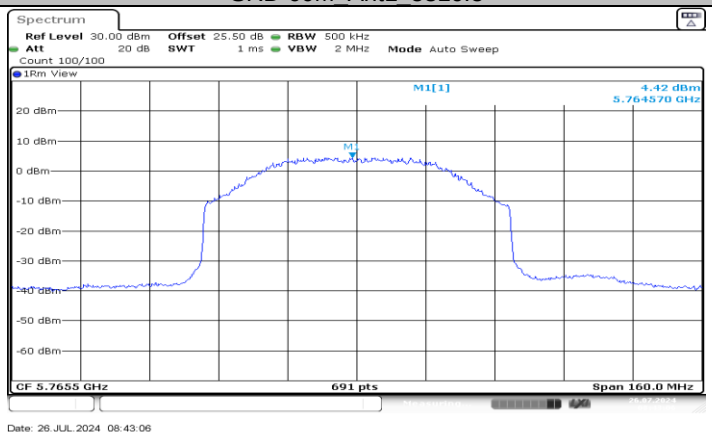
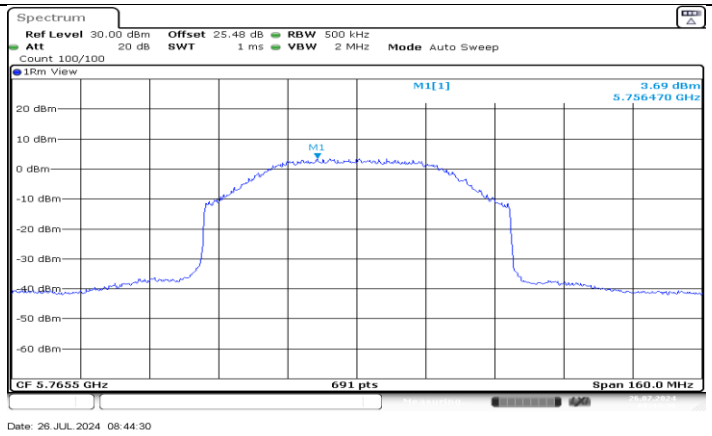


SRD 80M_Ant0_5765.5



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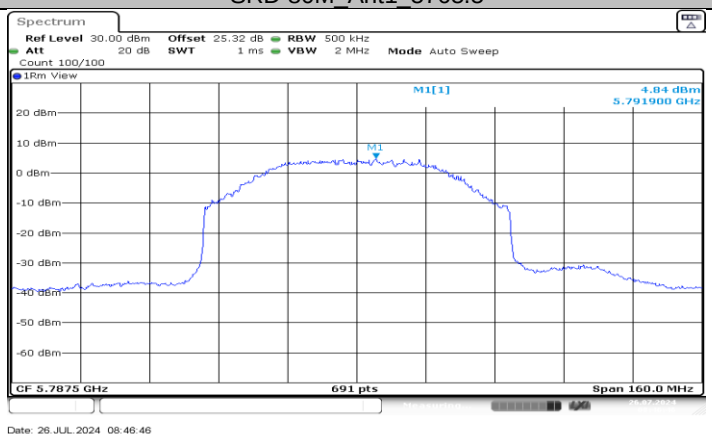

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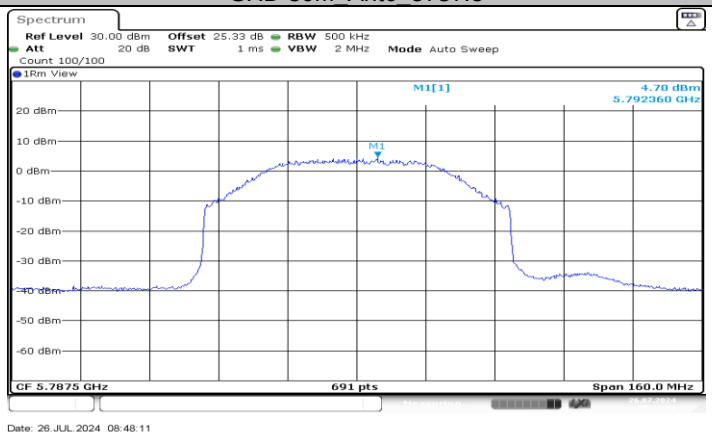

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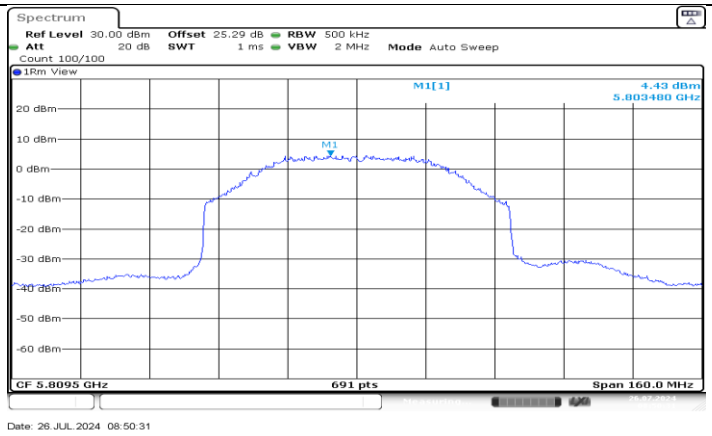
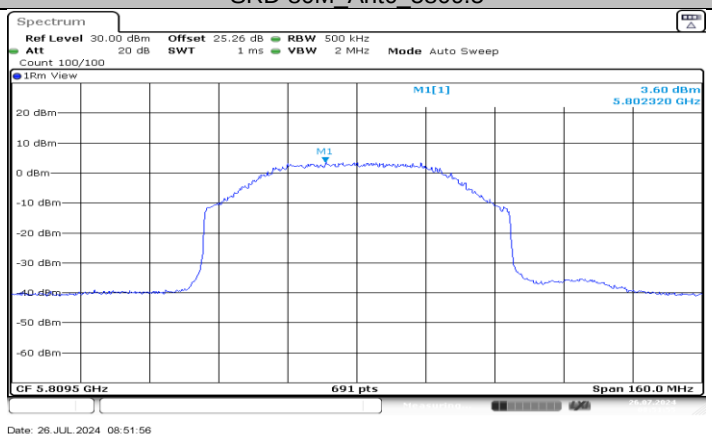


SRD 80M_Ant0_5809.5



SRD 80M_Ant1_5809.5

11.6. APPENDIX I: FREQUENCY STABILITY

11.6.1. Test Result

Frequency Error vs. Voltage									
SRD 1.4MHz:5846.12MHz									
Temp.	Volt.	0 Minute		2 Minute		5 Minute		10 Minute	
		Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)
TN	VL	5846.0989	-3.61	5846.1389	3.24	5846.1269	1.18	5846.1446	4.20
TN	VN	5846.1432	3.97	5846.1340	2.39	5846.1247	0.80	5846.1390	3.25
TN	VH	5846.1434	4.01	5846.1135	-1.12	5846.1393	3.31	5846.1371	2.93
Frequency Error vs. Temperature									
SRD 1.4MHz:5846.12MHz									
Temp.	Volt.	0 Minute		2 Minute		5 Minute		10 Minute	
		Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)
40	VN	5846.1078	-2.08	5846.1094	-1.82	5846.1149	-0.87	5846.1305	1.80
30	VN	5846.1022	-3.05	5846.1387	3.20	5846.1060	-2.39	5846.1307	1.83
20	VN	5846.1216	0.27	5846.1054	-2.50	5846.1096	-1.77	5846.1298	1.67
10	VN	5846.1297	1.66	5846.0974	-3.87	5846.1155	-0.77	5846.1188	-0.21
0	VN	5846.1224	0.41	5846.1178	-0.37	5846.1021	-3.06	5846.1160	-0.68
-10	VN	5846.1120	-1.36	5846.1300	1.70	5846.1423	3.82	5846.0997	-3.47

Note:
1. All antennas, test modes and test channels have been tested, only the worst data record in the report.
2. For the detail Test Conditions, please refer to section 7.5 TEST ENVIRONMENT.

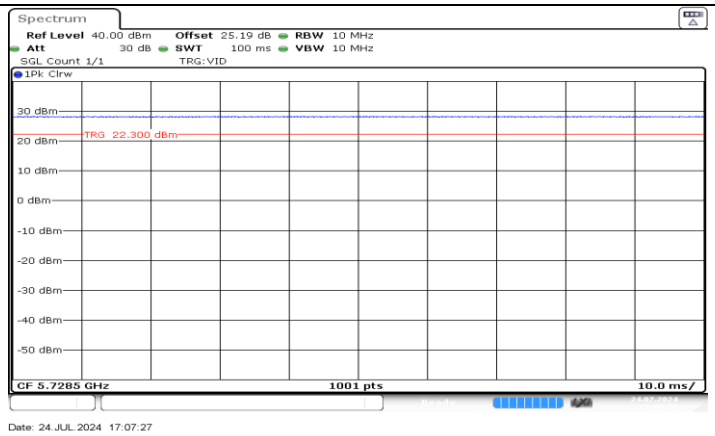
11.7. APPENDIX J: DUTY CYCLE

11.7.1. Test Result

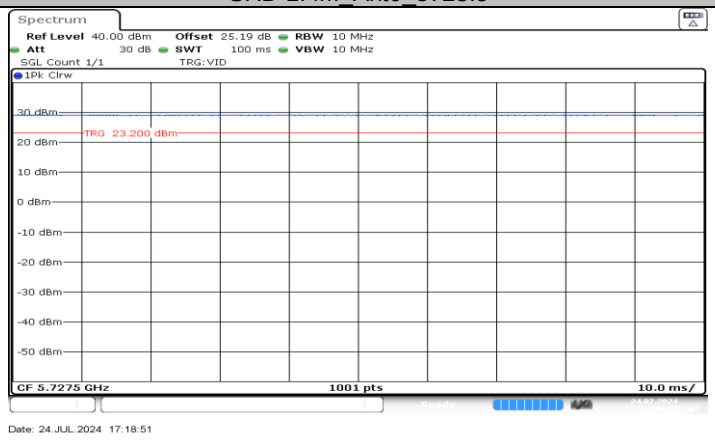
Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
SRD 1.4M	100.00	100.00	1.0000	100.00	0.00	N/A	0.01
SRD 3M	100.00	100.00	1.0000	100.00	0.00	N/A	0.01
SRD 5M	100.00	100.00	1.0000	100.00	0.00	N/A	0.01
SRD 10M	100.00	100.00	1.0000	100.00	0.00	N/A	0.01
SRD 20M	100.00	100.00	1.0000	100.00	0.00	N/A	0.01
SRD 40M	100.00	100.00	1.0000	100.00	0.00	N/A	0.01
SRD 60M	100.00	100.00	1.0000	100.00	0.00	N/A	0.01
SRD 80M	100.00	100.00	1.0000	100.00	0.00	N/A	0.01

Note:
Duty Cycle Correction Factor=10log (1/x).
Where: x is Duty Cycle (Linear)
Where: T is On Time
If that calculated VBW is not available on the analyzer then the next higher value should be used.

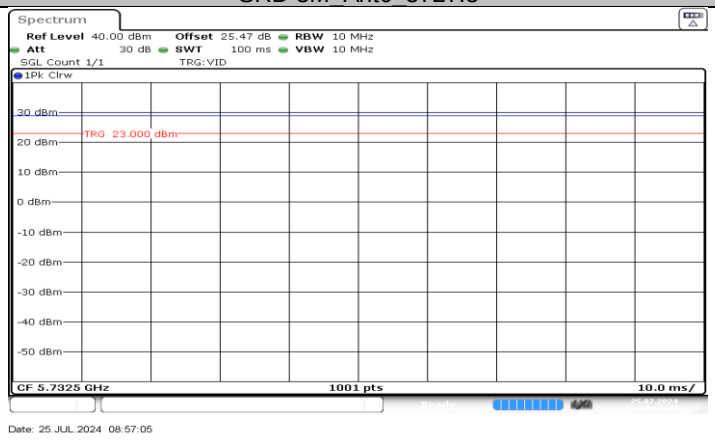
11.7.2. Test Graphs



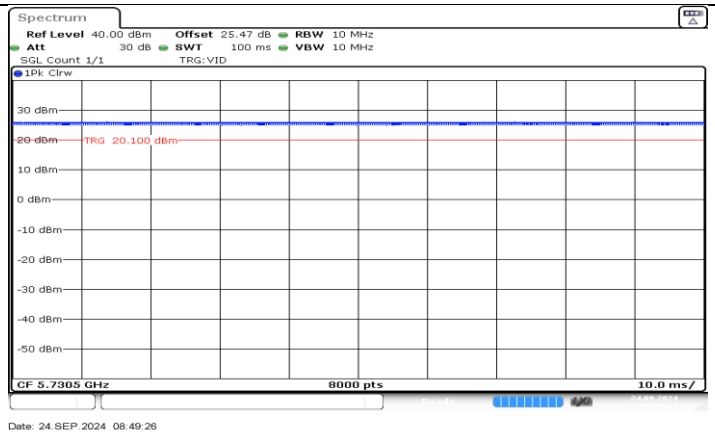
SRD 1.4M_Ant0_5728.5



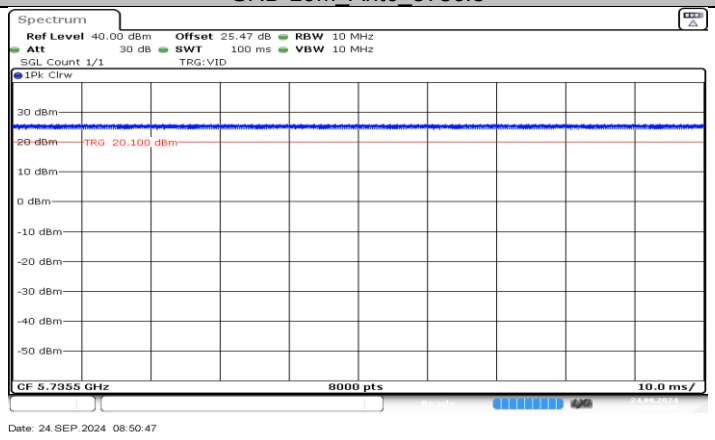
SRD 3M_Ant0_5727.5



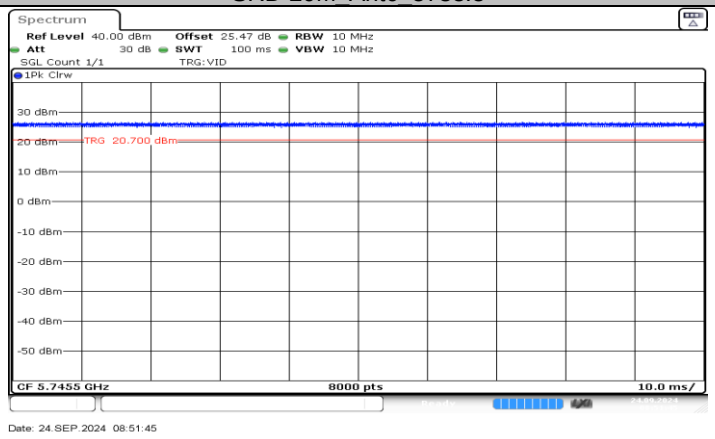
SRD 5M_Ant0_5732.5



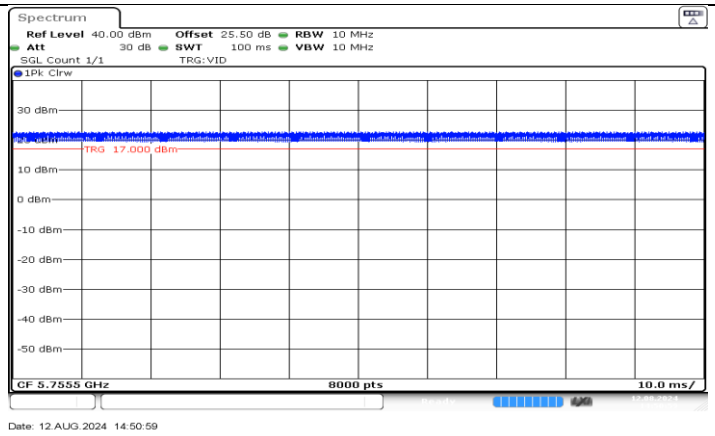
SRD 10M_Ant0_5730.5



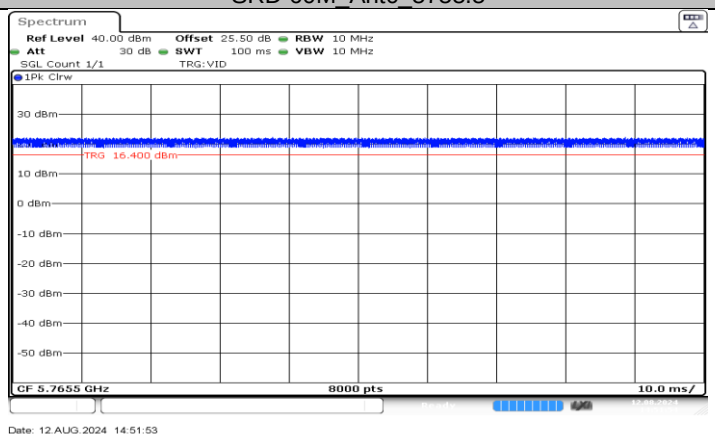
SRD 20M_Ant0_5735.5



SRD 40M_Ant0_5745.5



SRD 60M_Ant0_5755.5



SRD 80M_Ant0_5765.5

END OF REPORT