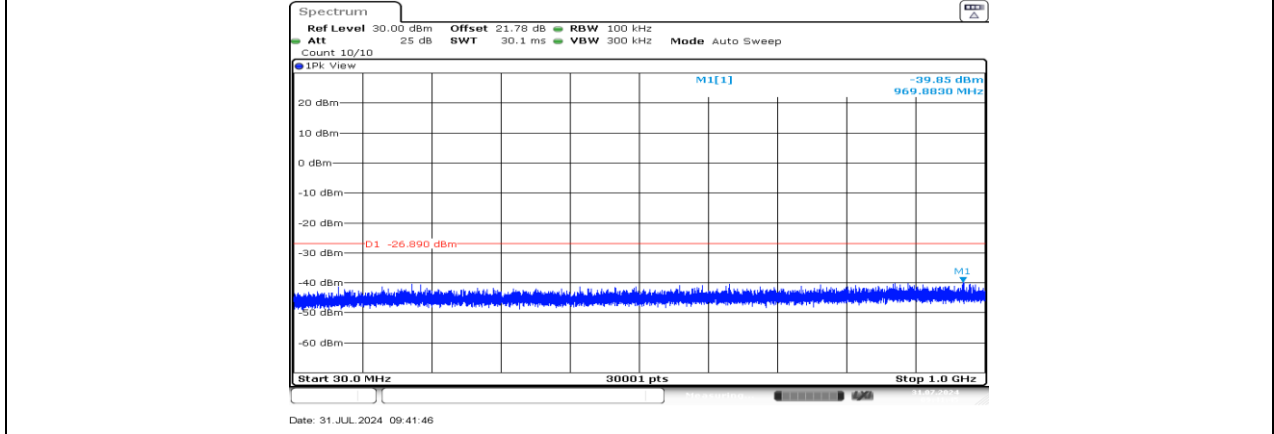
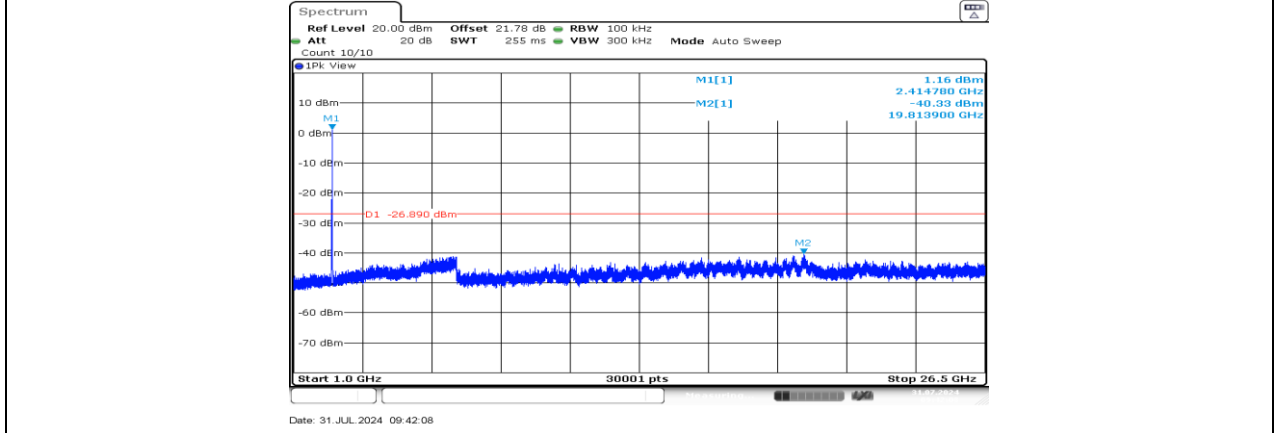


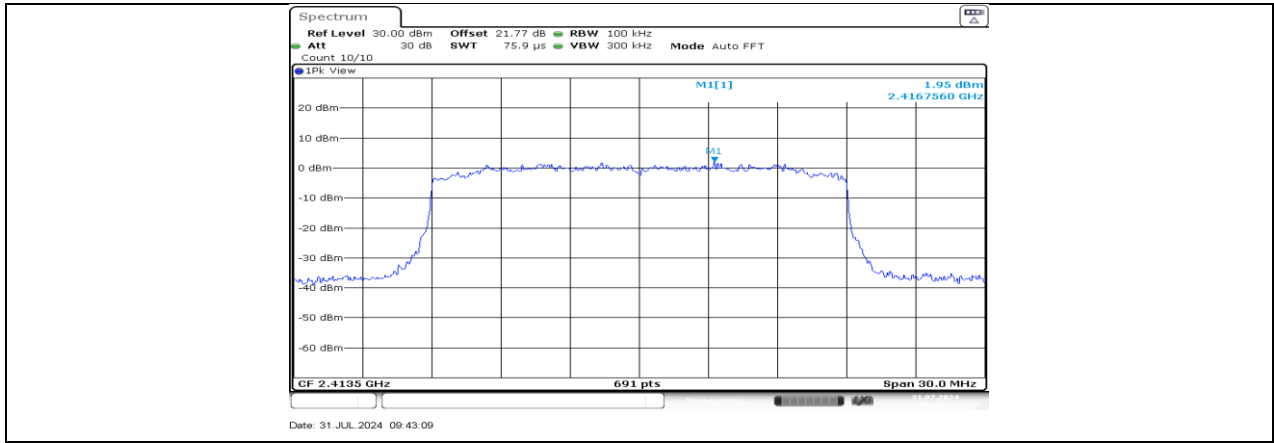
SRD 20M_Ant0_2413.5_0~Reference



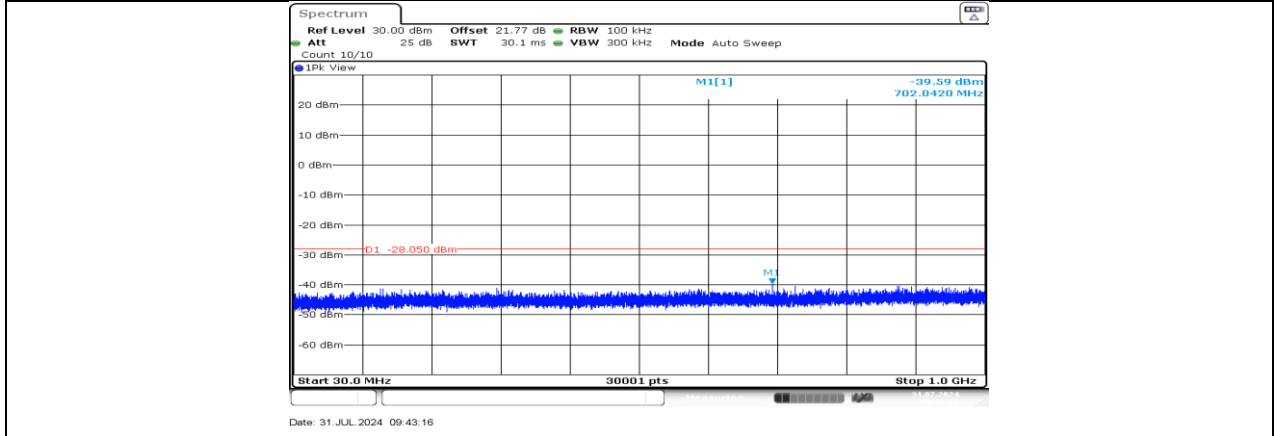
SRD 20M_Ant0_2413.5_30~1000



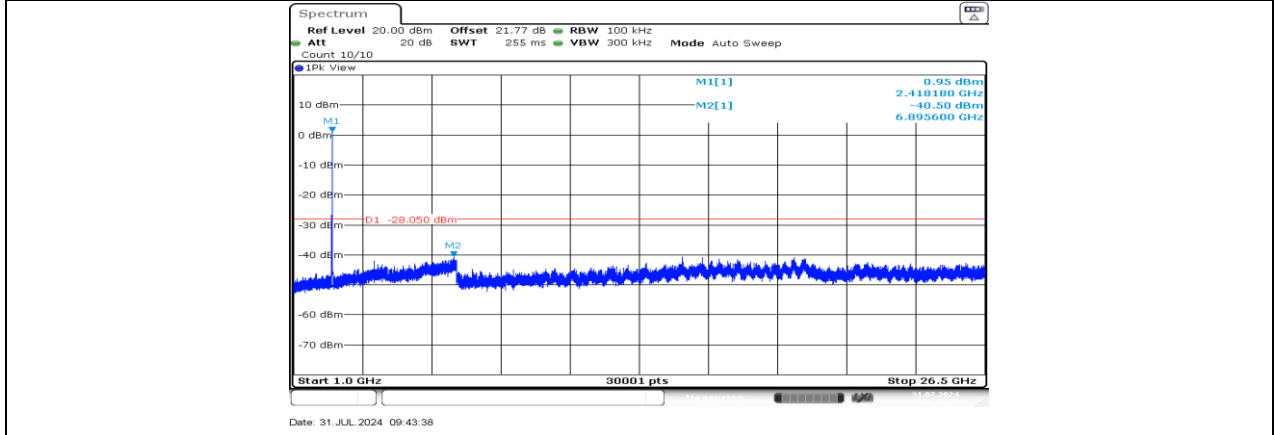
SRD 20M_Ant0_2413.5_1000~26500



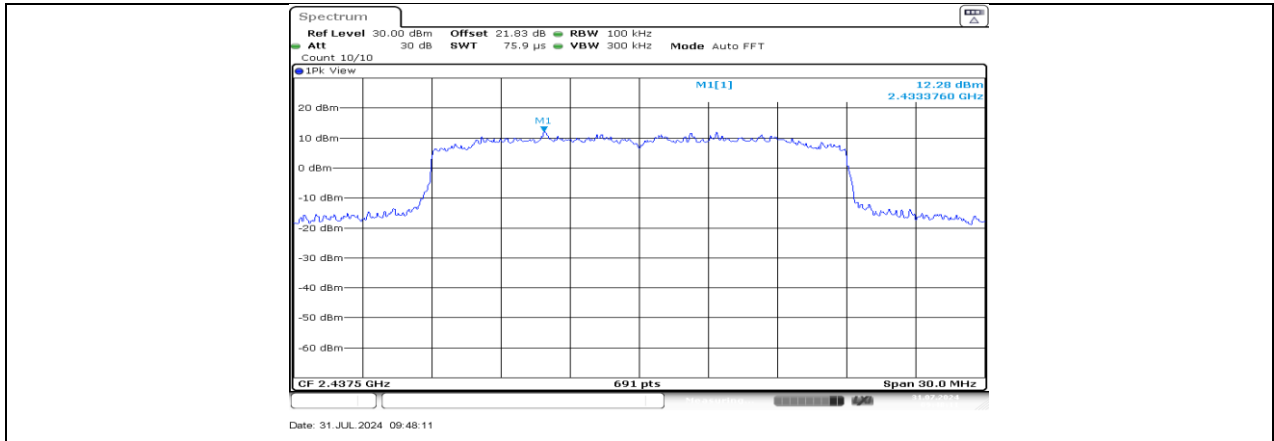
SRD 20M_Ant1_2413.5_0~Reference



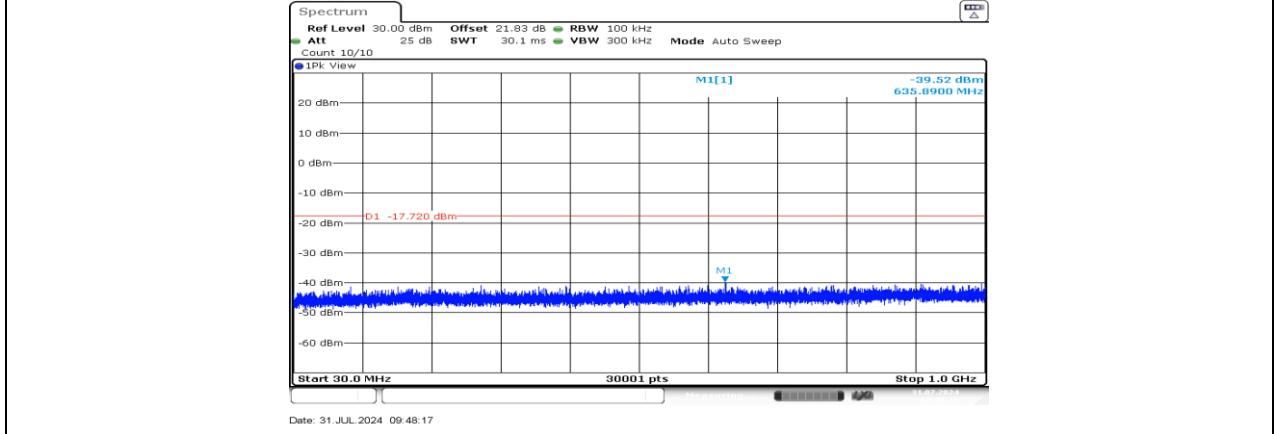
SRD 20M_Ant1_2413.5_30~1000



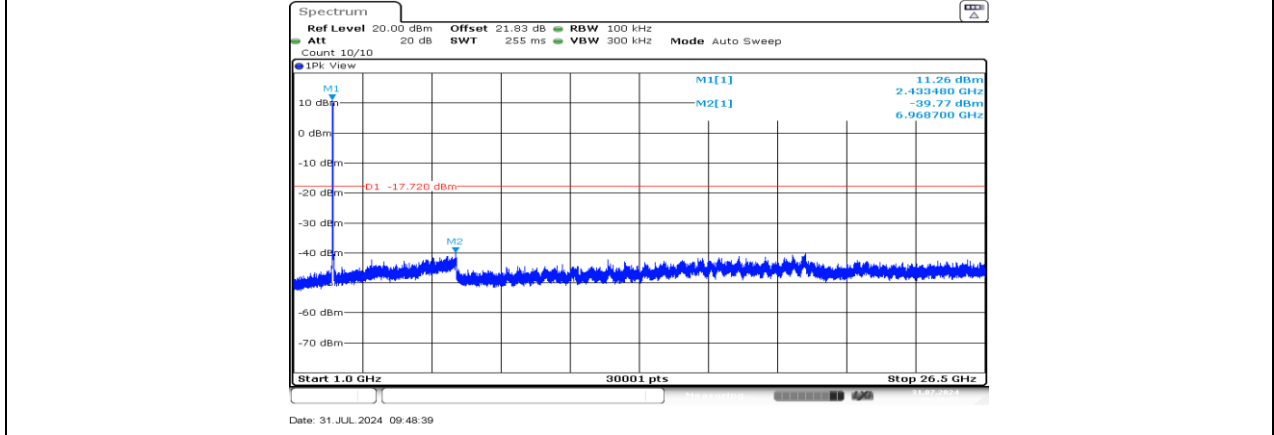
SRD 20M_Ant1_2413.5_1000~26500



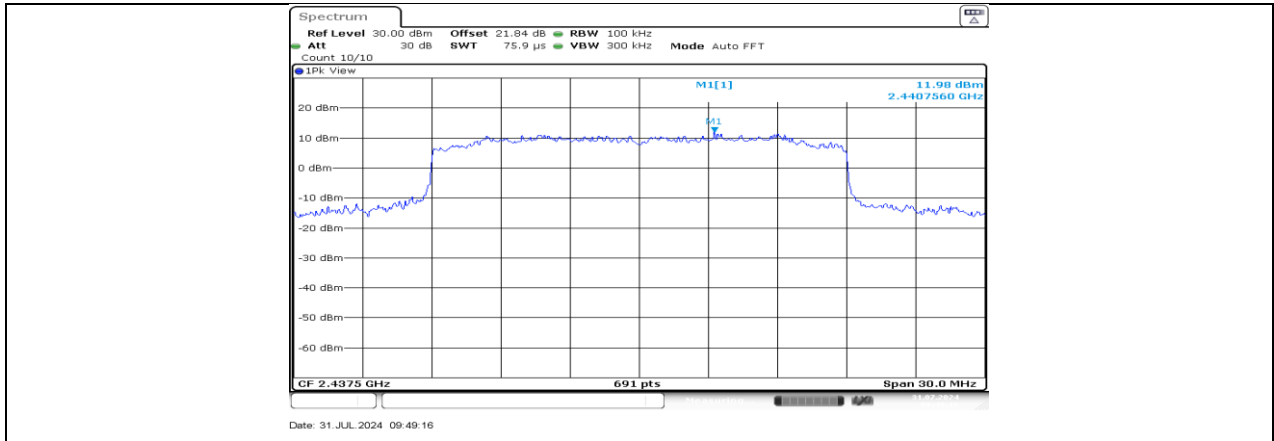
SRD 20M_Ant0_2437.5_0~Reference



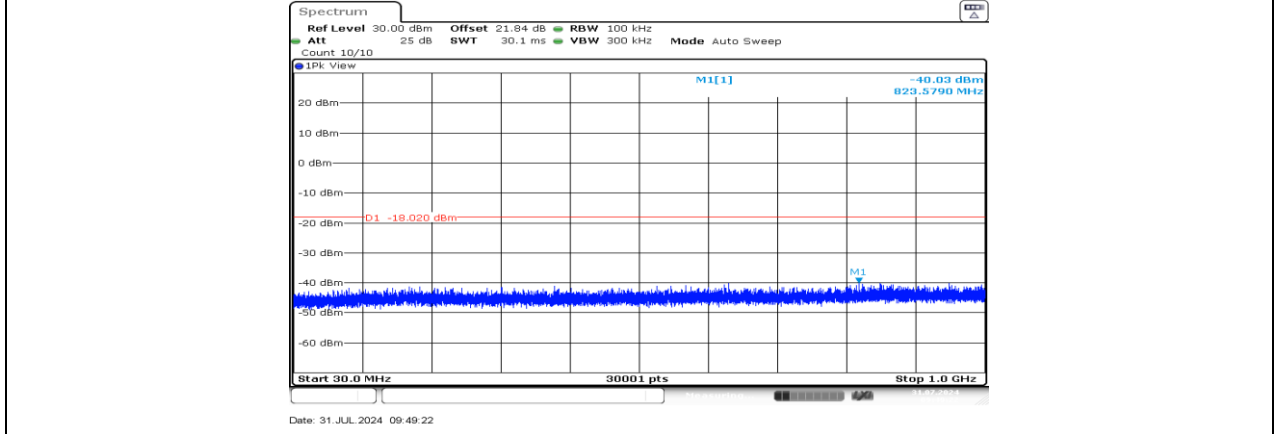
SRD 20M_Ant0_2437.5_30~1000



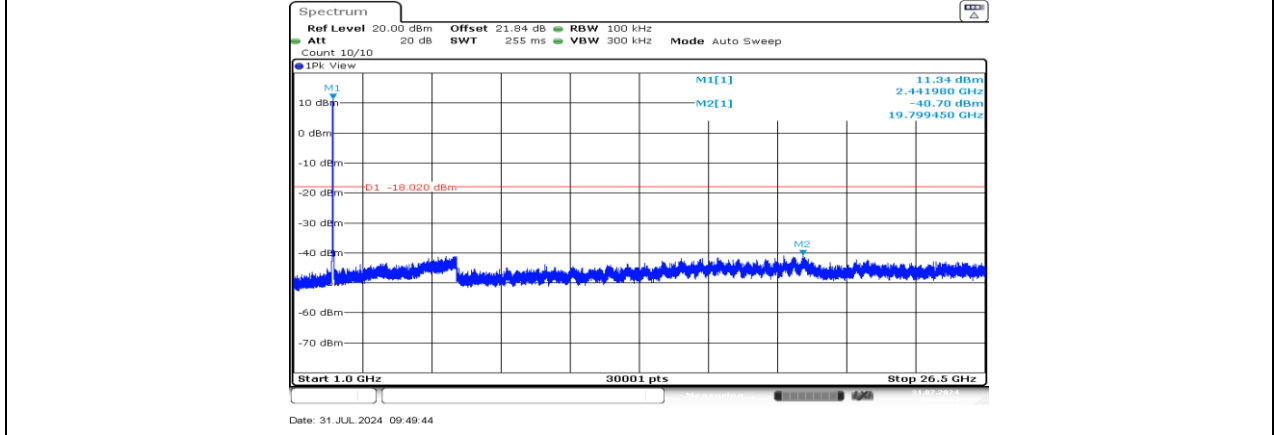
SRD 20M_Ant0_2437.5_1000~26500



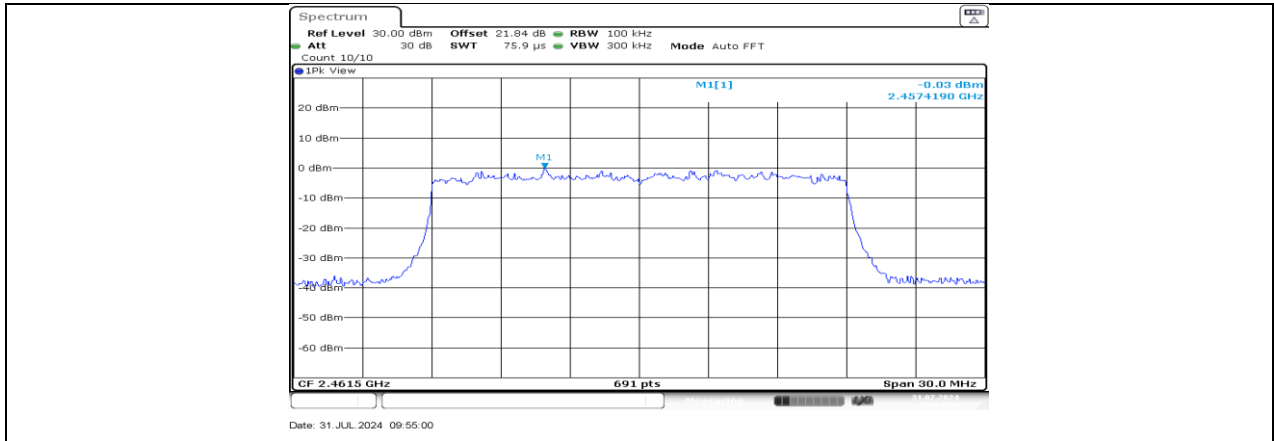
SRD 20M_Ant1_2437.5_0~Reference



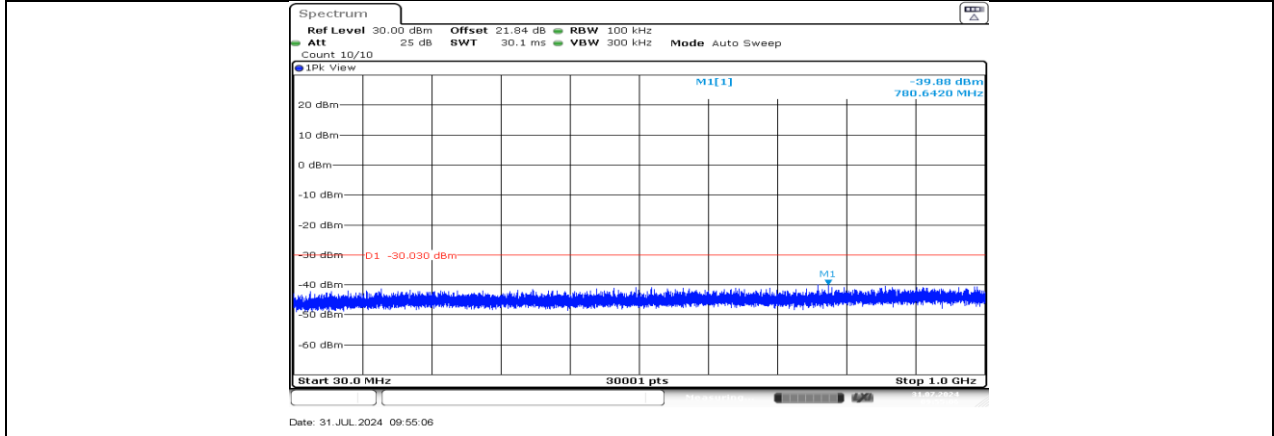
SRD 20M_Ant1_2437.5_30~1000



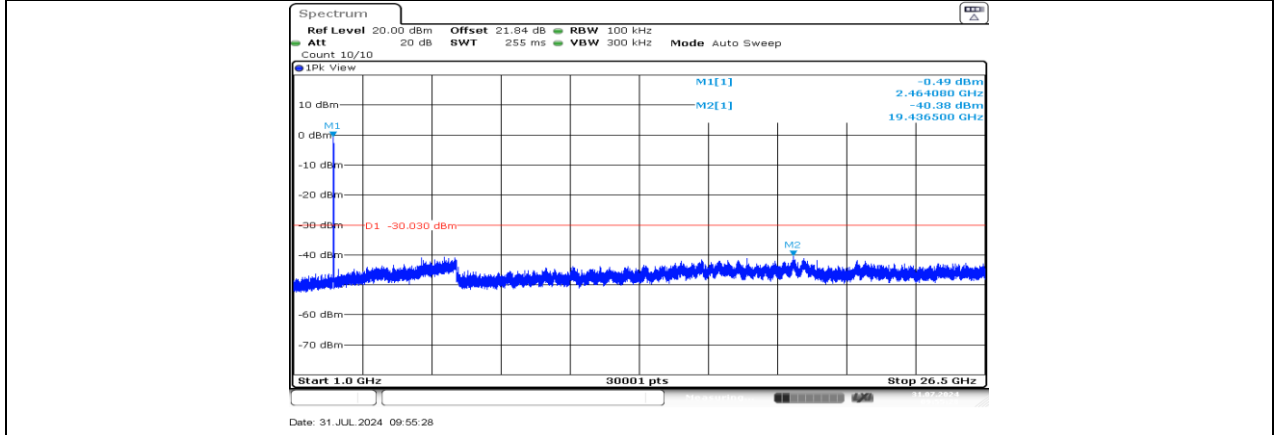
SRD 20M_Ant1_2437.5_1000~26500



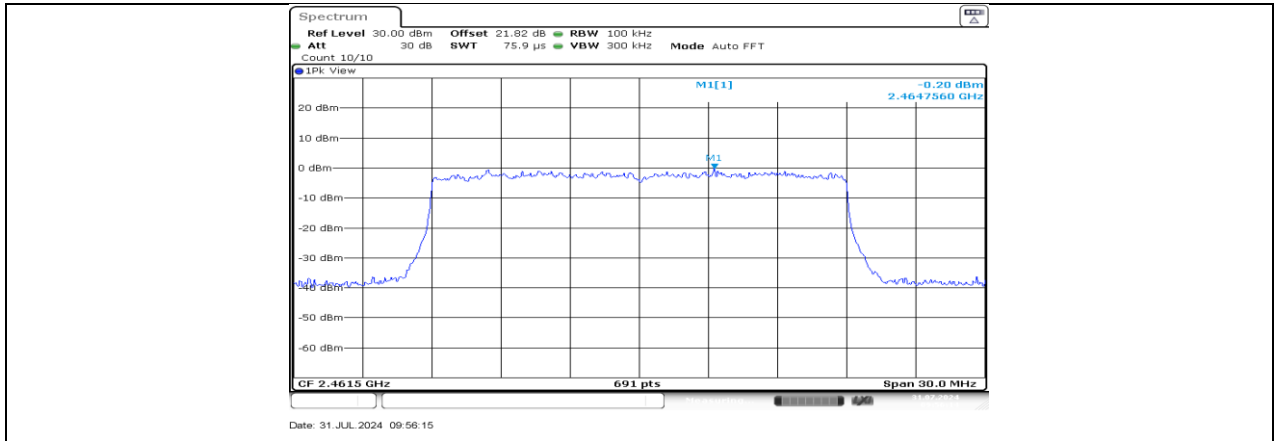
SRD 20M_Ant0_2461.5_0~Reference



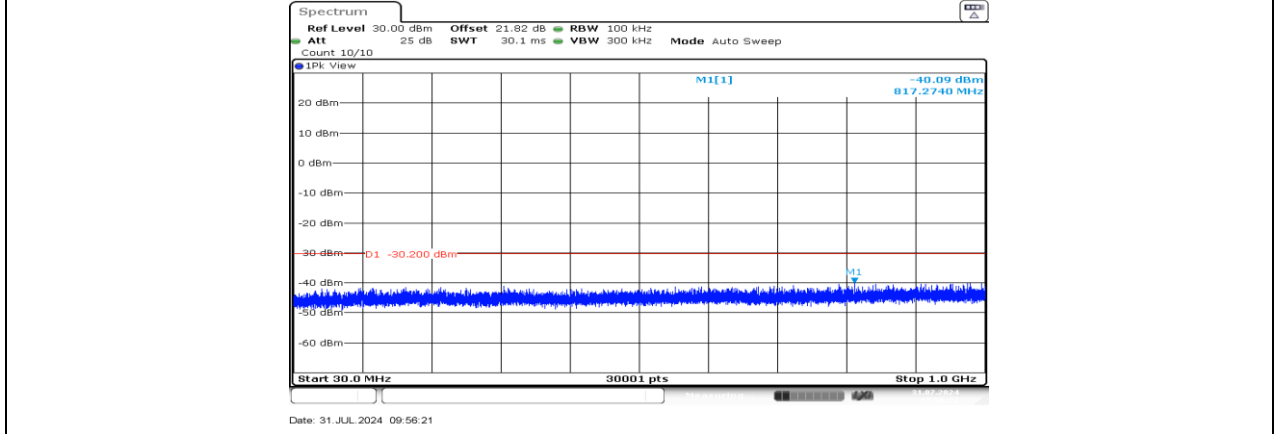
SRD 20M_Ant0_2461.5_30~1000



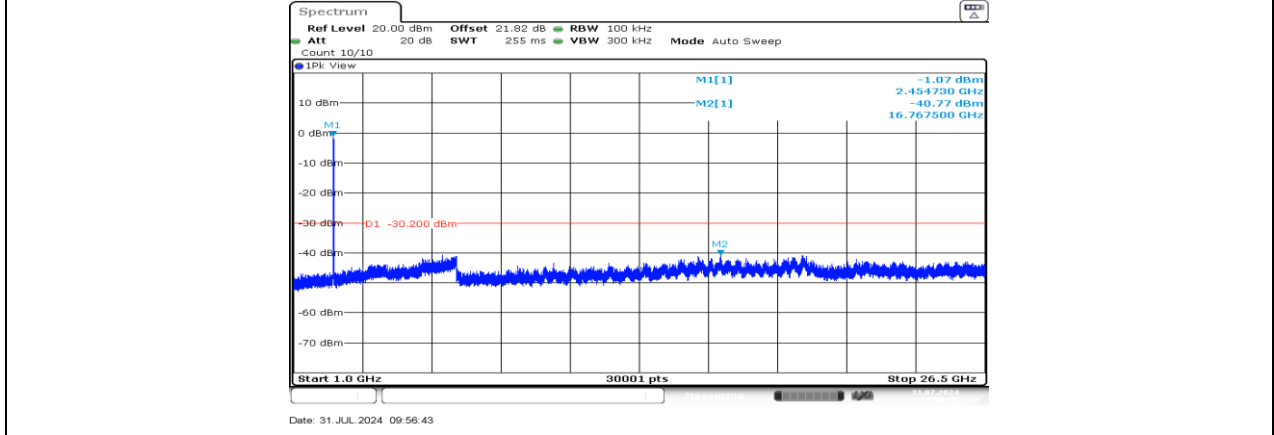
SRD 20M_Ant0_2461.5_1000~26500



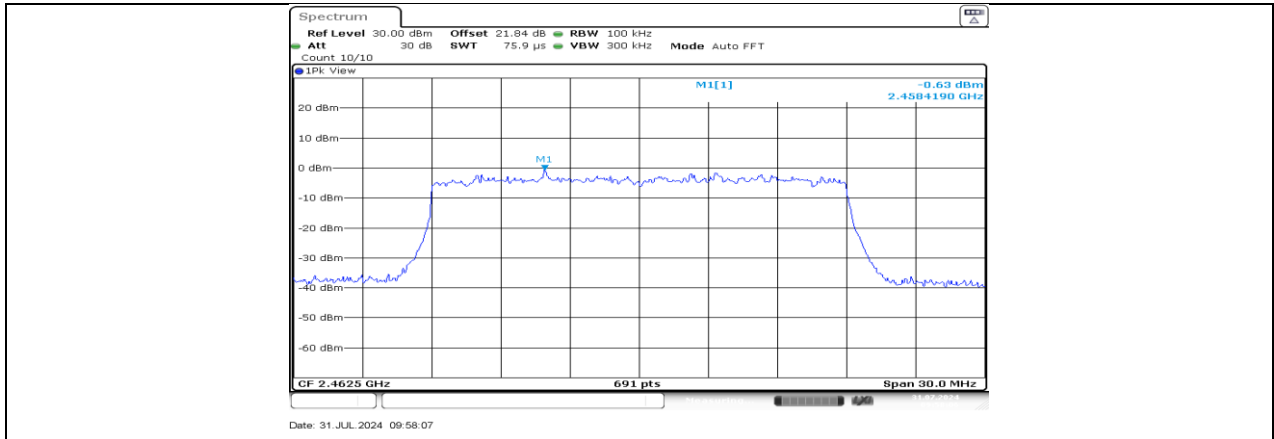
SRD 20M_Ant1_2461.5_0~Reference



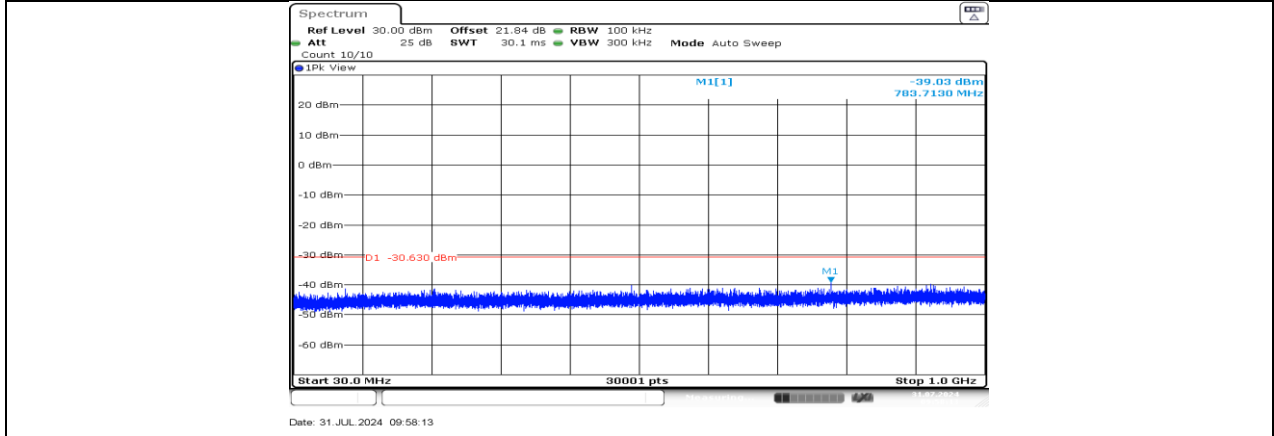
SRD 20M_Ant1_2461.5_30~1000



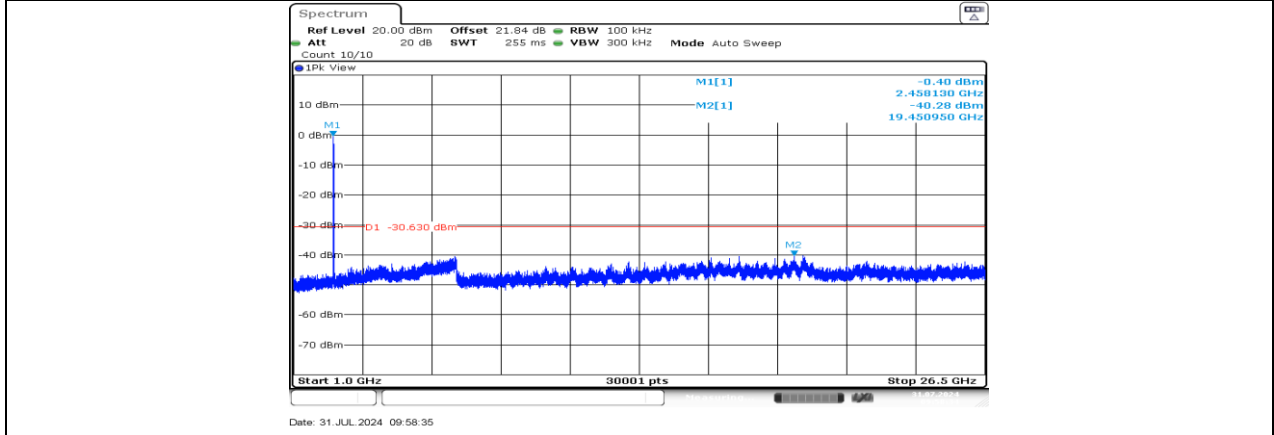
SRD 20M_Ant1_2461.5_1000~26500



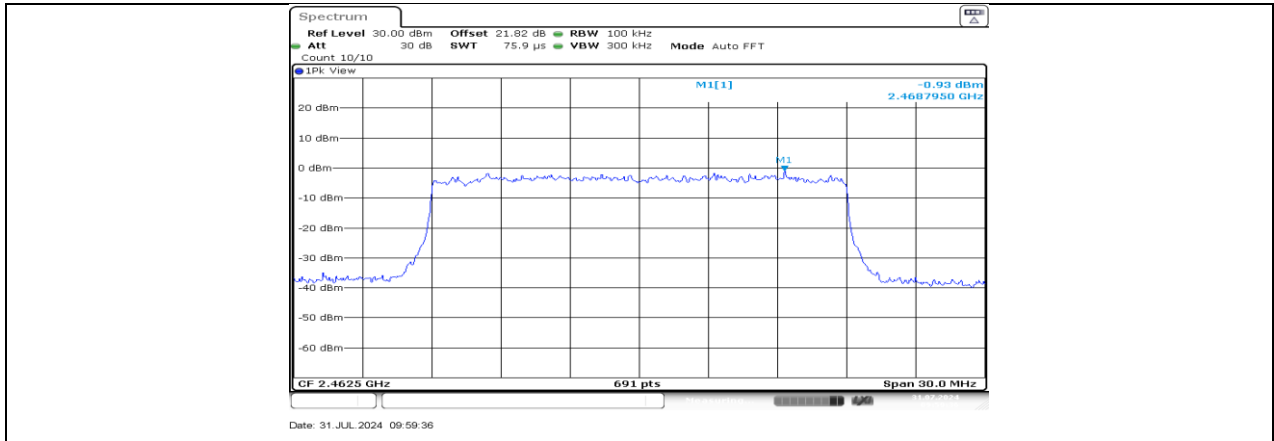
SRD 20M_Ant0_2462.5_0~Reference



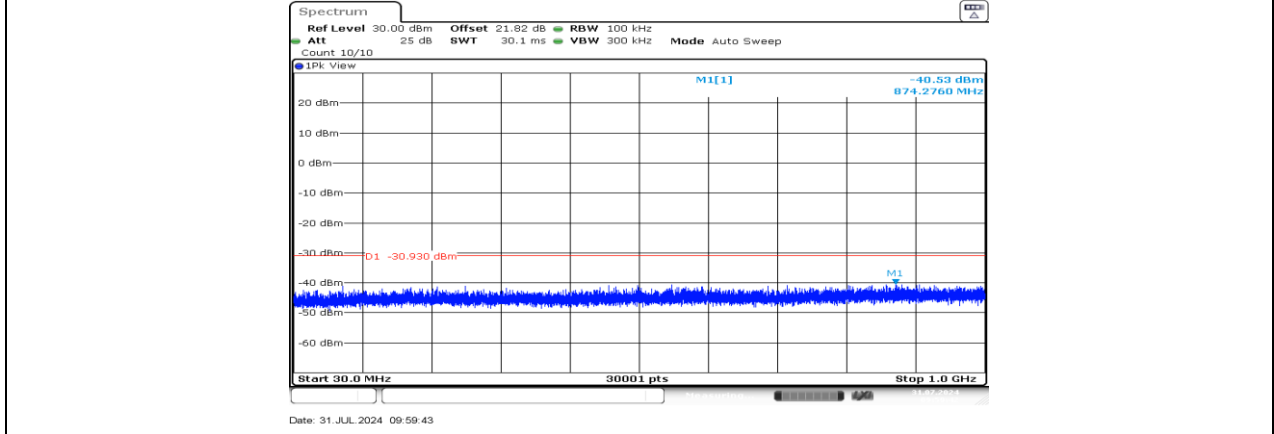
SRD 20M_Ant0_2462.5_30~1000



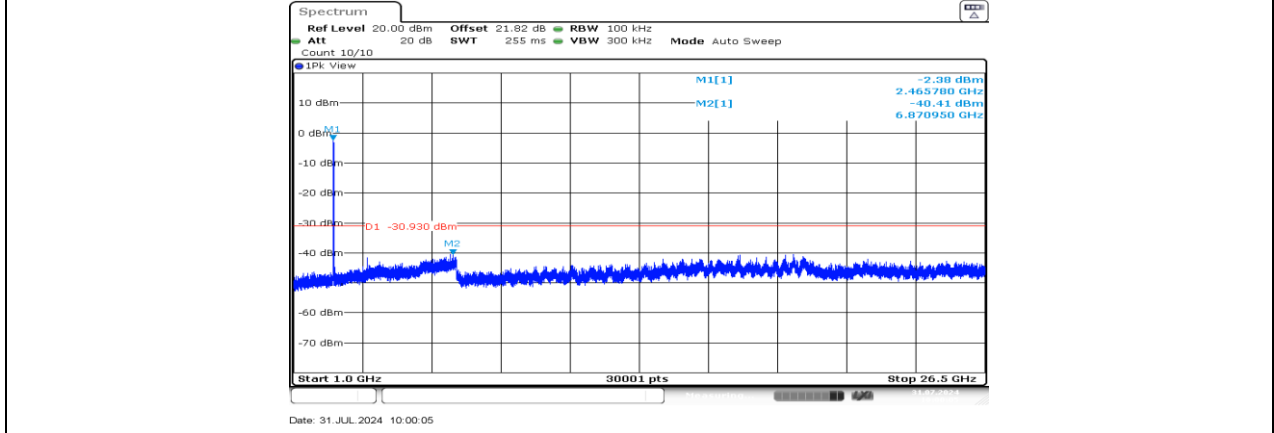
SRD 20M_Ant0_2462.5_1000~26500



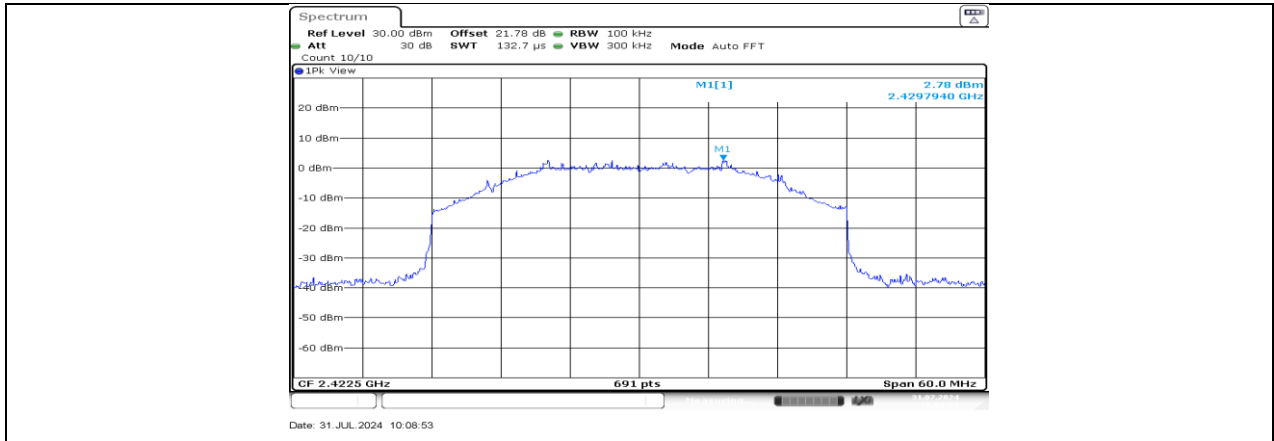
SRD 20M_Ant1_2462.5_0~Reference



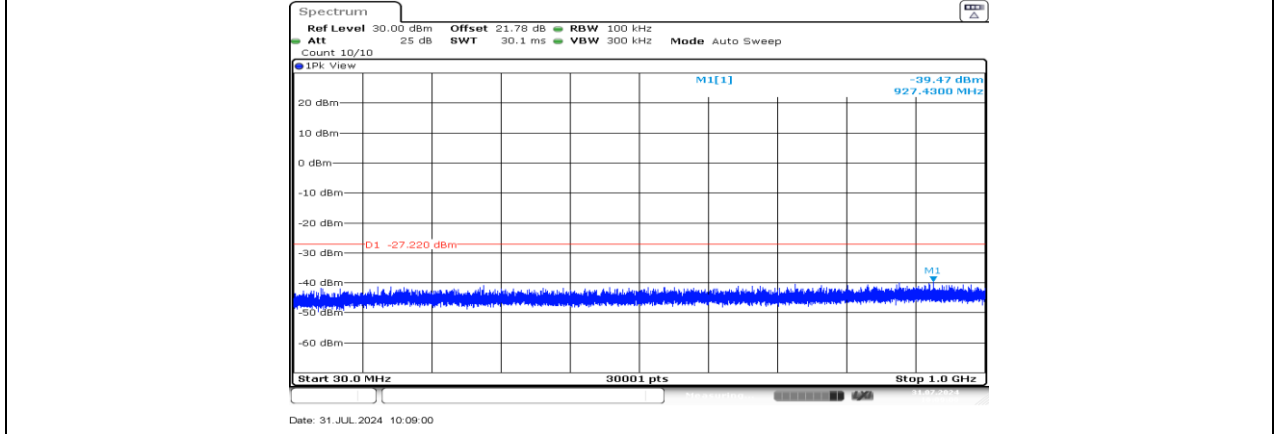
SRD 20M_Ant1_2462.5_30~1000



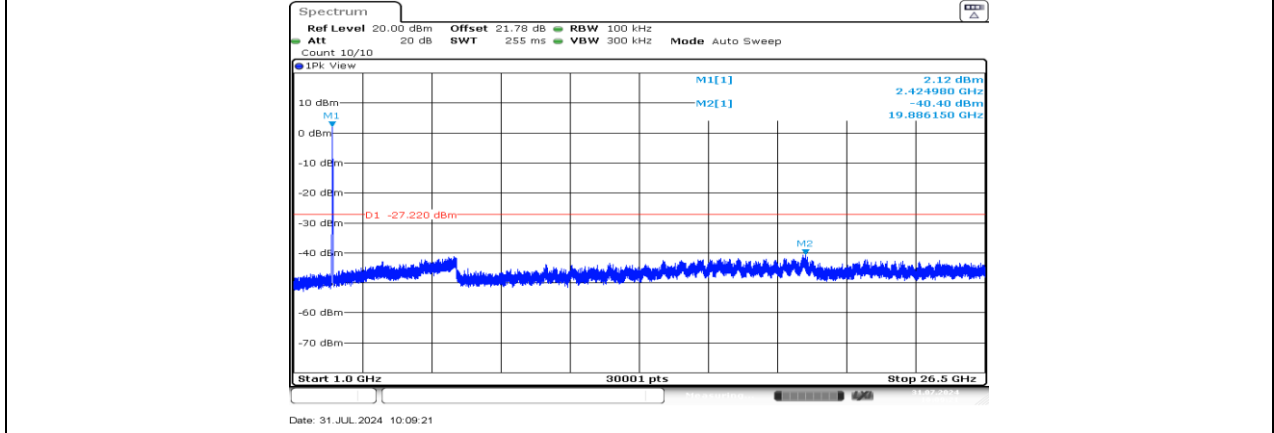
SRD 20M_Ant1_2462.5_1000~26500



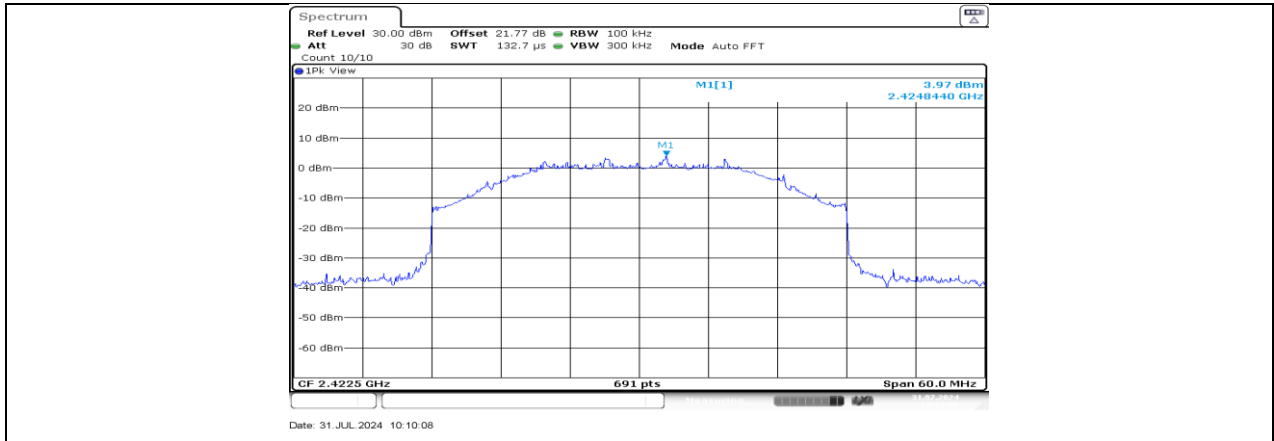
SRD 40M_Ant0_2422.5_0~Reference



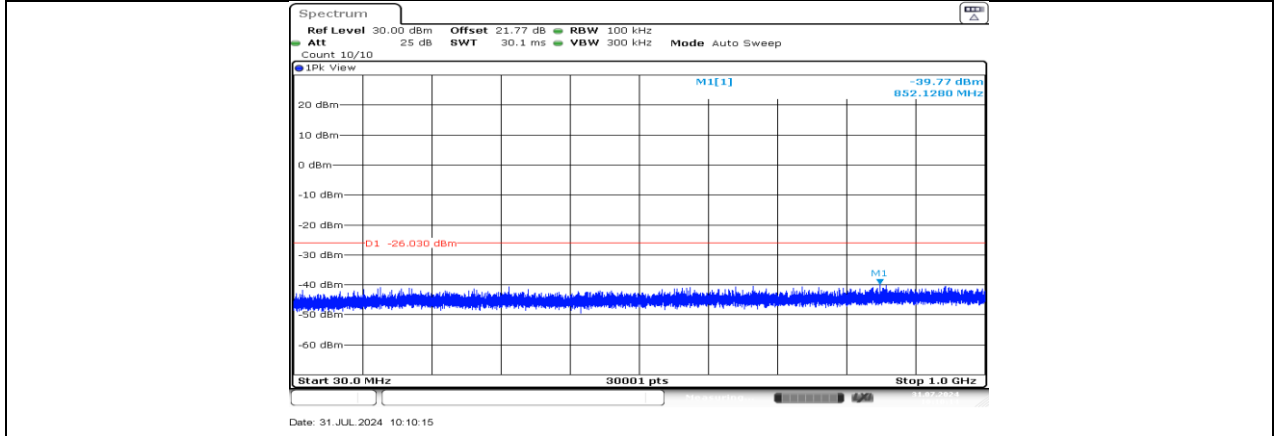
SRD 40M_Ant0_2422.5_30~1000



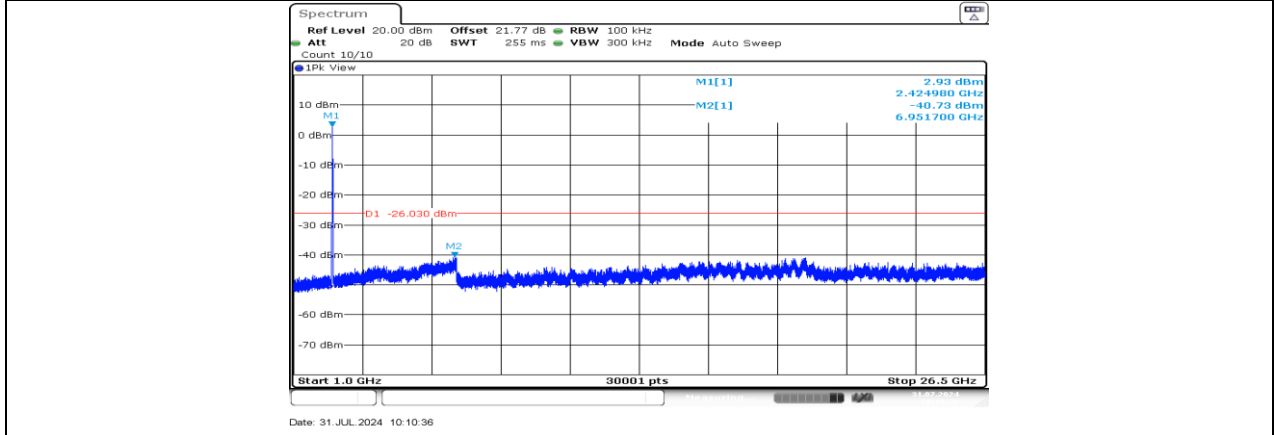
SRD 40M_Ant0_2422.5_1000~26500



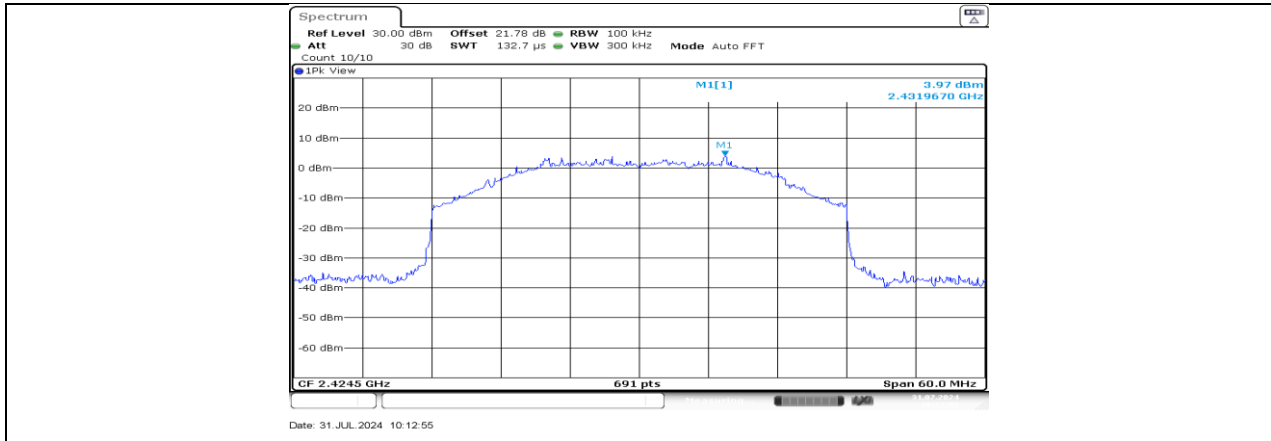
SRD 40M_Ant1_2422.5_0~Reference



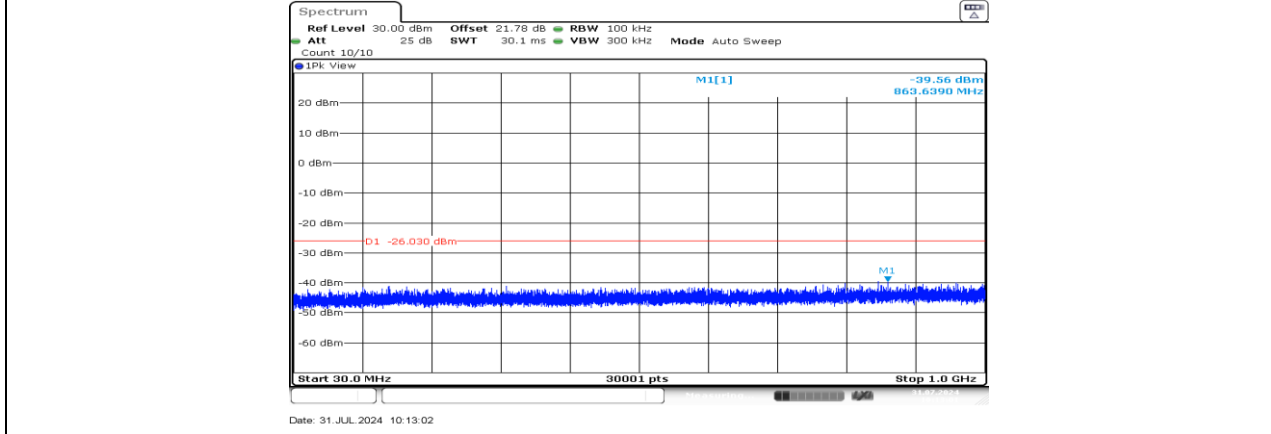
SRD 40M_Ant1_2422.5_30~1000



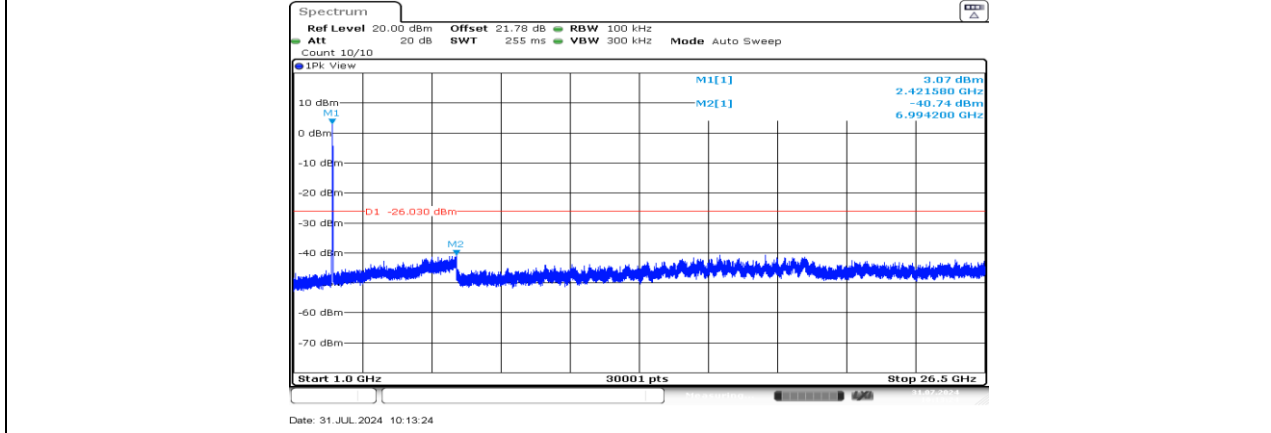
SRD 40M_Ant1_2422.5_1000~26500



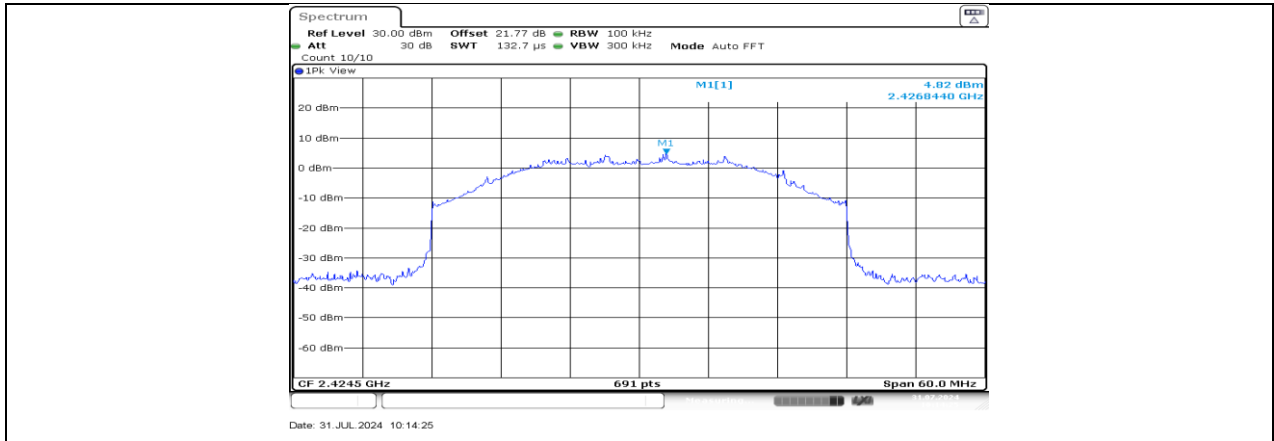
SRD 40M_Ant0_2424.5_0~Reference



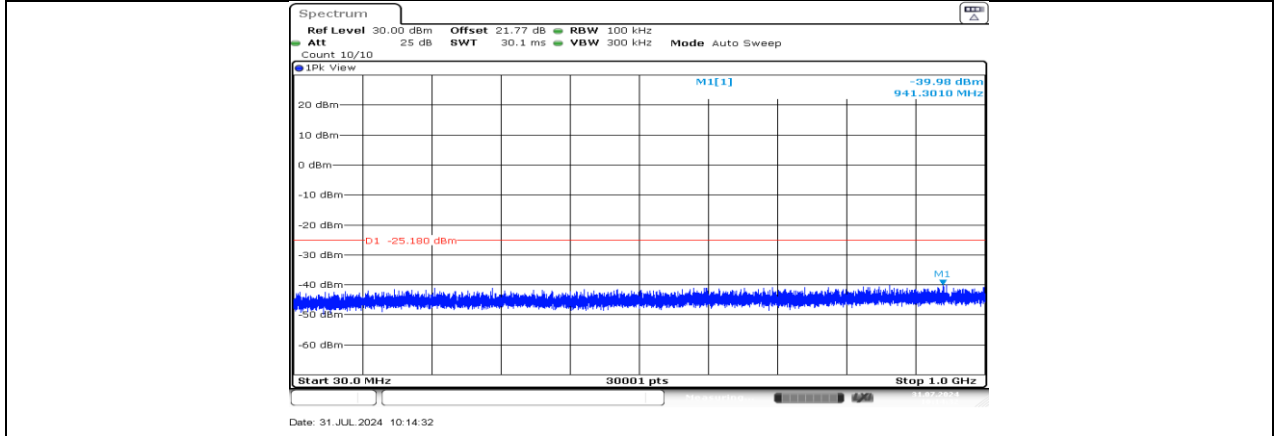
SRD 40M_Ant0_2424.5_30~1000



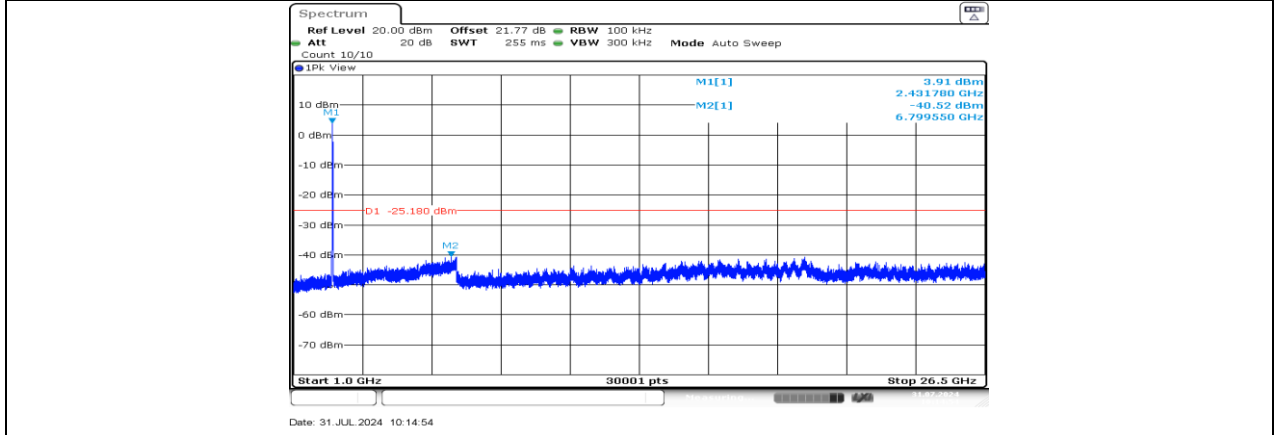
SRD 40M_Ant0_2424.5_1000~26500



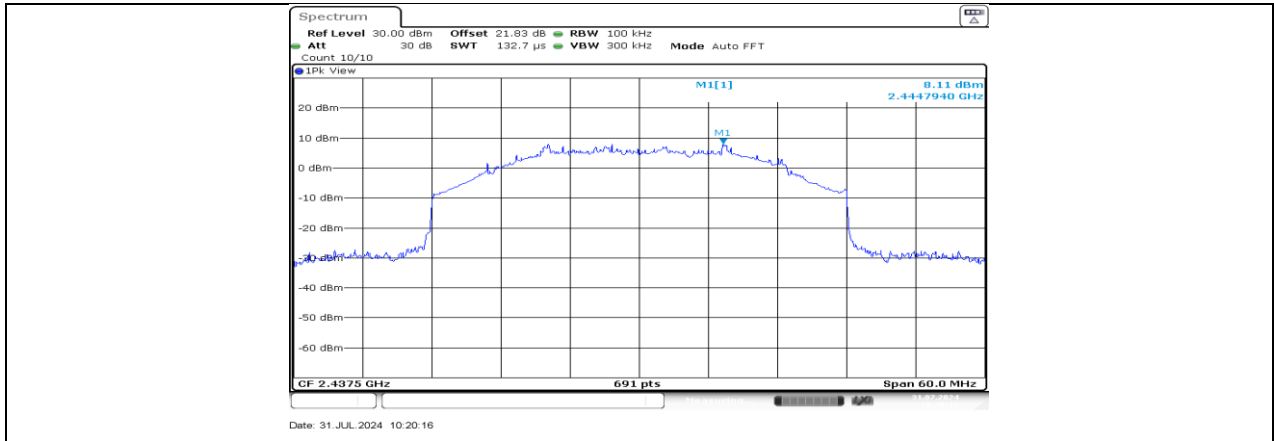
SRD 40M_Ant1_2424.5_0~Reference



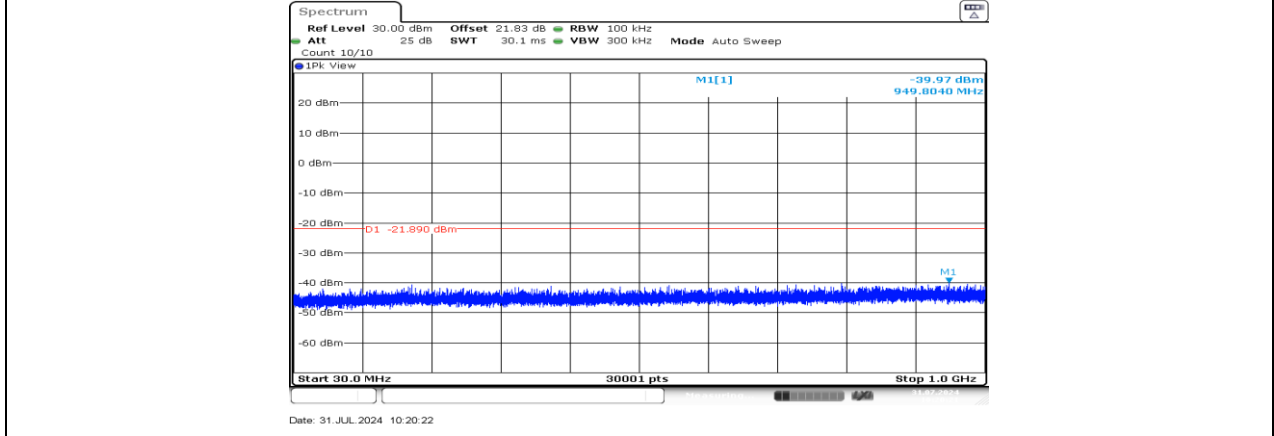
SRD 40M_Ant1_2424.5_30~1000



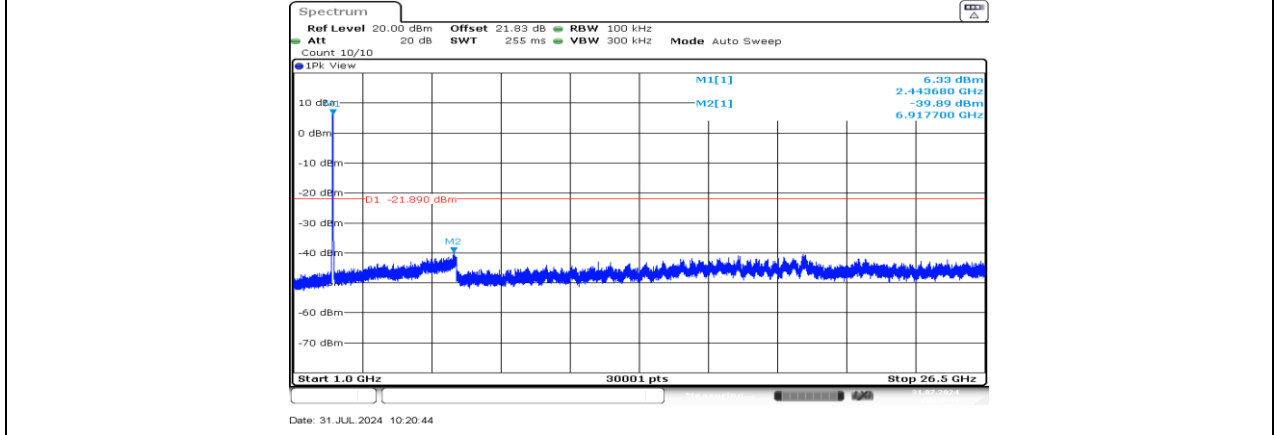
SRD 40M_Ant1_2424.5_1000~26500



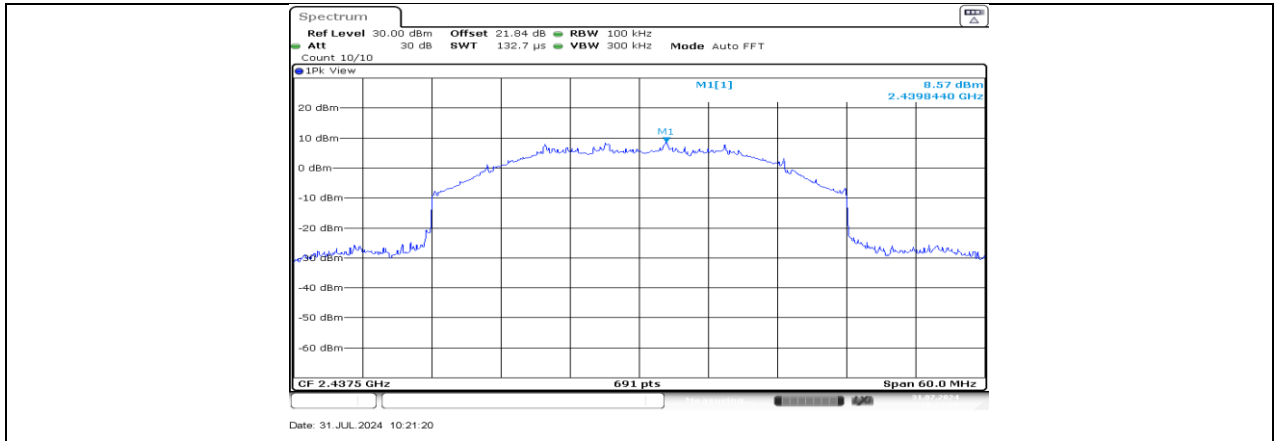
SRD 40M_Ant0_2437.5_0~Reference



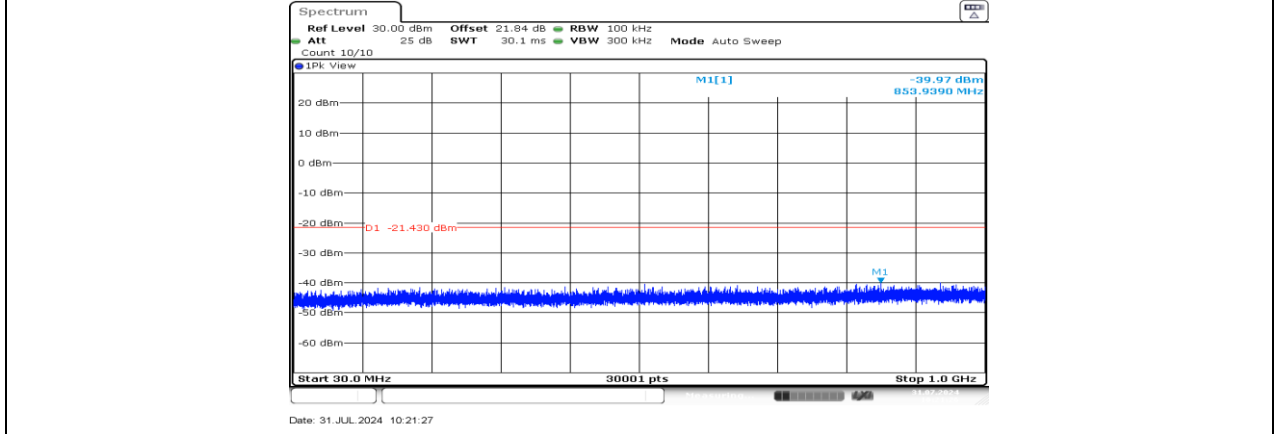
SRD 40M_Ant0_2437.5_30~1000



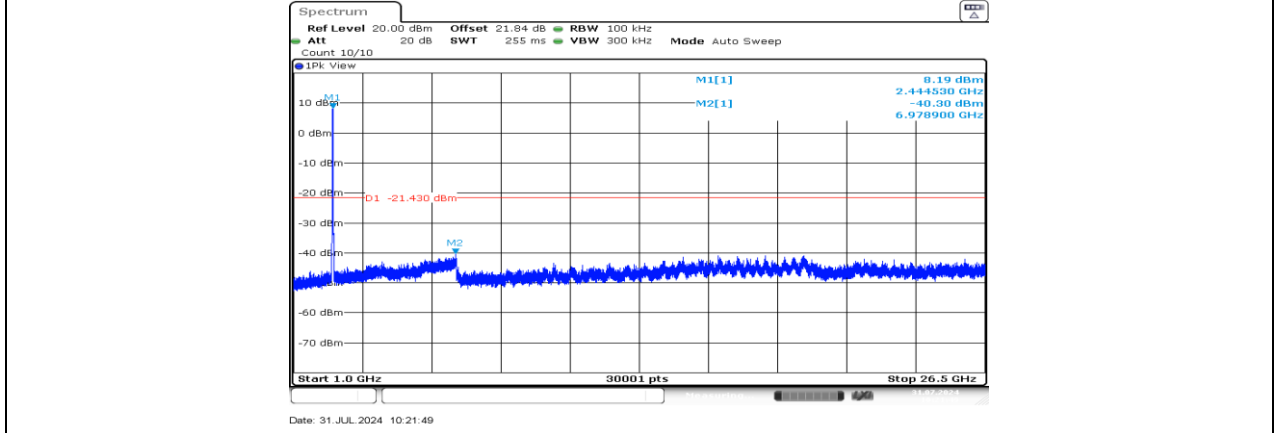
SRD 40M_Ant0_2437.5_1000~26500



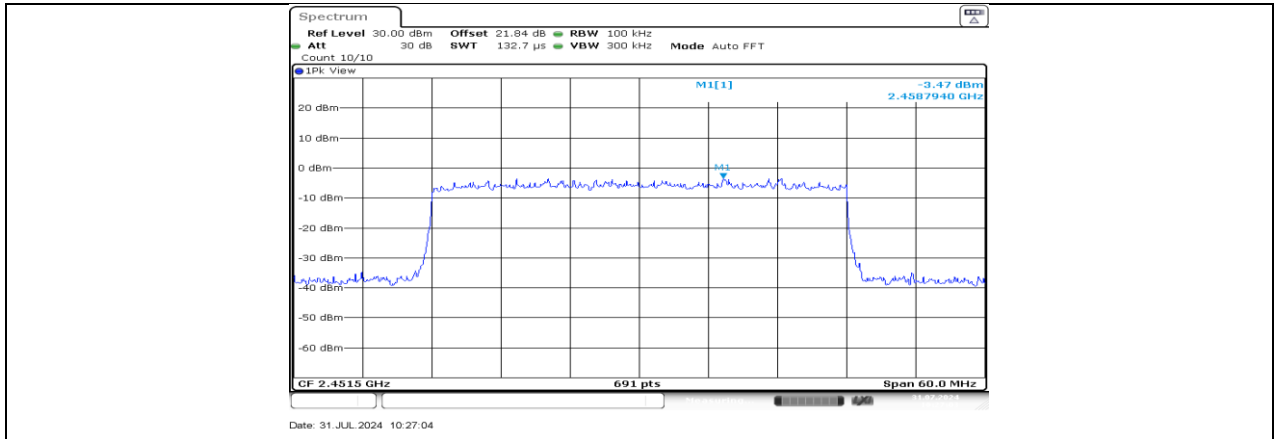
SRD 40M_Ant1_2437.5_0~Reference



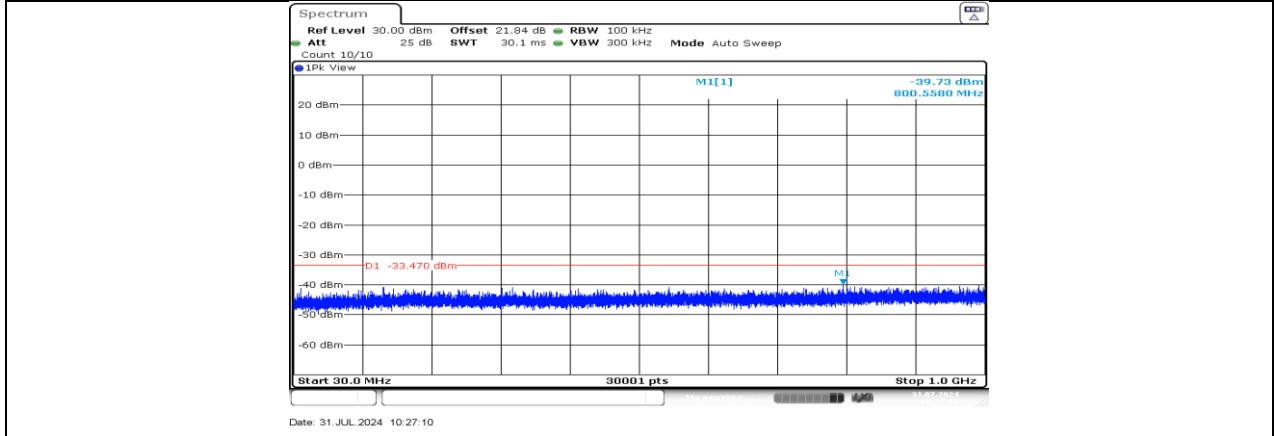
SRD 40M_Ant1_2437.5_30~1000



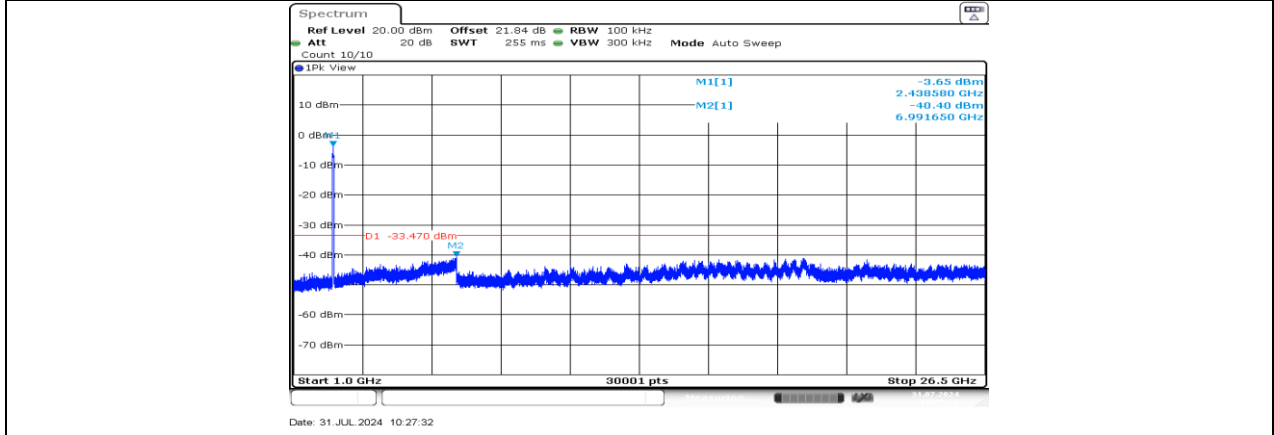
SRD 40M_Ant1_2437.5_1000~26500



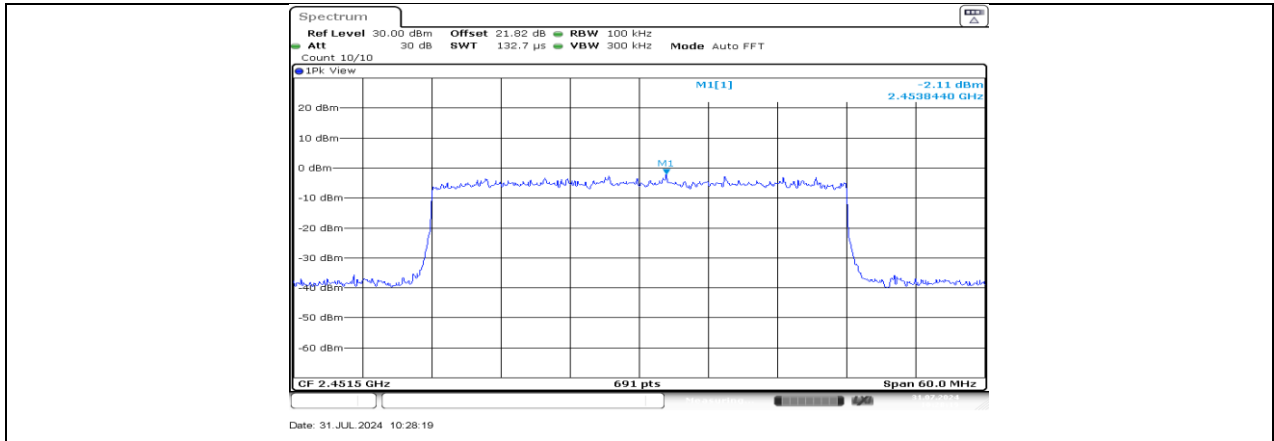
SRD 40M_Ant0_2451.5_0~Reference



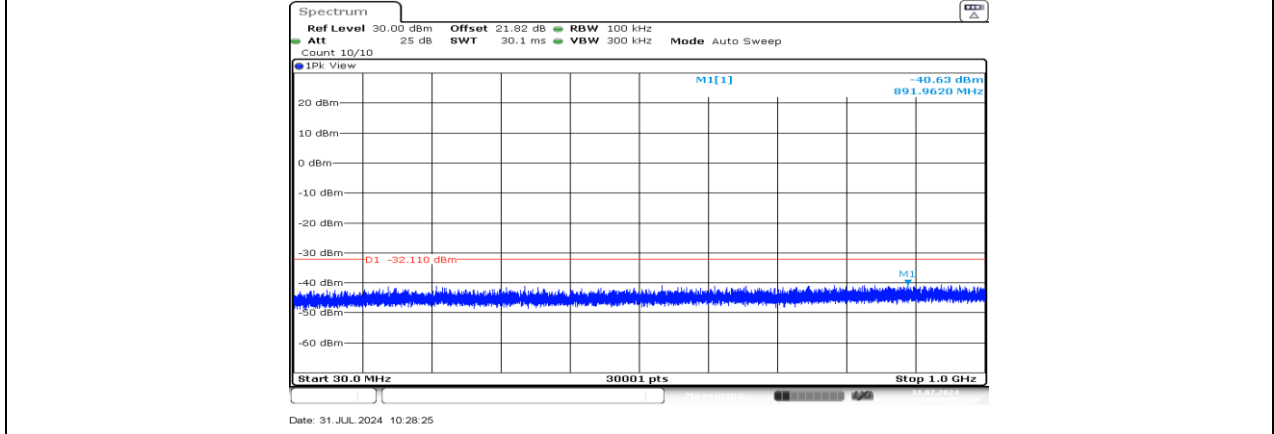
SRD 40M_Ant0_2451.5_30~1000



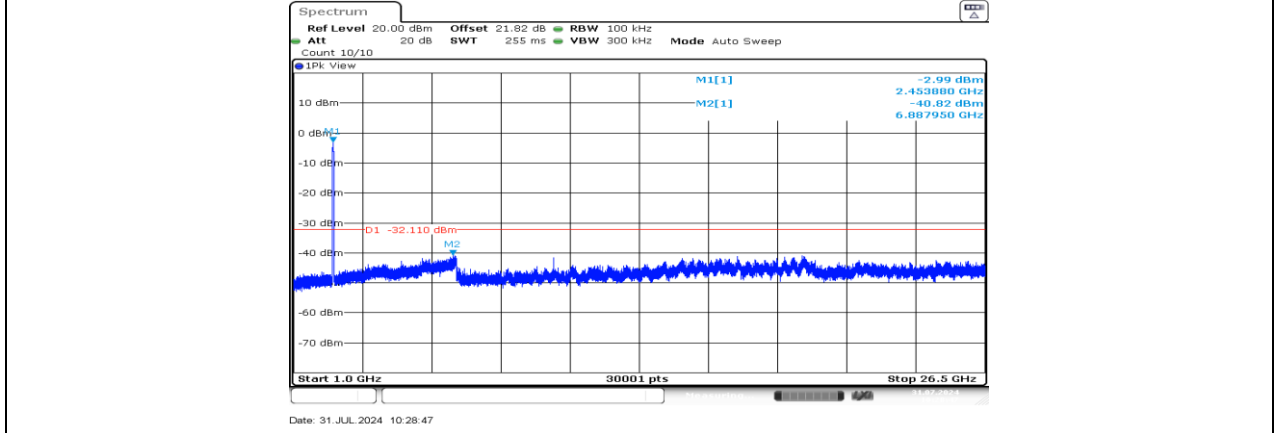
SRD 40M_Ant0_2451.5_1000~26500



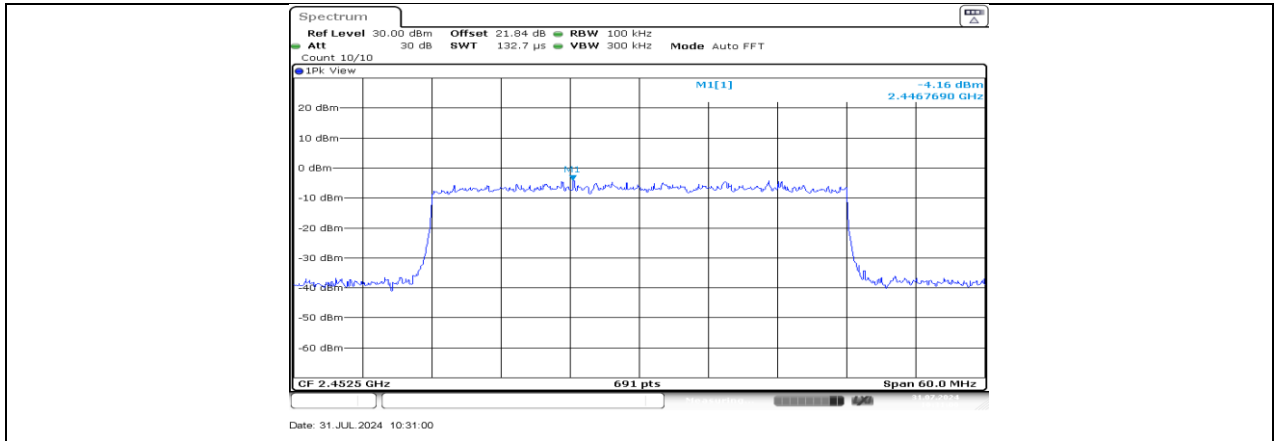
SRD 40M_Ant1_2451.5_0~Reference



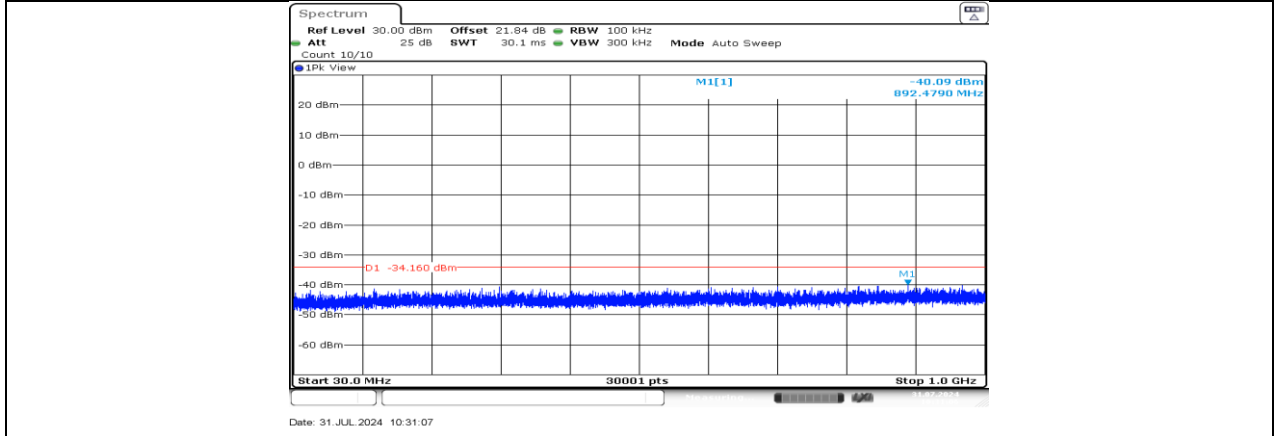
SRD 40M_Ant1_2451.5_30~1000



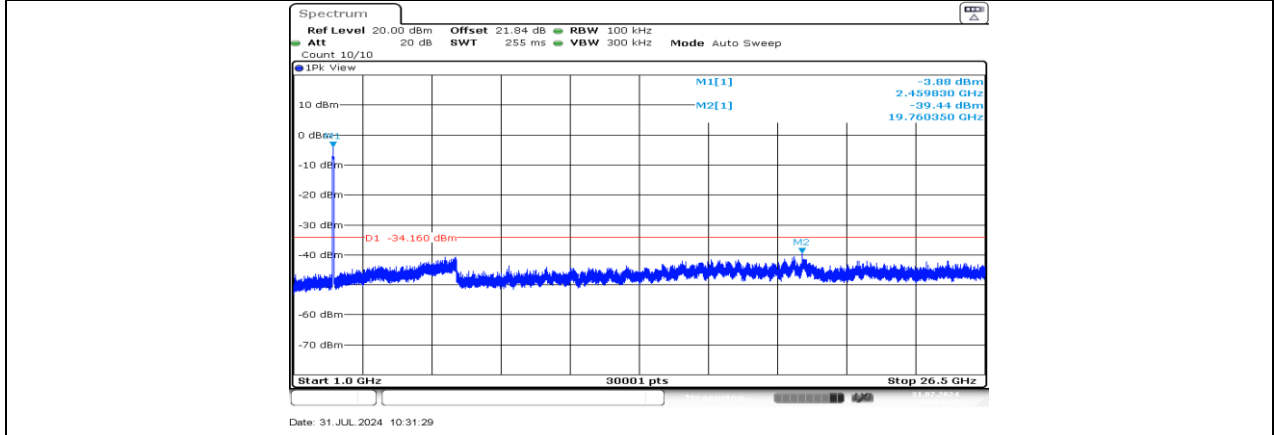
SRD 40M_Ant1_2451.5_1000~26500



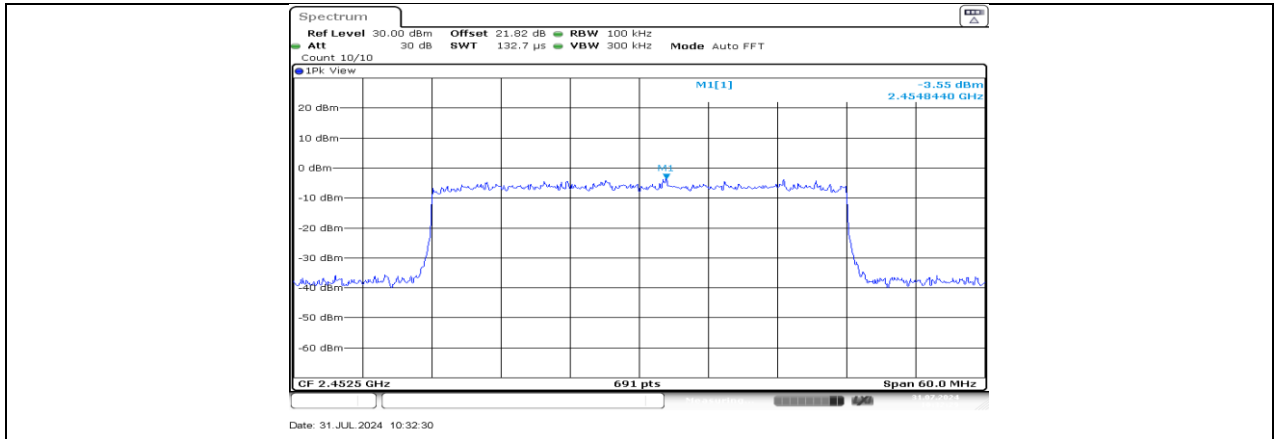
SRD 40M_Ant0_2452.5_0~Reference



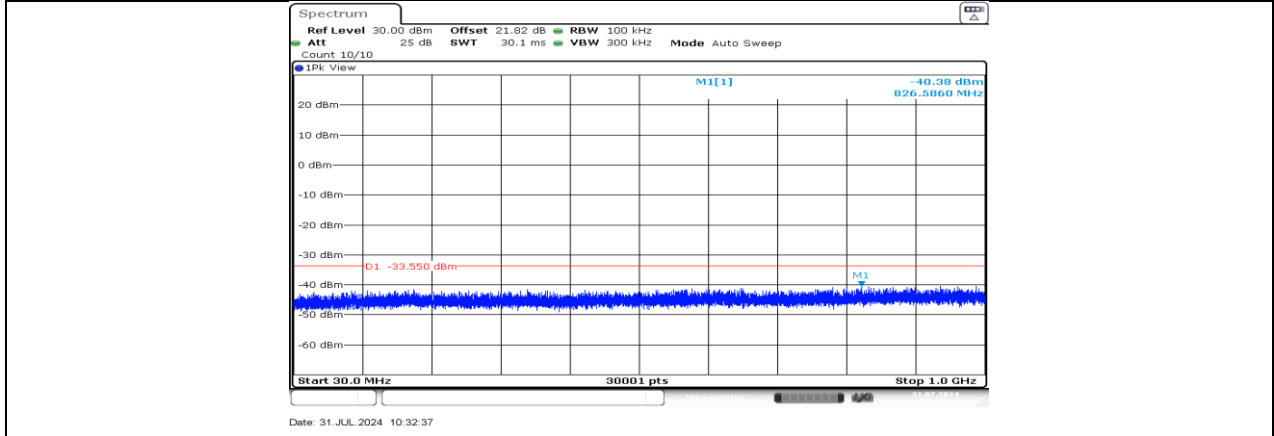
SRD 40M_Ant0_2452.5_30~1000



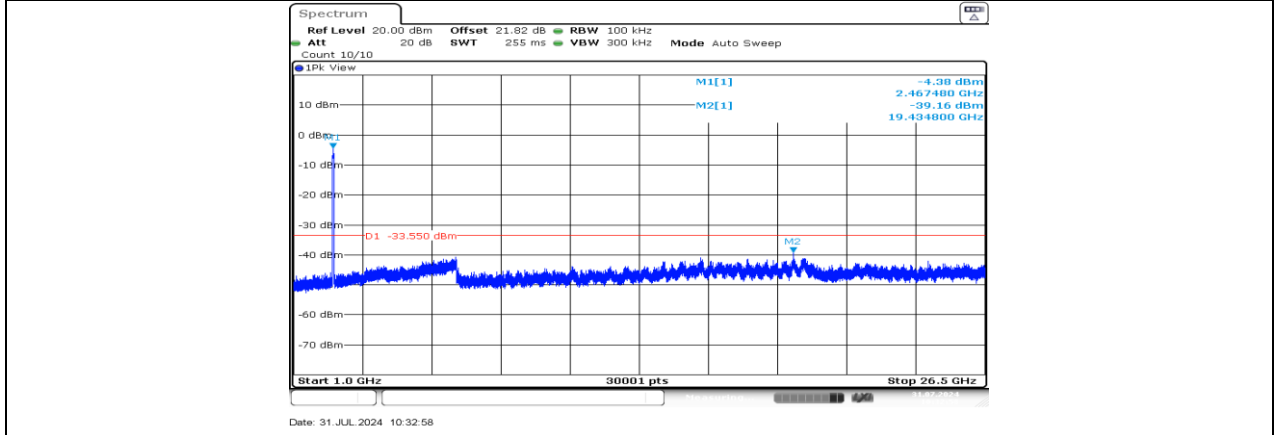
SRD 40M_Ant0_2452.5_1000~26500



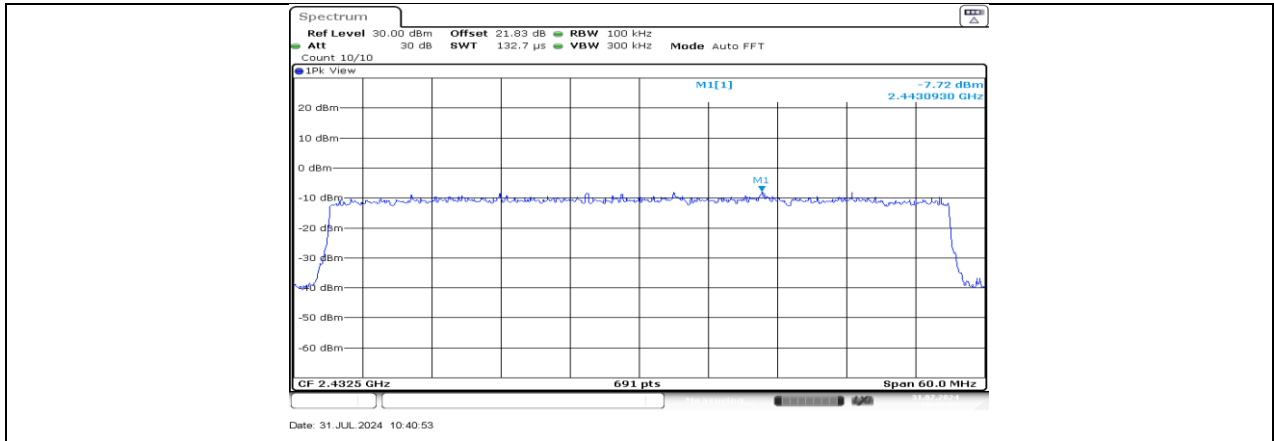
SRD 40M_Ant1_2452.5_0~Reference



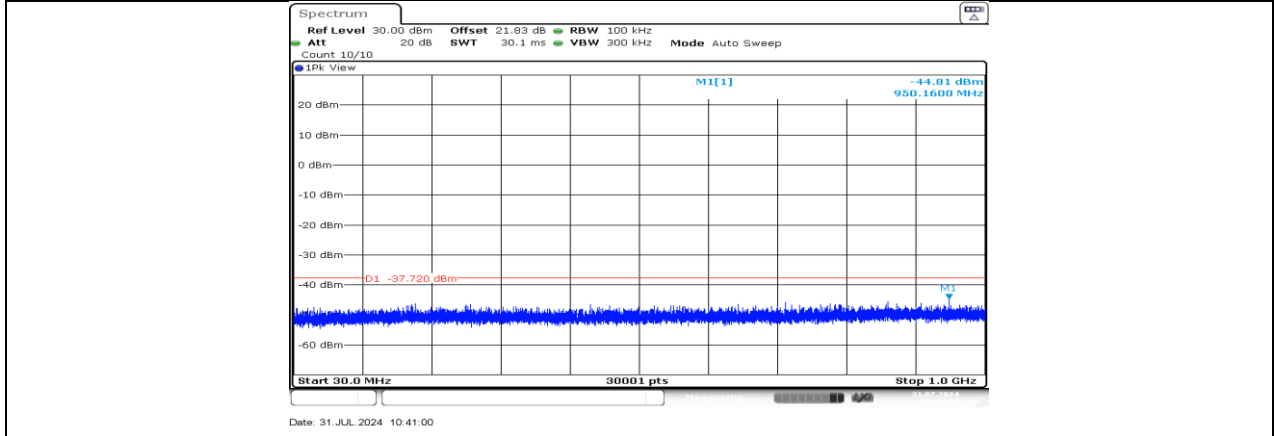
SRD 40M_Ant1_2452.5_30~1000



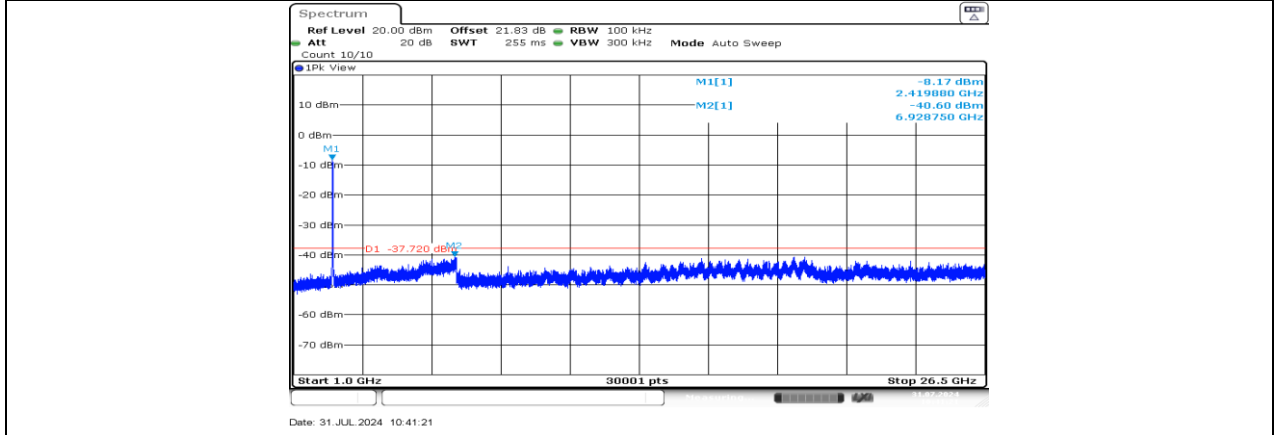
SRD 40M_Ant1_2452.5_1000~26500



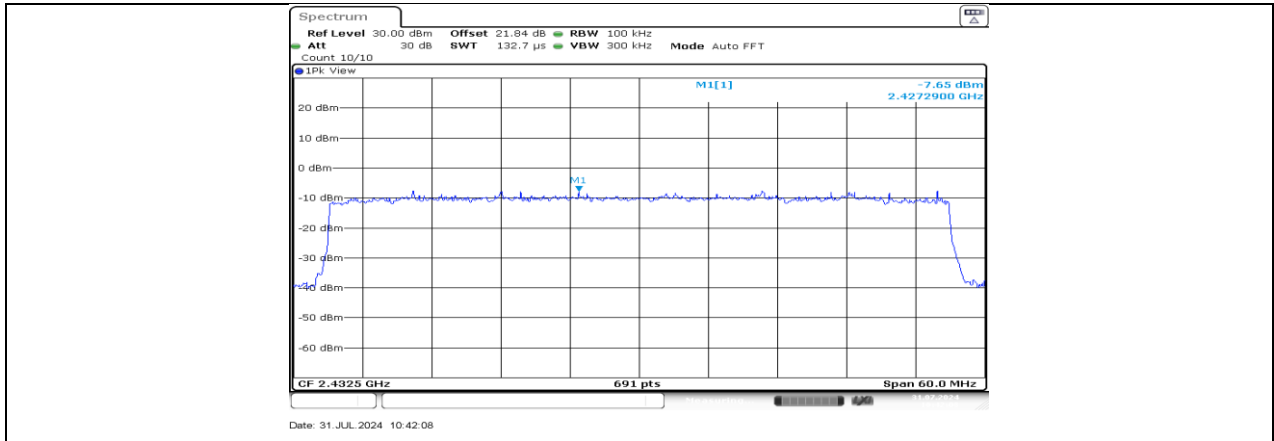
SRD 60M_Ant0_2432.5_0~Reference



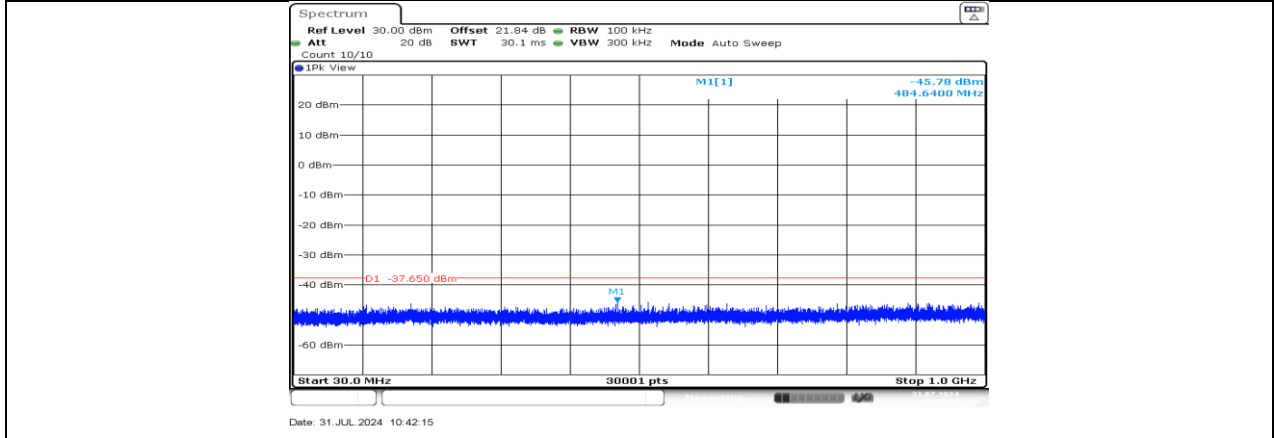
SRD 60M_Ant0_2432.5_30~1000



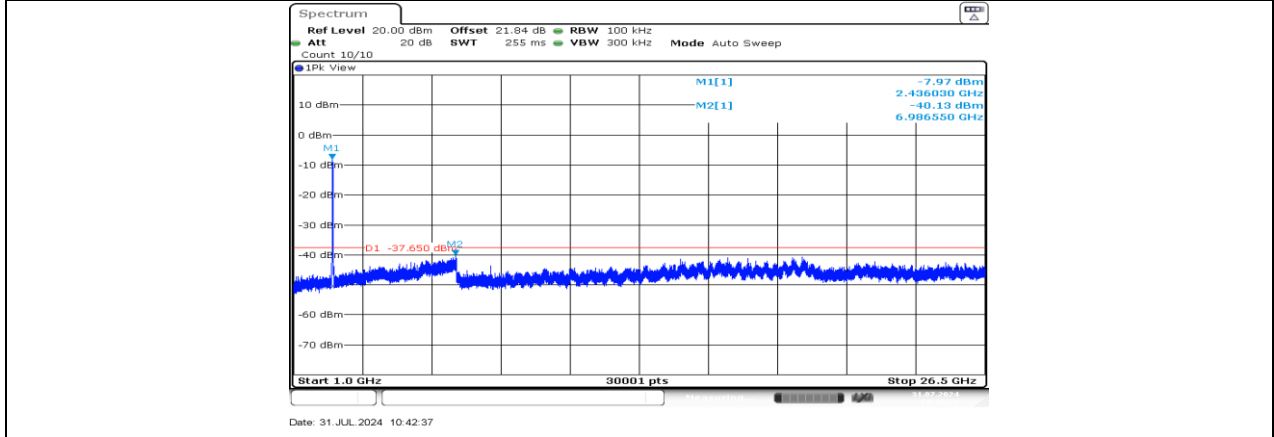
SRD 60M_Ant0_2432.5_1000~26500



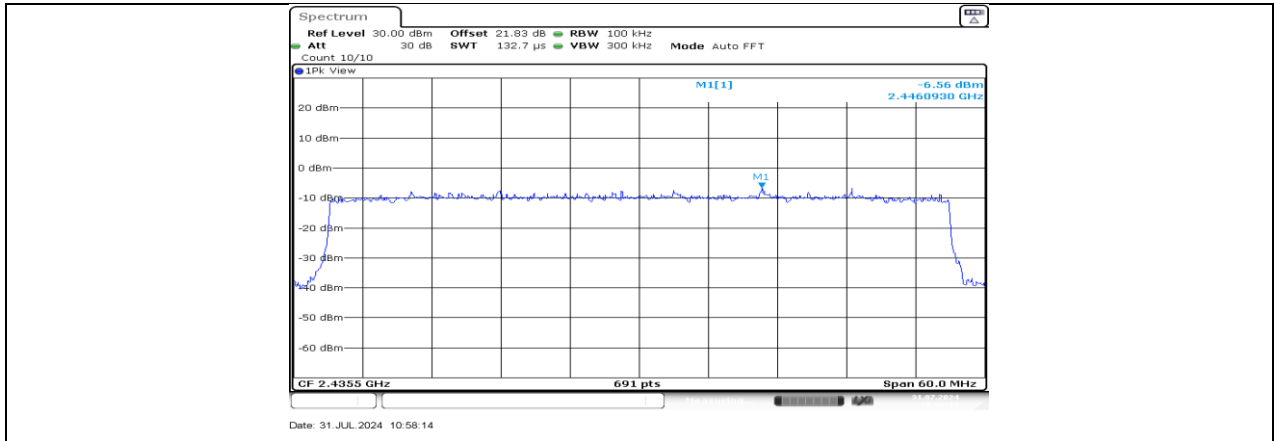
SRD 60M_Ant1_2432.5_0~Reference



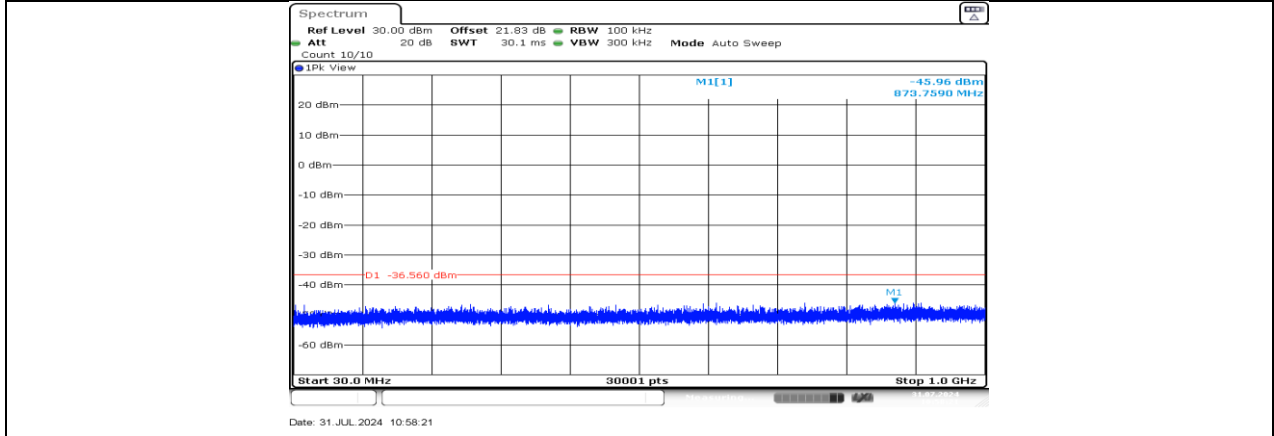
SRD 60M_Ant1_2432.5_30~1000



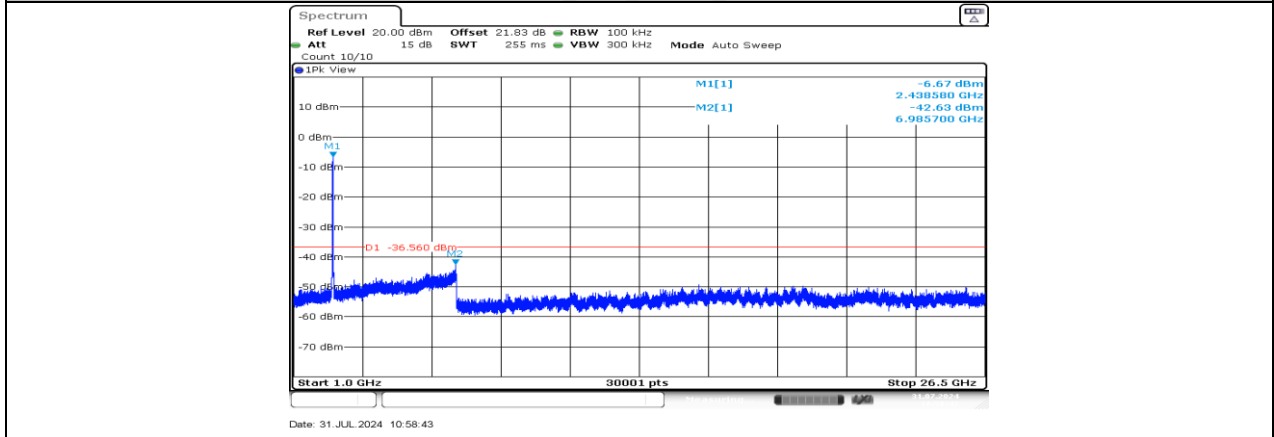
SRD 60M_Ant1_2432.5_1000~26500



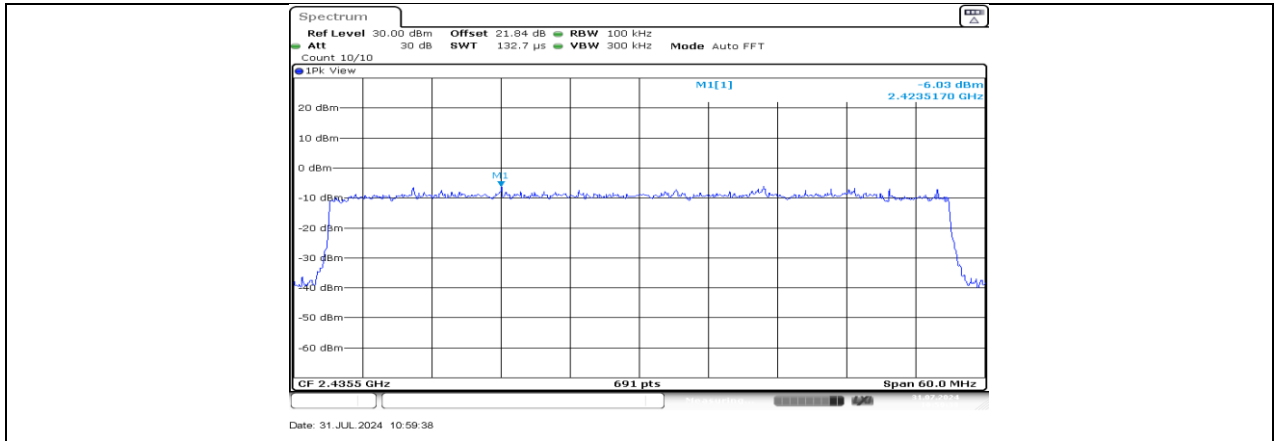
SRD 60M_Ant0_2435.5_0~Reference



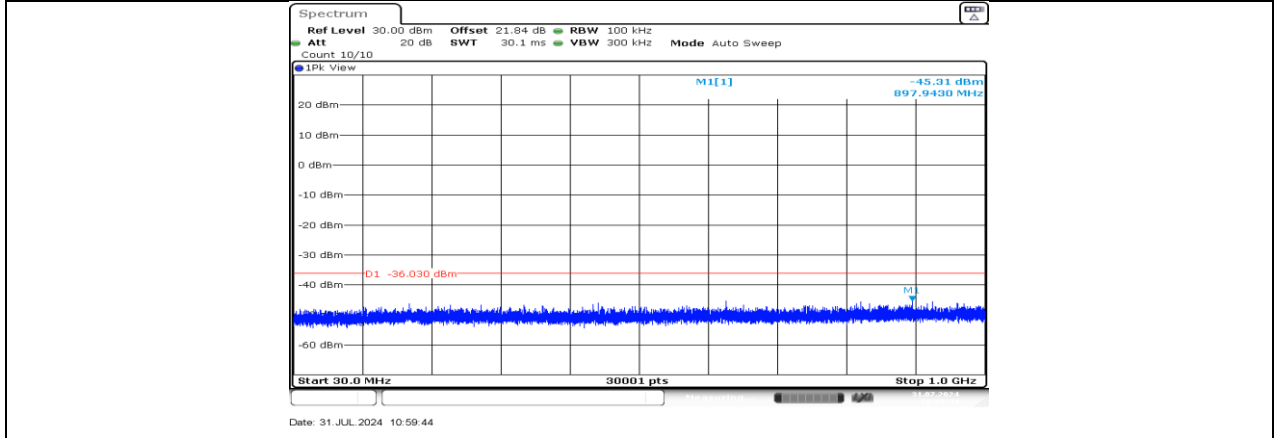
SRD 60M_Ant0_2435.5_30~1000



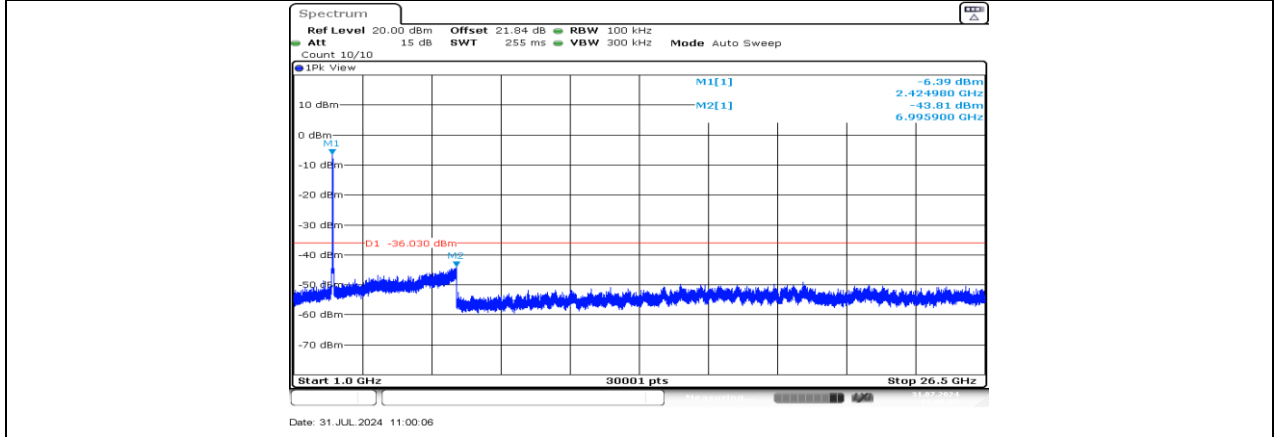
SRD 60M_Ant0_2435.5_1000~26500



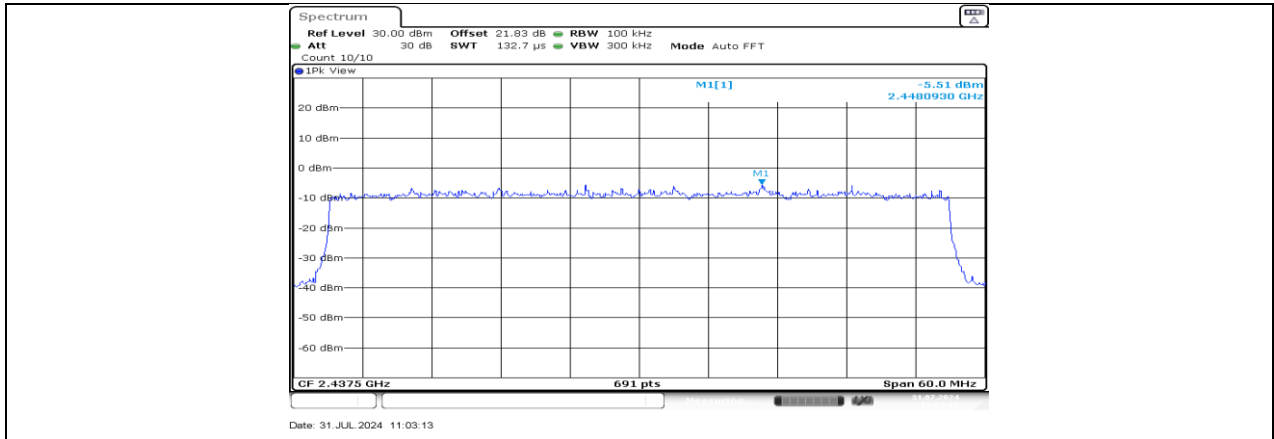
SRD 60M_Ant1_2435.5_0~Reference



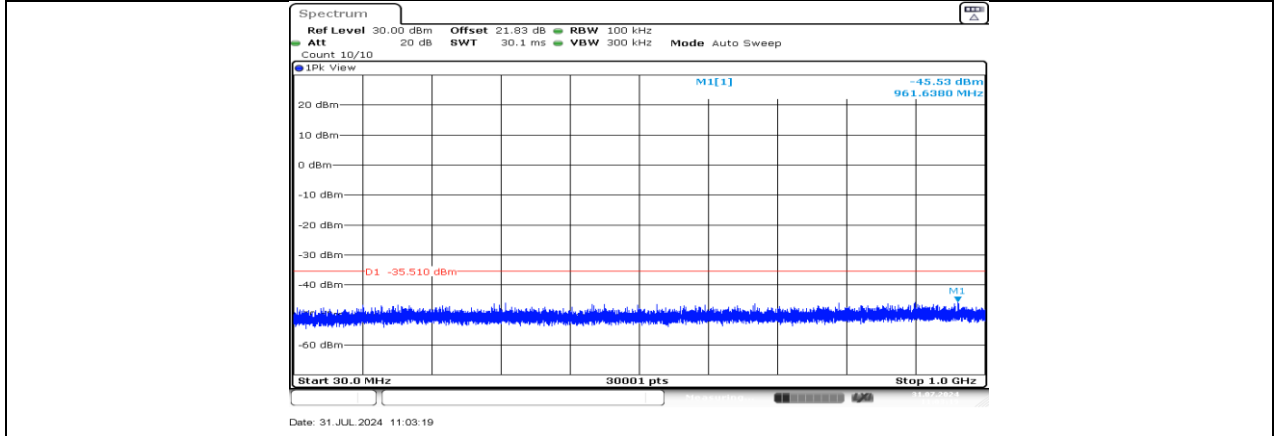
SRD 60M_Ant1_2435.5_30~1000



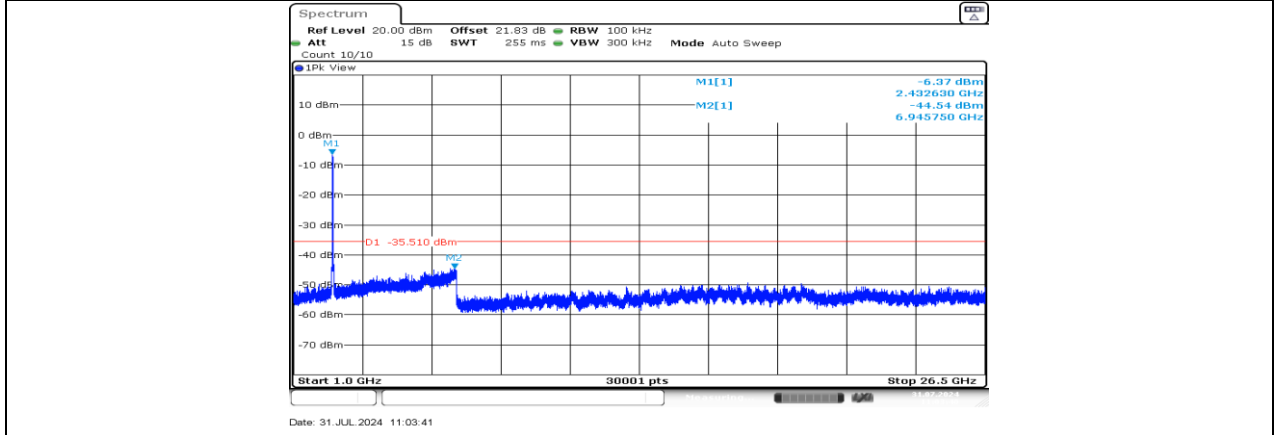
SRD 60M_Ant1_2435.5_1000~26500



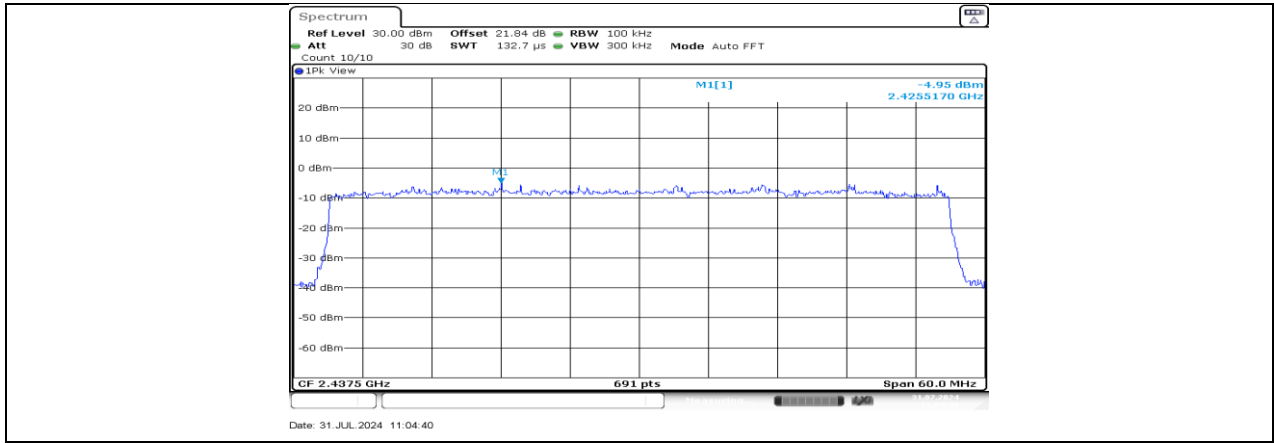
SRD 60M_Ant0_2437.5_0~Reference



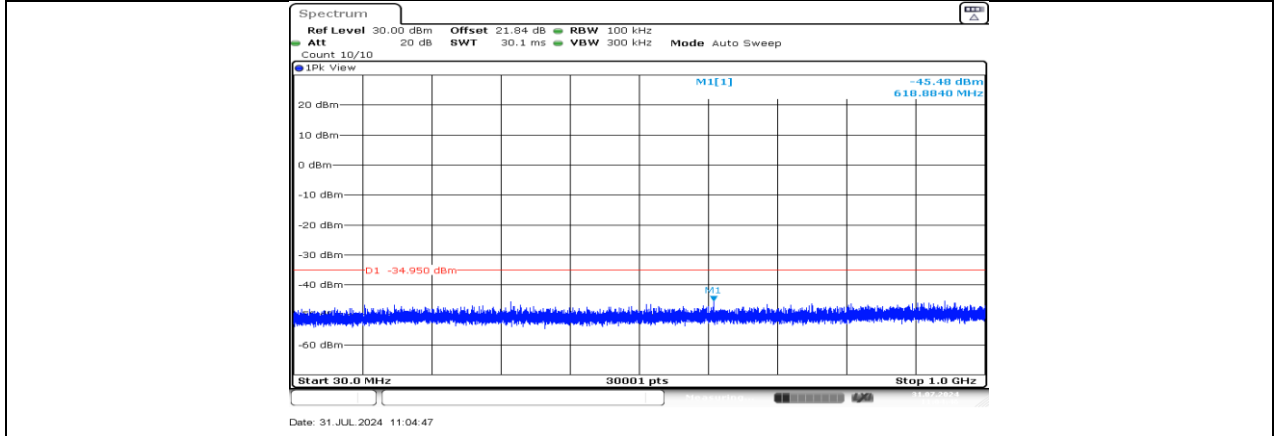
SRD 60M_Ant0_2437.5_30~1000



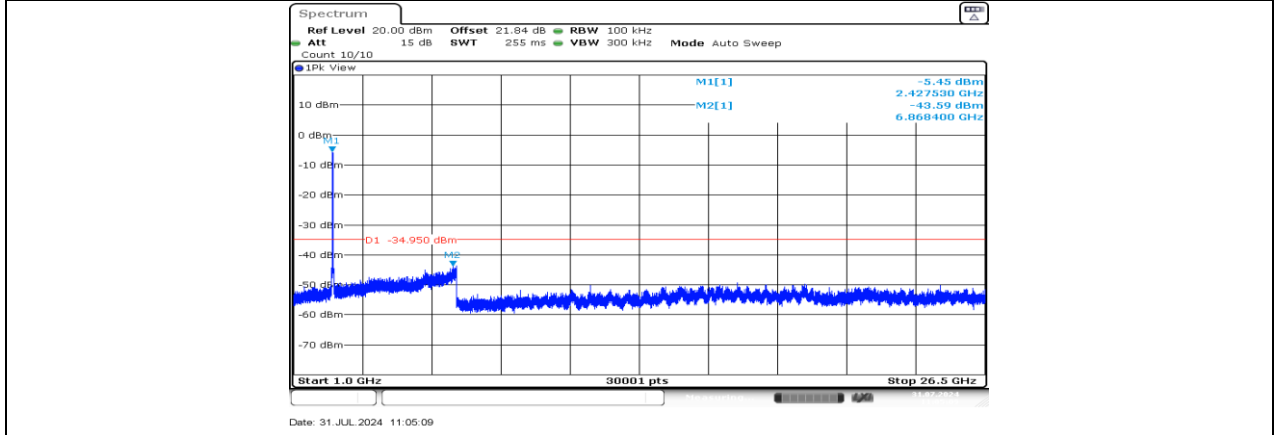
SRD 60M_Ant0_2437.5_1000~26500



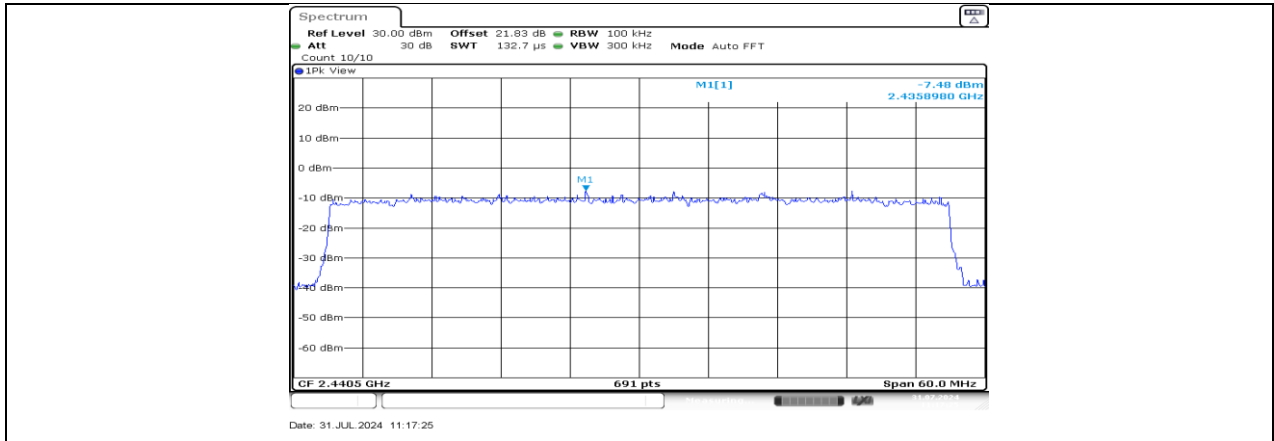
SRD 60M_Ant1_2437.5_0~Reference



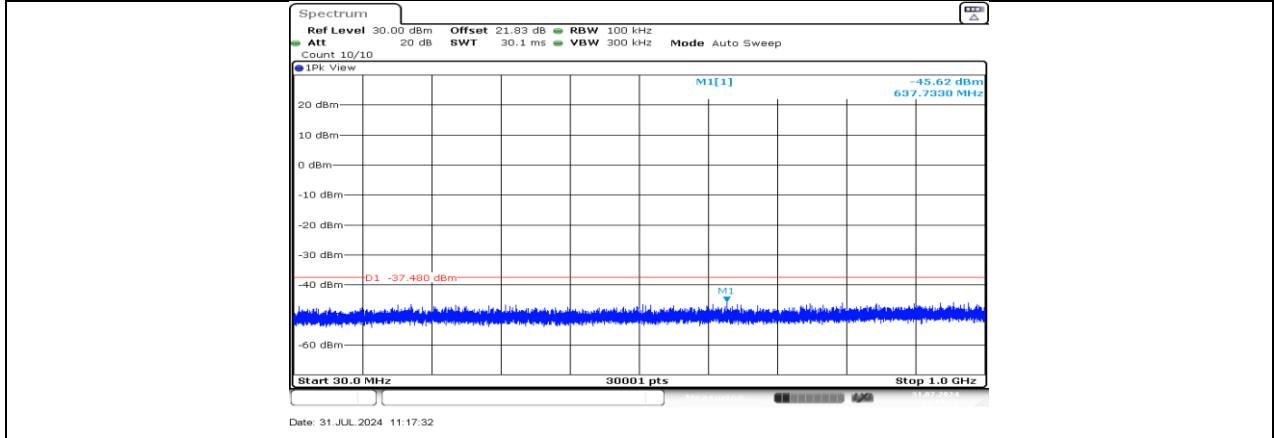
SRD 60M_Ant1_2437.5_30~1000



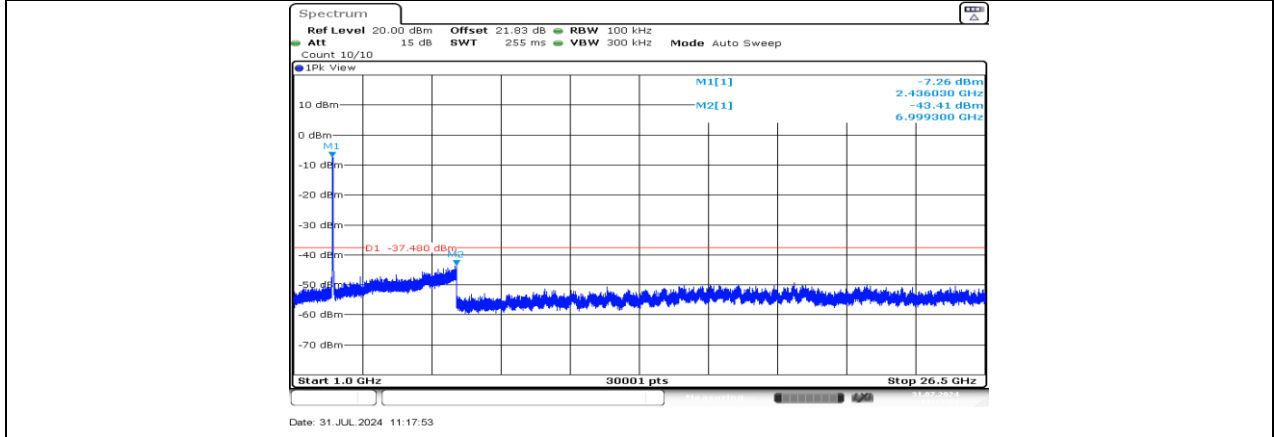
SRD 60M_Ant1_2437.5_1000~26500



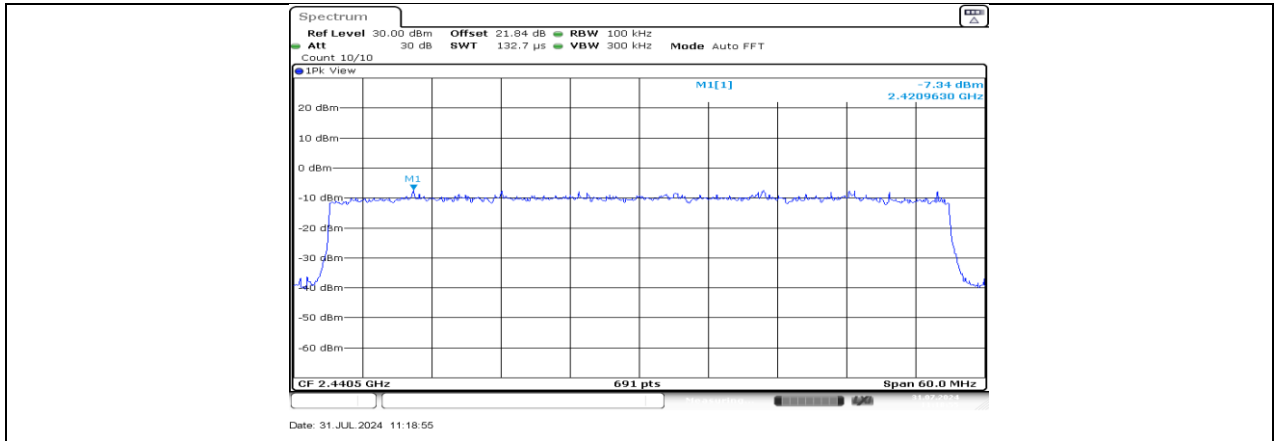
SRD 60M_Ant0_2440.5_0~Reference



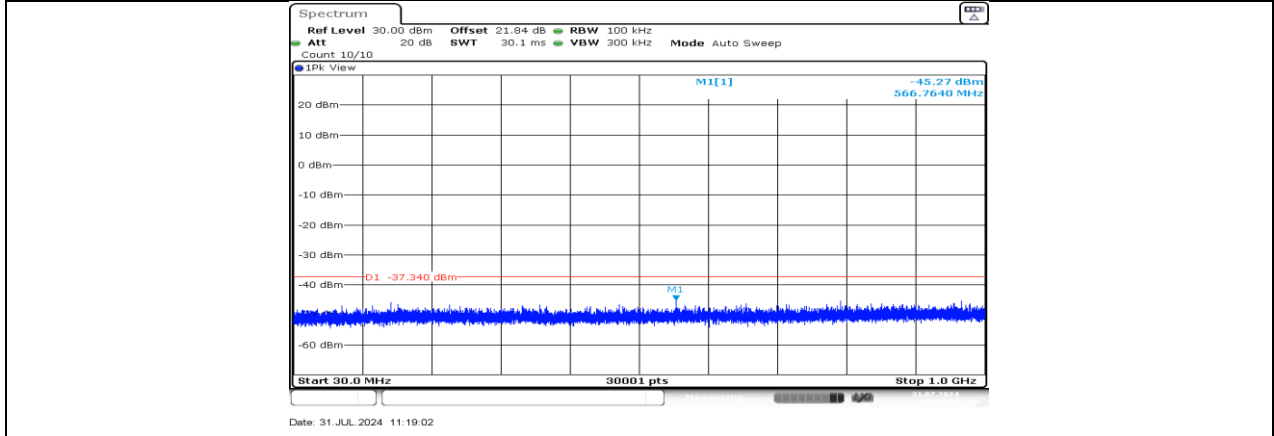
SRD 60M_Ant0_2440.5_30~1000



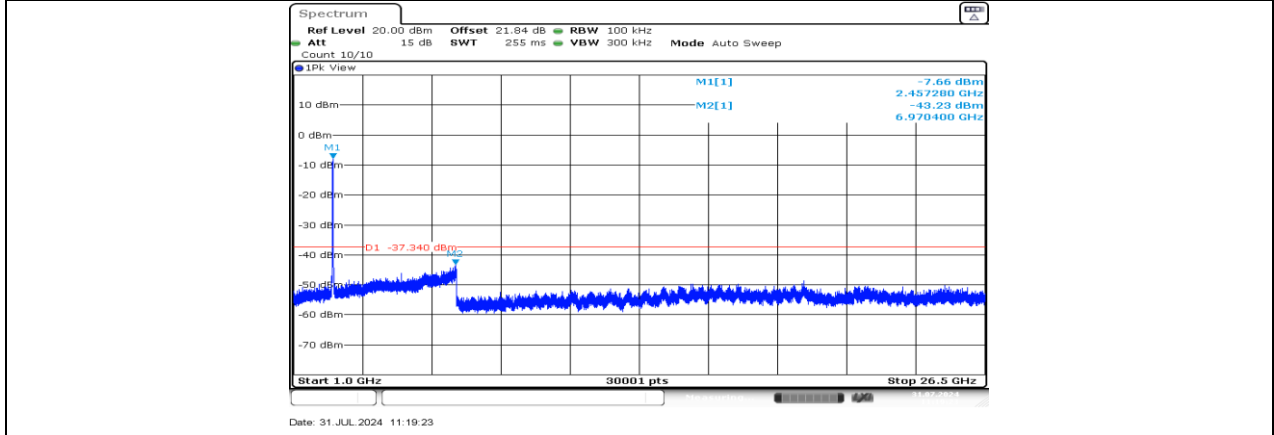
SRD 60M_Ant0_2440.5_1000~26500



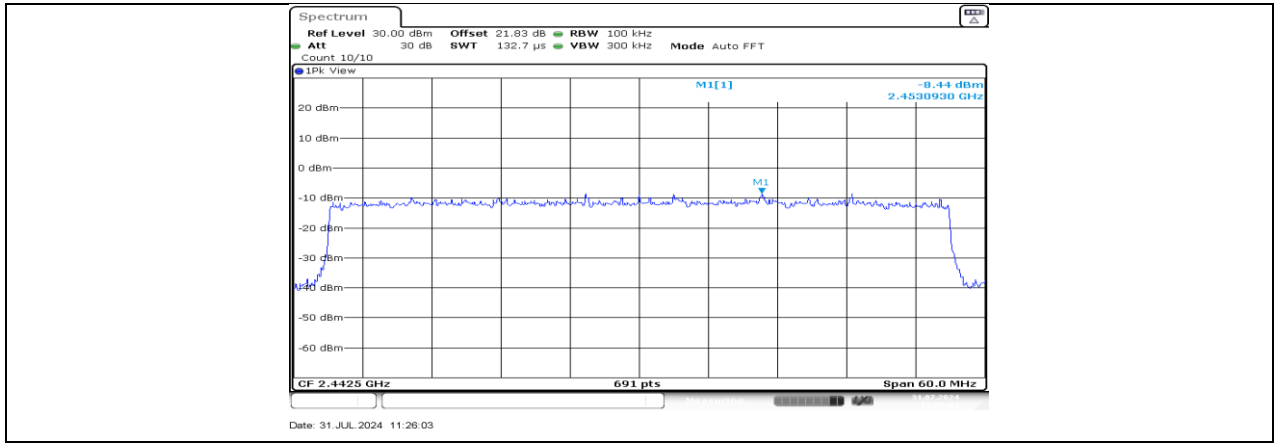
SRD 60M_Ant1_2440.5_0~Reference



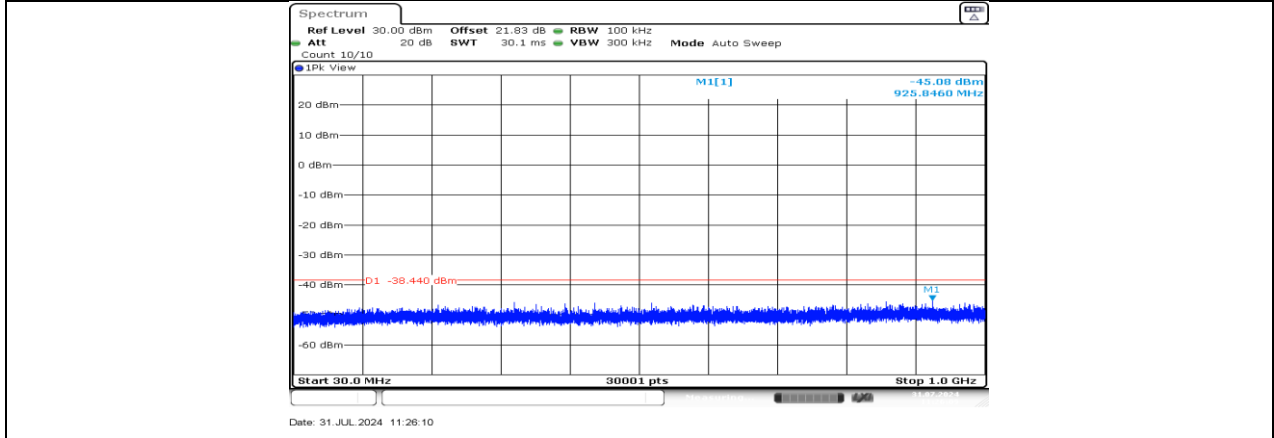
SRD 60M_Ant1_2440.5_30~1000



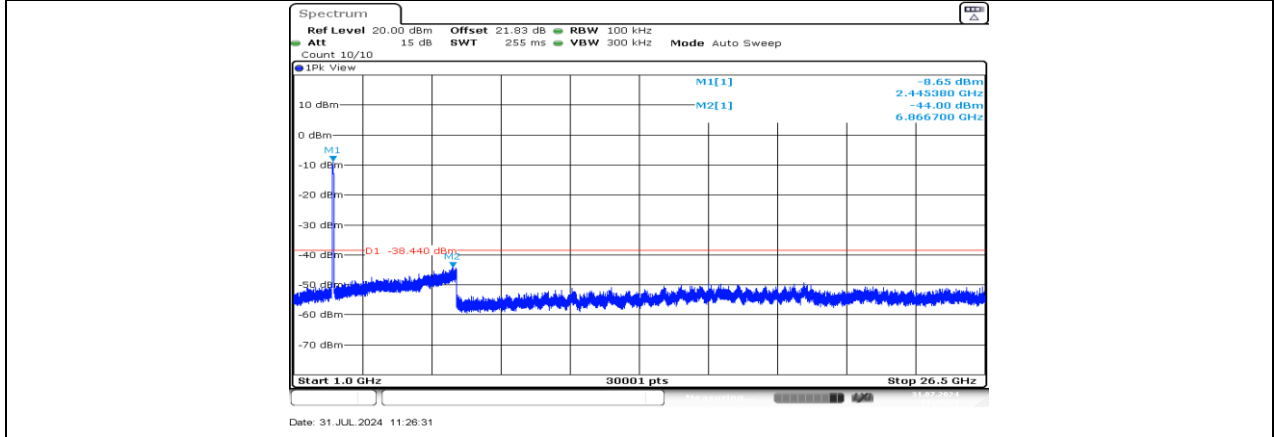
SRD 60M_Ant1_2440.5_1000~26500



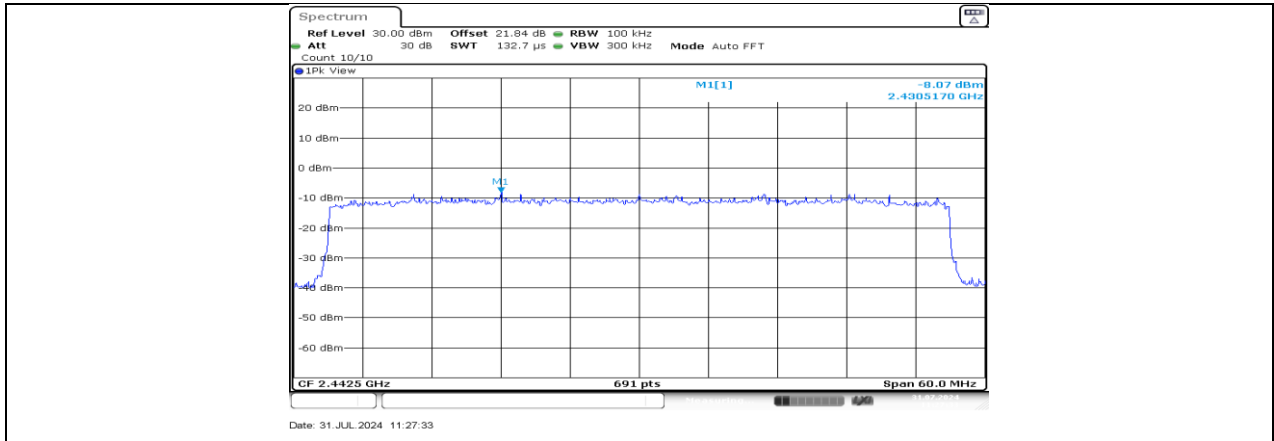
SRD 60M_Ant0_2442.5_0~Reference



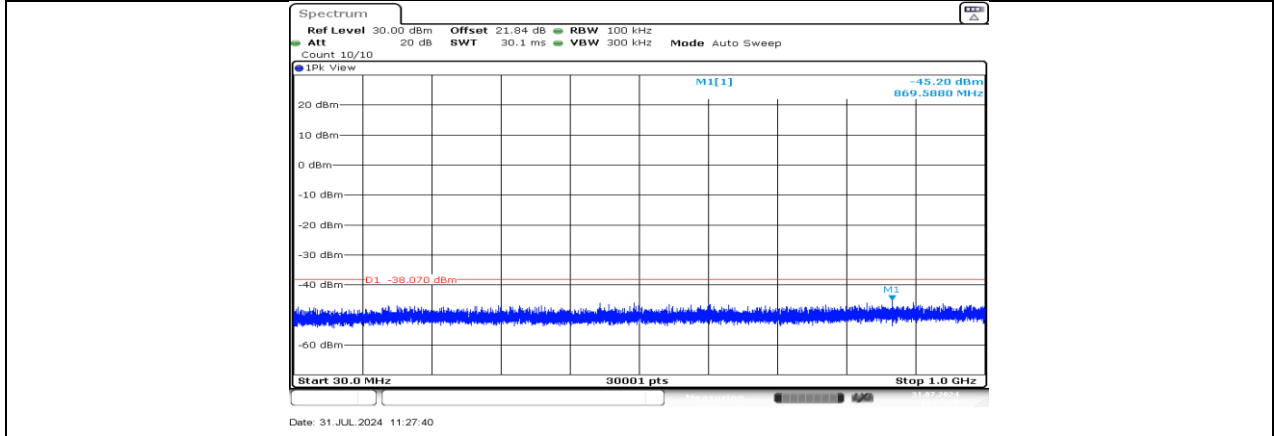
SRD 60M_Ant0_2442.5_30~1000



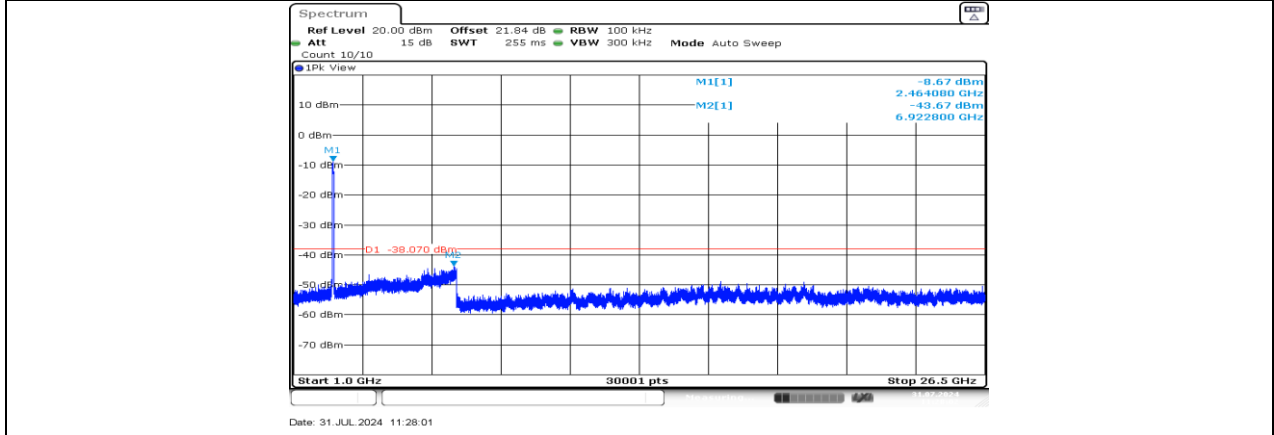
SRD 60M_Ant0_2442.5_1000~26500



SRD 60M_Ant1_2442.5_0~Reference



SRD 60M_Ant1_2442.5_30~1000



SRD 60M_Ant1_2442.5_1000~26500

11.7. APPENDIX G: DUTY CYCLE
11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
SRD 1.4M	100.00	100.00	1.0000	100.00	0.00	N/A	0.01
SRD 3M	100.00	100.00	1.0000	100.00	0.00	N/A	0.01
SRD 5M	100.00	100.00	1.0000	100.00	0.00	N/A	0.01
SRD 10M	50.00	50.00	1.0000	100.00	0.00	N/A	0.01
SRD 20M	50.00	50.00	1.0000	100.00	0.00	N/A	0.01
SRD 40M	50.00	50.00	1.0000	100.00	0.00	N/A	0.01
SRD 60M	50.00	50.00	1.0000	100.00	0.00	N/A	0.01

Note:

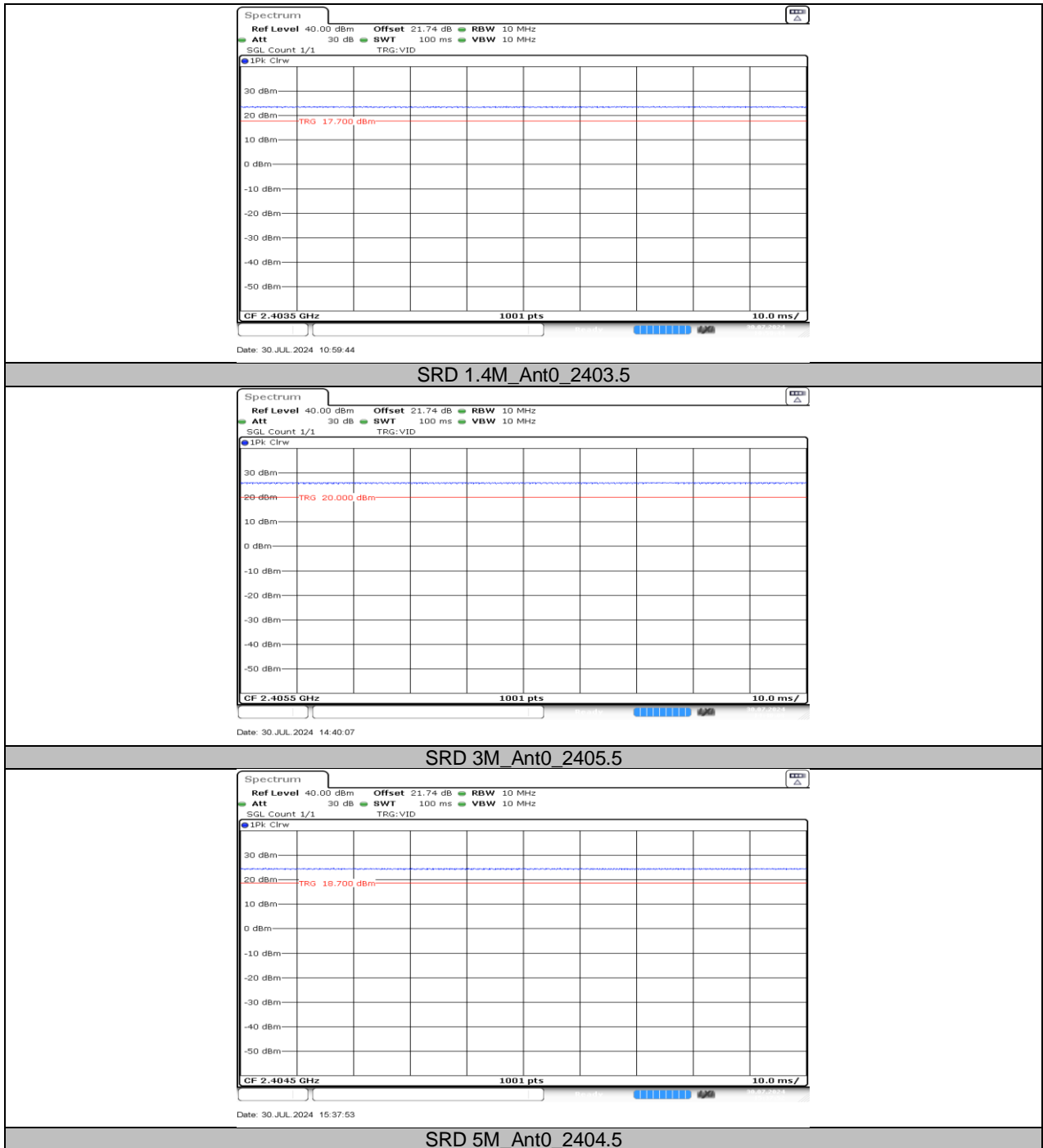
Duty Cycle Correction Factor=10log (1/x).

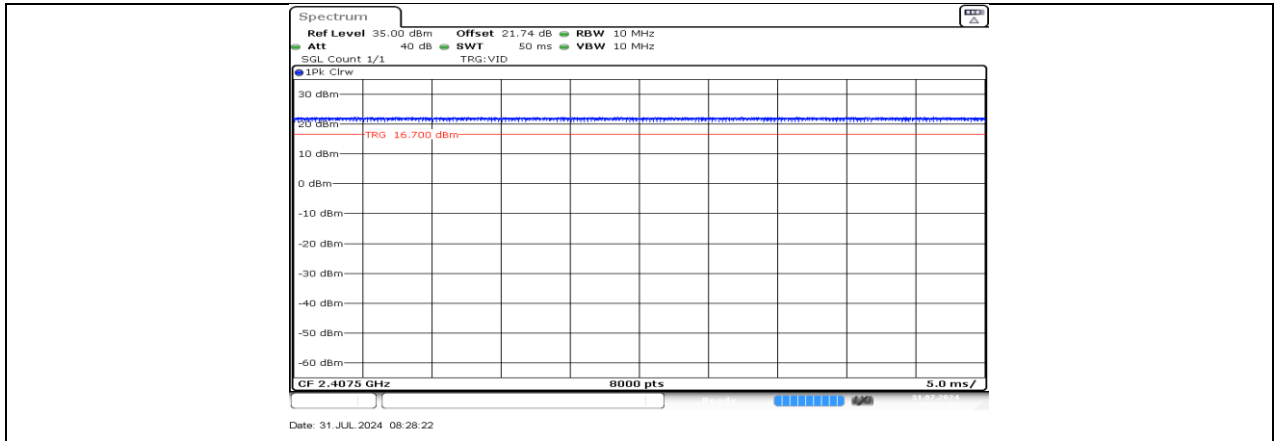
Where: x is Duty Cycle (Linear)

Where: T is On Time

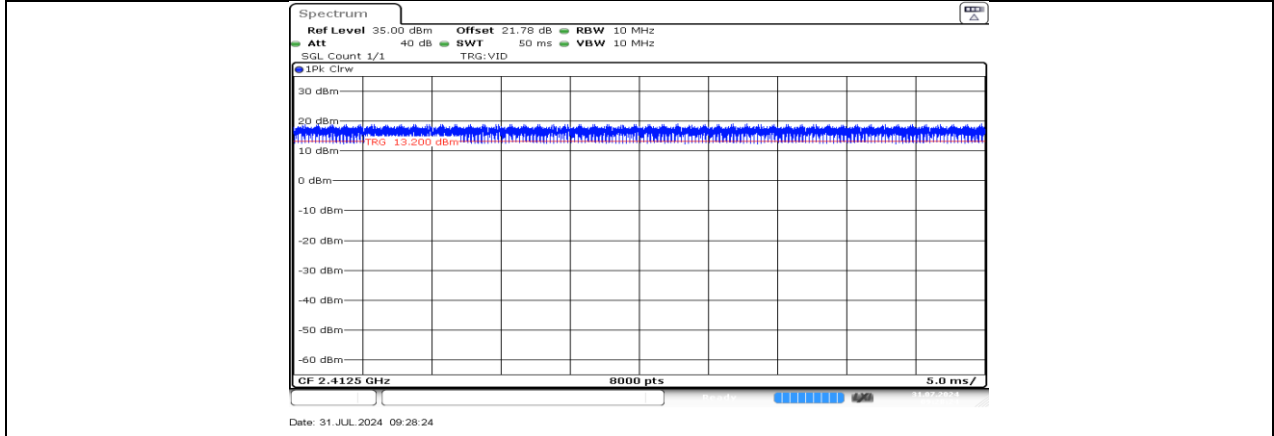
If that calculated VBW is not available on the analyzer then the next higher value should be used.

11.7.2. Test Graphs

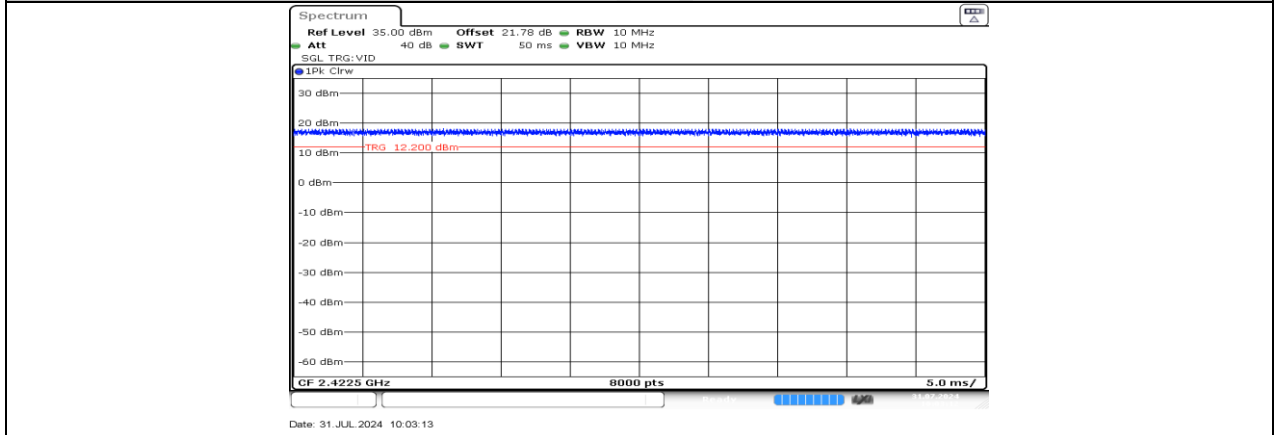




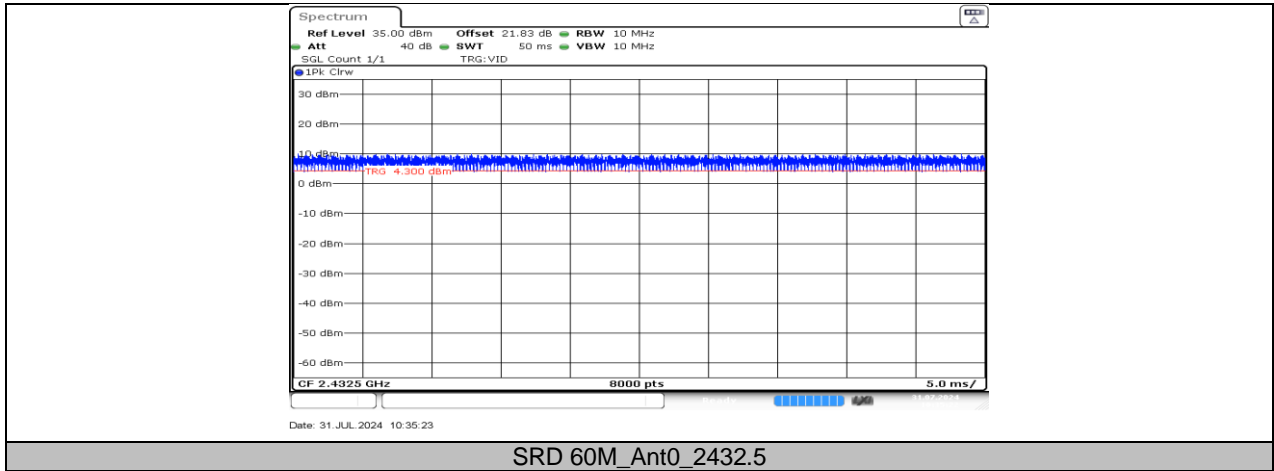
SRD 10M_Ant0_2407.5



SRD 20M_Ant0_2412.5



SRD 40M_Ant0_2422.5



END OF REPORT