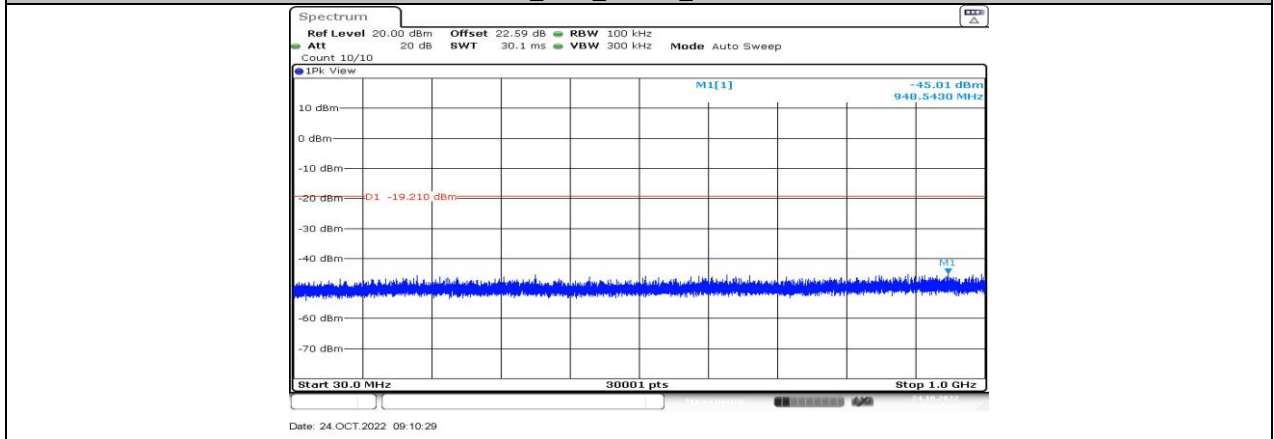
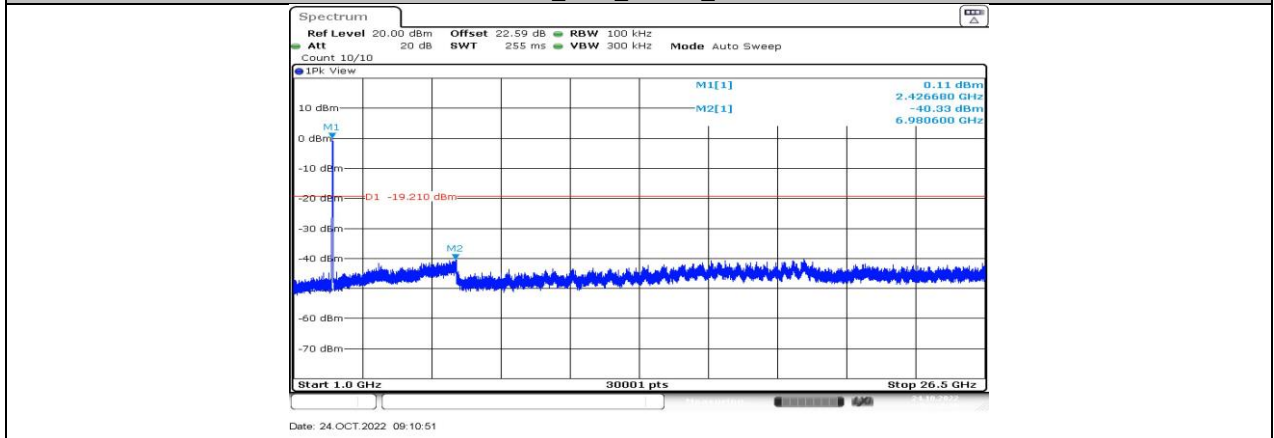


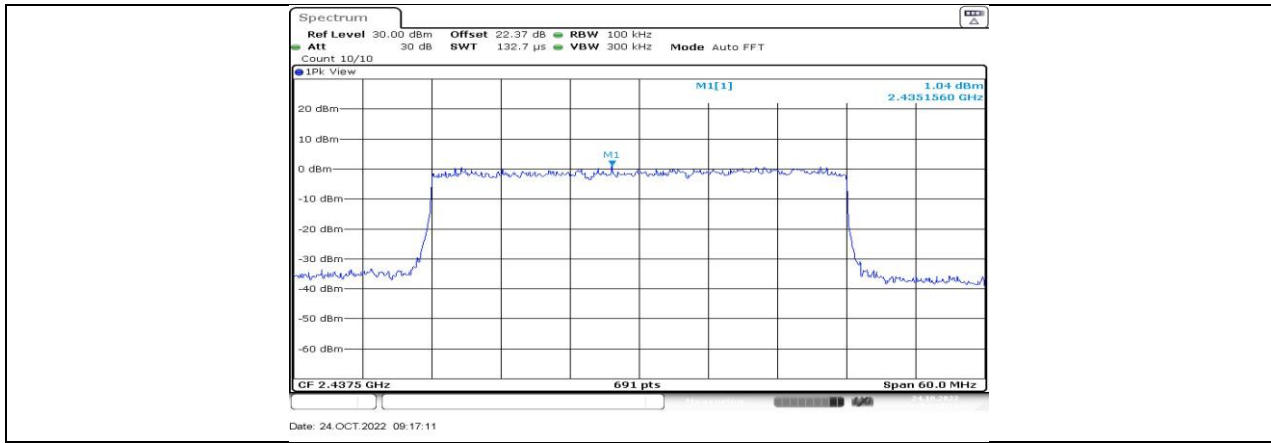
40 MHz_Ant1_2422.5_0~Reference



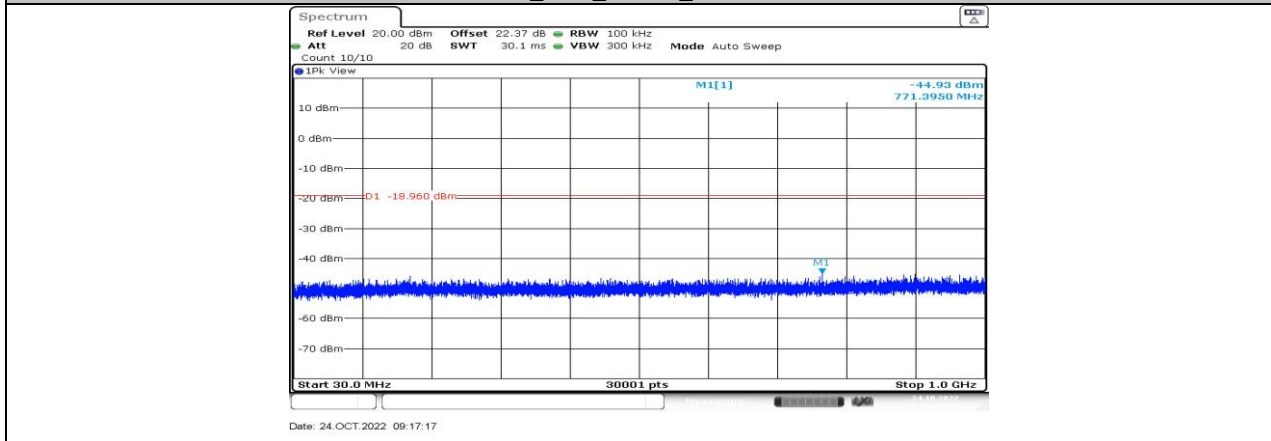
40 MHz_Ant1_2422.5_30~1000



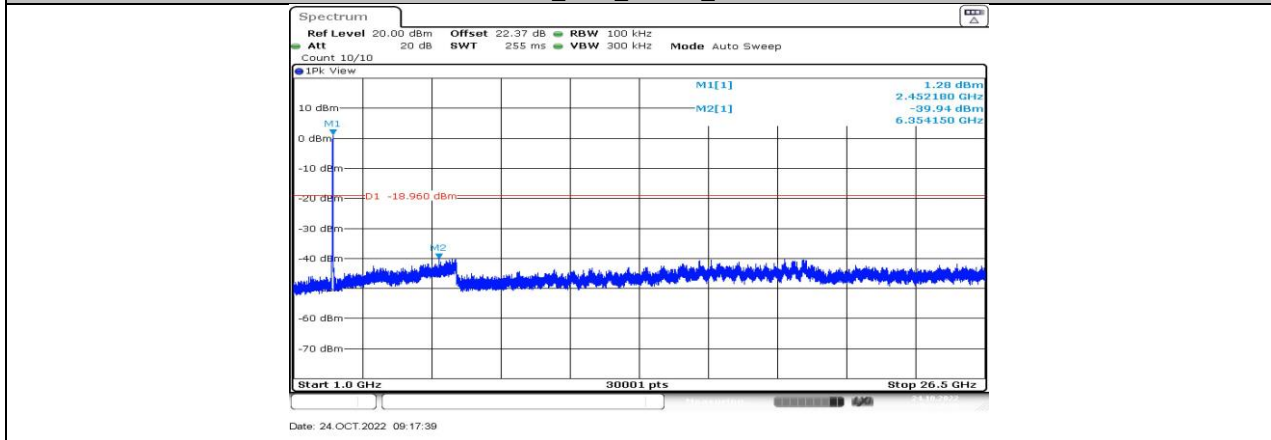
40 MHz_Ant1_2422.5_1000~26500



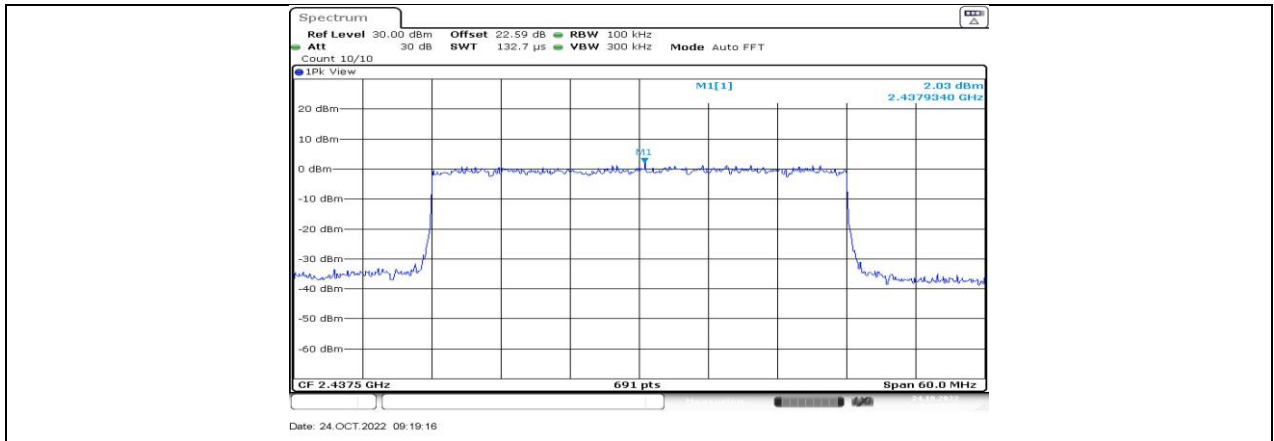
40 MHz_Ant0_2437.5_0~Reference



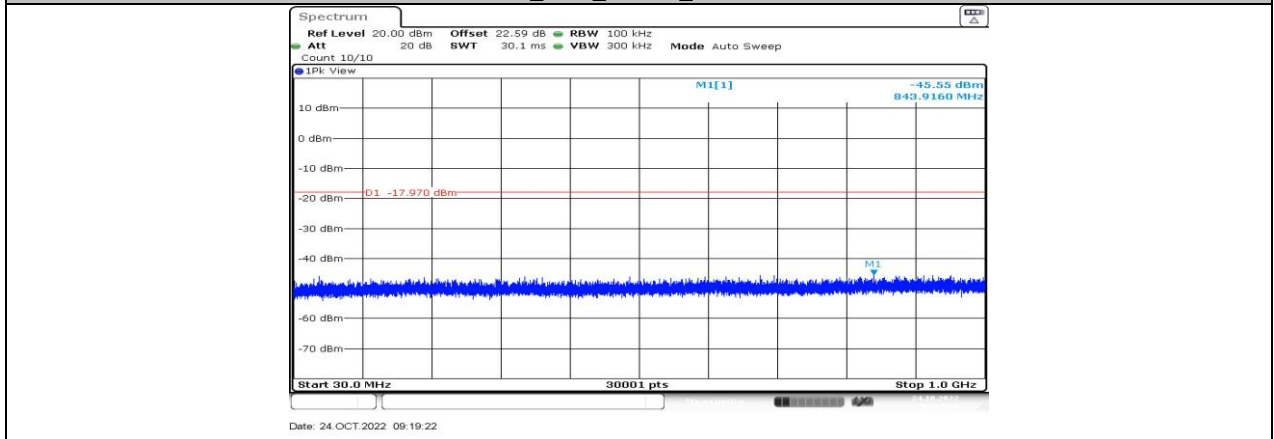
40 MHz_Ant0_2437.5_30~1000



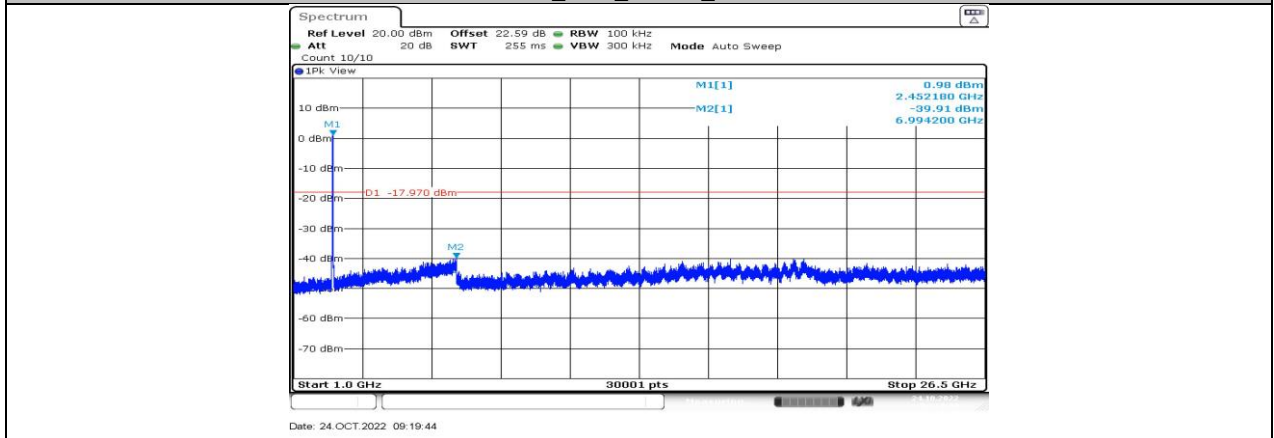
40 MHz_Ant0_2437.5_1000~26500



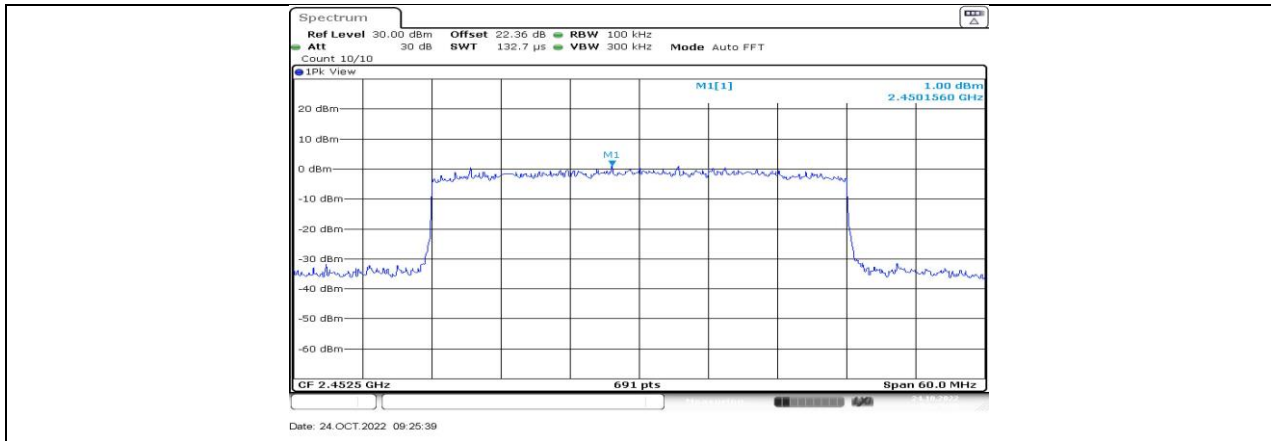
40 MHz_Ant1_2437.5_0~Reference



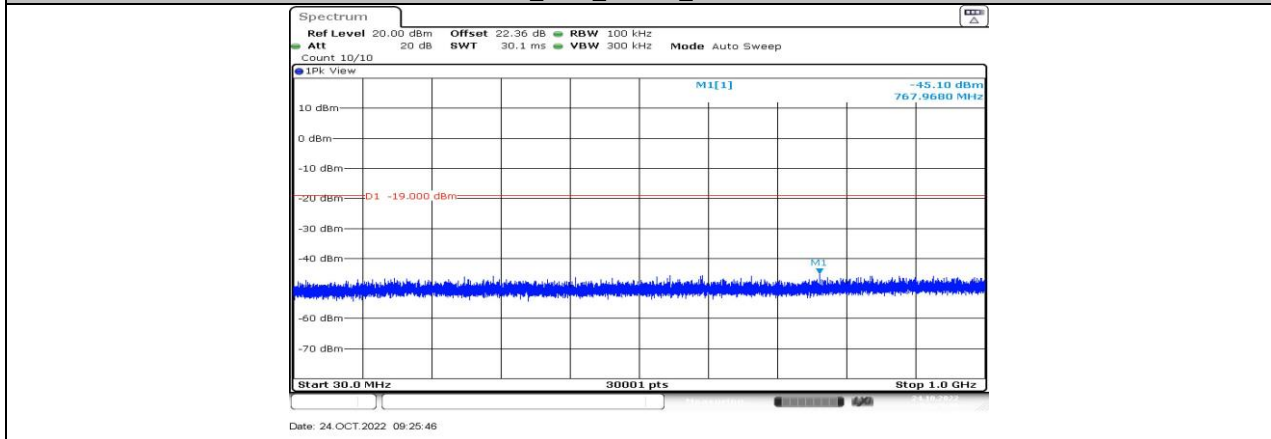
40 MHz_Ant1_2437.5_30~1000



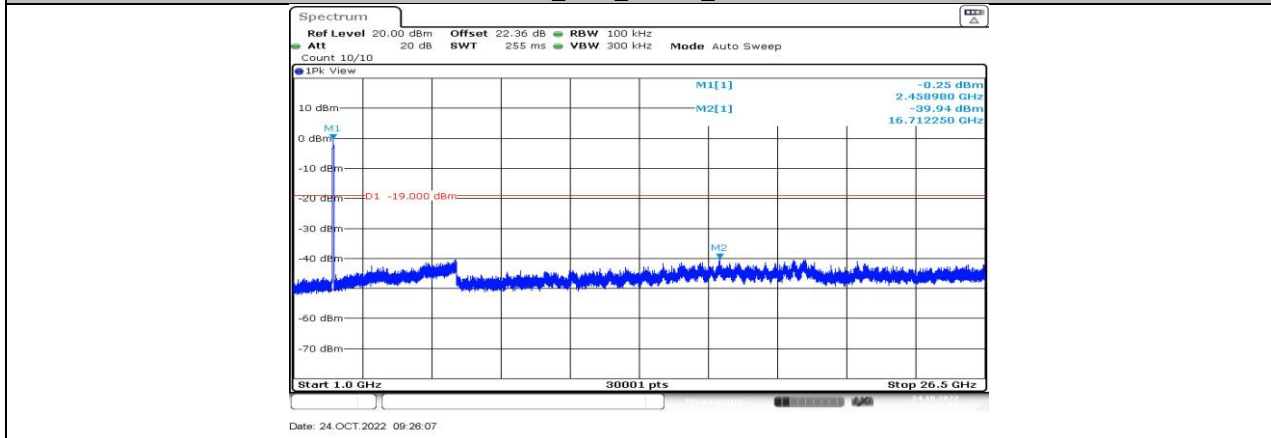
40 MHz_Ant1_2437.5_1000~26500



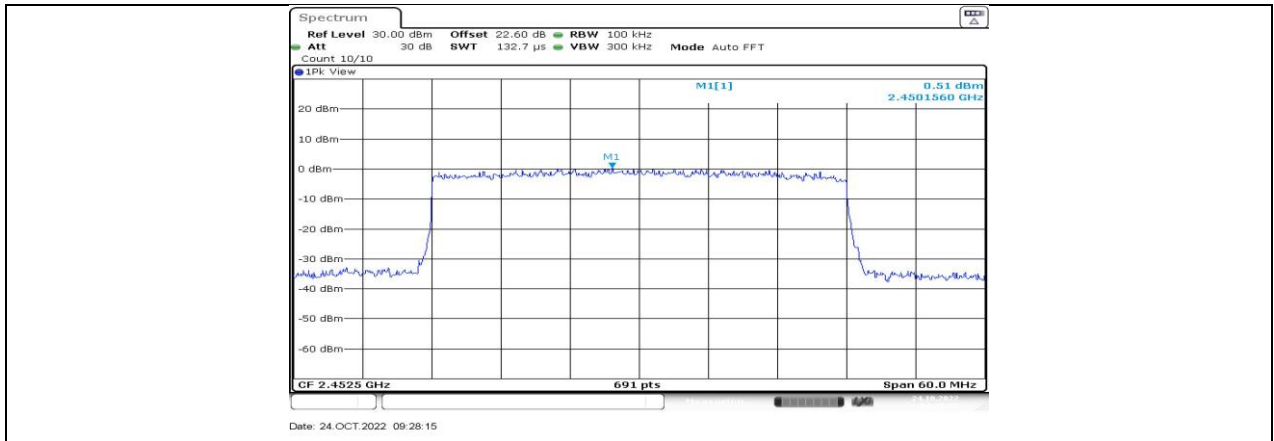
40 MHz_Ant0_2452.5_0~Reference



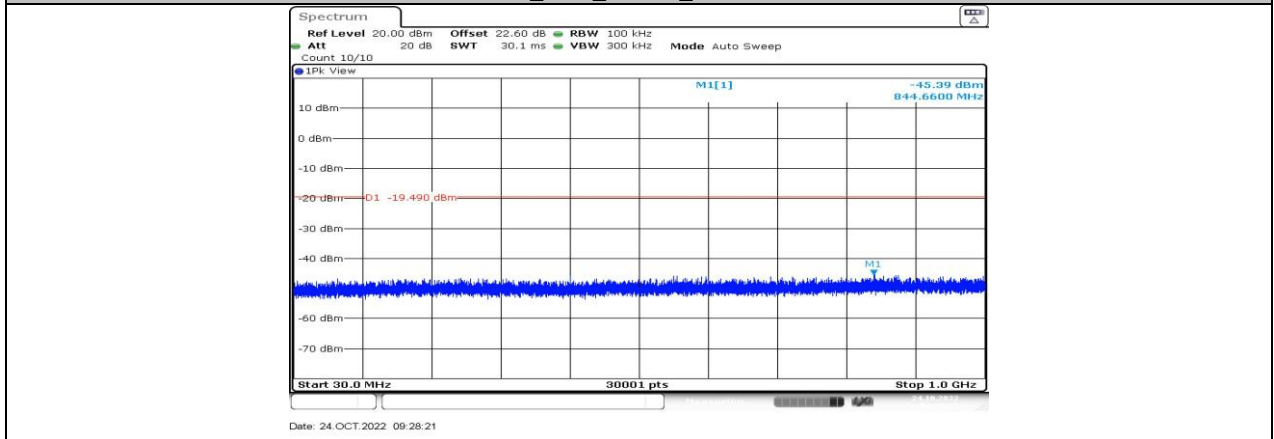
40 MHz_Ant0_2452.5_30~1000



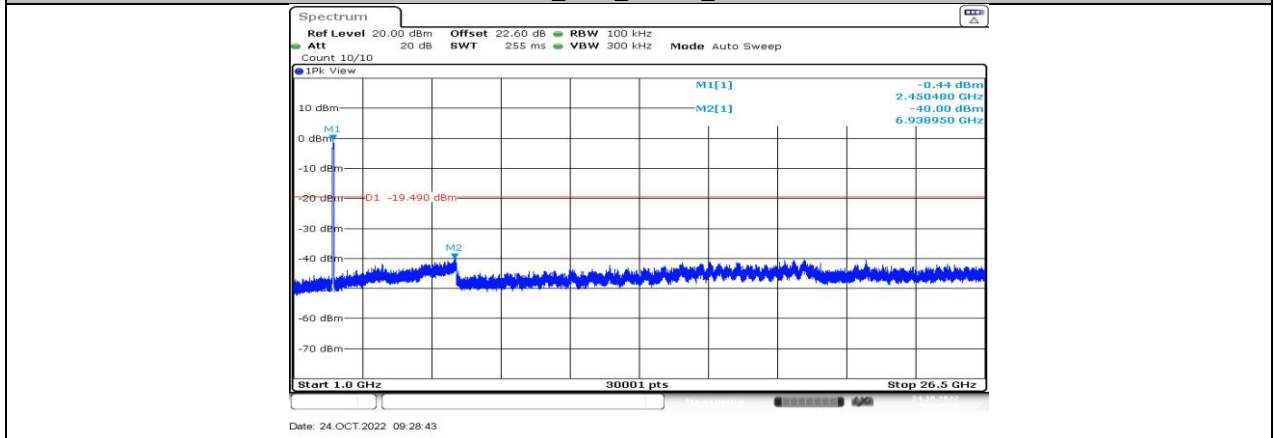
40 MHz_Ant0_2452.5_1000~26500



40 MHz_Ant1_2452.5_0~Reference



40 MHz_Ant1_2452.5_30~1000



40 MHz_Ant1_2452.5_1000~26500



11.7. APPENDIX G: DUTY CYCLE

11.7.1. Test Result

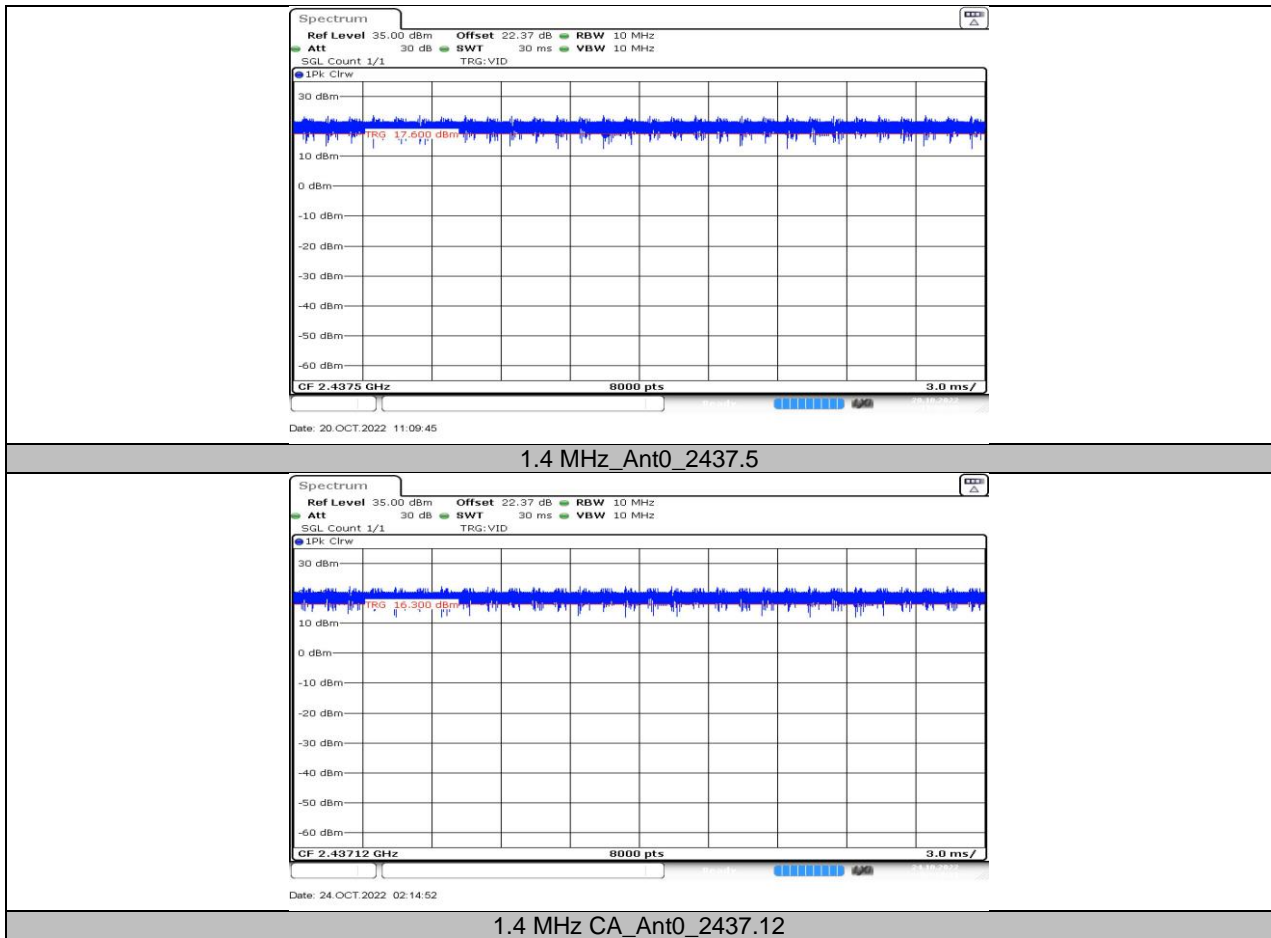
Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Final setting For VBW (kHz)
1.4 MHz	30	30	1.0000	100.00	0.01
1.4 MHz CA	30	30	1.0000	100.00	0.01

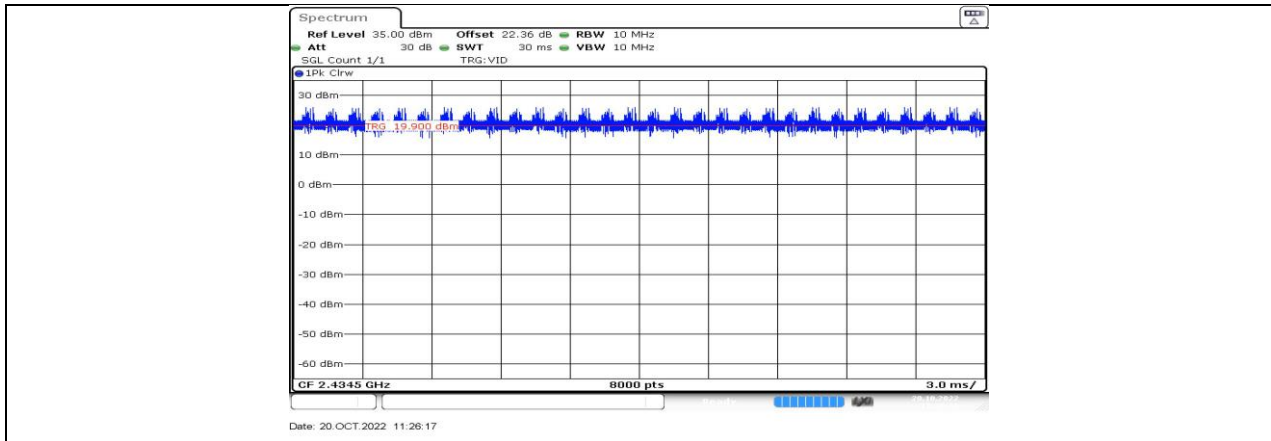
Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Final setting For VBW (kHz)
3 MHz	30	30	1.0000	100.00	0.01
3 MHz CA	30	30	1.0000	100.00	0.01

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Final setting For VBW (kHz)
10 MHz	50	50	1.0000	100.00	0.01
20 MHz	50	50	1.0000	100.00	0.01
40 MHz	50	50	1.0000	100.00	0.01

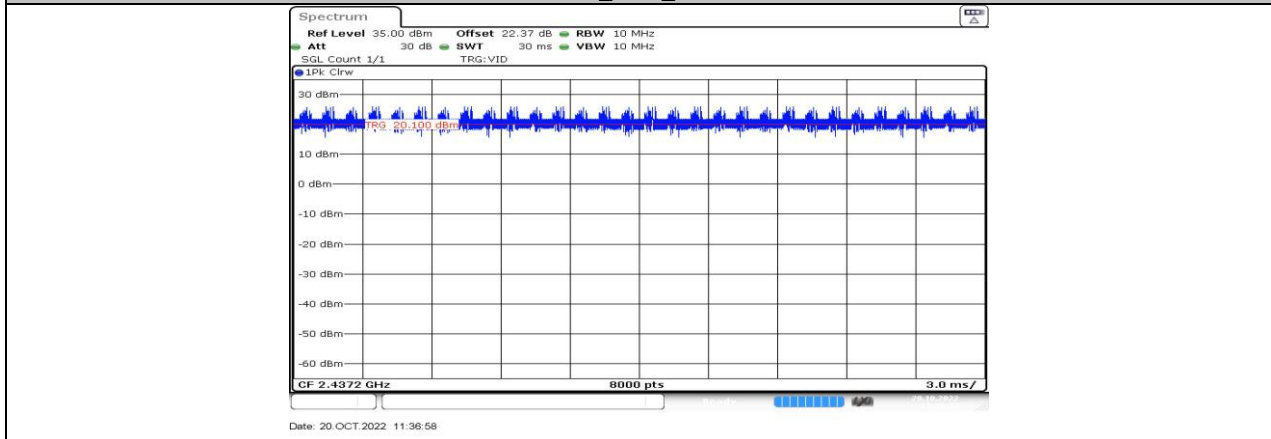


11.7.2. Test Graphs

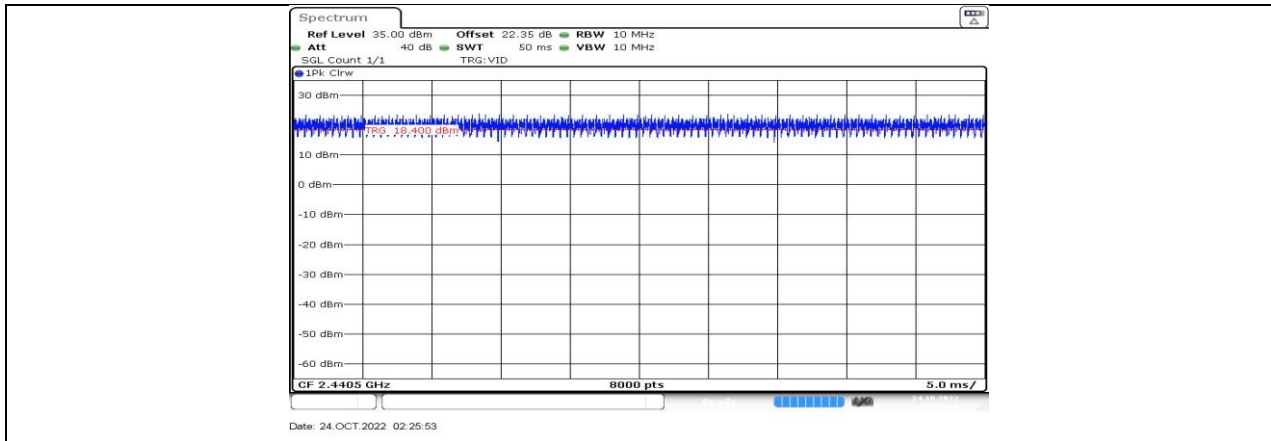




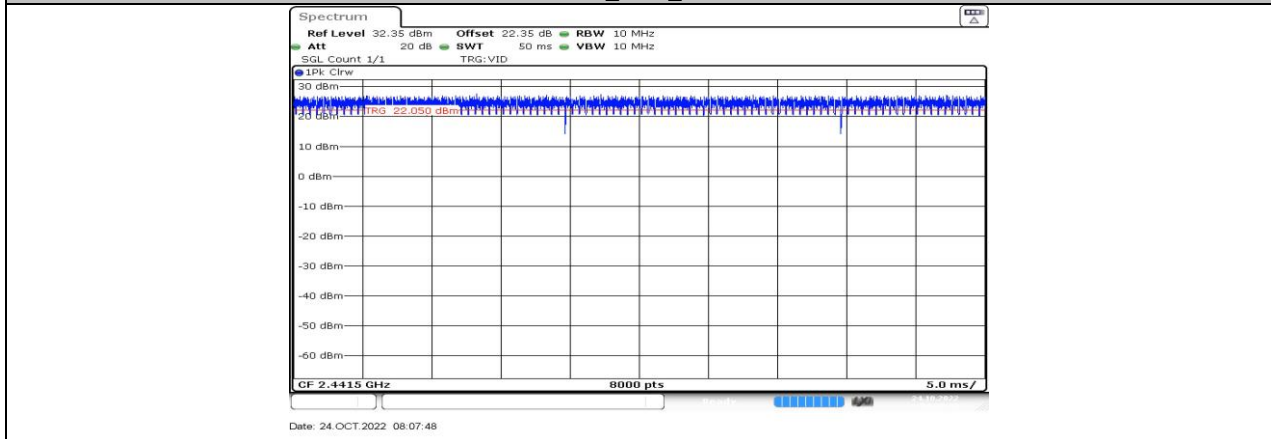
3 MHz_Ant0_2434.5



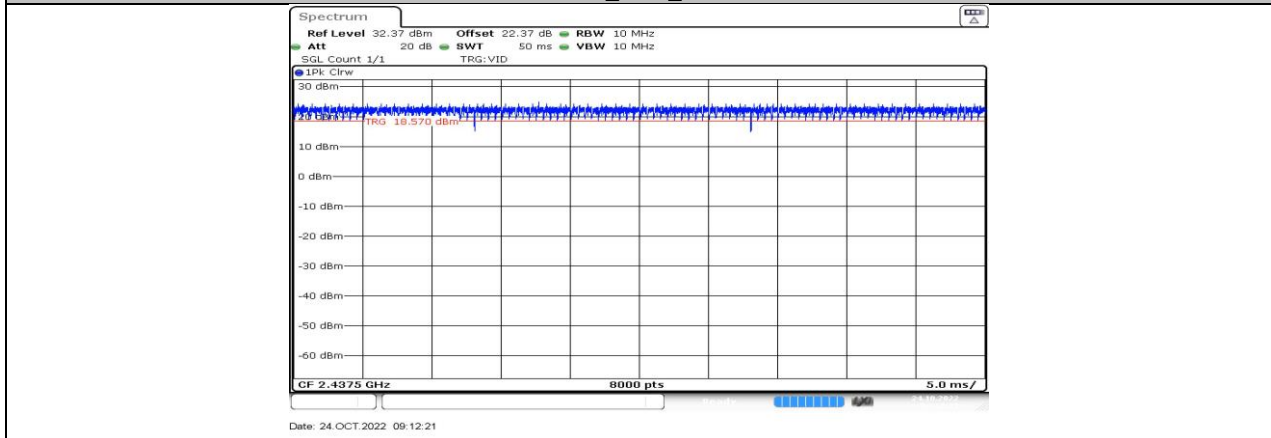
3MHz CA_Ant0_2437.2



10 MHz_Ant0_2440.5



20 MHz_Ant0_2441.5



40 MHz_Ant0_2437.5

END OF REPORT