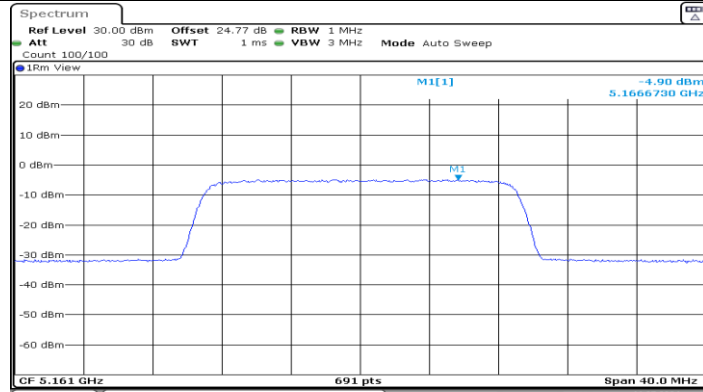


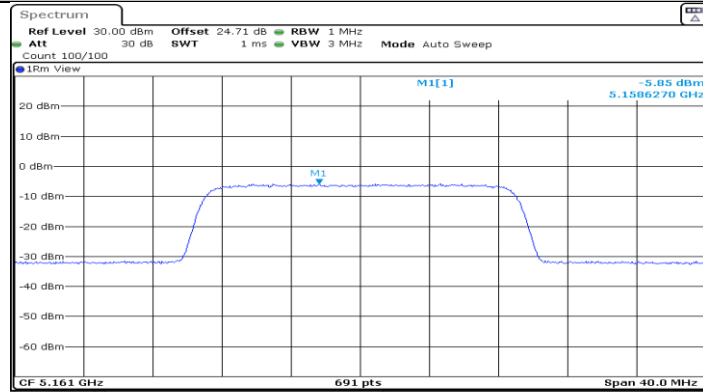
Date: 11 JUL 2024 08:26:02

SRD 10M_Ant5_5844.5



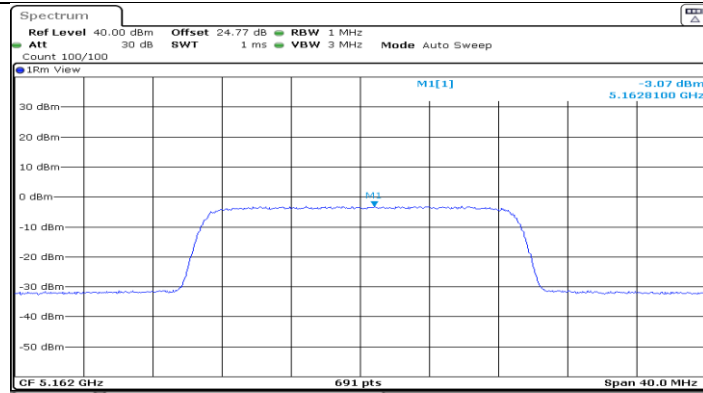
Date: 11 JUL 2024 04:18:34

SRD 20M_Ant4_5161



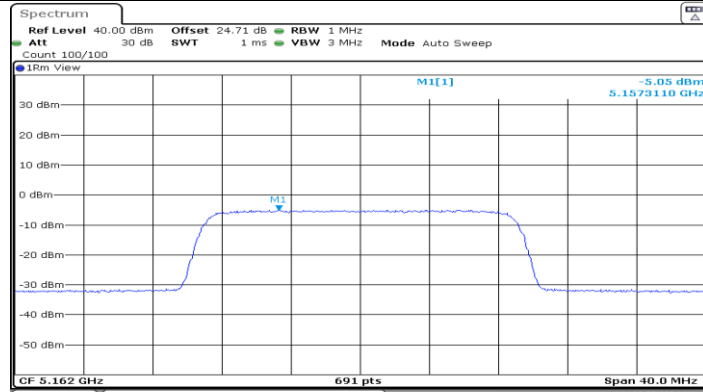
Date: 11 JUL 2024 04:19:22

SRD 20M_Ant5_5161



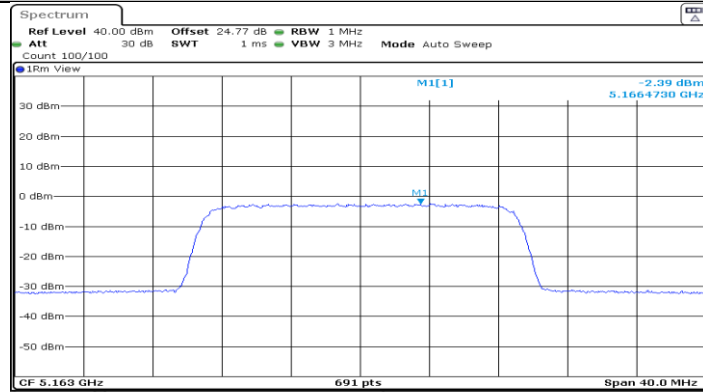
Date: 22 JUL 2024 09:25:12

SRD 20M_Ant4_5162



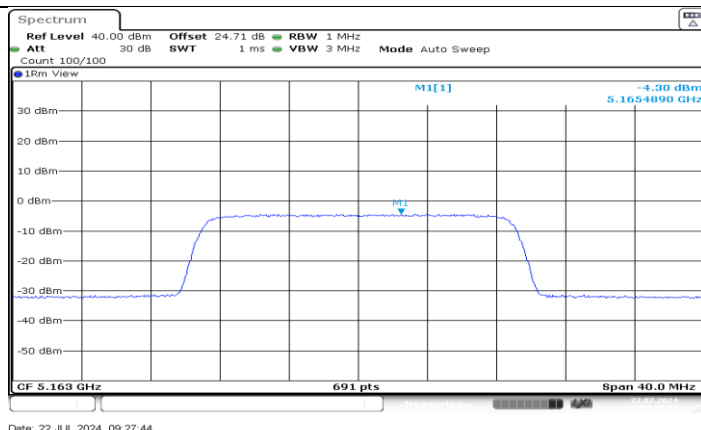
Date: 22 JUL 2024 09:25:45

SRD 20M_Ant5_5162



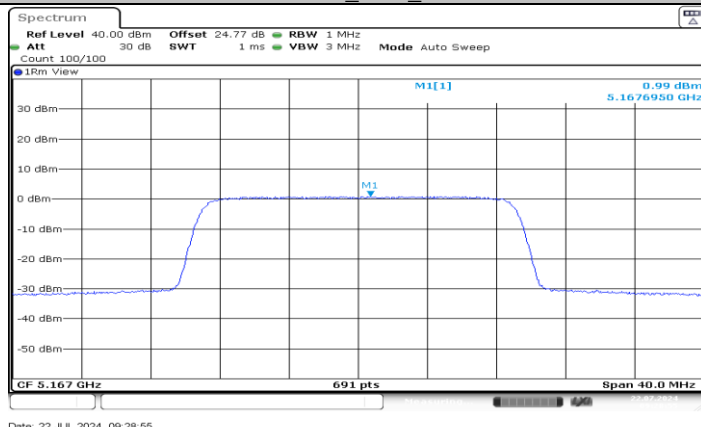
Date: 22 JUL 2024 09:27:11

SRD 20M_Ant4_5163



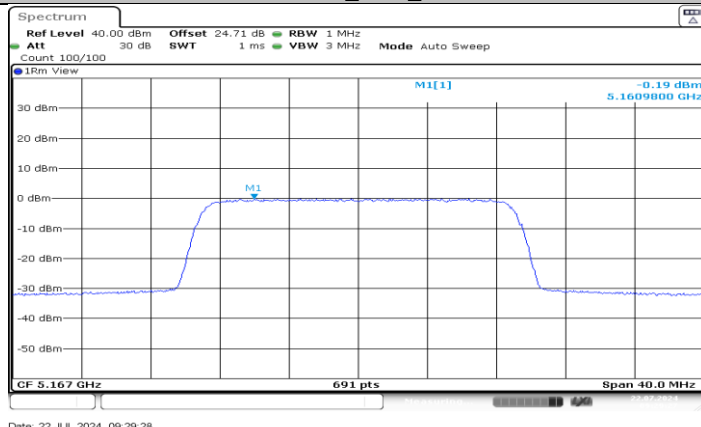
Date: 22 JUL 2024 09:27:44

SRD 20M_Ant5_5163



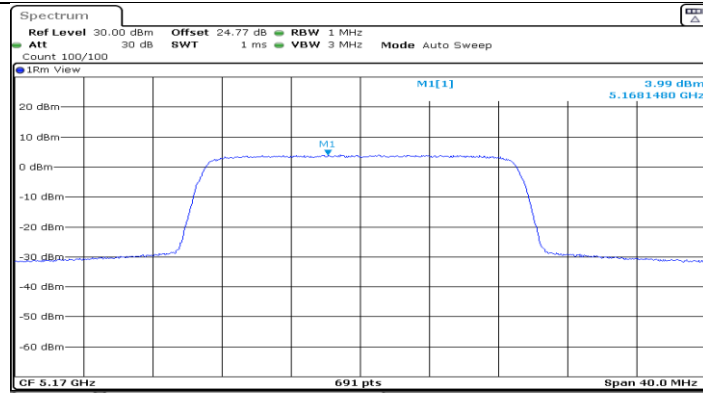
Date: 22 JUL 2024 09:28:55

SRD 20M_Ant4_5167



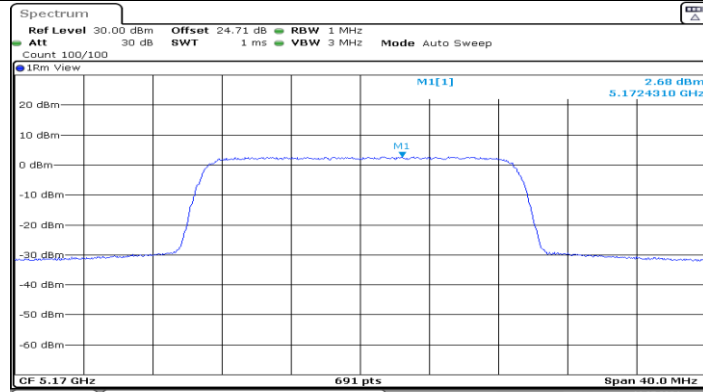
Date: 22 JUL 2024 09:29:28

SRD 20M_Ant5_5167



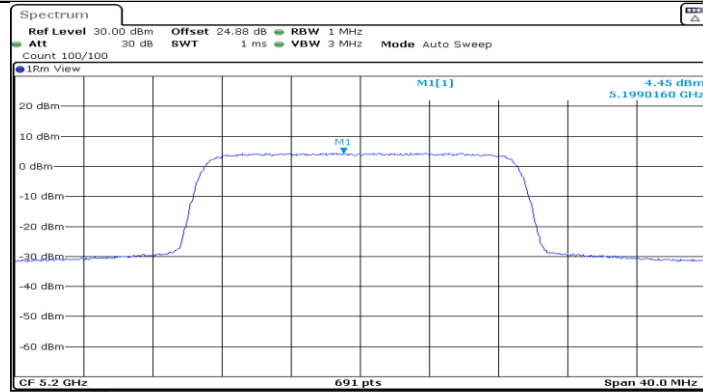
Date: 11 JUL 2024 04:45:07

SRD 20M_Ant4_5170



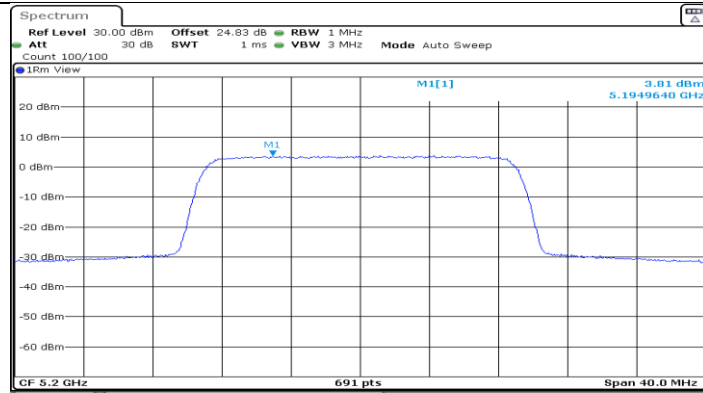
Date: 11 JUL 2024 04:45:39

SRD 20M_Ant5_5170



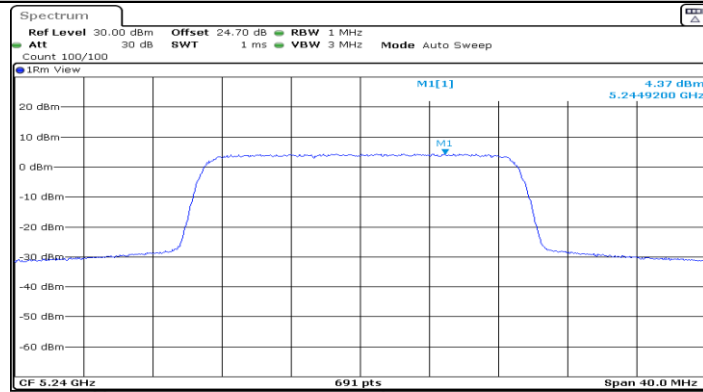
Date: 11 JUL 2024 04:49:18

SRD 20M_Ant4_5200



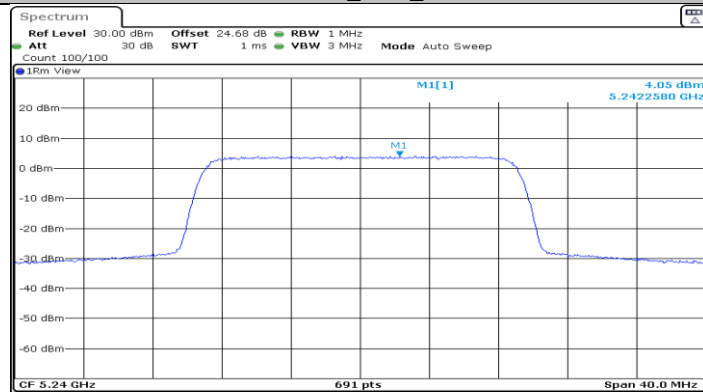
Date: 11 JUL 2024 04:50:06

SRD 20M_Ant5_5200



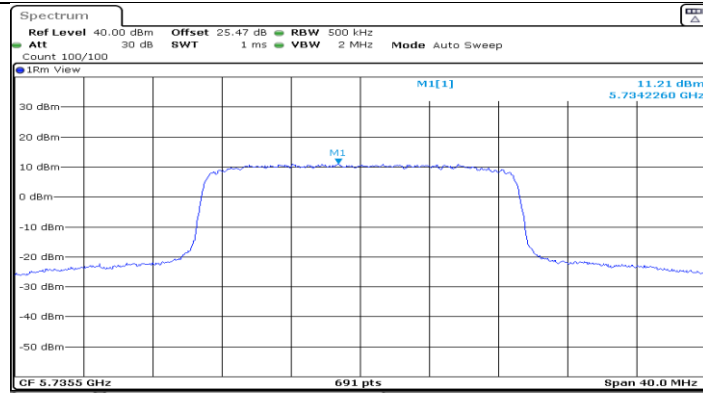
Date: 11 JUL 2024 04:53:42

SRD 20M_Ant4_5240



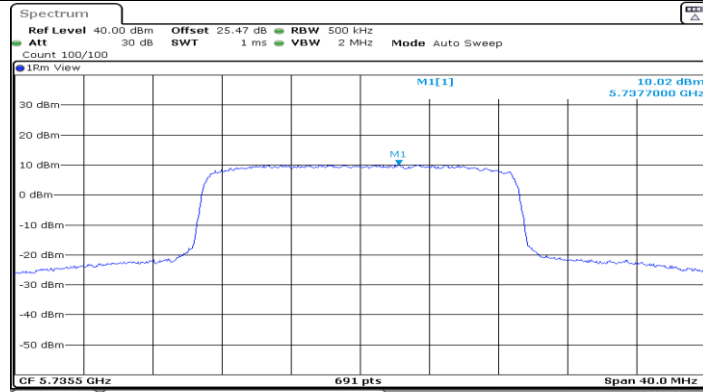
Date: 11 JUL 2024 04:54:30

SRD 20M_Ant5_5240



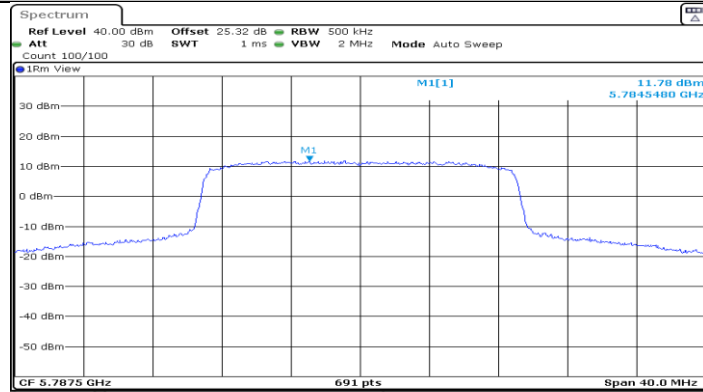
Date: 11 JUL 2024 08:34:36

SRD 20M_Ant4_5735.5



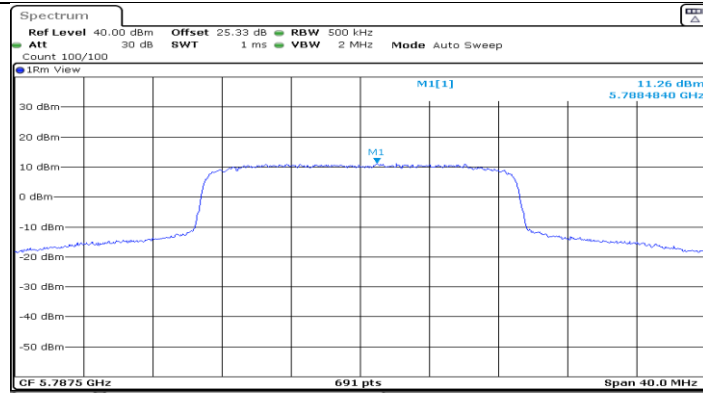
Date: 11 JUL 2024 08:35:40

SRD 20M_Ant5_5735.5



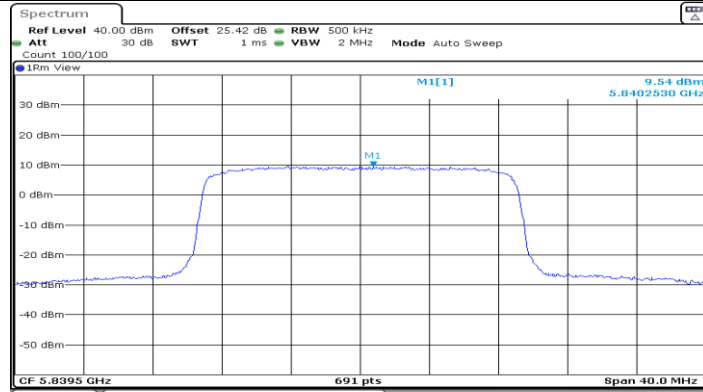
Date: 11 JUL 2024 08:50:24

SRD 20M_Ant4_5787.5



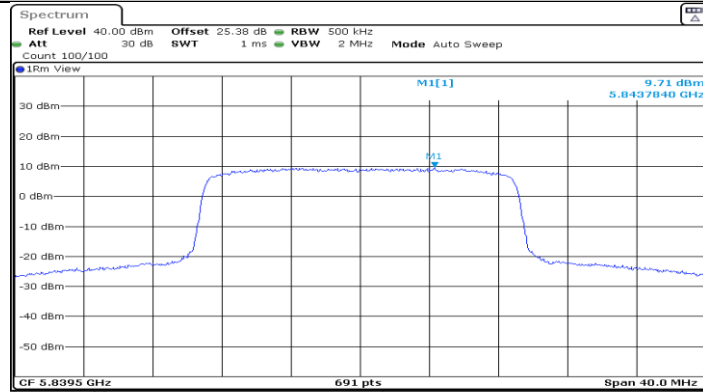
Date: 11 JUL 2024 08:51:28

SRD 20M_Ant5_5787.5



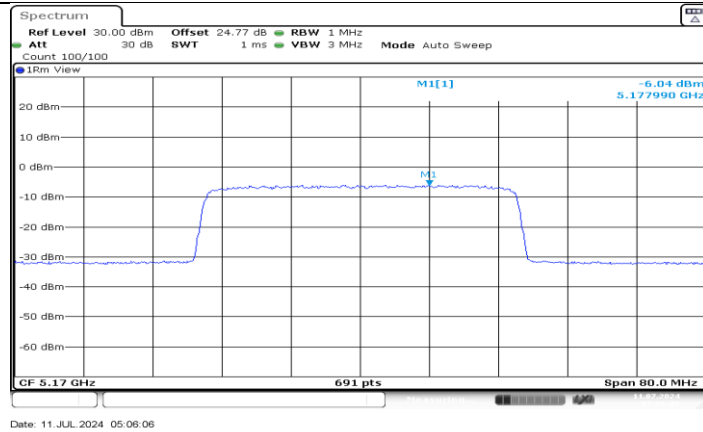
Date: 11 JUL 2024 09:00:06

SRD 20M_Ant4_5839.5

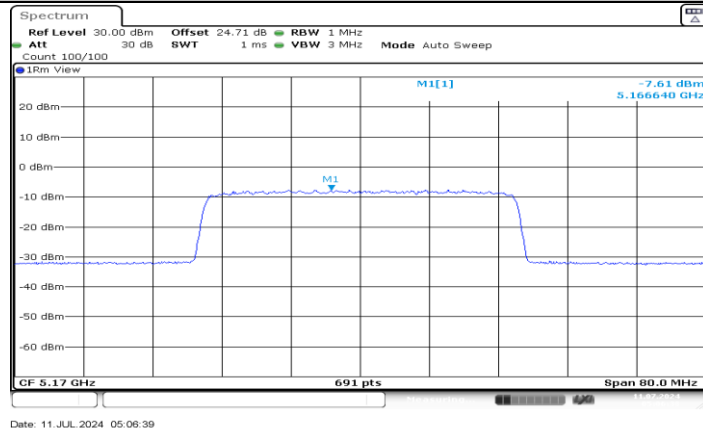


Date: 11 JUL 2024 09:01:11

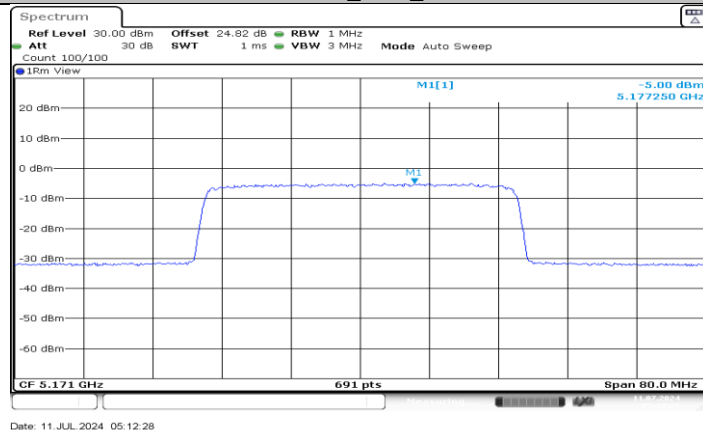
SRD 20M_Ant5_5839.5



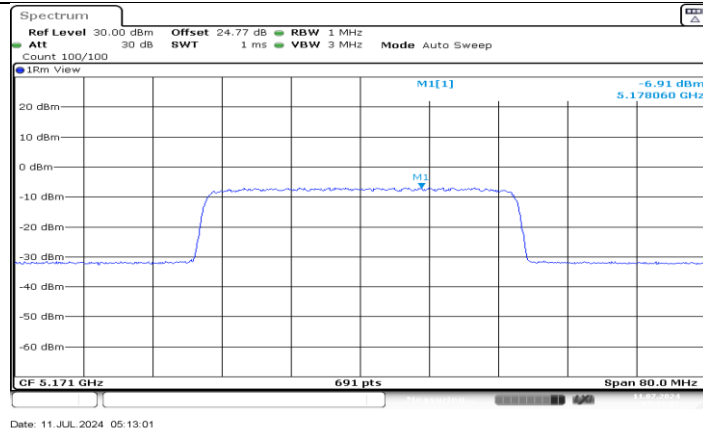
SRD 40M_Ant4_5170



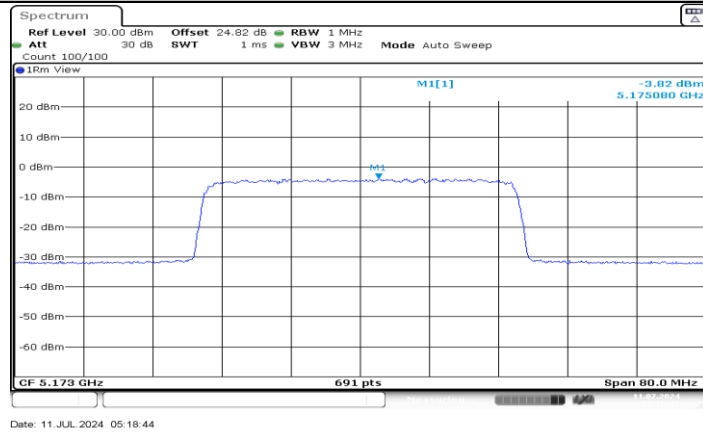
SRD 40M_Ant5_5170



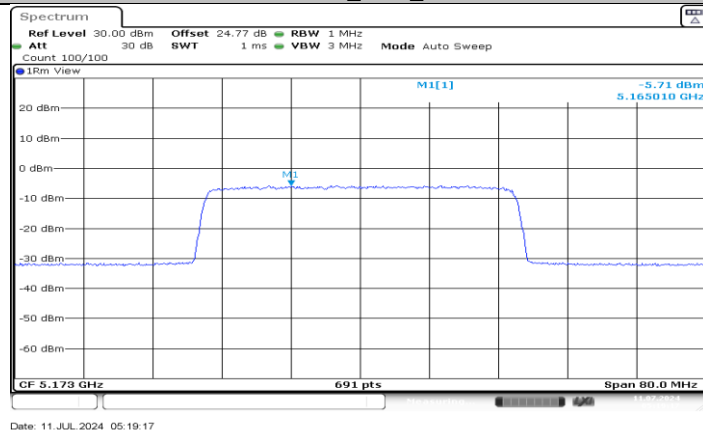
SRD 40M_Ant4_5171



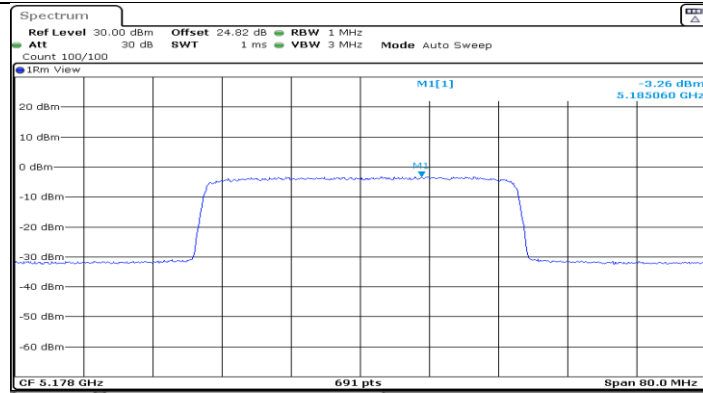
SRD 40M_Ant5_5171



SRD 40M_Ant4_5173

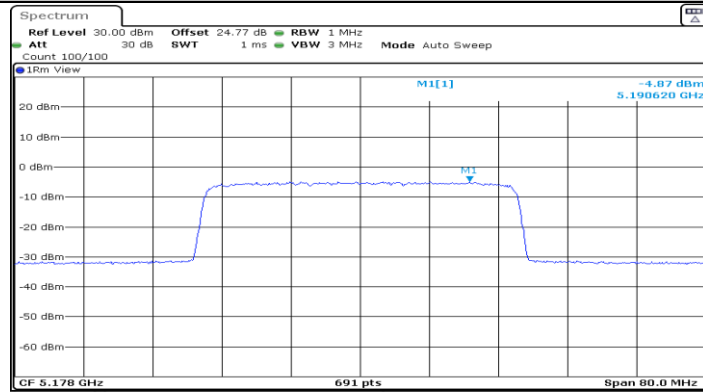


SRD 40M_Ant5_5173



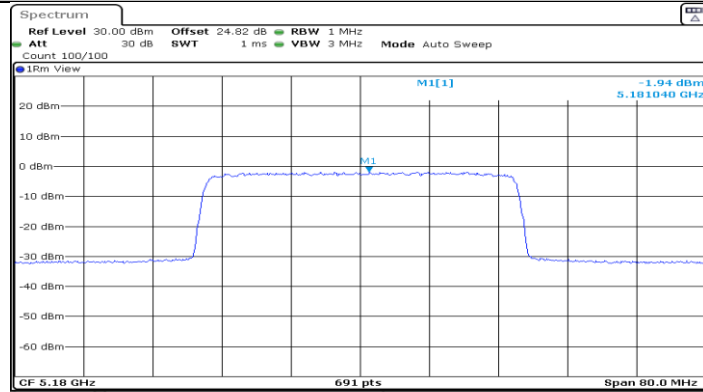
Date: 11 JUL 2024 05:25:21

SRD 40M_Ant4_5178



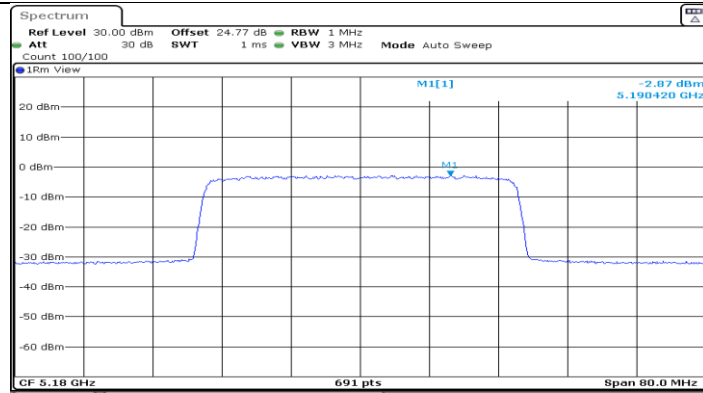
Date: 11 JUL 2024 05:26:09

SRD 40M_Ant5_5178



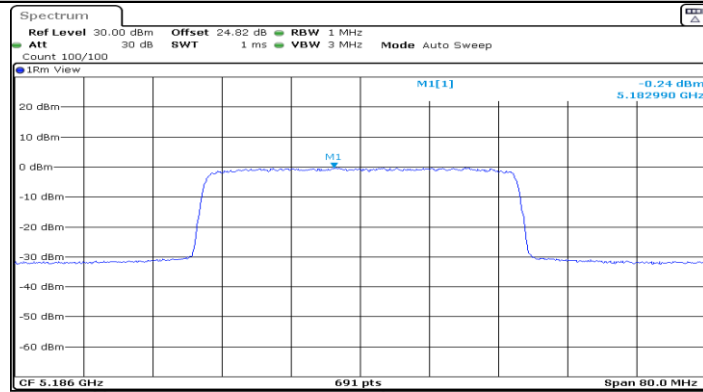
Date: 11 JUL 2024 05:34:06

SRD 40M_Ant4_5180



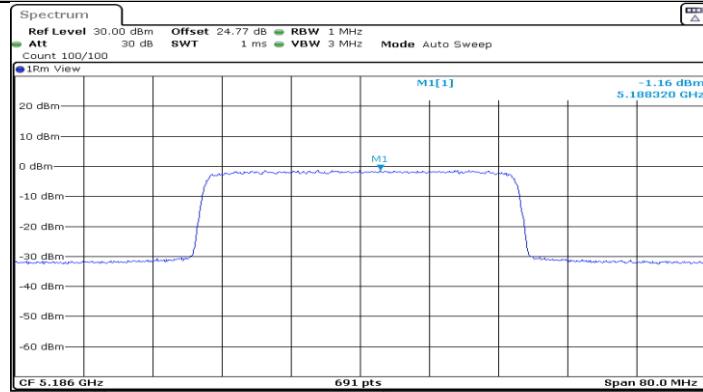
Date: 11 JUL 2024 05:34:39

SRD 40M_Ant5_5180



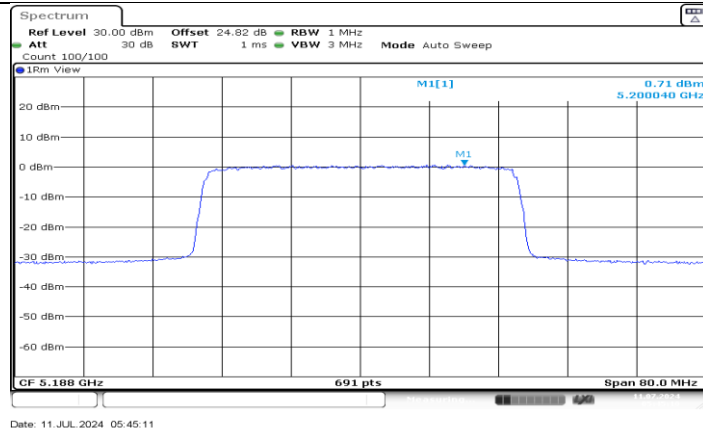
Date: 11 JUL 2024 05:39:48

SRD 40M_Ant4_5186

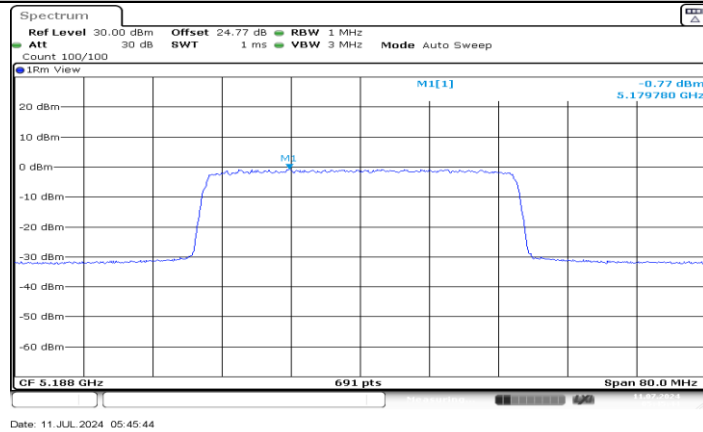


Date: 11 JUL 2024 05:40:21

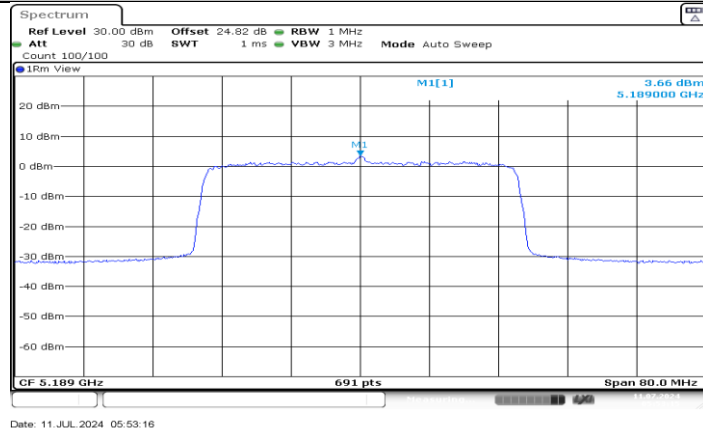
SRD 40M_Ant5_5186



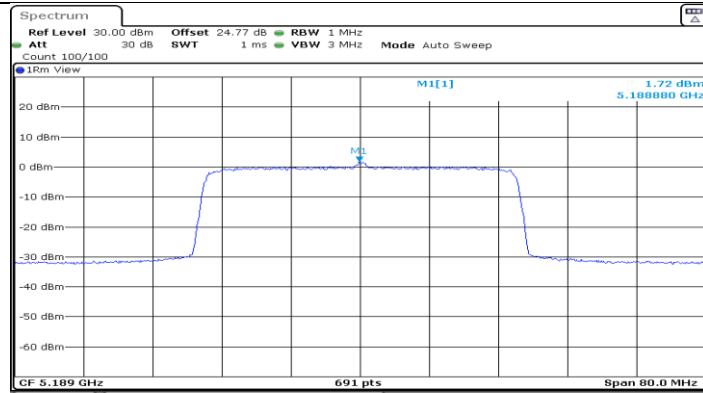
SRD 40M_Ant4_5188



SRD 40M_Ant5_5188

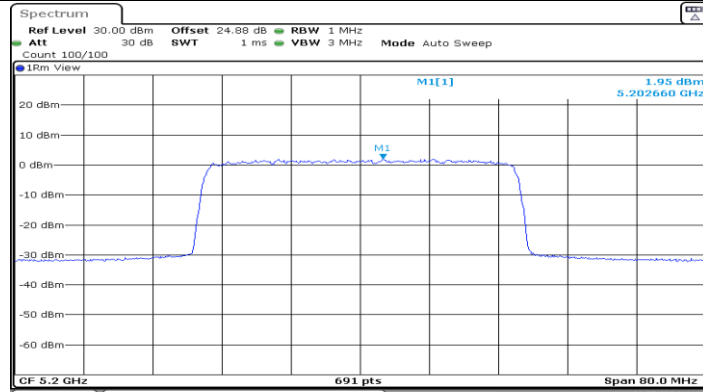


SRD 40M_Ant4_5189



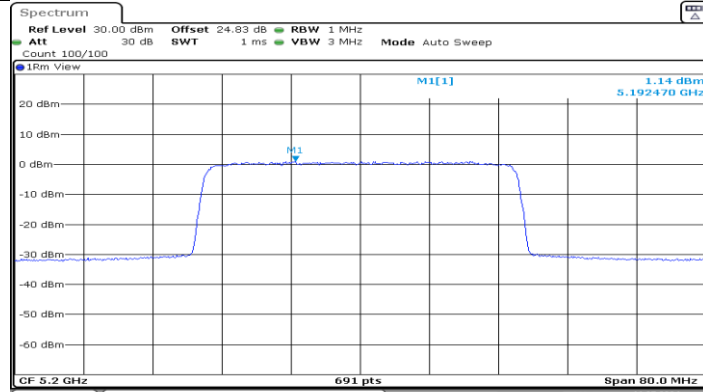
Date: 11 JUL 2024 05:53:48

SRD 40M_Ant5_5189



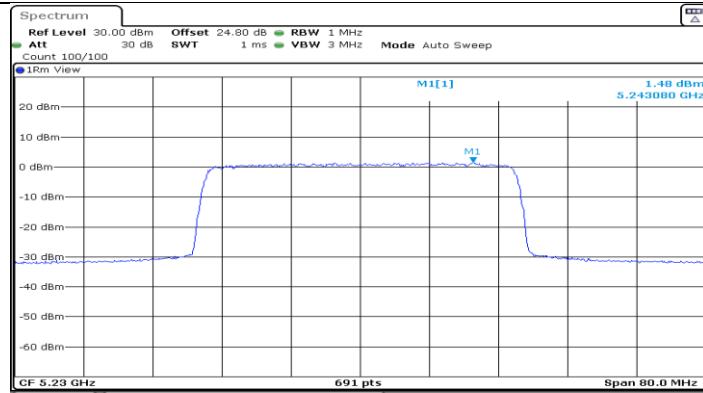
Date: 11 JUL 2024 05:58:20

SRD 40M_Ant4_5200



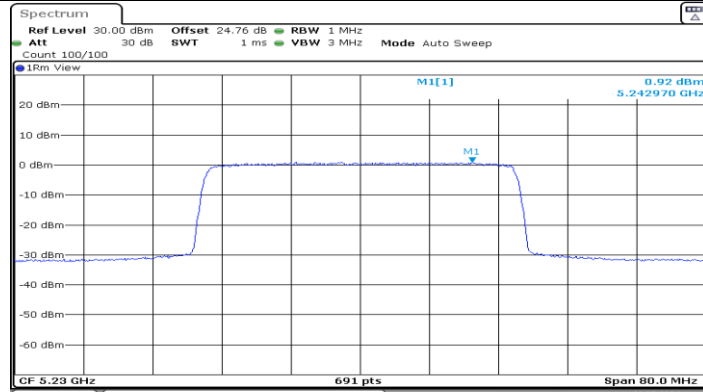
Date: 11 JUL 2024 05:58:53

SRD 40M_Ant5_5200



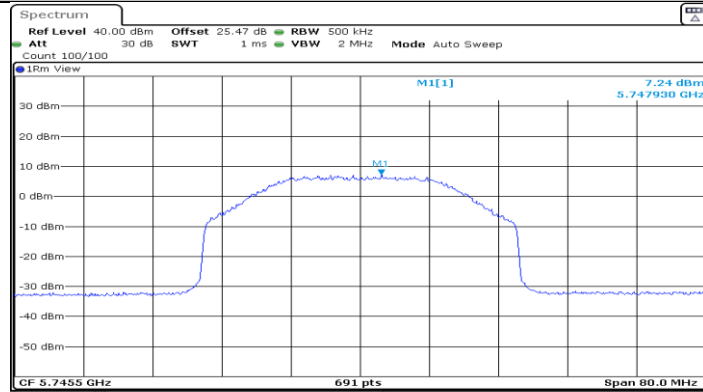
Date: 11 JUL 2024 06:04:02

SRD 40M_Ant4_5230



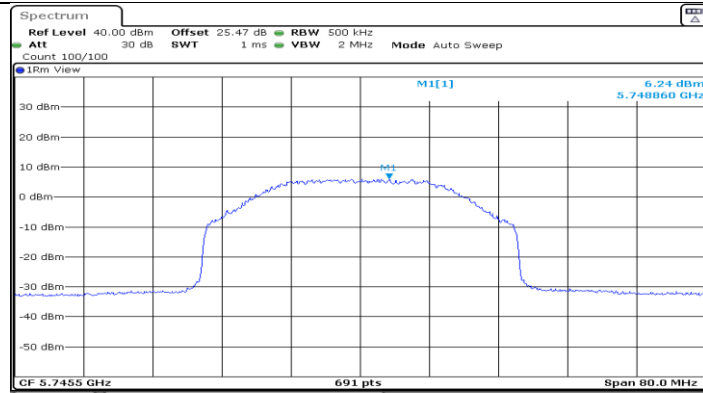
Date: 11 JUL 2024 06:04:35

SRD 40M_Ant5_5230



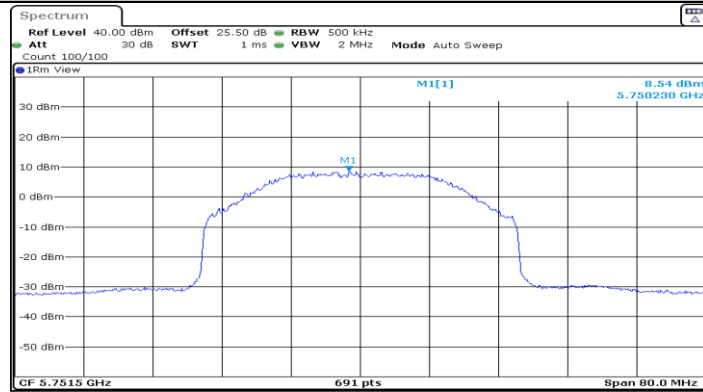
Date: 11 JUL 2024 09:13:29

SRD 40M_Ant4_5745.5



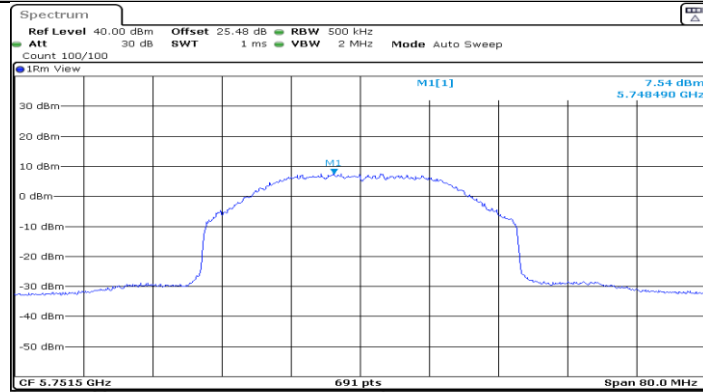
Date: 11 JUL 2024 09:14:33

SRD 40M_Ant5_5745.5



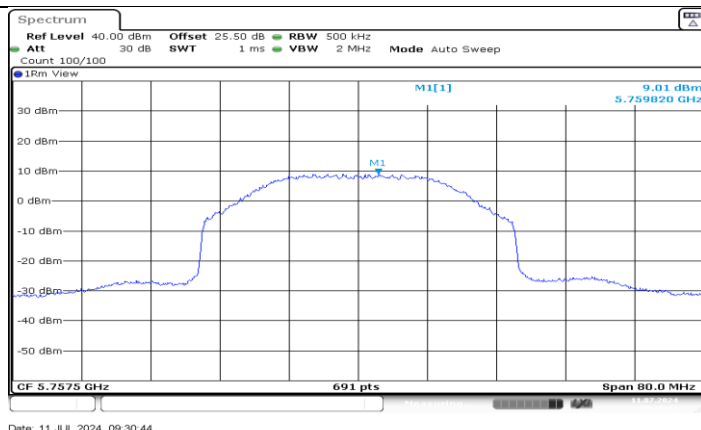
Date: 11 JUL 2024 09:26:44

SRD 40M_Ant4_5751.5



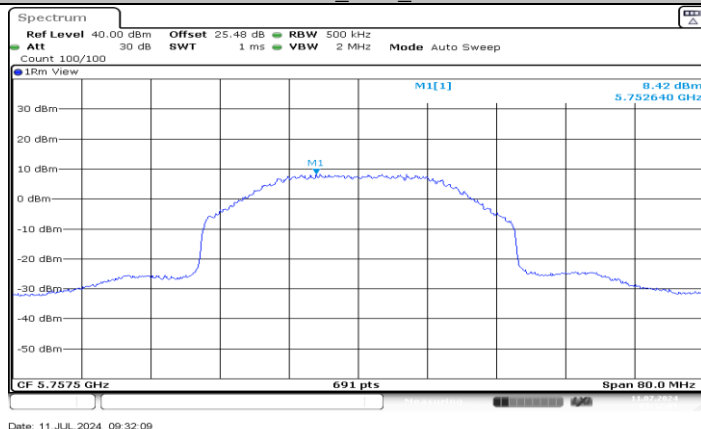
Date: 11 JUL 2024 09:27:48

SRD 40M_Ant5_5751.5



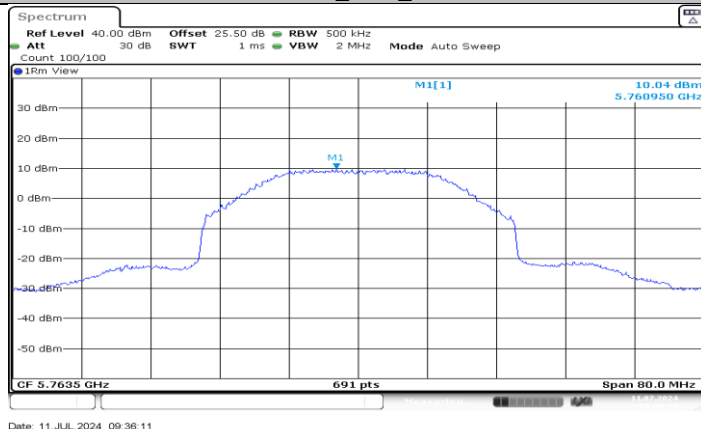
Date: 11 JUL 2024 09:30:44

SRD 40M_Ant4_5757.5



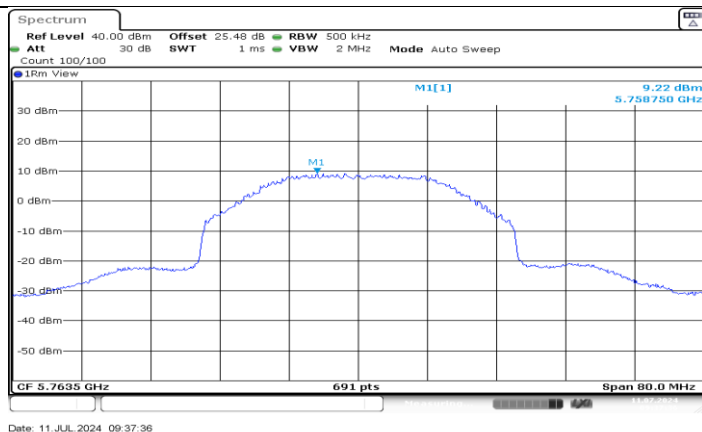
Date: 11 JUL 2024 09:32:09

SRD 40M_Ant5_5757.5

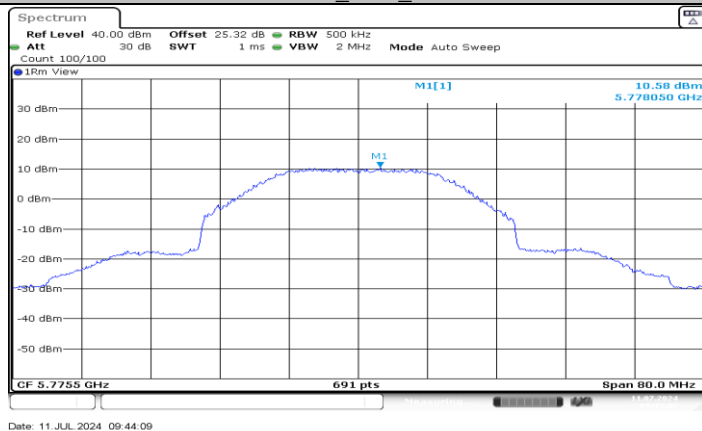


Date: 11 JUL 2024 09:36:11

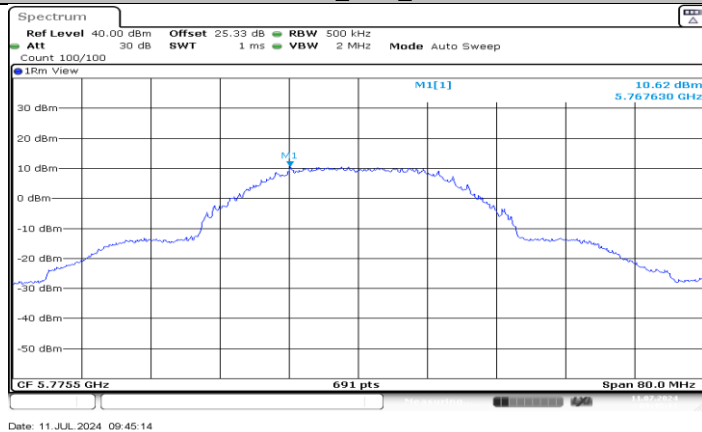
SRD 40M_Ant4_5763.5



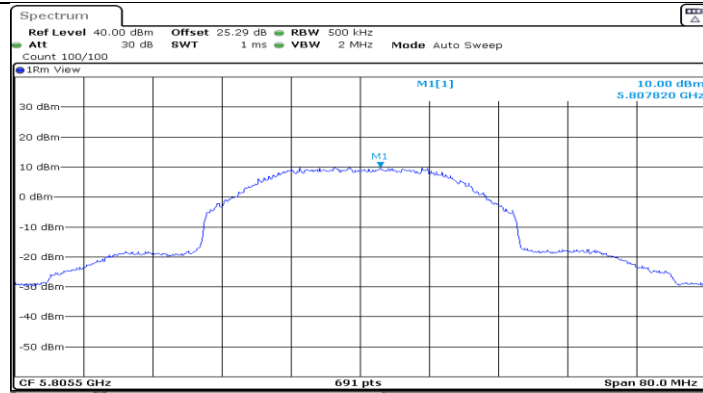
SRD 40M_Ant5_5763.5



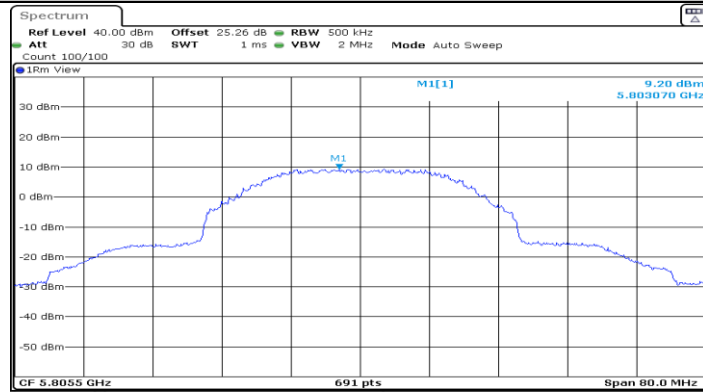
SRD 40M_Ant4_5775.5



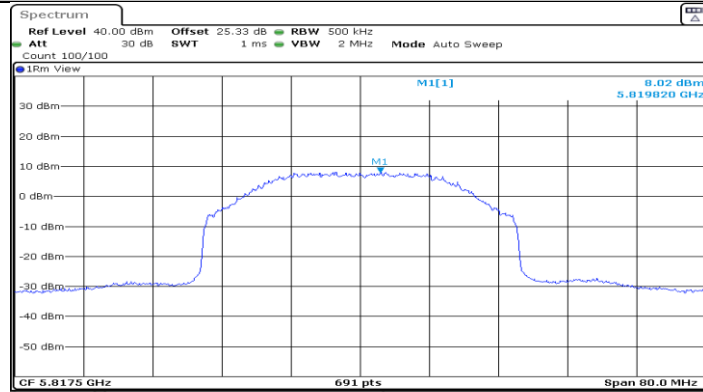
SRD 40M_Ant5_5775.5



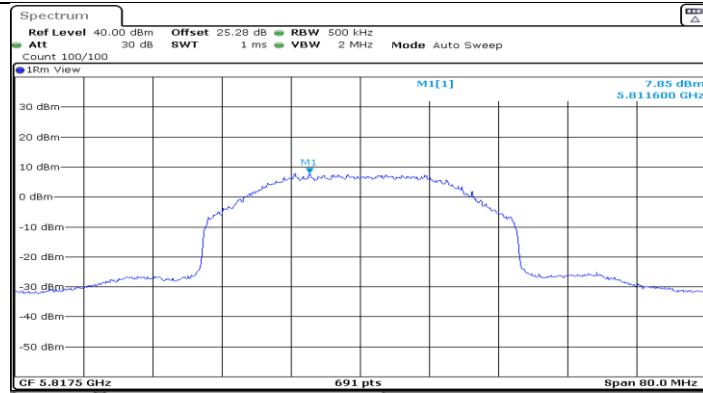
SRD 40M_Ant4_5805.5



SRD 40M_Ant5_5805.5

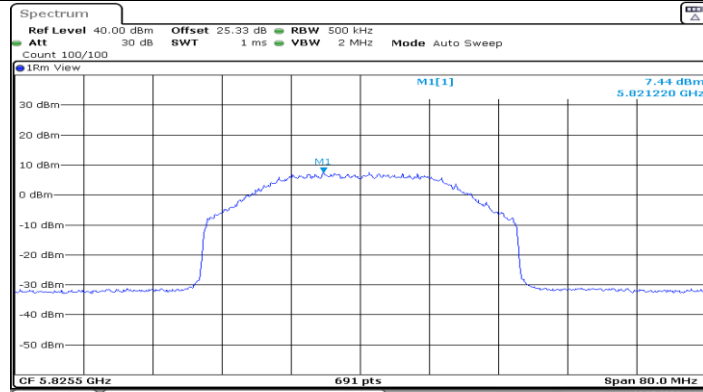


SRD 40M_Ant4_5817.5



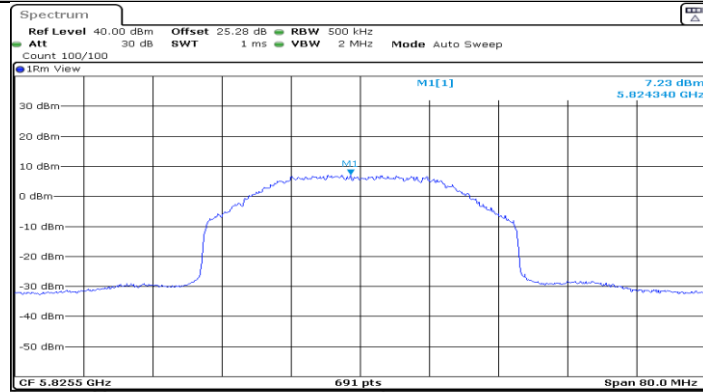
Date: 11 JUL 2024 09:57:28

SRD 40M_Ant5_5817.5



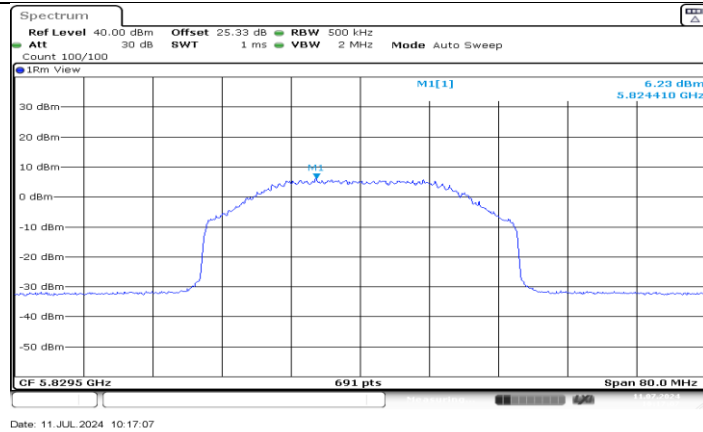
Date: 11 JUL 2024 10:10:51

SRD 40M_Ant4_5825.5

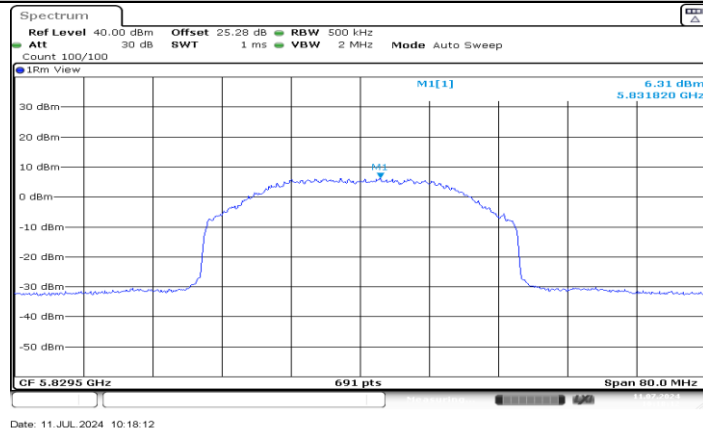


Date: 11 JUL 2024 10:11:55

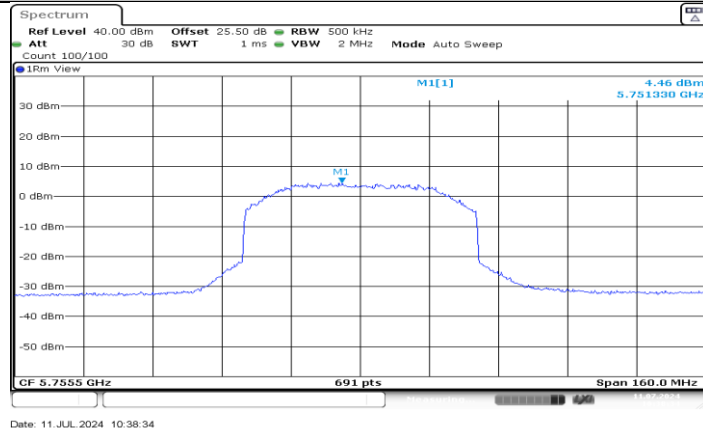
SRD 40M_Ant5_5825.5



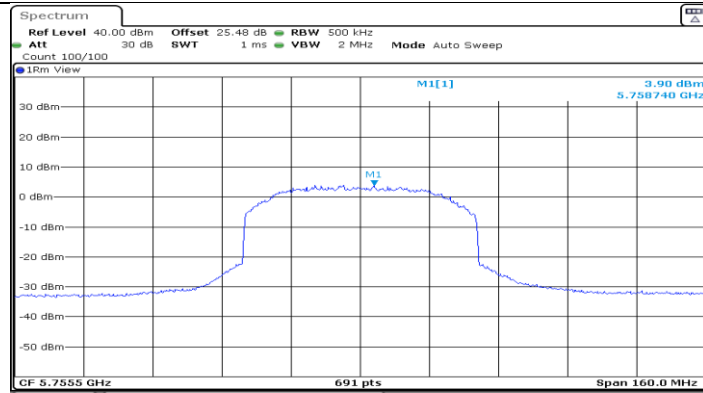
SRD 40M_Ant4_5829.5



SRD 40M_Ant5_5829.5

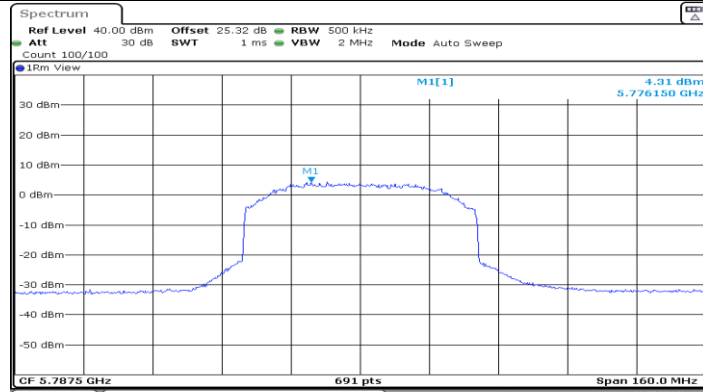


SRD 60M_Ant4_5755.5



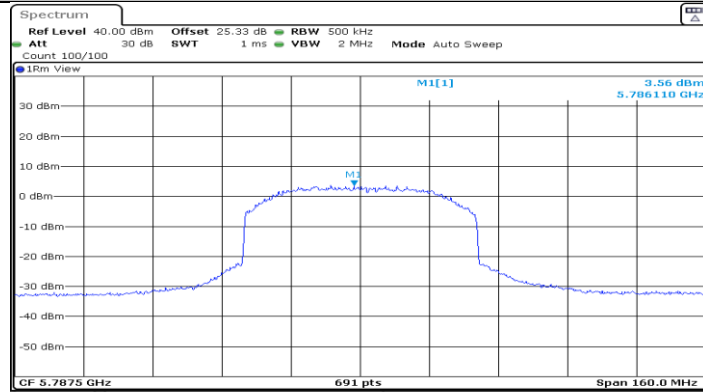
Date: 11 JUL 2024 10:39:38

SRD 60M_Ant5_5755.5



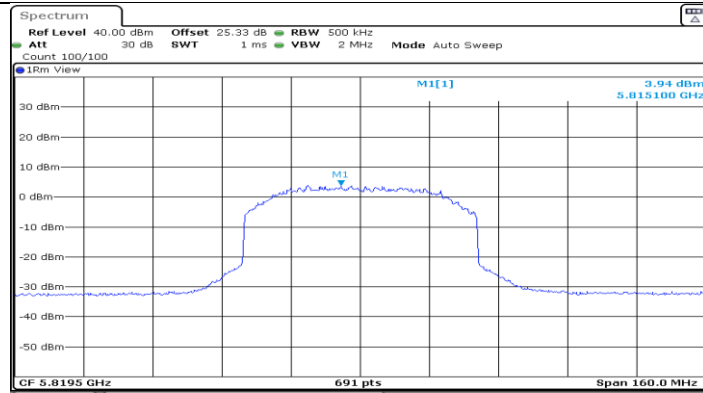
Date: 11 JUL 2024 10:54:10

SRD 60M_Ant4_5787.5



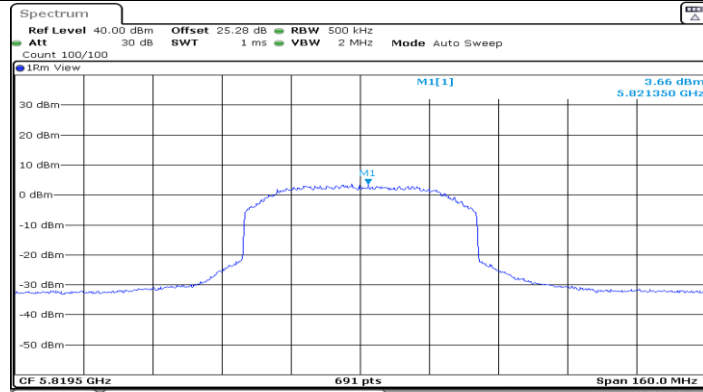
Date: 11 JUL 2024 10:55:14

SRD 60M_Ant5_5787.5



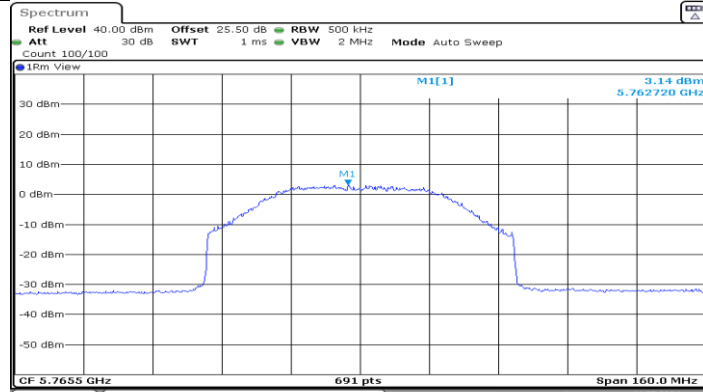
Date: 11 JUL 2024 10:49:12

SRD 60M_Ant4_5819.5



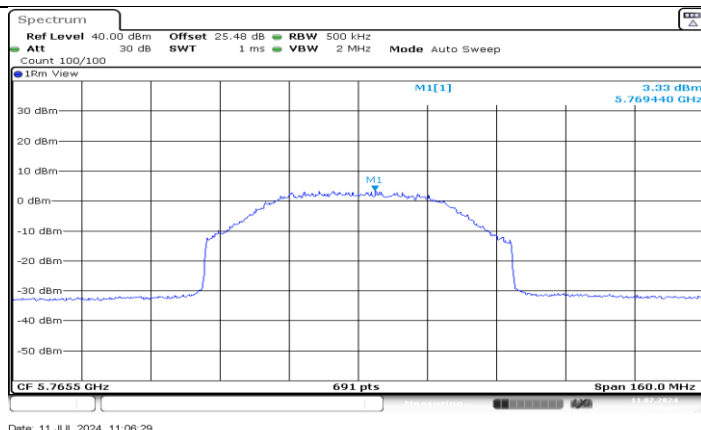
Date: 11 JUL 2024 10:51:33

SRD 60M_Ant5_5819.5



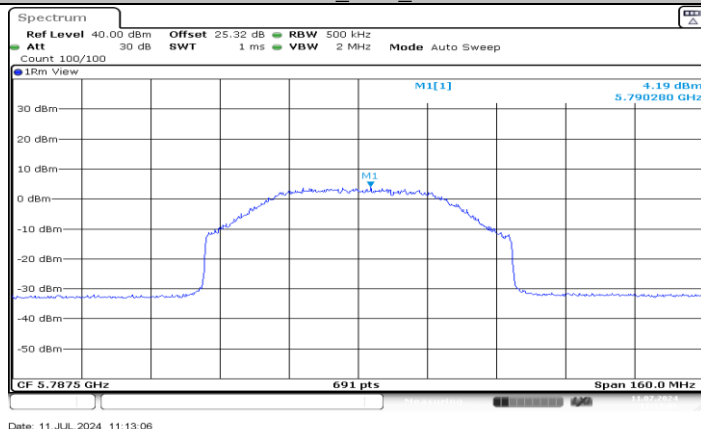
Date: 11 JUL 2024 11:05:24

SRD 80M_Ant4_5765.5



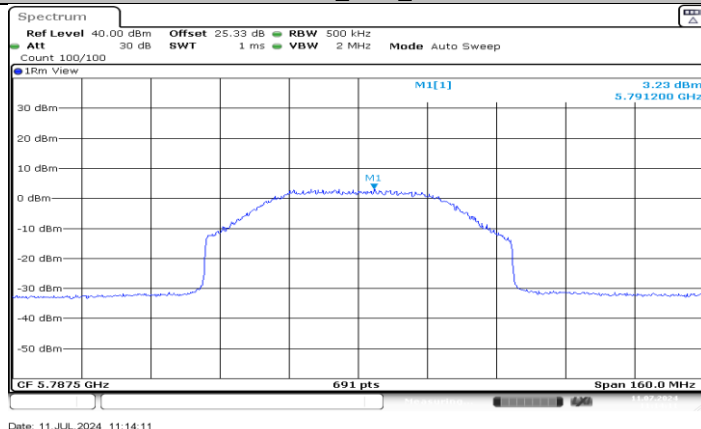
Date: 11 JUL 2024 11:06:29

SRD 80M_Ant5_5765.5



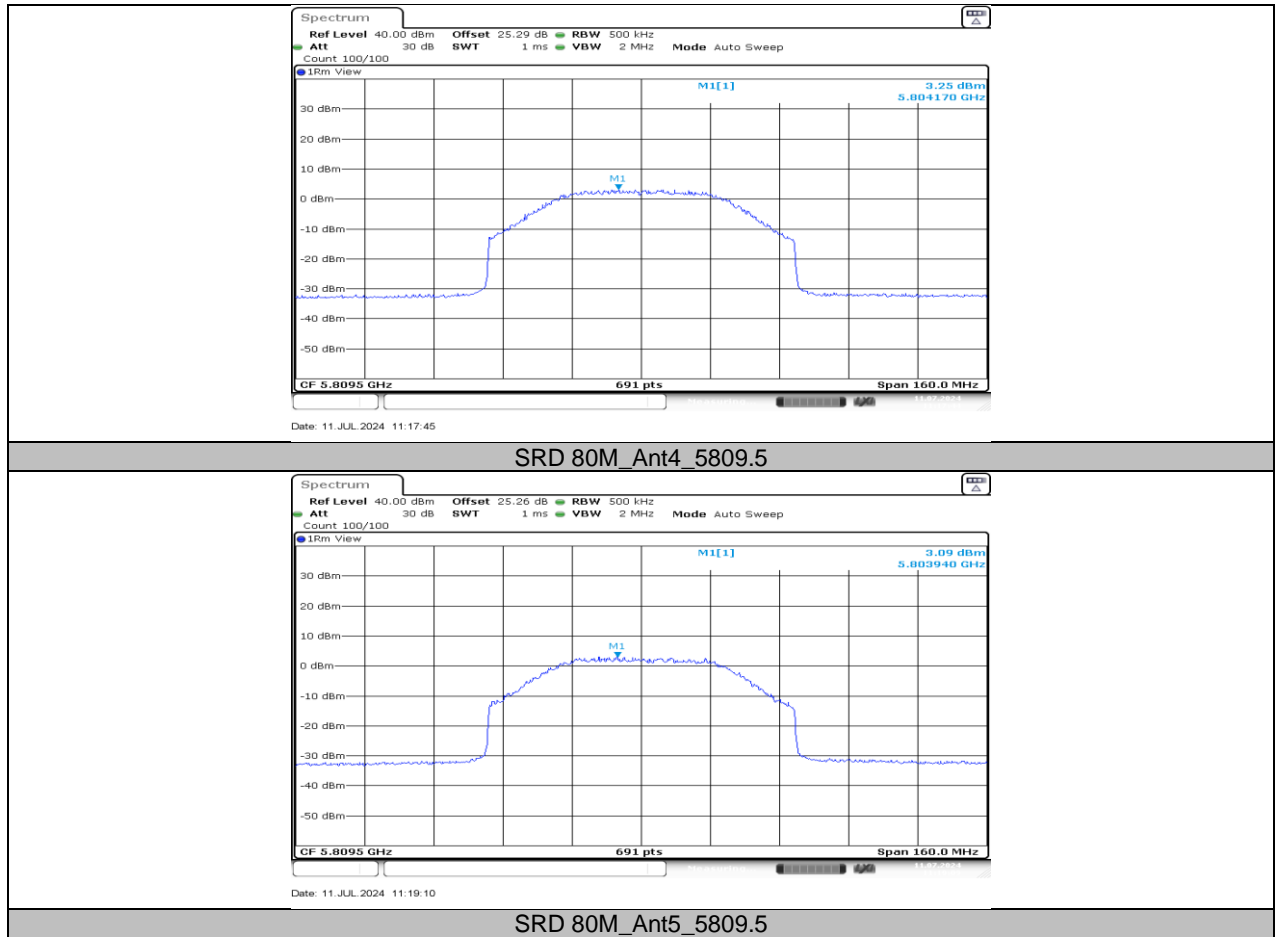
Date: 11 JUL 2024 11:13:06

SRD 80M_Ant4_5787.5



Date: 11 JUL 2024 11:14:11

SRD 80M_Ant5_5787.5



11.6. APPENDIX F: FREQUENCY STABILITY

11.6.1. Test Result

Frequency Error vs. Voltage									
802.11a:5159MHz									
Temp.	Volt.	0 Minute		2 Minute		5 Minute		10 Minute	
		Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)
TN	VL	5730.5051	0.89	5730.5129	2.26	5730.5096	1.68	5730.4889	-1.94
TN	VN	5730.5132	2.30	5730.5186	3.24	5730.4793	-3.61	5730.5106	1.85
TN	VH	5730.4761	-4.18	5730.5059	1.03	5730.4953	-0.81	5730.4929	-1.25
Frequency Error vs. Temperature									
802.11a:5159MHz									
Temp.	Volt.	0 Minute		2 Minute		5 Minute		10 Minute	
		Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)
40	VN	5730.4771	-3.99	5730.4771	-4.00	5730.4928	-1.26	5730.5041	0.72
30	VN	5730.5208	3.63	5730.5234	4.08	5730.4948	-0.90	5730.5054	0.95
20	VN	5730.4857	-2.50	5730.4751	-4.35	5730.5072	1.25	5730.5240	4.19
10	VN	5730.4821	-3.13	5730.4979	-0.37	5730.5235	4.10	5730.5175	3.05
0	VN	5730.4954	-0.80	5730.4797	-3.54	5730.4777	-3.89	5730.5172	2.99
-10	VN	5730.5207	3.61	5730.4849	-2.63	5730.4784	-3.76	5730.5021	0.36

Note:

1. All antennas, test modes and test channels have been tested, only the worst data record in the report.
2. For the detail Test Conditions, please refer to section 7.5 TEST ENVIRONMENT.

11.7. APPENDIX G: DUTY CYCLE

11.7.1. Test Result

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
SRD 10M	100.00	100.00	1.0000	100.00	0.00	0.01	0.01
SRD 20M	100.00	100.00	1.0000	100.00	0.00	0.01	0.01
SRD 40M	100.00	100.00	1.0000	100.00	0.00	0.01	0.01
SRD 60M	100.00	100.00	1.0000	100.00	0.00	0.01	0.01
SRD 80M	100.00	100.00	1.0000	100.00	0.00	0.01	0.01

Note:

Duty Cycle Correction Factor= $10\log(1/x)$.

Where: x is Duty Cycle (Linear)

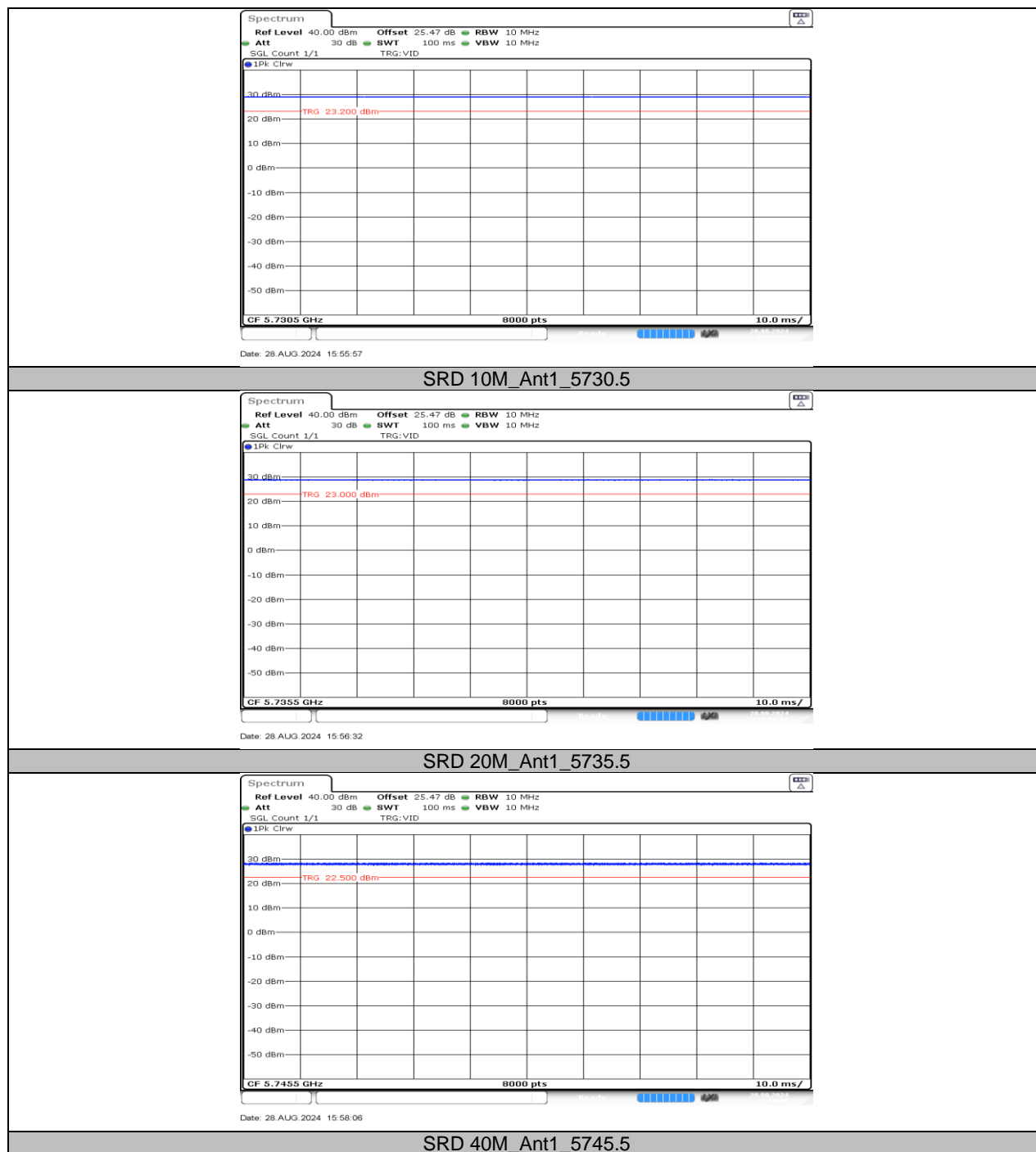
Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

If the EUT is configured to transmit with duty cycle $\geq 98\%$, set VBW \leq RBW/100 (i.e., 10 kHz) but not less than 10 Hz.

Note: All the modes and antennas had been tested, but only the worst data was recorded in the report.

11.7.2. Test Graphs





END OF REPORT