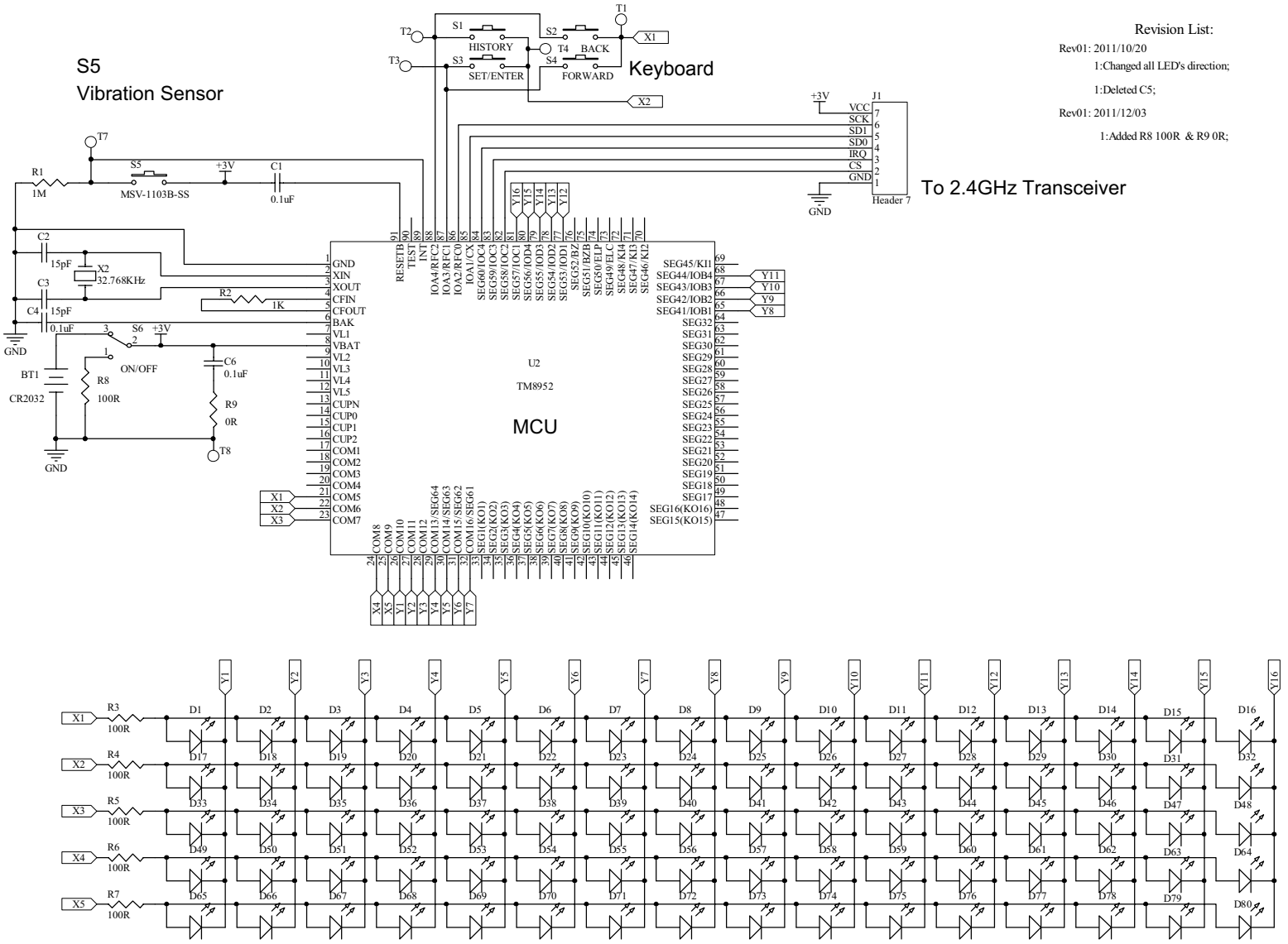

Revision List:

Rev01: 2011/10/20

1:Changed all LED's direction;

1:Deleted C5;

Rev01: 2011/12/03

1:Added R8 100R & R9 0R;

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Drawed By:	XXQ
Checked By:	XJ
Approved By:	

Title ***TextBands***

Size: B | Type: **02323** | Revision: 02

Date: 2012/2/2 Time: 下午 03:45:03 Sheet* of 1

File: D:\XXQ2\HZ\LED BUMPS\LED BUMPS_SCH REV02.SchDoc

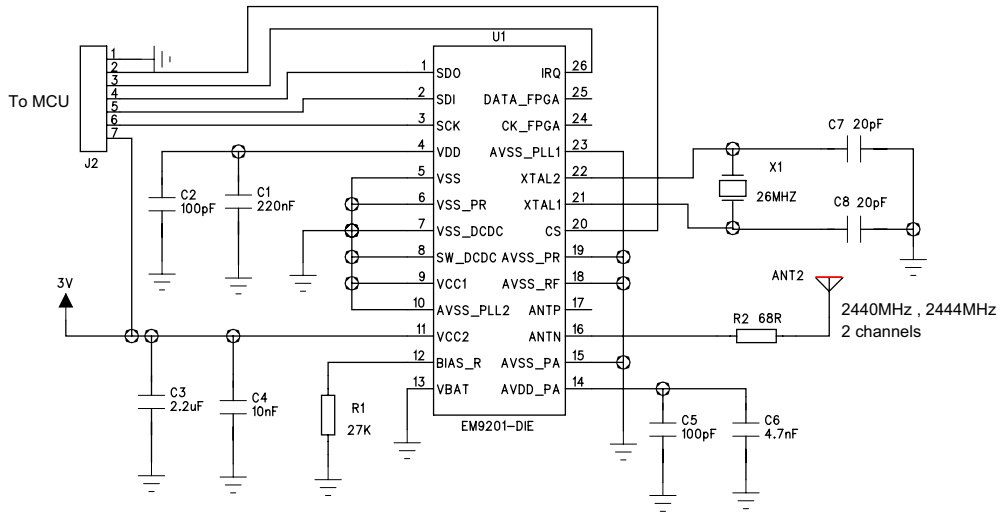
Jetta (China) Co. Ltd.

Corresponding PCB :

JETTA COMPANY LIMITED

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
A2	change R2 to 56 ohm,C3 to2.2uF	CAI	2011-12-26
A3	renane Y1 to X1	Cai	2011-12-29

Schematic of 2.4GHz Transceiver



DRAWN:	DATED:
CHECKED:	DATED:
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

COMPANY: C-MAX			
TITLE: CMM-9201-V7.1			
CODE:	SIZE:	DRAWING NO:	REV: A3
SCALE:		SHEET: 1 OF 1	

9. Typical Applications

In this chapter, typical application scenarios for the EM9201/02 are described – both for the DC/DC step-up configuration (Version 1) and for system using a direct battery (or LDO) supply.

9.1 Application schematic (Versions 1 and 2)

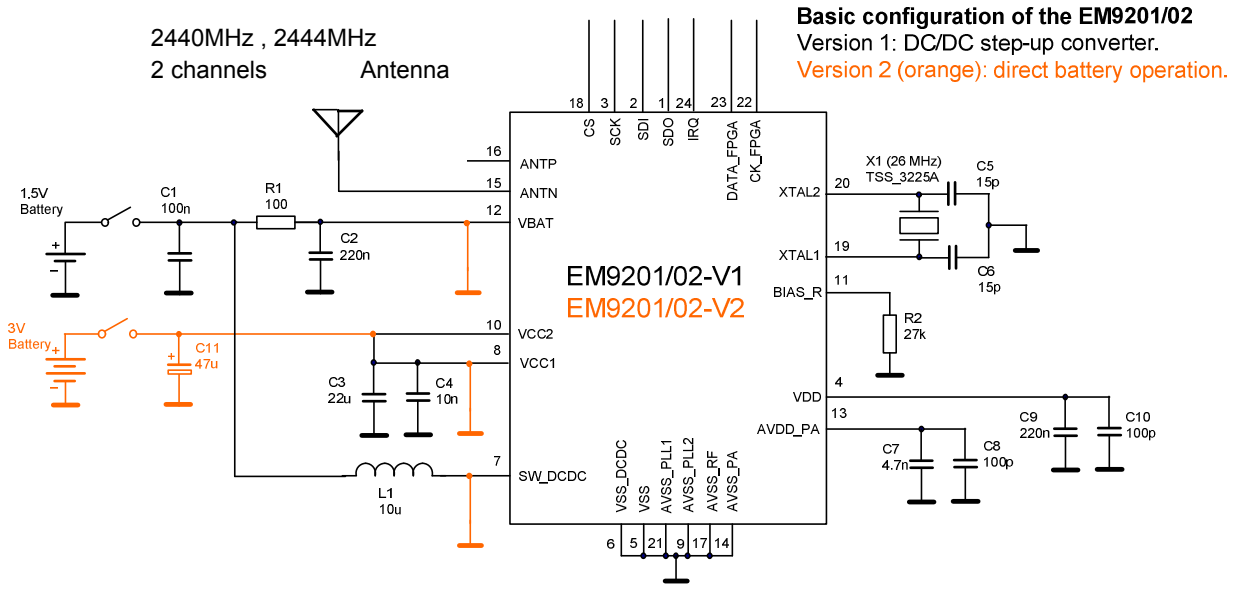


Figure 15: Application schematic for Version 1 (DC/DC) and Version 2 (no DC/DC). For Version 2 (orange), the pins VBAT, VCC1 and SW_DCDC are connected to system ground as well as components L1, R1, C2 and C3 need to be removed.

Component	Value	Footprint	Description
A1	200Ω	-	Printed loop antenna
C1	100n	0402	VBAT decoupling capacitor, +/- 10%
C2	220n	0805	VBAT filter capacitor, +/- 10%
C3	22uF	0805	DC/DC storage capacitor, X5R +/- 10%
C4	10nF	0402	VCC2 decoupling, +/- 10%
C5	15pF 1)	0402	XTAL load capacitor , +/- 5%
C6	15pF 1)	0402	XTAL load capacitor, +/- 5%
C7	4.7nF	0402	LDO-PA decoupling capacitor, +/- 10%
C8	100pF	0402	LDO-PA decoupling capacitor, +/- 10%
C9	220nF	0805	LDO-Digital decoupling capacitor, +/- 10%
C10	100pF	0402	LDO-Digital decoupling capacitor, +/- 10%
C11	47uF	1206	Version 2: VCC2 decoupling capacitor
L1	10uH	-	DC/DC coil: recommended ESR < 120mΩ, +/- 20%
R1	100 Ω	0402	VBAT filter resistor, +/- 10%
R2	27k Ω	0402	RF-biasing resistor, +/- 2%
X1	26 MHz	-	Crystal, +/- 50ppm, C0 = 10pF. Example: TSS_3225A

Note 1: C5 and C6 must have values that match the crystal load capacitance.