

## Operational description



### Explanation of G6S block diagram ( Model 6400)

- 1- The main CPU, connected to Flash memory and DRAM memory, manages all peripherals on the board. It has a 12 Mhz crystal oscillator.
- 2- The auxiliary controller manages
  - the power supply (+1.15V, +1.2V, +1.8V, +3.3V, +5V)
  - the real time clock with 32.768khz crystal
  - the battery charger.
- 3- Memory of the product is provided by DRAM memory, Flash memory and Hard drive (HD). The HDD is connected to main CPU via a CE-ATA.
- 4- The LCD is controlled by main CPU through a CPLD.
- 5- Audio functionality (Input /Output) are managed by Audio Codec
- 6- WiFi is provided by on board circuits, including Base Band, Power Amplifier, switches and antennae. It uses a 40MHz crystal. It works in 802.11b and 802.11g modes.
- 7- Connectors are used for USB host, USB device, and digital video in/out functionalities.