

Technical Description

ARCHOS 704 WiFi

Explanation of A704Wifi block diagram (model 42725)

- 1- The main CPU (DM420 with Flash eeprom and DDRAM) manage all peripherals on the board
- 2- The auxiliary CPU(MSP) manage the power supply (+1.2v/+1.8v/+3.3v/+5v) and provide to the main CPU the real time clock with 32.768khz crystal ,and manage the battery charger.
- 3-Memorys of the product is provided by DDR2 memory(DDRAM), Flash memory and Hard drive (HD)
- 4-The LCD is controlled by main CPU through LCD driver, LCD power
- 5-Audio functionality (Input /Output) managed by Audio Codec (AIC31)
- 6- Wifi components are on a additional PCB. Wifi is provided by one complete module, on a and managed by the main CPU via SDIO bus.