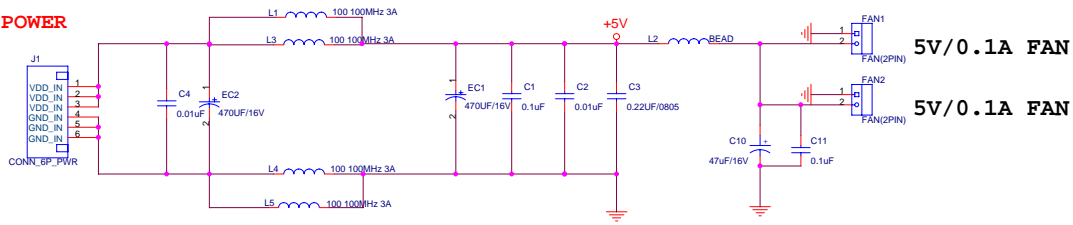
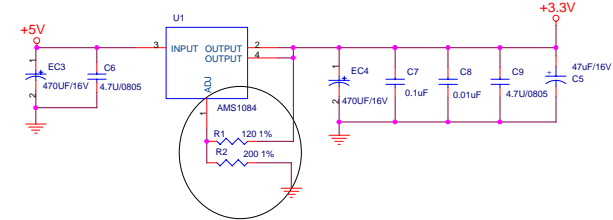


Title		
<b>Block Diagram</b>		
Size	Document Number	Rev
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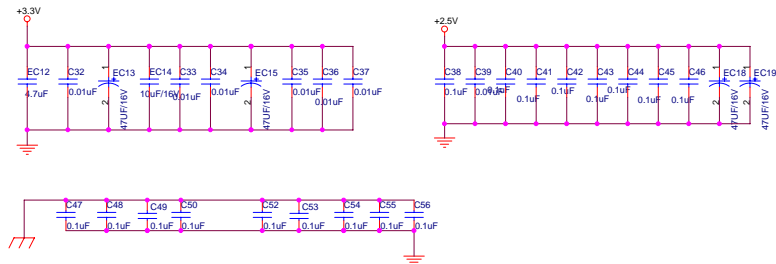
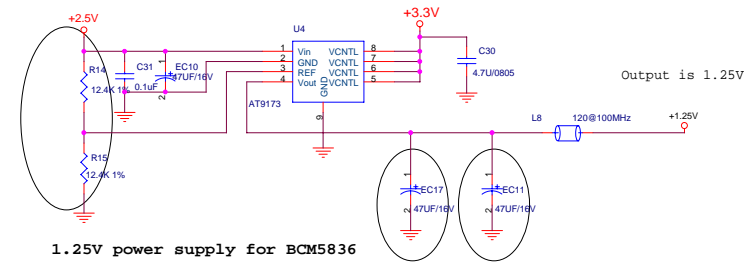
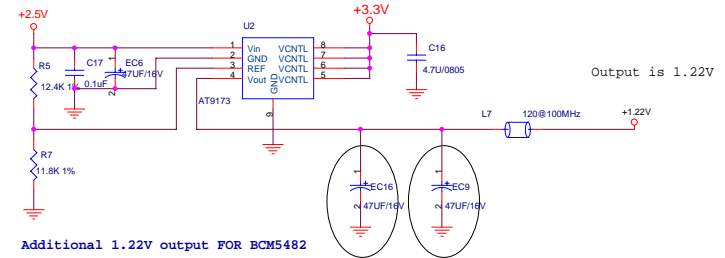
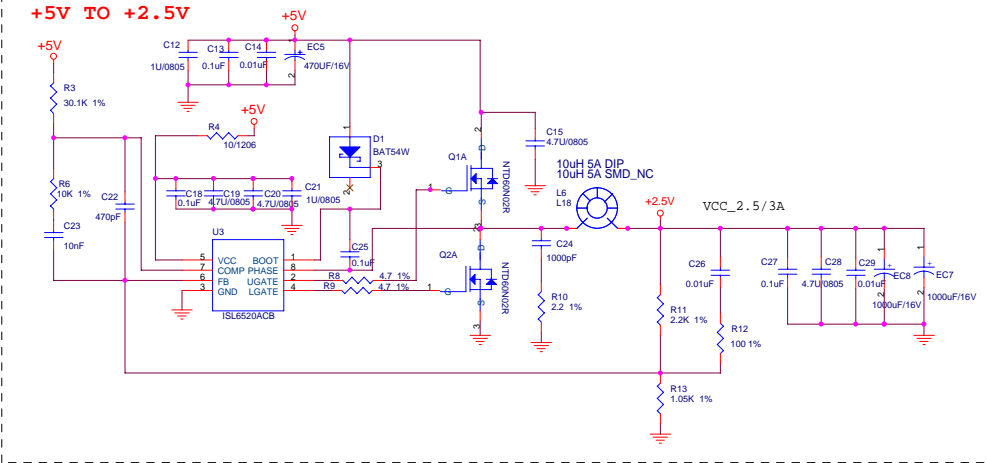
**MAIN POWER**



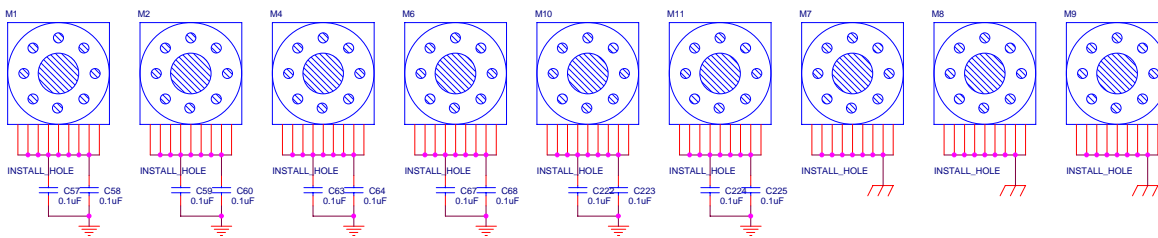
**+5V TO +3.3V**



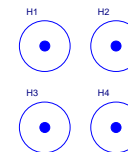
**+5V TO +2.5V**



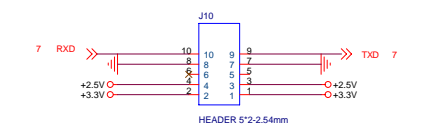
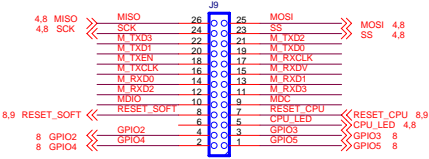
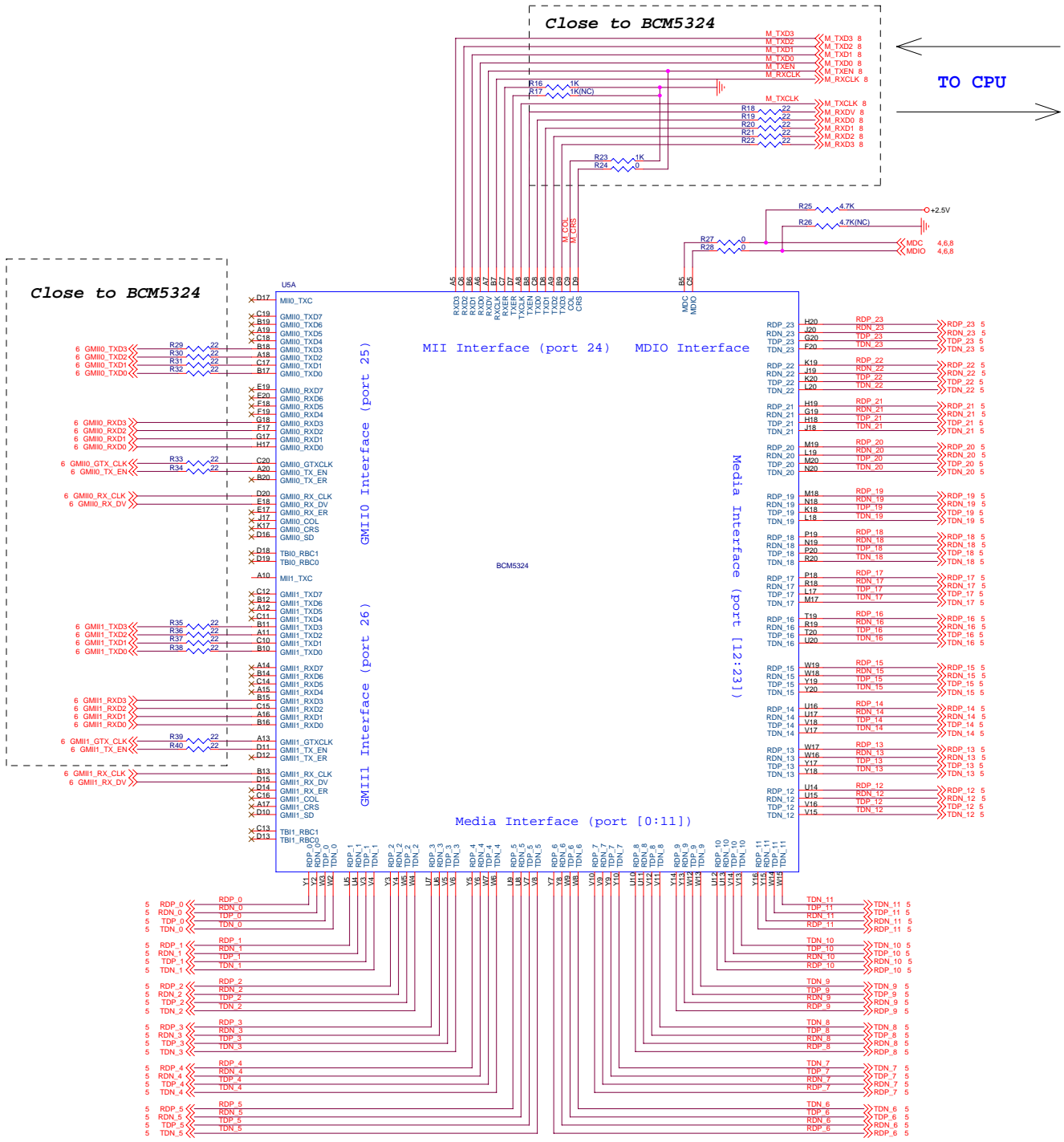
**定位螺孔**



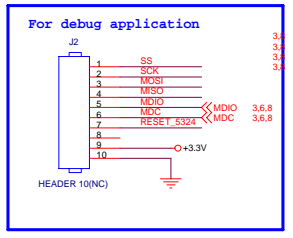
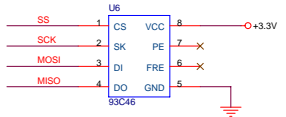
**光学定位点**



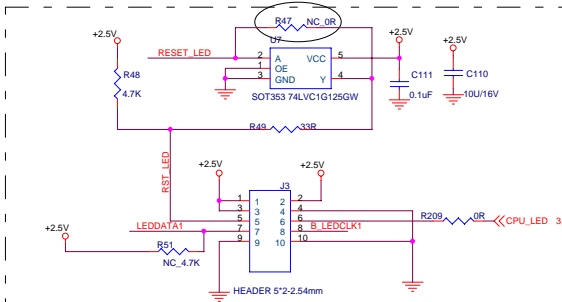
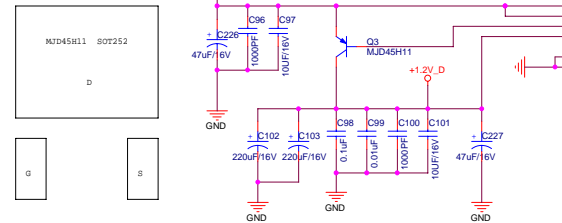
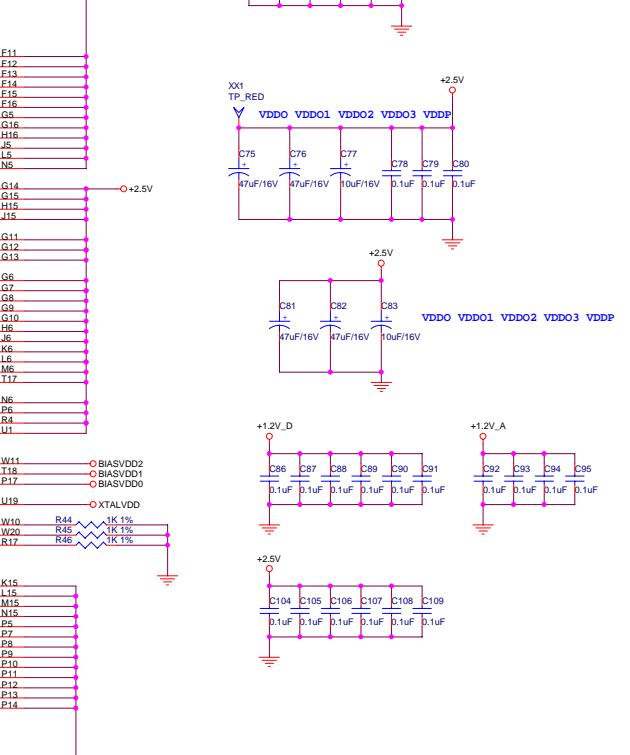
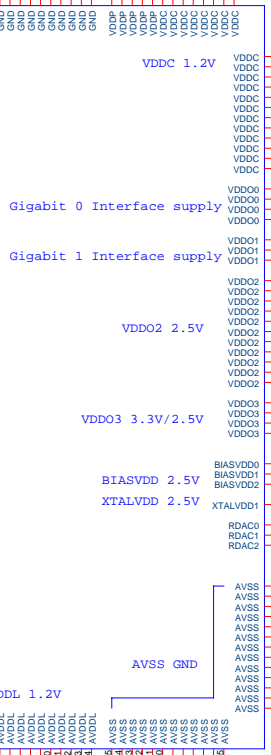
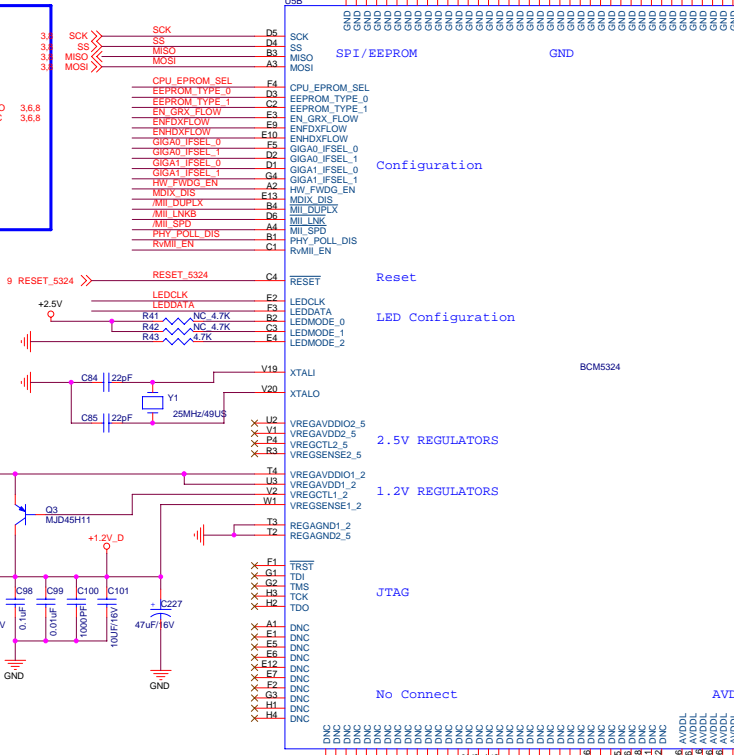
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DC-DC		
Size	Document Number	Rev
C	BCM5324 24+2G Managed Switch	2.0
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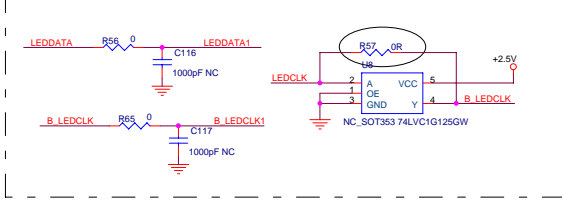
File		
5324 Integrated PHY, RGMII		
Size	Document Number	Rev
C	BCM5324 24+2G Managed Switch	2.0
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LEDNODE[0:2]=000,LINK/ACT: SPEED: DUPLEX

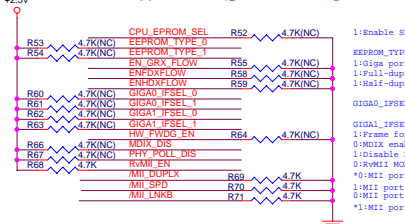


Change for facilitating adding connect RESET1

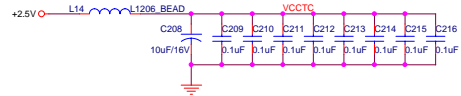
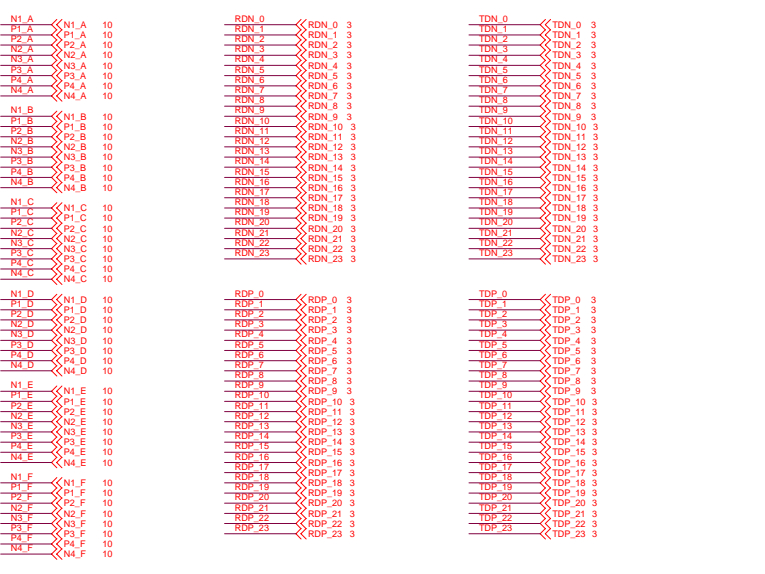
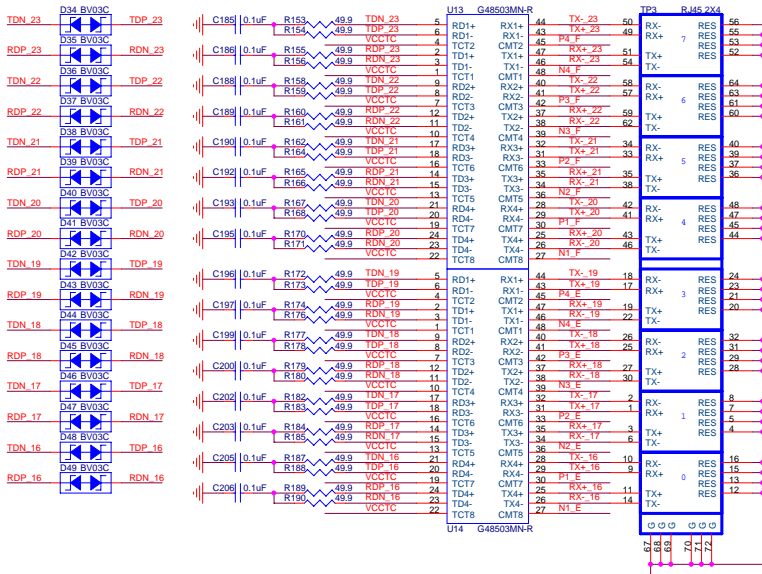
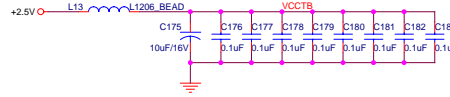
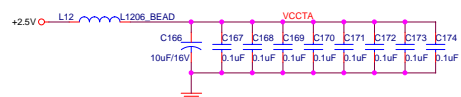
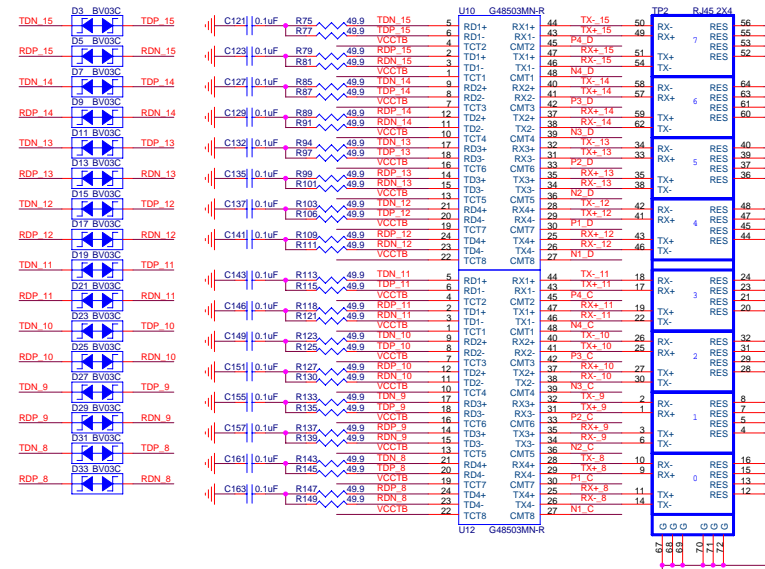
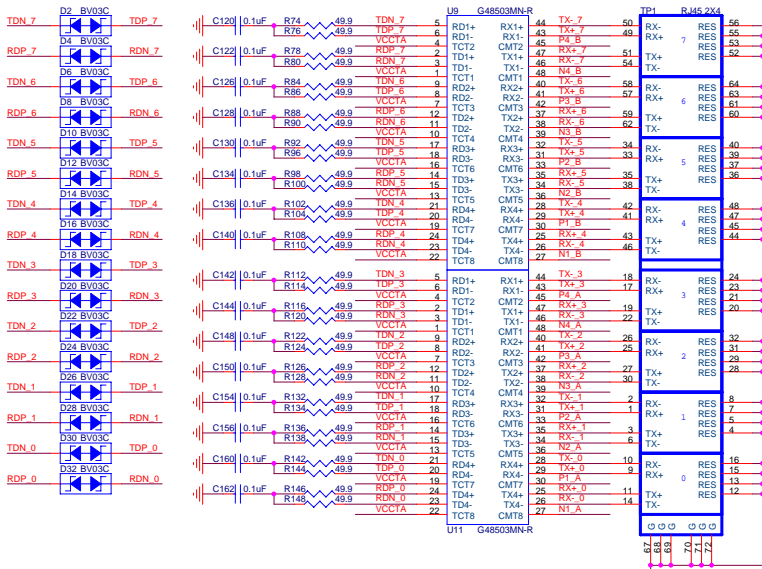


R54	R53	EEPROM TYPE
OFF	OFF	93C46
OFF	ON	93C56
ON	OFF	93C56
ON	ON	93C86

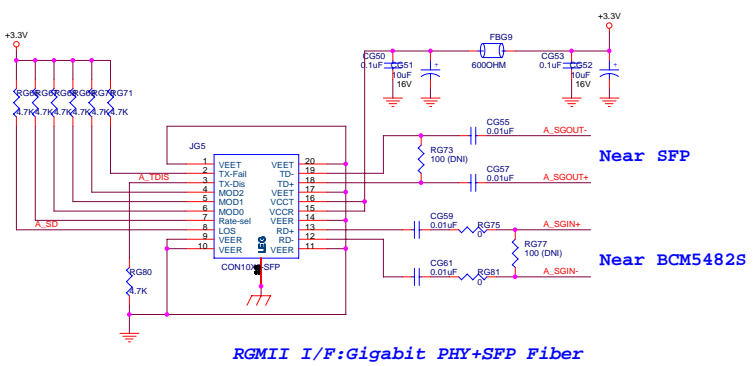
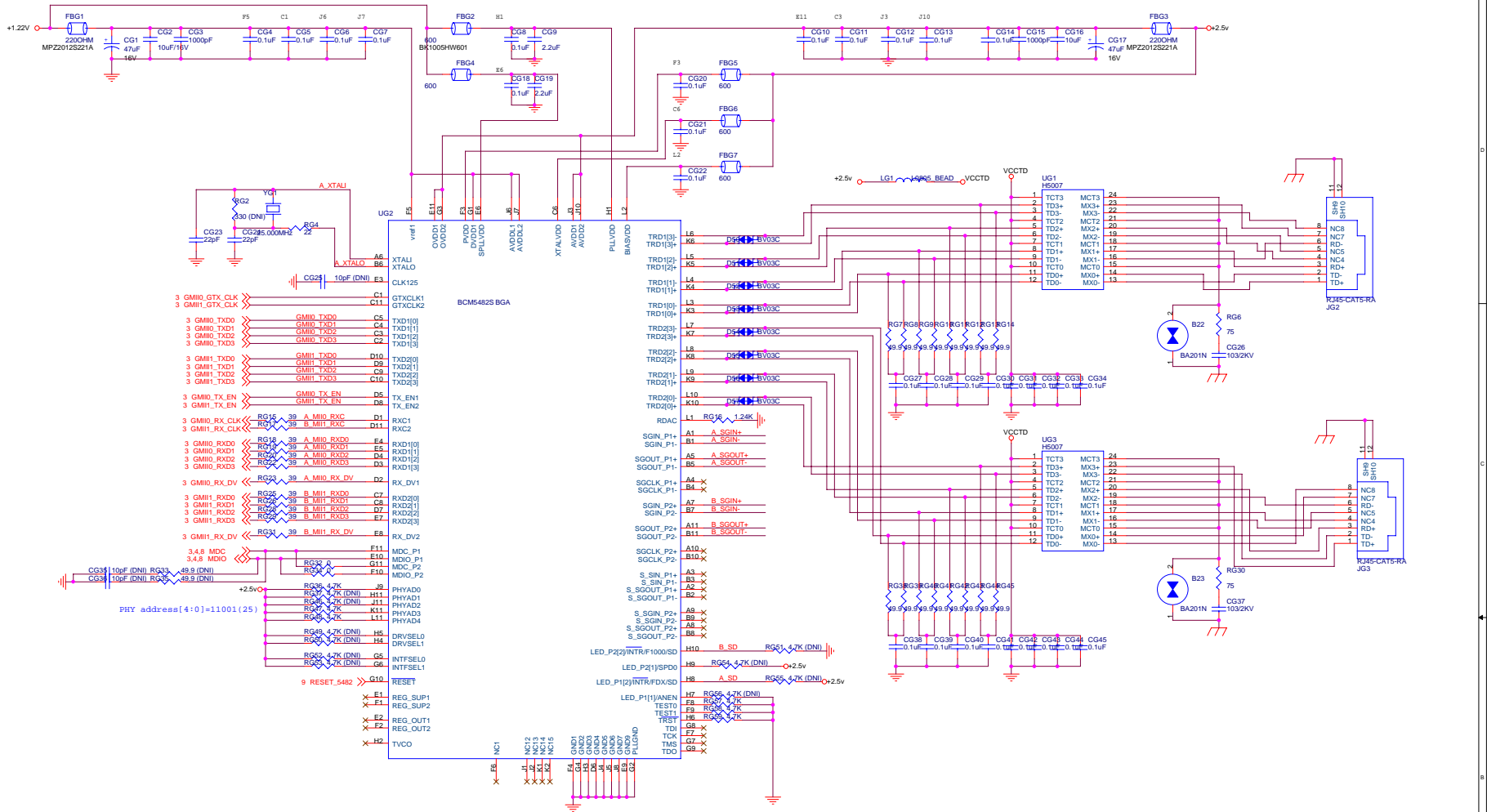
### HARDWARE SETTING



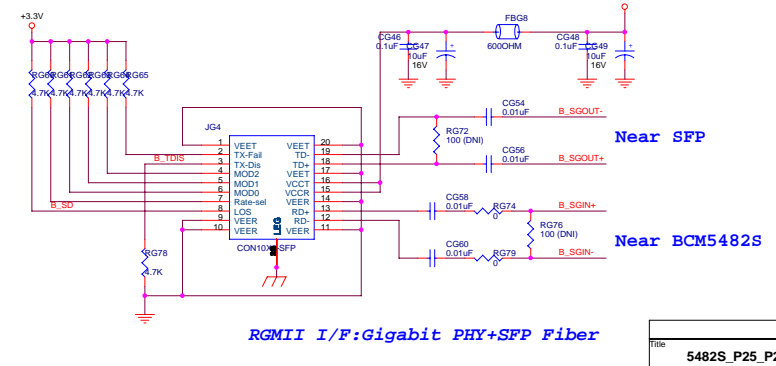
1: Enable SPI interface connect to CPU (default = 1)  
 EEPROM\_TYPE[1:0]=10 : Select 93C66 Type EEPROM (default = 00)  
 1: Giga ports Half-duplex flow control enable (default = 1)  
 1: Full-duplex flow control enable (default = 1)  
 1: Half-duplex flow control enable (default = 1)  
 GIGAO\_IPSEL[1:0]=00 : RMII Mode (default = 00)  
 1: Preamble forwarding is enabled (default = 1)  
 0: MDIX enable (default = 0)  
 1: Disable External PHY Polling (default = 0)  
 0: MII port defaults to full-duplex operation  
 \*0: MII port defaults to full-duplex operation  
 1: MII port defaults to half-duplex operation  
 0: MII port defaults to 10Mb/s operation  
 \*1: MII port defaults to 10Mb/s operation



0	2	4	6	8	10	12	14	16	18	20	22
1	3	5	7	9	11	13	15	17	19	21	23

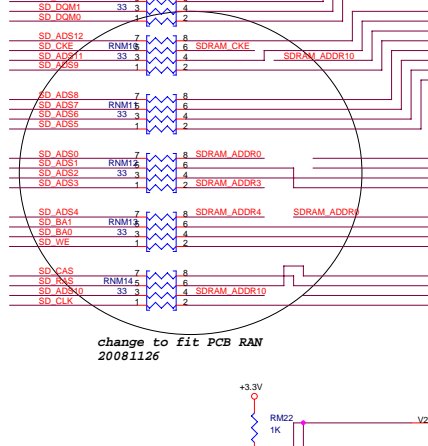
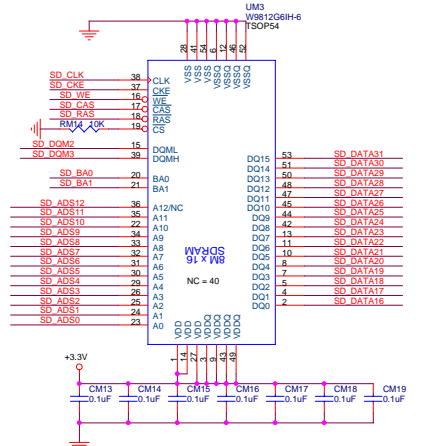
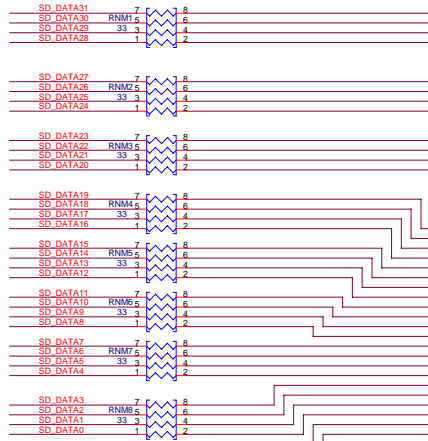
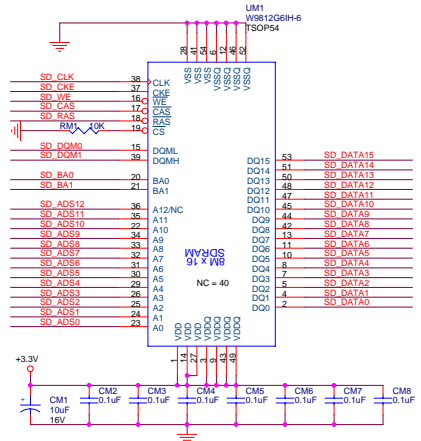


RGMII I/F:Gigabit PHY+SFP Fiber

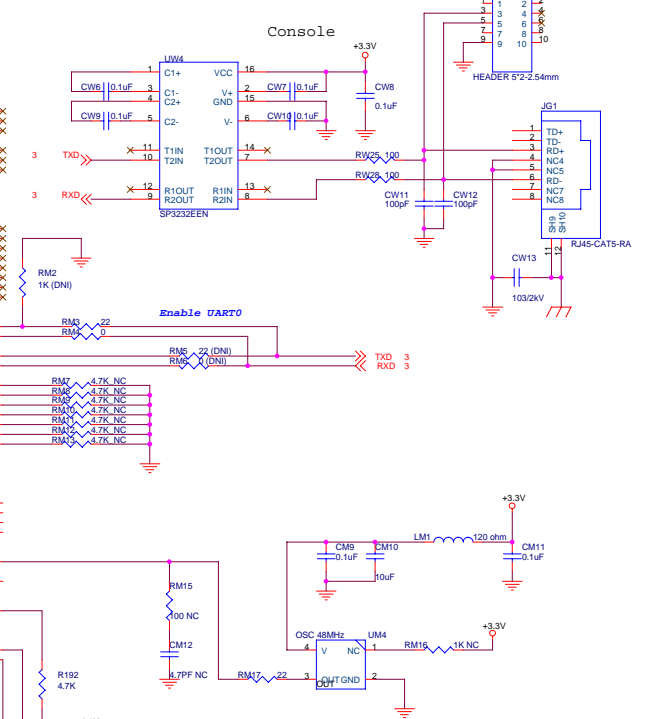
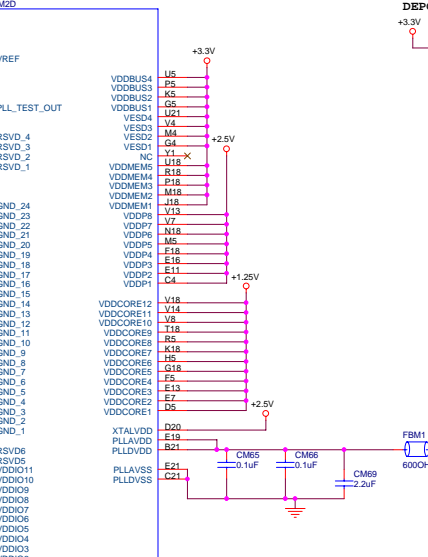
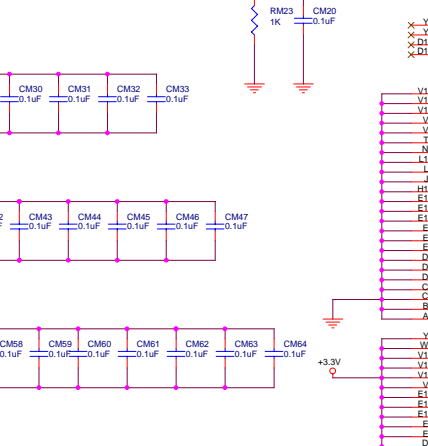
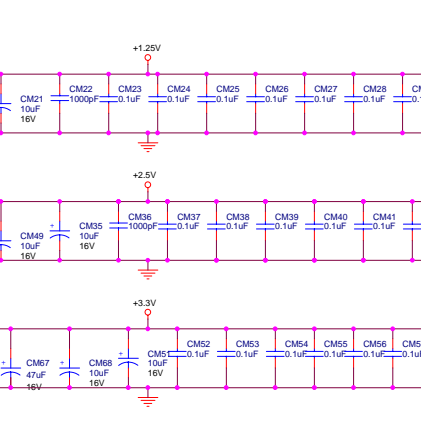


RGMII I/F:Gigabit PHY+SFP Fiber

Title			5482S_P25_P26
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Custom			Rev 2.0
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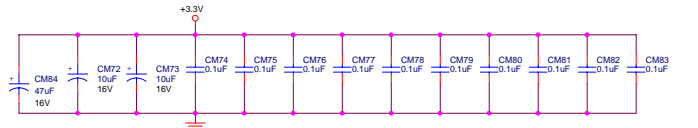
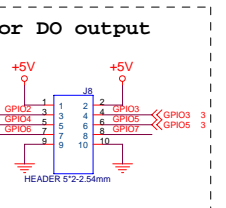
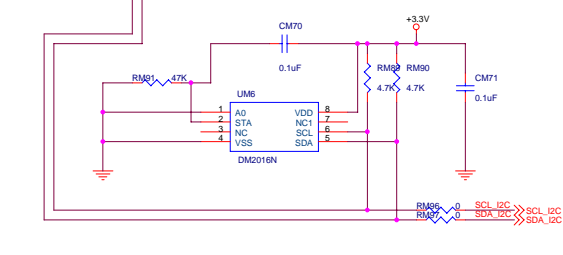
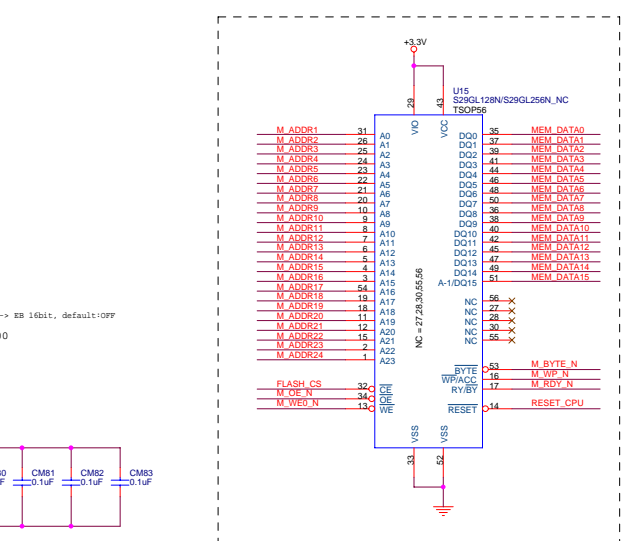
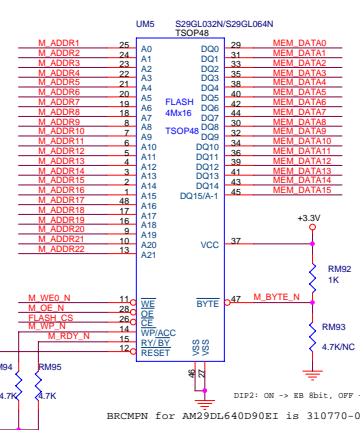
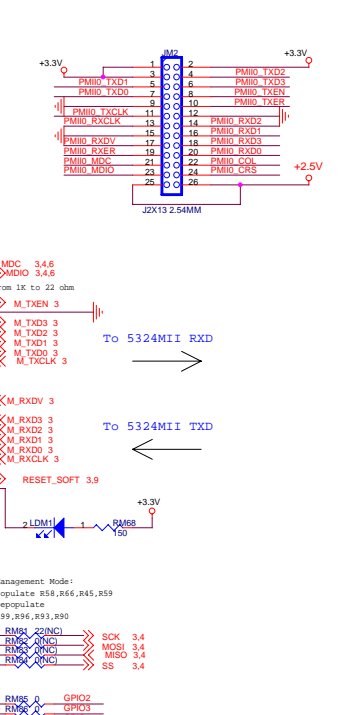
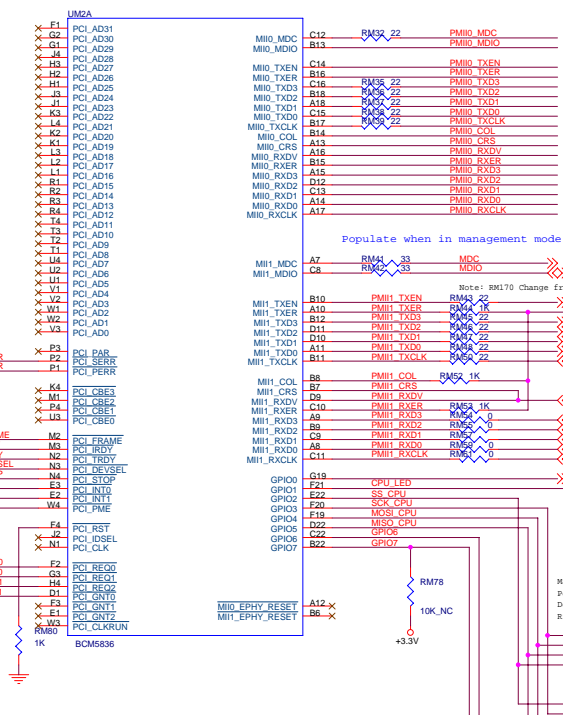
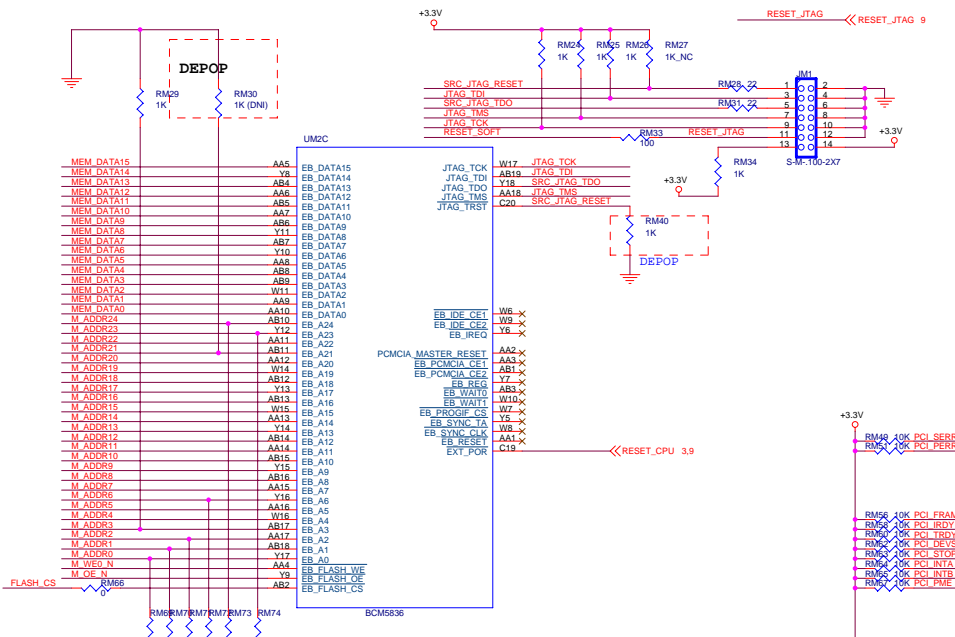


change to fit PCB RAN 20081126

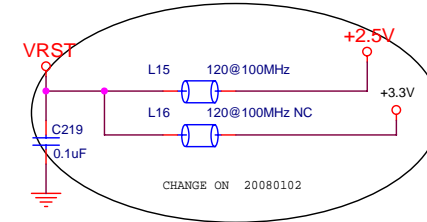
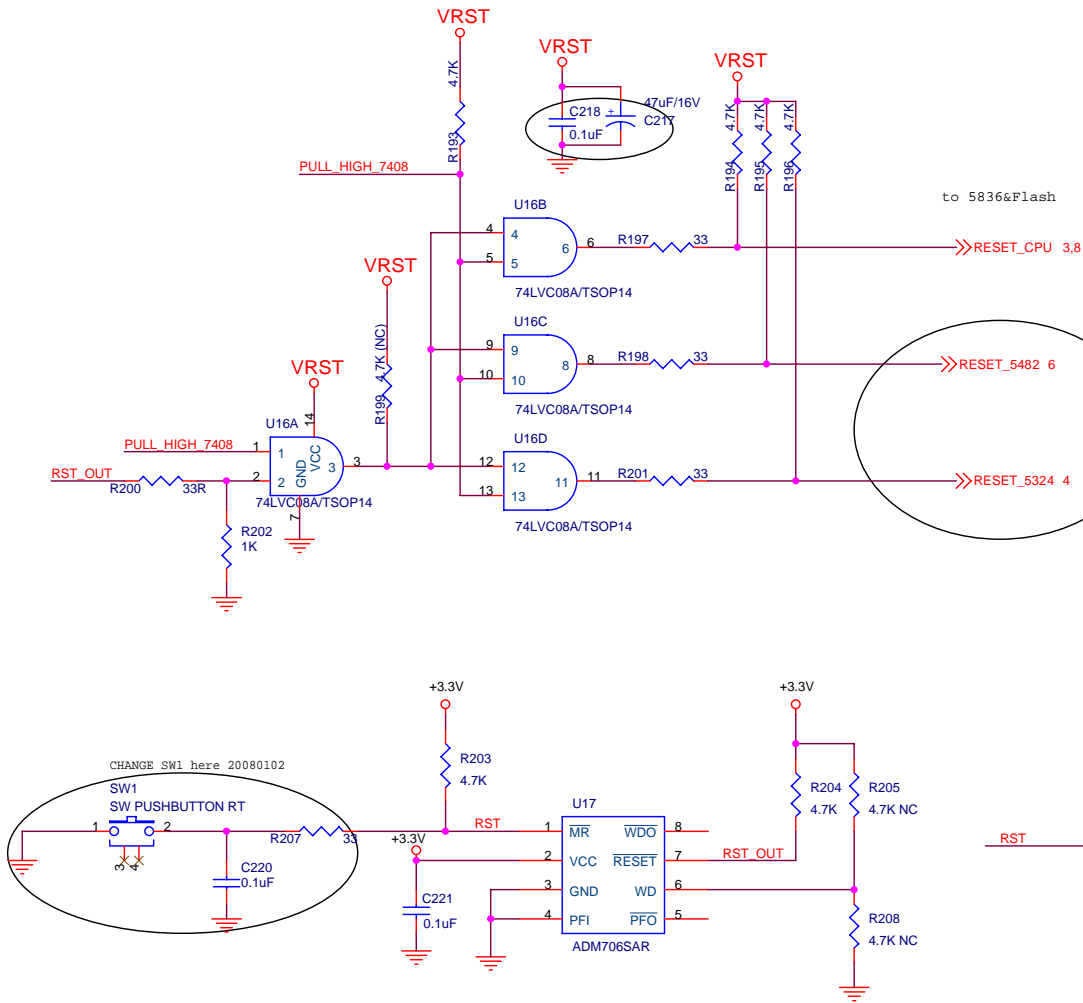


0 -> Little endian: Linux  
1 -> Big endian: Vxworks

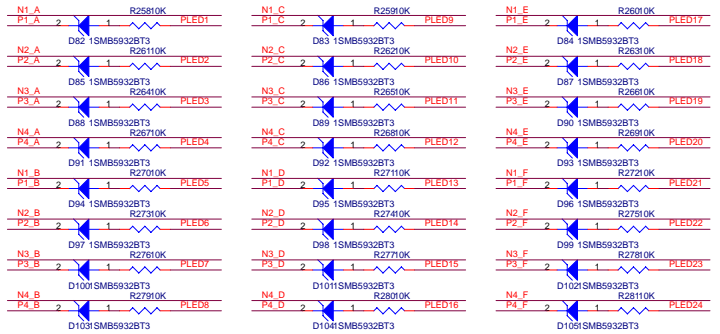
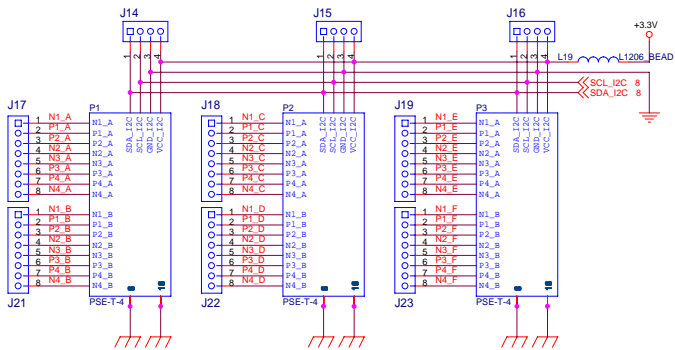
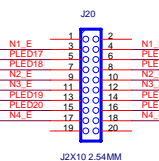
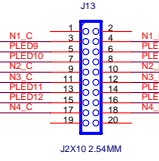
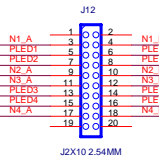
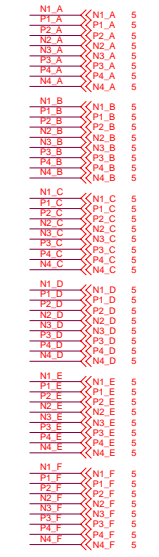
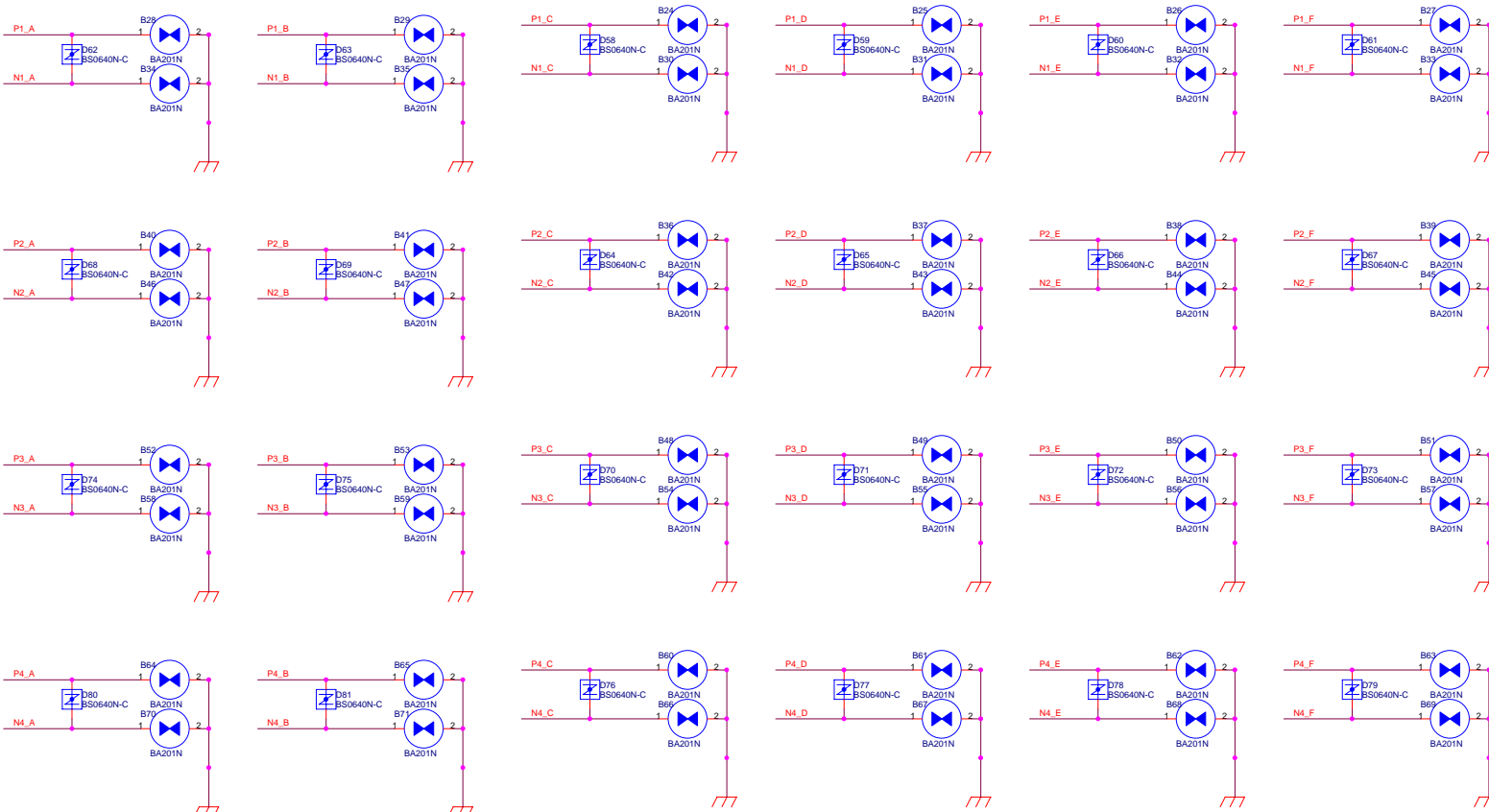
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5836_SDRAM		
Size	Document Number	Rev
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RESET		
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File		
5324 R.J45		
Size	Document Number	Rev
C	BCM5324 24+2G Managed Switch	2.0
Date	Wednesday, July 14, 2010	Sheet 10 of 10