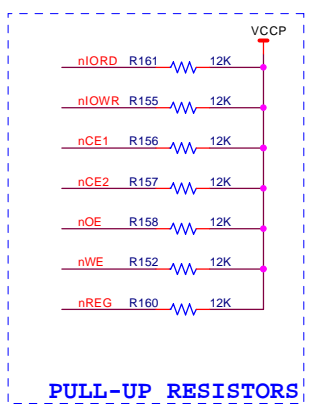
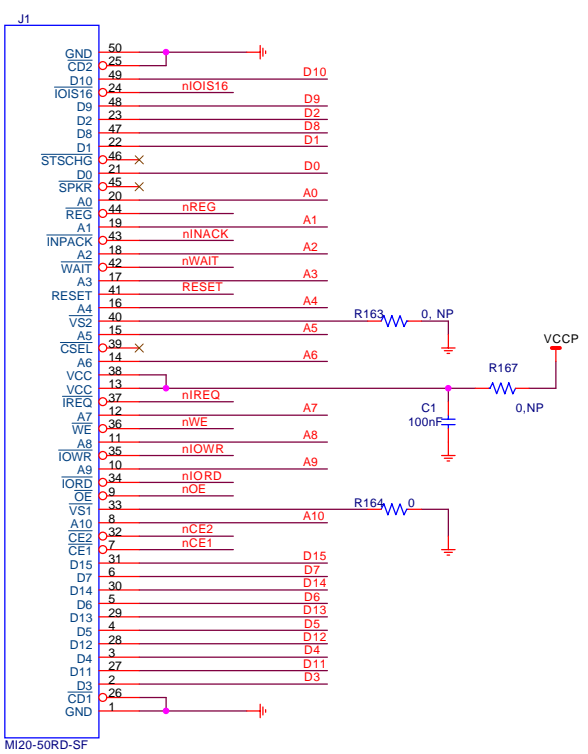
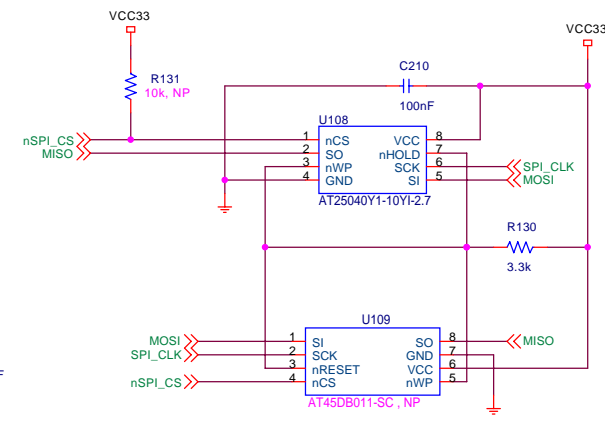
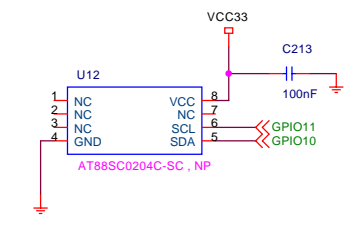


**COMPACT FLASH CONNECTOR**

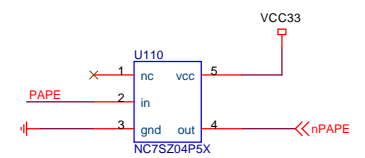
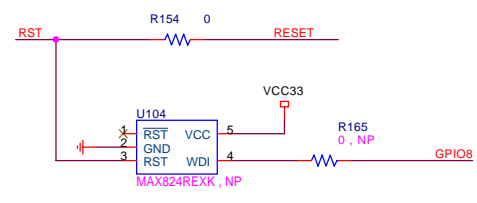
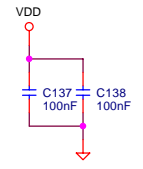
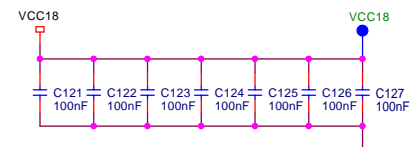
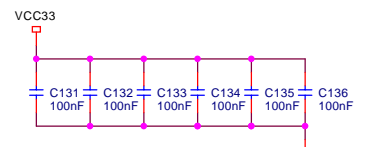
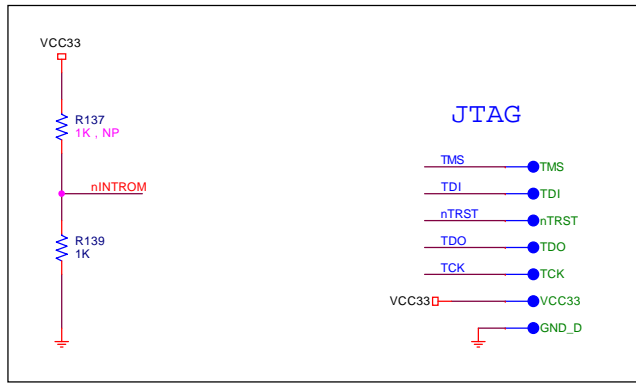


**Cryptomemory**



**FOR EEPROM CONFIGURATION:**  
 POPULATE U108,  
 U109=NP,  
 R136=1K,  
 R138=NP

**FOR DATAFLASH CONFIGURATION:**  
 POPULATE U109,  
 U108=NP,  
 R138=1K,  
 R136=NP



**AT76C504A**

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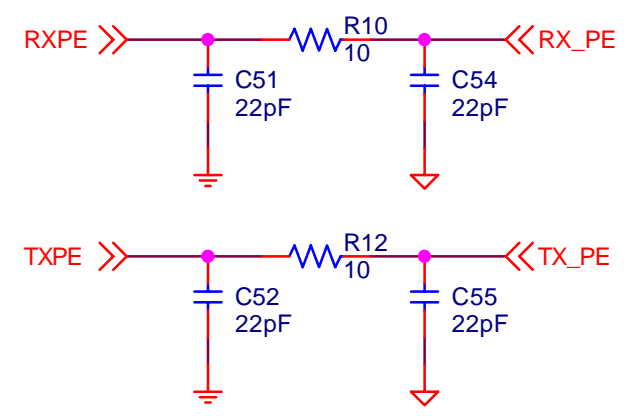
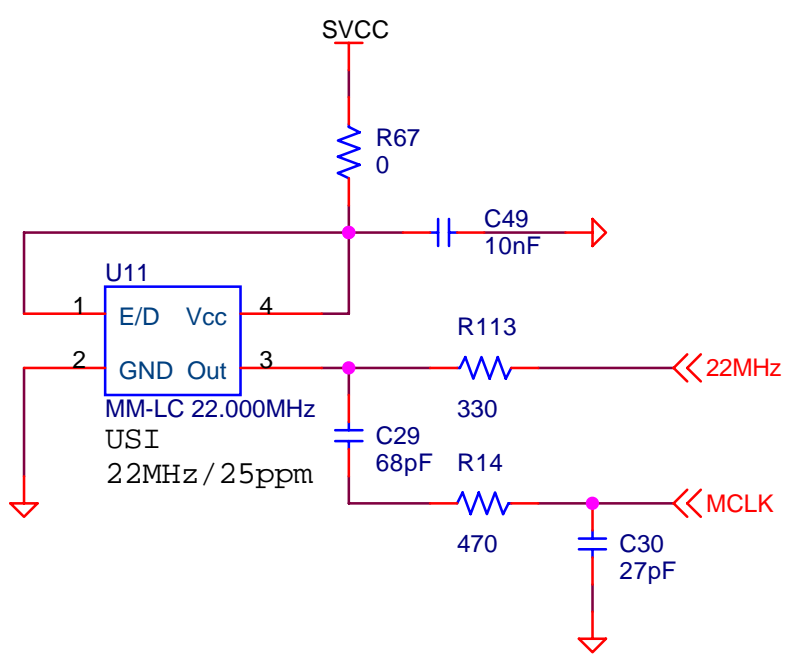
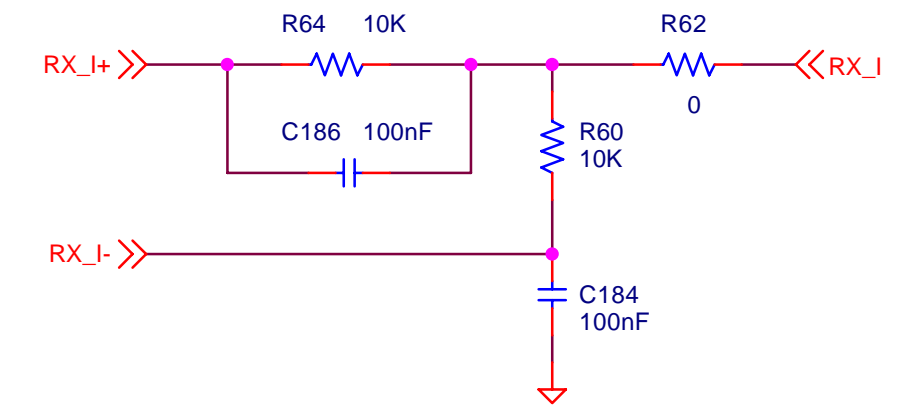
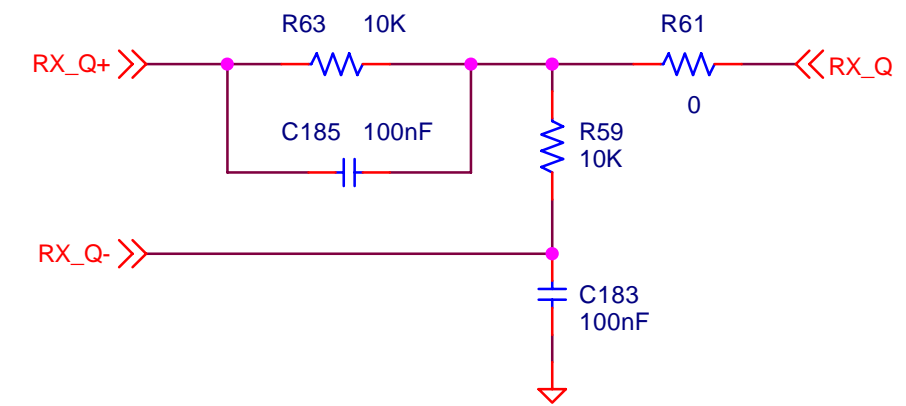
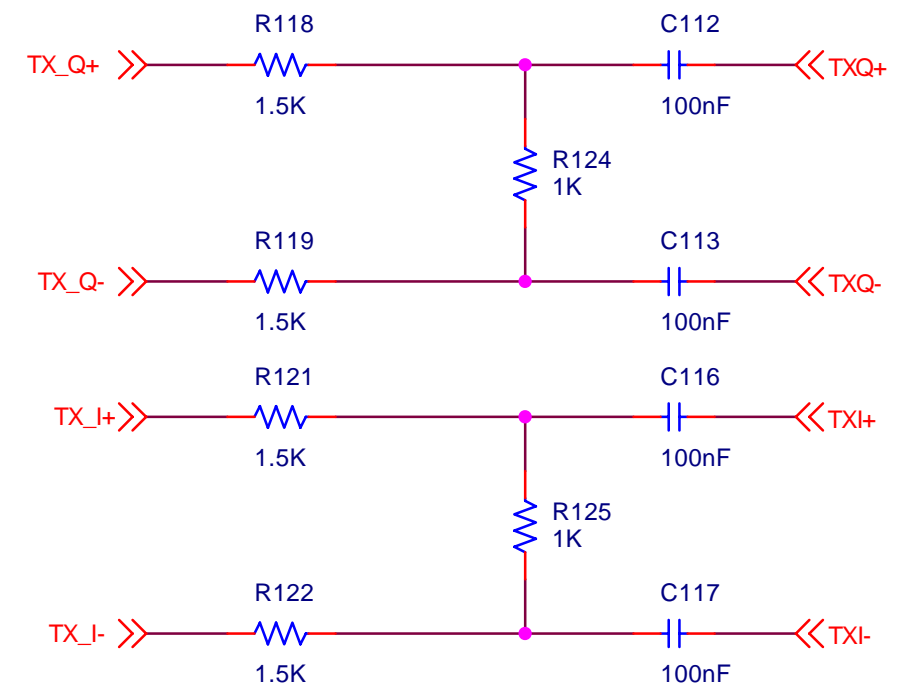
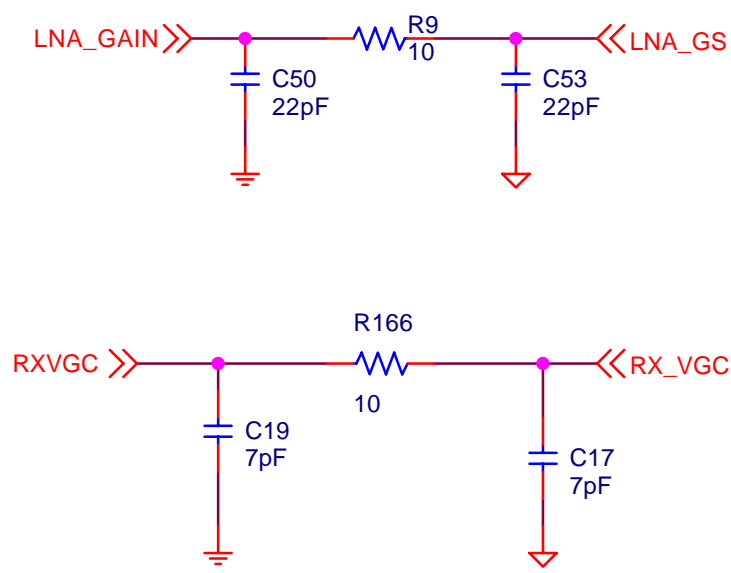
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**AT76C504A REFERENCE DESIGN**


Engineer: ATMEL HELLAS SA

Date: Friday, May 14, 2004 Sheet 1 of 4

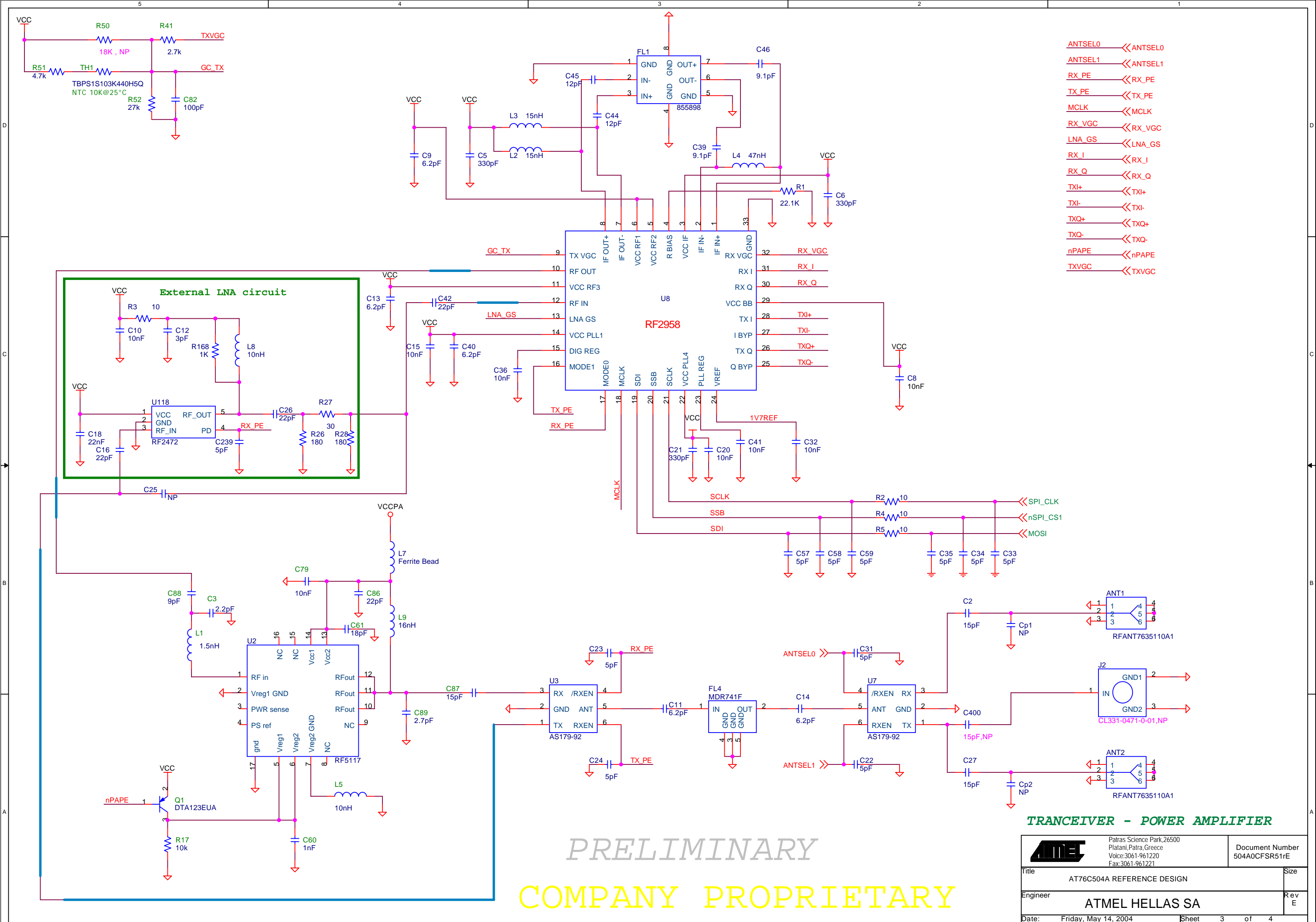
# I and Q signals Interface



## INTERFACE STAGE

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Engineer ATMEL HELLAS SA		Size Rev E
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PRELIMINARY  
 COMPANY PROPRIETARY



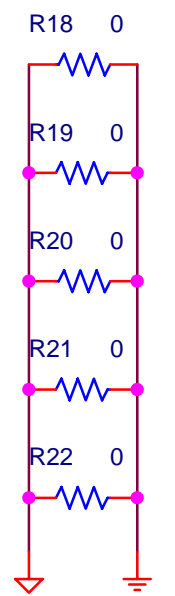
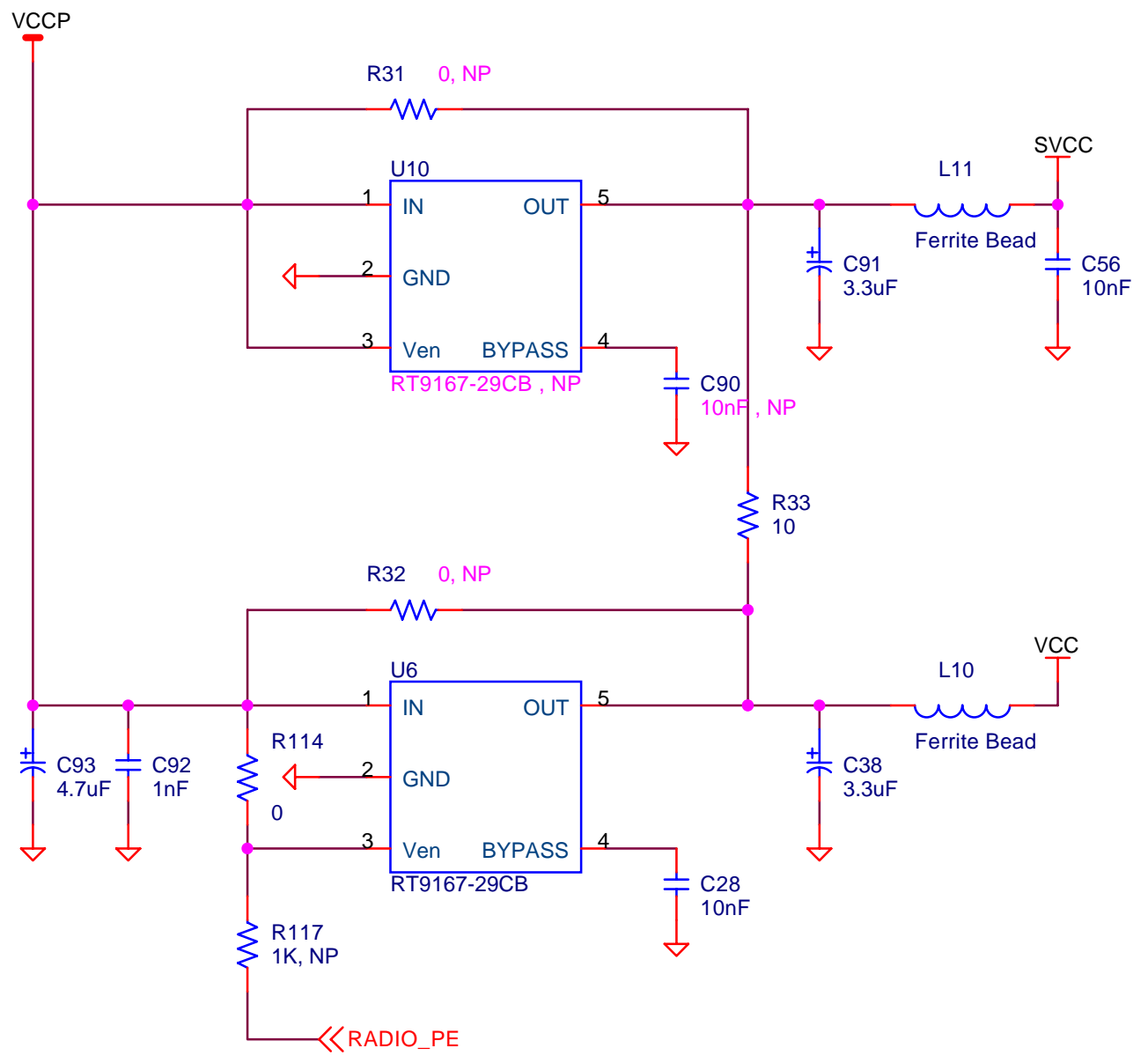
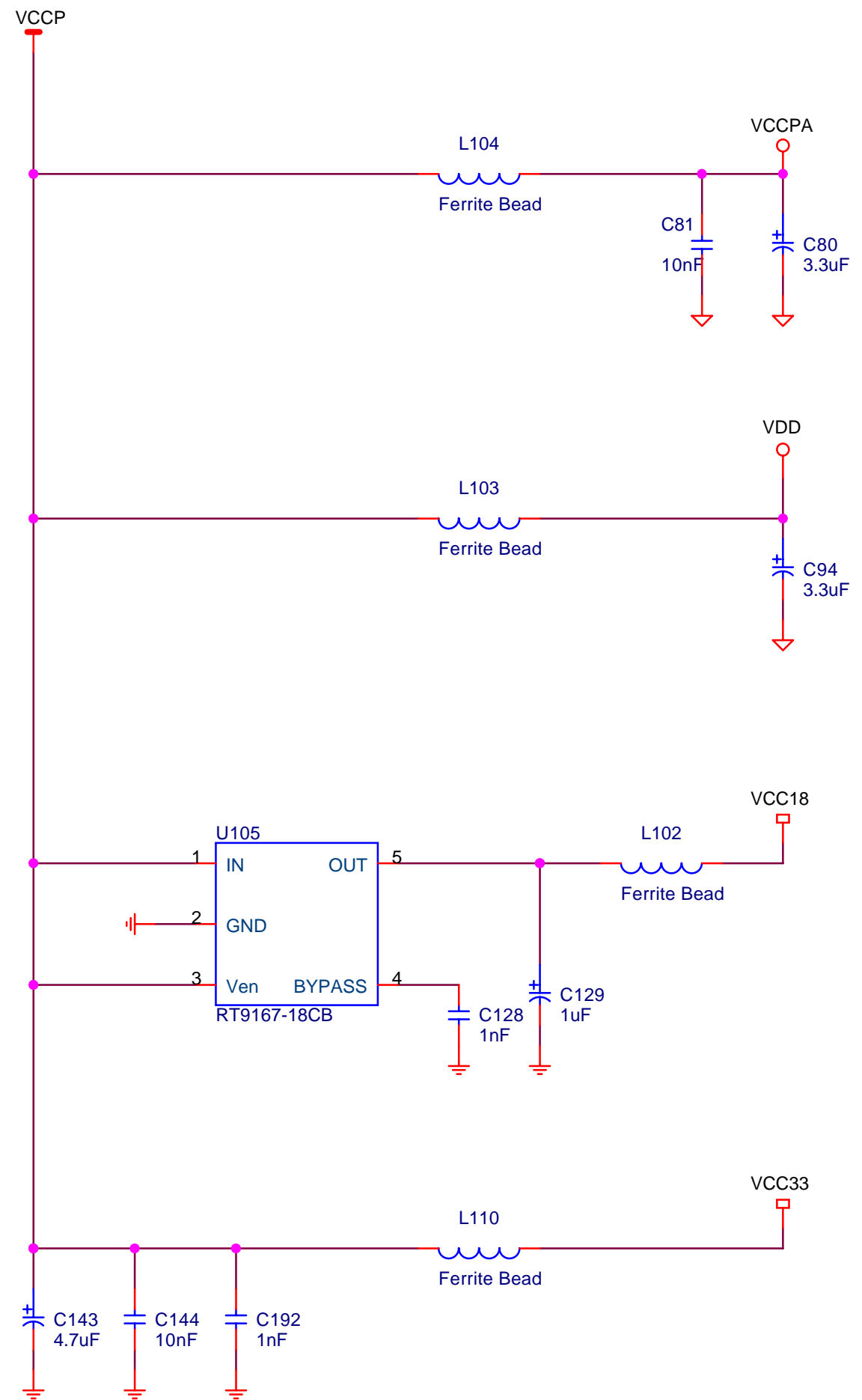
- ANTSEL0 << ANTSEL0
- ANTSEL1 << ANTSEL1
- RX\_PE << RX\_PE
- TX\_PE << TX\_PE
- MCLK << MCLK
- RX\_VGC << RX\_VGC
- LNA\_GS << LNA\_GS
- RX\_I << RX\_I
- RX\_Q << RX\_Q
- TXI+ << TXI+
- TXI- << TXI-
- TXQ+ << TXQ+
- TXQ- << TXQ-
- nPAPE << nPAPE
- TXVGC << TXVGC

PRELIMINARY

COMPANY PROPRIETARY

TRANSEIVER - POWER AMPLIFIER

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## VOLTAGE REGULATORS



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 504A0CF5R51rE

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Engineer ATMEL HELLAS SA		Rev E
Date: Friday, May 14, 2004	Sheet 4	of 4