

Declaration Letter

Date: 2015/5/11

FCC ID : SK6-XDR130

To whom it may concern,

We, Xirrus, Inc., declare as below:

There are two RF transceiver sources (QCA9890 & QCA9880). They are pin to pin compatible. These two RF chipset are electrically identical. The deviation table is listed as below.

Features	QCA9880	QCA9890
WLAN CPU supports low-level setup of PHY and RF to offload the host processor for other tasks	√	√
Dynamic frequency selection (DFS) in required 5-GHz bands when used as an AP	√	√
3x3 MIMO technology improves effective throughput and range over existing 802.11a/b/g products	√	√
Supports spatial multiplexing, cyclic-delay diversity (CDD), low-density parity check (LDPC), maximal ratio combining (MRC), Space Time Block Code (STBC)	√	√
Smart antenna diversity	√	√
Single-ended RF ports with integrated matching simplify board design and layout	√	√
Integrated 1.2 V switching regulator	√	√
Data rates of up to 1.3 Gbps in 802.11ac 80 MHz channels	√	√
Data rates of up to 216.7 Mbps for 20 MHz channels and 450 Mbps for 40 MHz channels using reduced (short) guard interval in 802.11n mode	√	√
Supports 20/40 MHz at 2.4 GHz	√	√
Supports 20/40/80 MHz at 5 GHz	√	√
On-Chip One-Time Programmable (OTP) memory	√	√
Support for IEEE 802.11d, e, h, i, k, r, v time stamp, and w standards	√	√
WEP, TKIP, AES, and WAPI hardware encryption	√	√
PCI Express 1.1 interface	√	√
20 and 40 MHz channelization	√	√

5 and 10 MHz channelization supported in 4.9 GHz frequency band only	X	V
Loopback mode to assist FIPS AES certification	X	V
High Tx power accuracy at lower power level	X	V
Small packet size (96 Bytes) in AES encryption at full packet rate	X	V
8 bits spectral analysis resolution	X	V
Frame aggregation, block ACK	V	V
802.11e-compatible bursting	V	V
Extended temperature range	X	V
108-pin, 12 mm x 12 mm LPCC package	V	V

Regards,

FCC Grantee contact person information.

Applicant's company name : Xirrus, Inc.

Applicant's company address : 2101 Corporate Center DR Thousand Oaks, CA 91320

Signature :



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