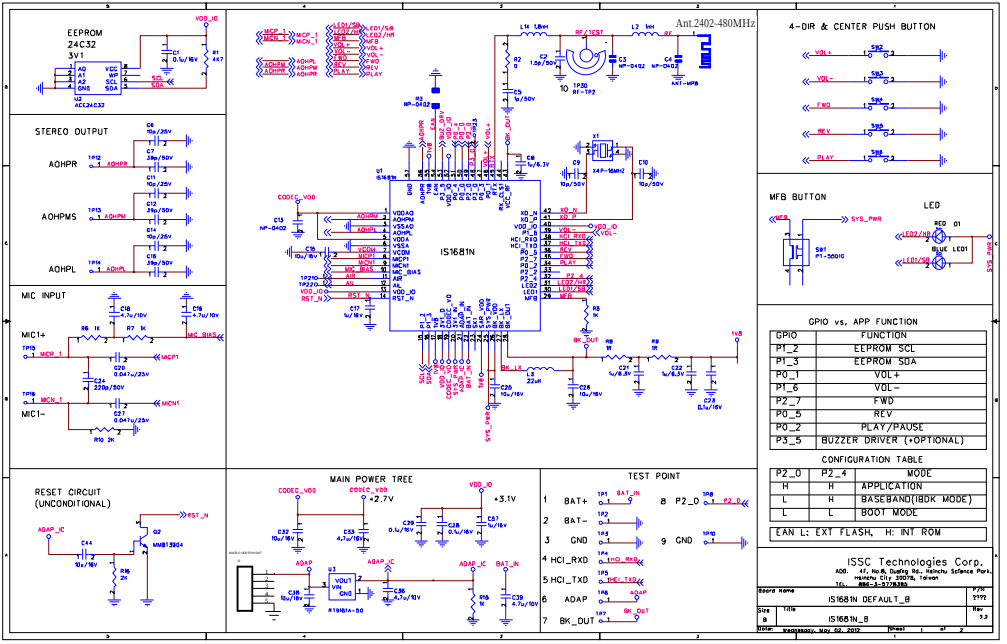
EEPROM
24C32
3V1
U2
AC24C32
STEREO OUTPUT
AOHPR
AOHPMS
AOHPL
MIC INPUT
MIC1+
MIC1-
RESET CIRCUIT
(UNCONDITIONAL)
MAIN POWER TREE
TEST POINT
1 BAT+
2 BAT-
3 GND
4 HCI_RXD
5 HCI_TXD
6 ADAP
7 BK_OUT
8 P2_0
9 GND
Ant.2402-480MHz
IS1681N
4-DIR & CENTER PUSH BUTTON
MFB BUTTON
LED
GPIO vs. APP FUNCTION
GPIO FUNCTION
P1_2 EEPROM SCL
P1_3 EEPROM SDA
P0_1 VOL+
P1_6 VOL-
P2_7 FWD
P0_5 REV
P0_2 PLAY/PAUSE
P3_5 BUZZER DRIVER (+OPTIONAL)
CONFIGURATION TABLE
P2_0 P2_4 MODE
H H APPLICATION
L H BASEBAND(IBDK MODE)
L L BOOT MODE
EAN L: EXT FLASH, H: INT ROM
ISSC Technologies Corp.
Board Name
IS1681N DEFAULT_B
P/N 2172
Size B
Title IS1681N_B
Rev 7.2
Date: Wednesday, May 02, 2012
Sheet 1 of 2