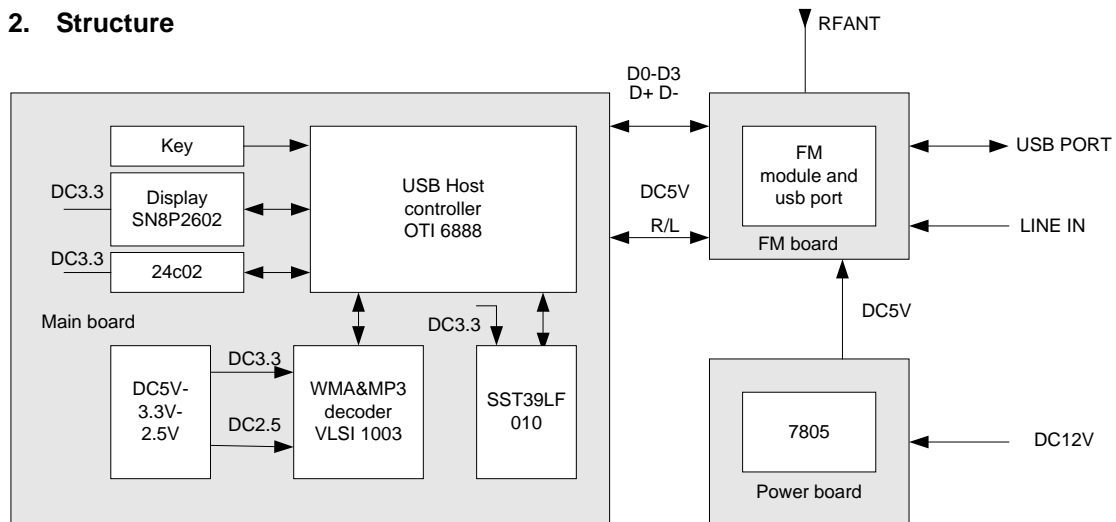


## PRODUCT illustrate

### 1. system introduce

The XF195T is a Car MP3 player that can be read the MP3 audio file in the USB flash disk and transfer the audio signal to the car FM tuner through the built-in FM transmitter. The users can be receive the MP3 music through the car FM tuner. It also has LINE-IN feature which transfers the audio source from the external audio device such as CD, MD player to the car FM tuner. Just insert the USB flash disk which stores MP3 files to the USB port or connect to an external audio device, and adjust the car FM tuner to the channel which is indicated on the car MP3 player.

### 2. Structure



#### 2.1 structure introduce

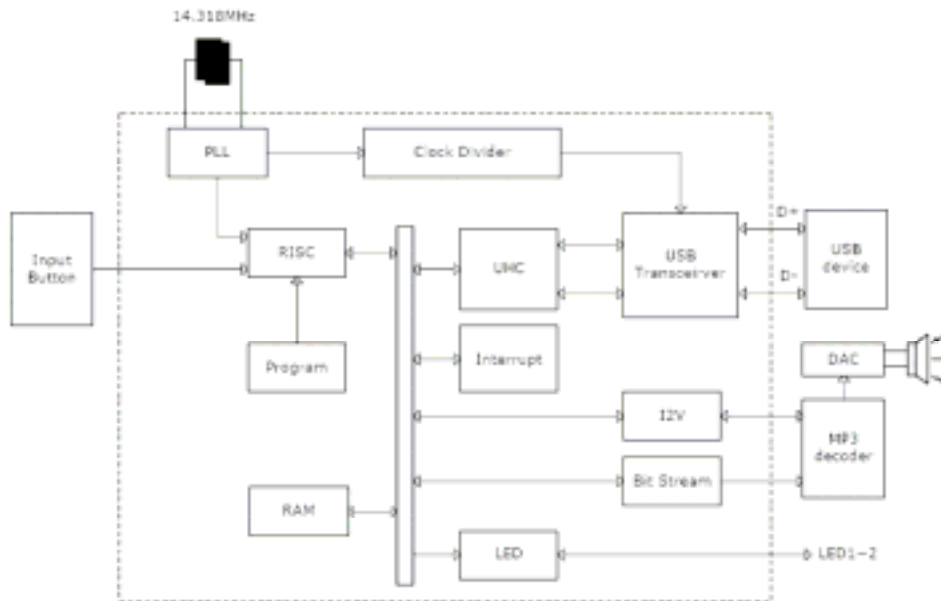
The system is consist of Main board and FM module and Power board,

#### 2.2 Main board

The Main board include USB controller and decoder and Frequency display and key input, OTI6888 is a USB HOST controller ,it can be read the MP3 audio file in the USB flash disk, the MP3&WMA audio data is distill by 6888 and sent to decoder VLSI 1003 for audio comeback. the output audio signal is stereo. Four function key ,one is volume+, one is volume-, one is frequency change key, one is play/stop key. the change frequency value can be real time display by one 4-digital segment LED display. Users set parameter of volume and channel number and last song can be save and recovery in power on.

The USB interface is full speed, it conforms to USB specification Version 1.1 .

BLOCK DIAGARM OF OTI6888



VLSI 1003 is a single-chip solution for an MPEG layer 1,2 and 3 audio decoder. It receives input bitstream through a serial input bus from the OTI-6888, which it works in slave mode. The input stream is decoded and controlled to an 18-bit oversample, multi-bit, sigma-delta DAC. The decoding is controlled via a serial control bus; after decoded, the stereo audio signal is amplified and sent to the FM circuit.

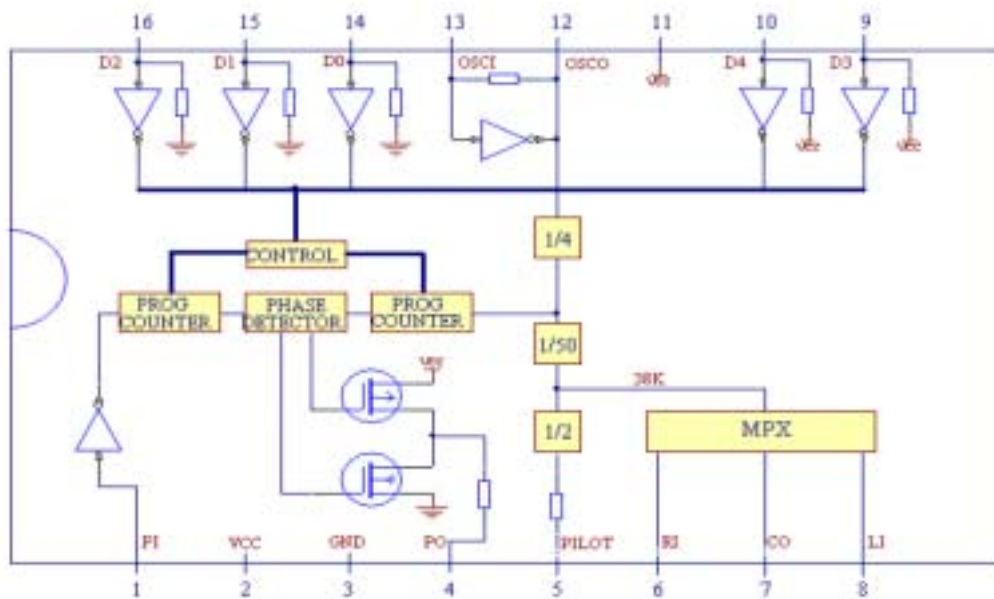
### 2.3 FM board

The FM board includes a USB port, an earphone jack, and an FM transmitter.

The HY1417 is a low-power stereo FM transmitter. It uses CMOS technology with a supply voltage from 2.8V to 5.5V and a very low standard current of 2.5mA. This chip consists of a stereo modulator for broadcasting an FM signal on the air. The PLL (phase-locked loop) circuit, a built-in 38KHz oscillator circuit, and a programmable control circuit are included.

Stereo audio signal from VLSI-1003 and the 38KHz signal will be mixed and processed. Then this stereo modulated signal will be added to the outside oscillator (L2, DF1, DF2, CX, Q1 and assembly circuit). The voltage/frequency control pulse signal from pin 5 will be added to it too. After amplified FM radio signal is sent to space by one antenna (the antenna is internal wire connected to transmitter PCB) at last. Stereo audio signal can also be input from the audio jack which connects to the outside audio player.

Change the voltage of the programmable control port (pin 9, 14, 15, 16 connected to the controller OTI-6888), the output frequency from the antenna could be changed into 12 points. Low 5 points are 88.1MHz, 88.3MHz, 88.5MHz, 88.7MHz, 88.9MHz. High 7 points are 106.7MHz, 106.9MHz, 107.1MHz, 107.3MHz, 107.5MHz, 107.7MHz, 107.9MHz.



## 2.4 Power

the main supply power circuit made of UTC7805 which conform voltage from DC 12V to DC 5V. the DC 3.3V voltage by XC62FP3302PR, the DC 2.5V voltage can be get by serial one diode through DC 3.3V supply power. these supply all voltage for system work need.

