

Appendix for 2A_N41A

Product Name: 5G Indoor CPE

Model No: TITAN 5000

Appendix A: Average Power Output Data

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	23.55	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Inner_Full	24.55	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	23.55	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	23.59	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	22.55	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Inner_Full	23.55	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.41	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	22.58	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	24.10	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Inner_Full	25.08	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	24.00	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	24.07	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	23.08	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Inner_Full	24.10	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	22.96	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	23.18	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	24.00	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Inner_Full	24.99	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	24.18	PC2	PASS
DC_2A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	23.79	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	23.05	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Inner_Full	24.04	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.99	PC2	PASS
DC_2A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.75	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	23.43	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Inner_Full	24.34	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	23.28	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	23.10	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	22.34	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Inner_Full	23.40	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	22.06	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	21.95	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	24.19	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Inner_Full	25.11	PC2	PASS

DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	24.04	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	24.49	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	23.20	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Inner_Full	24.18	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	23.04	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	23.37	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	24.09	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Inner_Full	25.08	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	24.13	PC2	PASS
DC_2A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	23.87	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	23.08	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Inner_Full	24.06	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	22.90	PC2	PASS
DC_2A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	22.57	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	23.33	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Inner_Full	24.43	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	23.34	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	23.32	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	22.34	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Inner_Full	23.32	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.14	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.05	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	24.18	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Inner_Full	25.11	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	23.97	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	24.56	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	23.16	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Inner_Full	24.10	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	22.74	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	23.18	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	24.07	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Inner_Full	25.14	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	24.24	PC2	PASS
DC_2A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	23.90	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	23.13	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Inner_Full	24.12	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	22.85	PC2	PASS
DC_2A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	22.60	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Outer_Full	23.52	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Inner_Full	24.46	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	23.53	PC2	PASS

DC_2A_n41A	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	23.77	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Outer_Full	22.60	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Inner_Full	23.48	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	22.15	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	22.40	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Outer_Full	23.82	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Inner_Full	25.05	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	23.52	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	23.94	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Outer_Full	22.84	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Inner_Full	23.95	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	22.34	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	22.80	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Outer_Full	23.78	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Inner_Full	24.98	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	23.82	PC2	PASS
DC_2A_n41A	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	23.38	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Outer_Full	22.88	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Inner_Full	23.90	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	22.73	PC2	PASS
DC_2A_n41A	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	22.58	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	23.17	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Inner_Full	24.35	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	22.91	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	23.10	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	22.27	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Inner_Full	23.29	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	21.39	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	21.75	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	23.82	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Inner_Full	24.88	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	23.14	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	23.65	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	22.80	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Inner_Full	23.98	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	22.01	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	22.48	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	23.74	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Inner_Full	24.87	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	23.40	PC2	PASS
DC_2A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	23.12	PC2	PASS

DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	22.71	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Inner_Full	23.87	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.23	PC2	PASS
DC_2A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	21.83	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Outer_Full	23.39	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Inner_Full	24.31	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Left	23.24	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+L	Edge_1RB_Right	23.52	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Outer_Full	22.41	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Inner_Full	23.29	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Left	22.10	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+L	Edge_1RB_Right	22.54	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Outer_Full	23.91	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Inner_Full	24.87	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Left	23.35	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+M	Edge_1RB_Right	23.96	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Outer_Full	22.88	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Inner_Full	23.88	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Left	22.24	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+M	Edge_1RB_Right	22.90	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Outer_Full	23.99	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Inner_Full	24.89	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Left	23.80	PC2	PASS
DC_2A_n41A	30	5+70	DFT-QPSK	M+H	Edge_1RB_Right	23.45	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Outer_Full	22.97	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Inner_Full	23.92	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Left	22.68	PC2	PASS
DC_2A_n41A	30	5+70	DFT-16QAM	M+H	Edge_1RB_Right	22.40	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	23.46	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Inner_Full	24.43	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	23.29	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	23.88	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	22.47	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Inner_Full	23.40	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.04	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	22.64	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	23.83	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Inner_Full	24.95	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	23.23	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	24.09	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	22.95	PC2	PASS

DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Inner_Full	23.89	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	22.27	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	23.07	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	24.00	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Inner_Full	25.05	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	23.76	PC2	PASS
DC_2A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	23.57	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	23.00	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Inner_Full	24.02	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	22.55	PC2	PASS
DC_2A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	22.30	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Outer_Full	23.59	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Inner_Full	24.44	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	23.27	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	23.91	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Outer_Full	22.66	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Inner_Full	23.45	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.09	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	22.89	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Outer_Full	23.87	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Inner_Full	24.96	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	23.24	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	24.02	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Outer_Full	22.94	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Inner_Full	23.92	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	22.18	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	23.14	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Outer_Full	24.01	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Inner_Full	25.06	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	23.76	PC2	PASS
DC_2A_n41A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	23.58	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Outer_Full	23.02	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Inner_Full	24.06	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.68	PC2	PASS
DC_2A_n41A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	22.35	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	23.66	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Inner_Full	24.45	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	23.19	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	23.84	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Outer_Full	22.56	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Inner_Full	23.53	PC2	PASS

DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	21.86	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.62	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	23.82	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Inner_Full	24.95	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	23.03	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	24.05	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Outer_Full	22.93	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Inner_Full	23.96	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	22.03	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	22.99	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	23.99	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Inner_Full	24.99	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	23.57	PC2	PASS
DC_2A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	23.31	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Outer_Full	23.08	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Inner_Full	24.05	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	22.29	PC2	PASS
DC_2A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.16	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_2A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	4.90	≤13	PASS
DC_2A_n4 1A	30	5+20	DFT-16QAM	M+L	Outer_Full	5.84	≤13	PASS
DC_2A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	5.08	≤13	PASS
DC_2A_n4 1A	30	5+20	DFT-16QAM	M+M	Outer_Full	6.00	≤13	PASS
DC_2A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	4.94	≤13	PASS
DC_2A_n4 1A	30	5+20	DFT-16QAM	M+H	Outer_Full	5.86	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-QPSK	M+L	Outer_Full	4.81	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-16QAM	M+L	Outer_Full	5.73	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-QPSK	M+M	Outer_Full	4.97	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-16QAM	M+M	Outer_Full	5.94	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-QPSK	M+H	Outer_Full	4.93	≤13	PASS
DC_2A_n4 1A	30	5+30	DFT-16QAM	M+H	Outer_Full	5.81	≤13	PASS
DC_2A_n4 1A	30	5+40	DFT-QPSK	M+L	Outer_Full	5.00	≤13	PASS
DC_2A_n4 1A	30	5+40	DFT-16QAM	M+L	Outer_Full	5.88	≤13	PASS
DC_2A_n4 1A	30	5+40	DFT-QPSK	M+M	Outer_Full	5.27	≤13	PASS
DC_2A_n4 1A	30	5+40	DFT-16QAM	M+M	Outer_Full	6.11	≤13	PASS
DC_2A_n4 1A	30	5+40	DFT-QPSK	M+H	Outer_Full	5.18	≤13	PASS

DC_2A_n4 1A	30	5+40	DFT-16QAM	M+H	Outer_Full	5.95	≤13	PASS
DC_2A_n4 1A	30	5+50	DFT-QPSK	M+L	Outer_Full	5.58	≤13	PASS
DC_2A_n4 1A	30	5+50	DFT-16QAM	M+L	Outer_Full	6.09	≤13	PASS
DC_2A_n4 1A	30	5+50	DFT-QPSK	M+M	Outer_Full	5.07	≤13	PASS
DC_2A_n4 1A	30	5+50	DFT-16QAM	M+M	Outer_Full	6.15	≤13	PASS
DC_2A_n4 1A	30	5+50	DFT-QPSK	M+H	Outer_Full	4.77	≤13	PASS
DC_2A_n4 1A	30	5+50	DFT-16QAM	M+H	Outer_Full	6.01	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-QPSK	M+L	Outer_Full	5.17	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-16QAM	M+L	Outer_Full	5.98	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-QPSK	M+M	Outer_Full	5.33	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-16QAM	M+M	Outer_Full	6.19	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-QPSK	M+H	Outer_Full	5.19	≤13	PASS
DC_2A_n4 1A	30	5+60	DFT-16QAM	M+H	Outer_Full	6.04	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-QPSK	M+L	Outer_Full	5.23	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-16QAM	M+L	Outer_Full	6.00	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-QPSK	M+M	Outer_Full	5.36	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-16QAM	M+M	Outer_Full	6.14	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-QPSK	M+H	Outer_Full	4.97	≤13	PASS
DC_2A_n4 1A	30	5+70	DFT-16QAM	M+H	Outer_Full	5.81	≤13	PASS
DC_2A_n4 1A	30	5+80	DFT-QPSK	M+L	Outer_Full	5.39	≤13	PASS
DC_2A_n4 1A	30	5+80	DFT-16QAM	M+L	Outer_Full	6.11	≤13	PASS

DC_2A_n4 1A	30	5+80	DFT-QPSK	M+M	Outer_Full	5.42	≤13	PASS
DC_2A_n4 1A	30	5+80	DFT-16QAM	M+M	Outer_Full	6.20	≤13	PASS
DC_2A_n4 1A	30	5+80	DFT-QPSK	M+H	Outer_Full	5.05	≤13	PASS
DC_2A_n4 1A	30	5+80	DFT-16QAM	M+H	Outer_Full	5.88	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-QPSK	M+L	Outer_Full	5.31	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-16QAM	M+L	Outer_Full	6.14	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-QPSK	M+M	Outer_Full	5.37	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-16QAM	M+M	Outer_Full	6.23	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-QPSK	M+H	Outer_Full	5.04	≤13	PASS
DC_2A_n4 1A	30	5+90	DFT-16QAM	M+H	Outer_Full	5.89	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-QPSK	M+L	Outer_Full	5.09	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-16QAM	M+L	Outer_Full	5.98	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-QPSK	M+M	Outer_Full	5.26	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-16QAM	M+M	Outer_Full	6.12	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-QPSK	M+H	Outer_Full	5.55	≤13	PASS
DC_2A_n4 1A	30	5+100	DFT-16QAM	M+H	Outer_Full	6.15	≤13	PASS

Test Graphs























