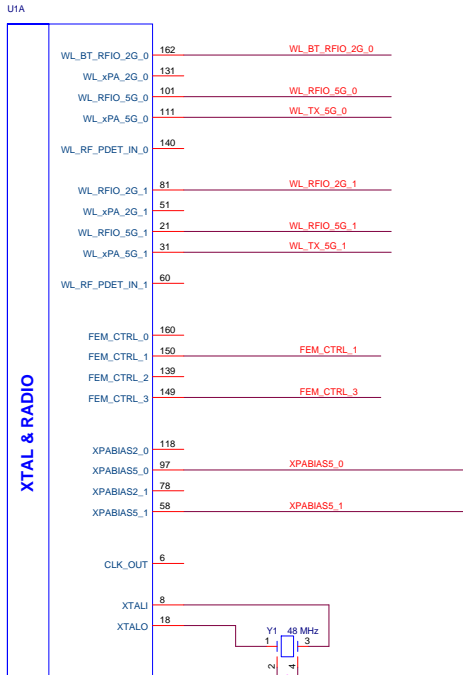


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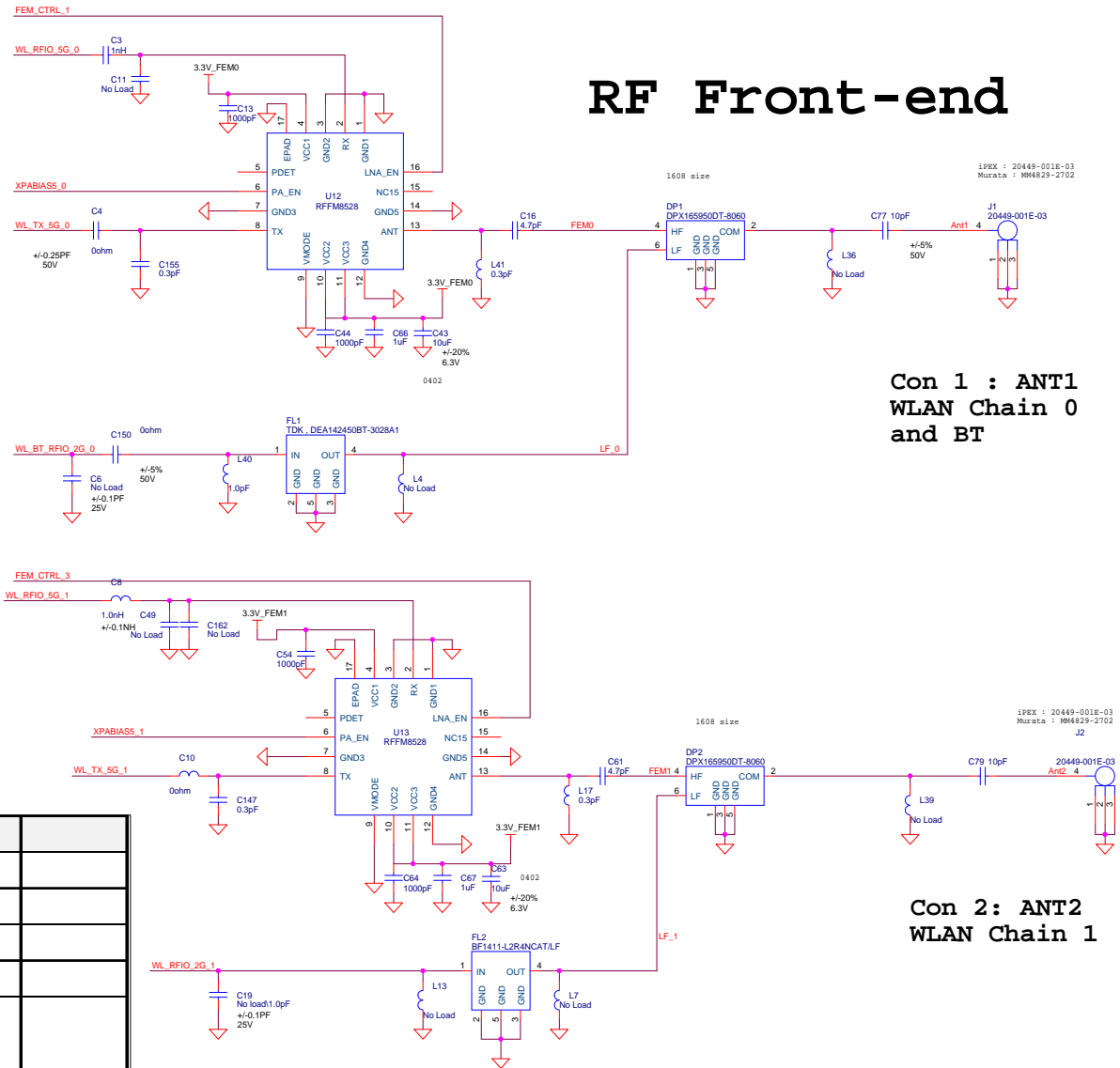
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QCA6174A-5

Operation Mode	XPABIASS_0	XPABIASS_1	FEM_CTR_1	FEM_CTR_3
5G_TX_0	H	L	L	L
5G_RX_0	L	L	H	L
5G_TX_1	L	H	L	L
5G_RX_1	L	L	L	H
2G_TXXR or BT_TXXR or 5G_LNA_Bypass or SLEEP or IDLE	L	L	L	L
5G_TX_0_Low_Power	H	L	L	L
5G_TX_1_Low_Power	L	H	L	L

RF Front-end



Con 1 : ANT1
 WLAN Chain 0
 and BT

Con 2: ANT2
 WLAN Chain 1

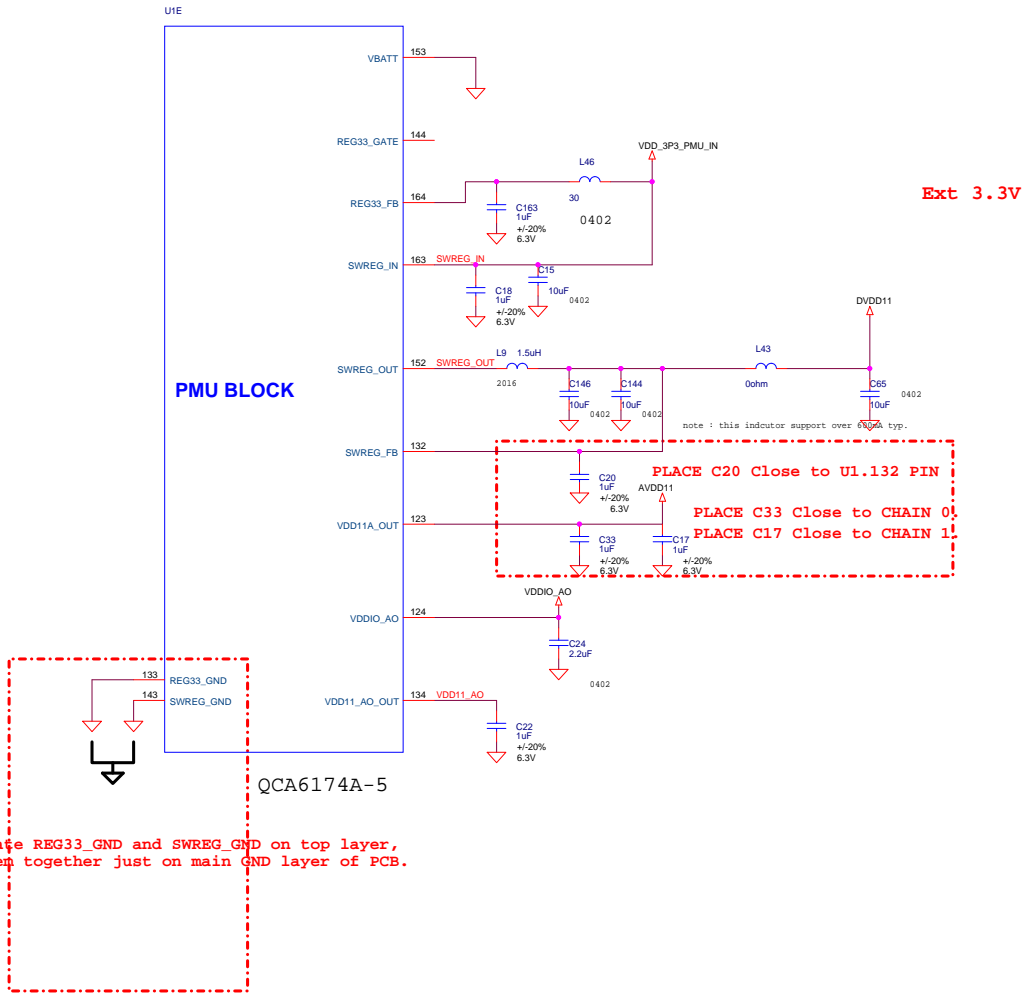
線路圖/T77H643.02/00/3 OF 7

TITLE		
CCA, NFA364AXP-5, 020, QCA6174A-5, 802.11AC/A/B/G/N, 2X2 WIFI_PCIE_BTUSB M.2_2230		
SIZE	DRAWING NO	REV
C	LD20-Y8878	B
SCALE	NONE	SHEET 3 OF 7

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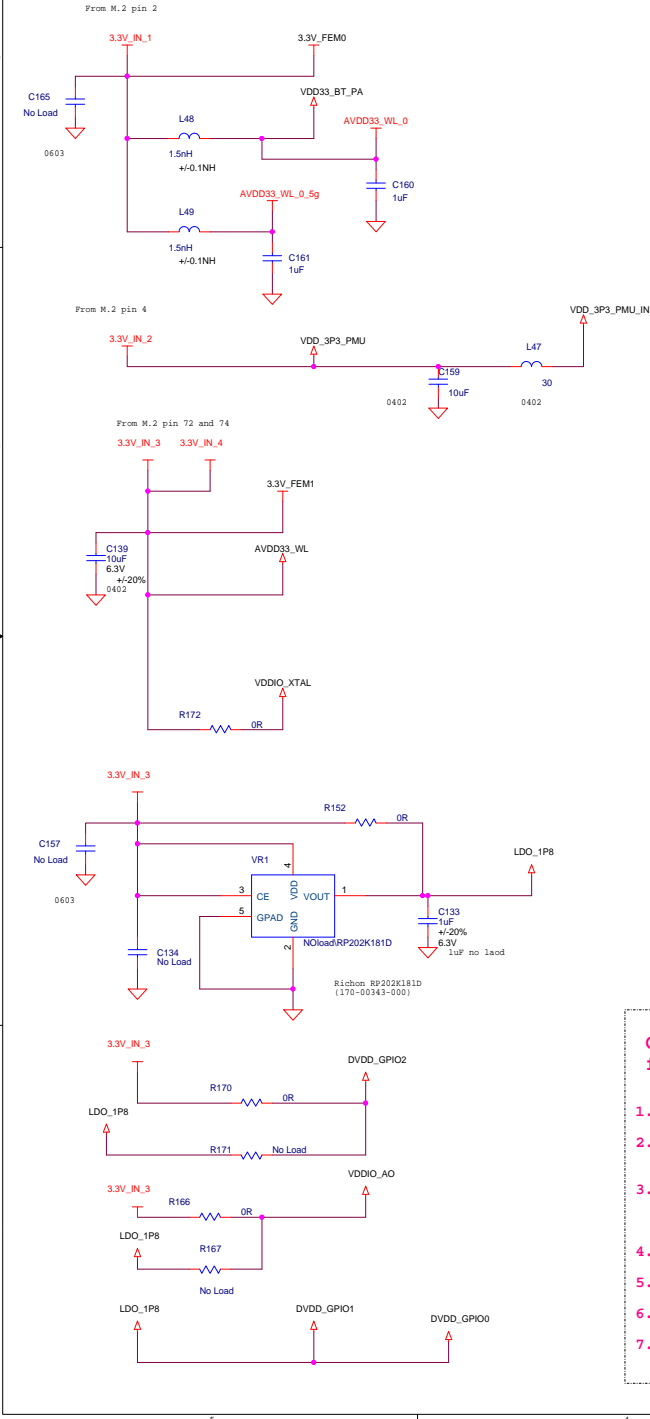
線路圖/T77H643.02/00/4 OF 7

TITLE			
CCA, NFA364XP-5, 020, QCA6174A-5, 802.11AC/AB/G/N, 2X2 WIFI_PCIE_BTUSB M.2_2320			
SIZE	DRAWING NO	REV	
C	LD20-Y8878	B	
SCALE	NONE	SHEET	4 OF 7

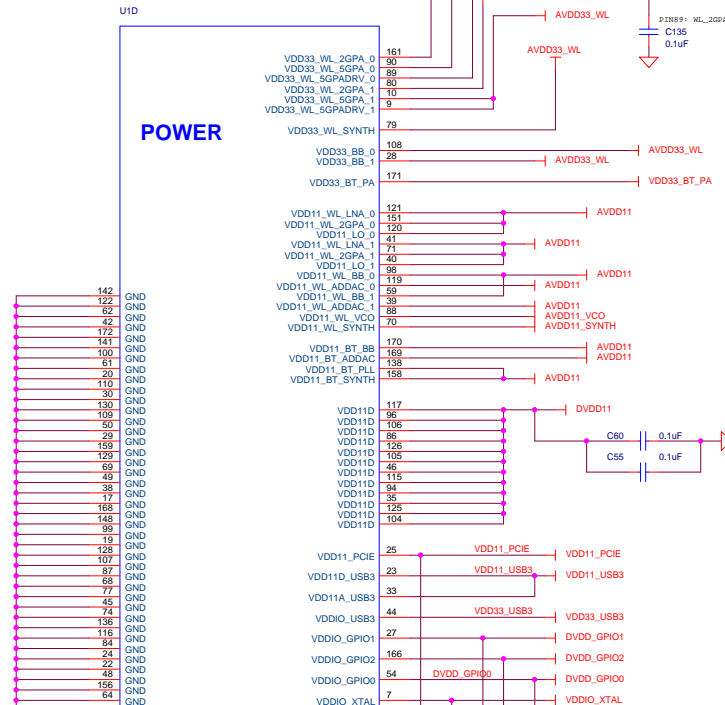
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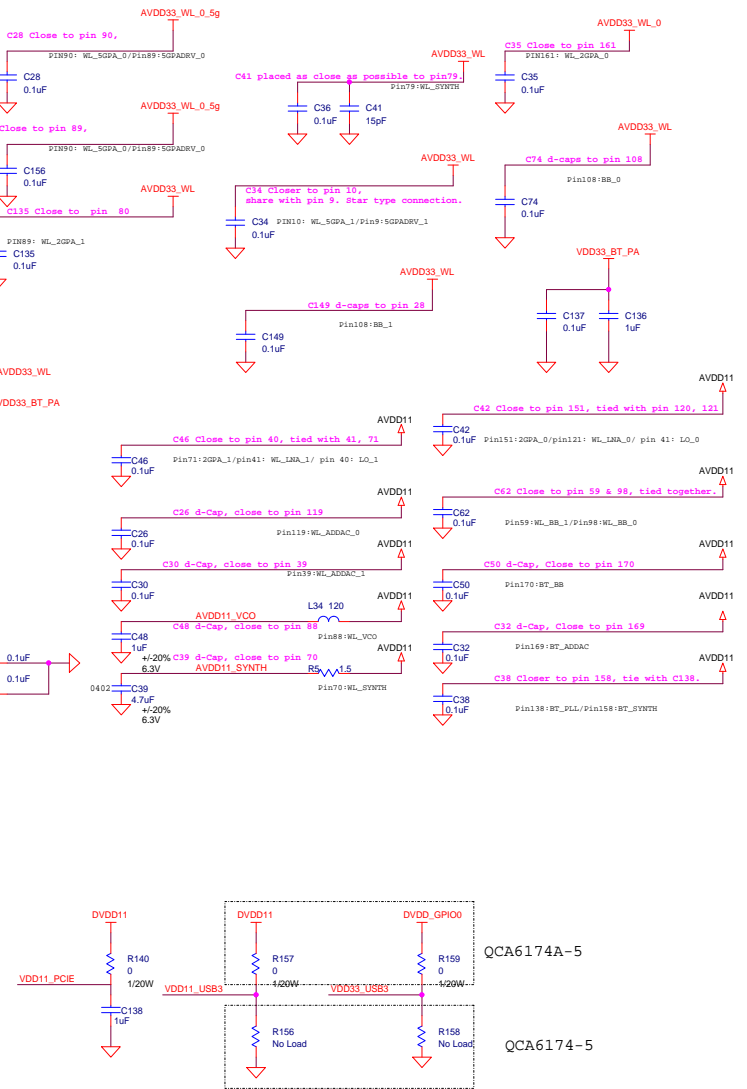
Bypass CAPS Need to place as close as possible to QCA6174 chip



線路圖/T77H643.02/00/5 OF 7

General Remarks about D-CAPS Placement Rule for QCA6174(A) IC:

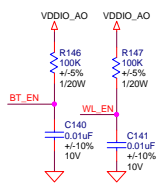
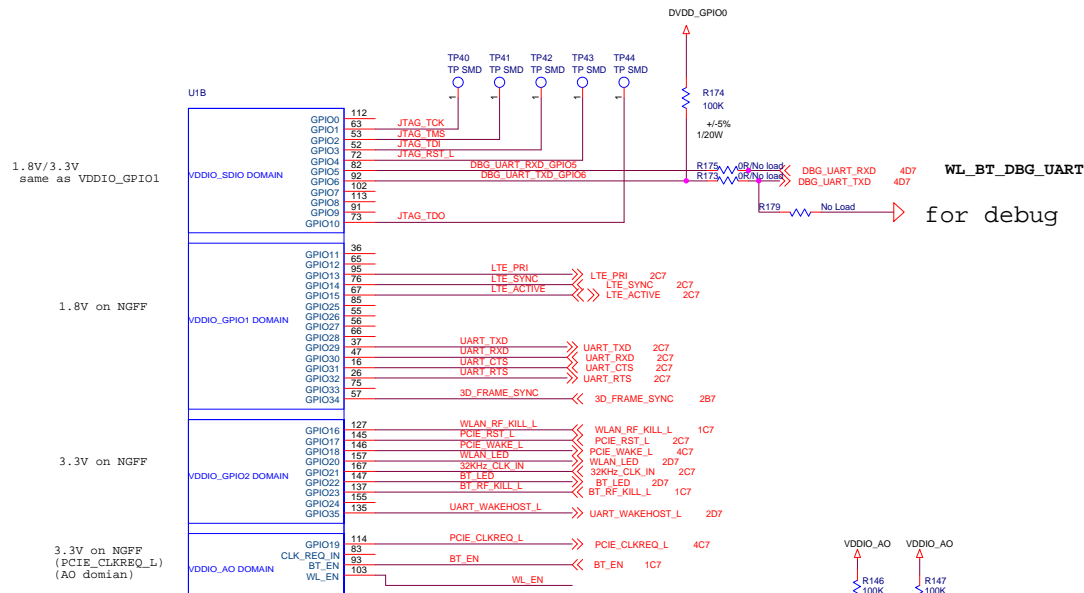
1. Bypass cap Values can be changing over the bringup period.
2. Bypass Caps that are not close to the IC will not have a significant contribution to lowering the noise level
3. If caps are fairly far, the supply trace should be as wide as possible if vias are needed, have at least 2 vias for every DC connection.
4. All digital should have their own traces and not mixed with Analog Power
5. RF and BB and LO can be tied together
6. Synth must have its own supply
7. ADC/DAC circuits must have their own supply



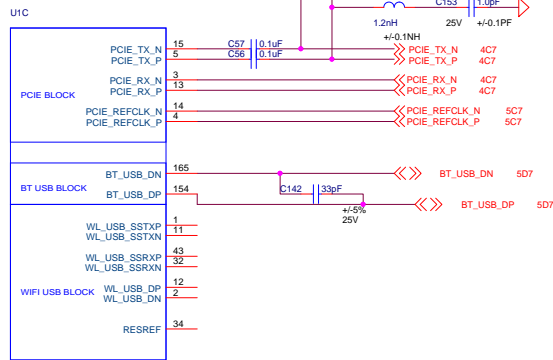
Host Interface Selection			
Host Interface	VDD11_PCIE	VDD11_USB3	VDD33_USB3
WLAN PCIE (QCA6174-5)	1.1V	0.0V	0.0V
WLAN PCIE (QCA6174A-5)	1.1V	1.1V (DVDD11)	GPI00

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QCA6174A-5



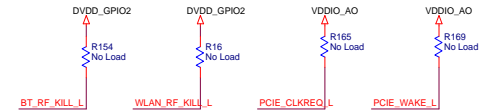
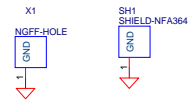
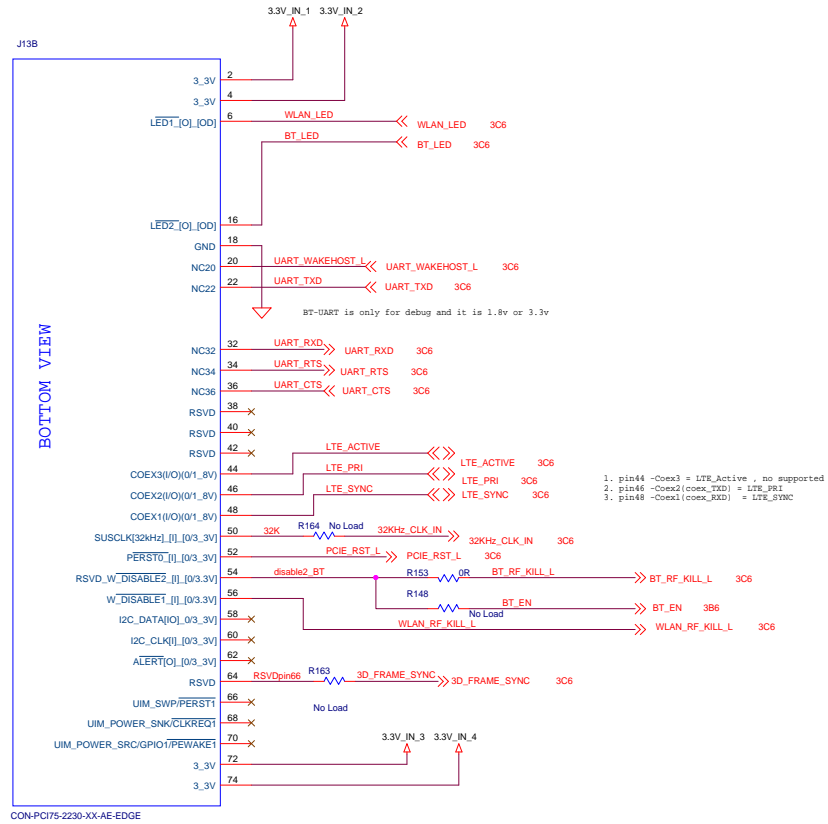
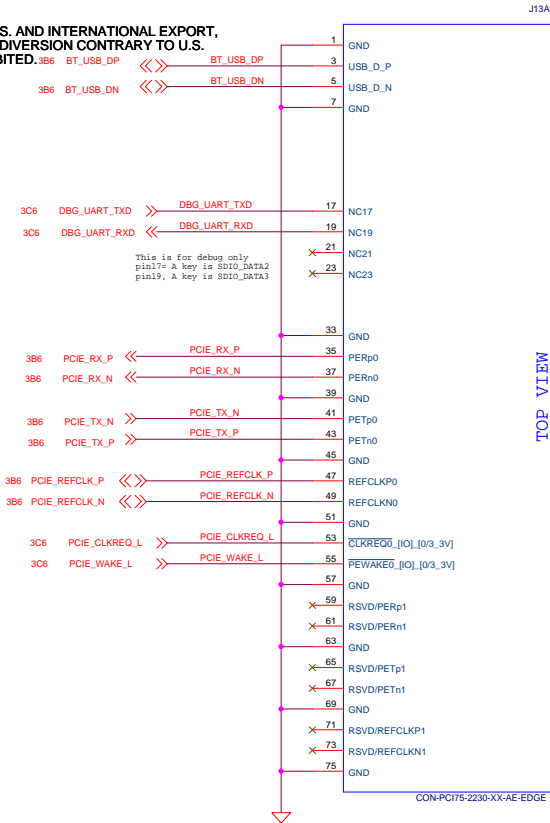
QCA6174A-5

線路圖/T77H643.02/00/6 OF 7

TITLE		
CCA, NFA364AXP-5, 020, QCA6174A-5, 802.11AC/A/B/G/N, 2X2 WIFI_PCIE_BTUSB M.2_2230		
SIZE C	DRAWING NO LD20-Y8878	REV B
SCALE NONE	SHEET 6	OF 7

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線路圖/T77H643.02/00/7 OF 7

TITLE CCA, NFA364AXP-5, 020, QCA6174A-5, 802.11AC/A/B/G/N, 2X2 WIFI_PCIE_BTUSB M.2_2230			
SIZE C	DRAWING NO LD20-Y8878	REV B	
SCALE NONE	SHEET 7 OF 7		QUALCOMM Atheros, INC. 1700 TECHNOLOGY DRIVE SAN JOSE, CA 95110 U.S.A