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2	NOTES
3	Block Diagram
4	MAIN SCHEMATIC

Revisions			
Rev	Description	Date	Approved
X1	Initial Release	31/03/2011	Julio Muñoz
A	Proto release	21/04/2011	Julio Muñoz

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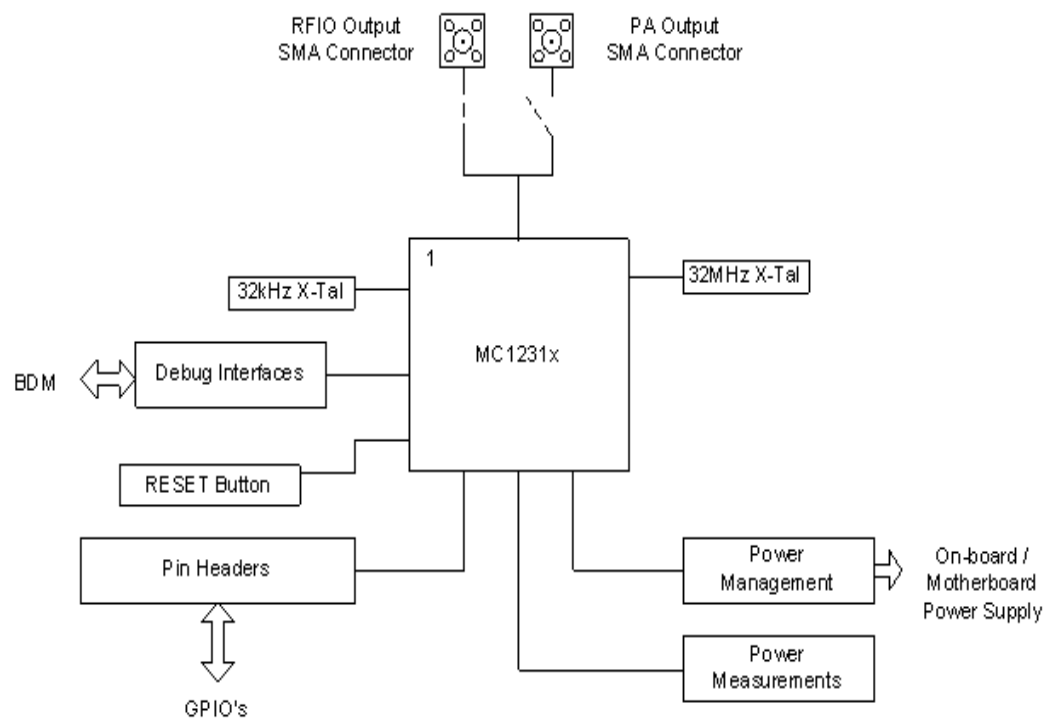
Designer: K.Mangula	Drawing Title: X-1231x-MRB(Navajo)		
Drawn by: K.Mangula	Page Title: TITLE PAGE		
Approved: J. MUNOZ	Size C	Document Number SCH-27108 PDF: SPF-27108	Rev A
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- 54321
- D
- C
- B
- A
- Unless Otherwise Specified:
 - All resistors are in ohms, 5%, 1/8 Watt
 - All capacitors are in uF, 20%, 50V
 - All voltages are DC
 - All polarized capacitors are aluminum electrolytic
 - Interrupted lines coded with the same letter or letter combinations are electrically connected.
 - Device type number is for reference only. The number varies with the manufacturer.
 - Special signal usage:
 - _B Denotes - Active-Low Signal
 - <> or [] Denotes - Vectored Signals
 - Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.



ICAP Classification:				FCP: X	FIUC:	PUB:
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Modular Reference Board



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Drawing Title: **X-1231x-MRB(Navajo)**

Page Title: **Block Diagram**

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