






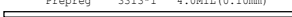
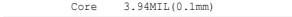

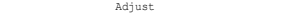

CONTENT INDEXING

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05. SYSTEM POWER
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10. YPbPr out/SPDIF out
11. HDMI out
12. RMI Ethernet
13. WIFI

PCB POWER WIRE WIDTH INDICATE

	above 80 miles
	above 50 miles
	above 30 miles
	above 16 miles
No indicate	Under needs

6 LAYERS PCB STACK

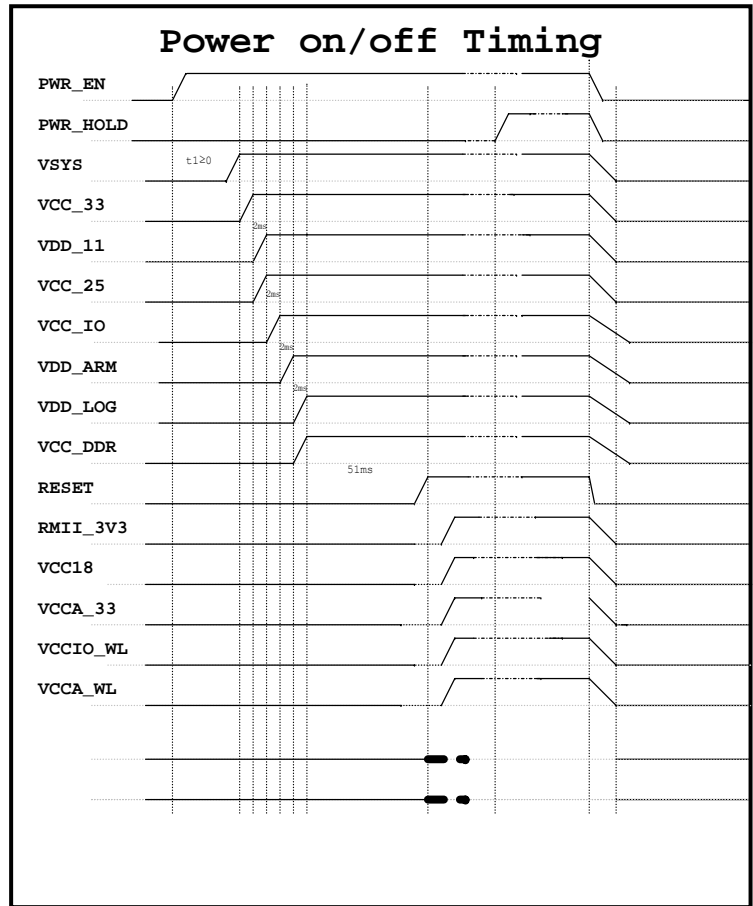
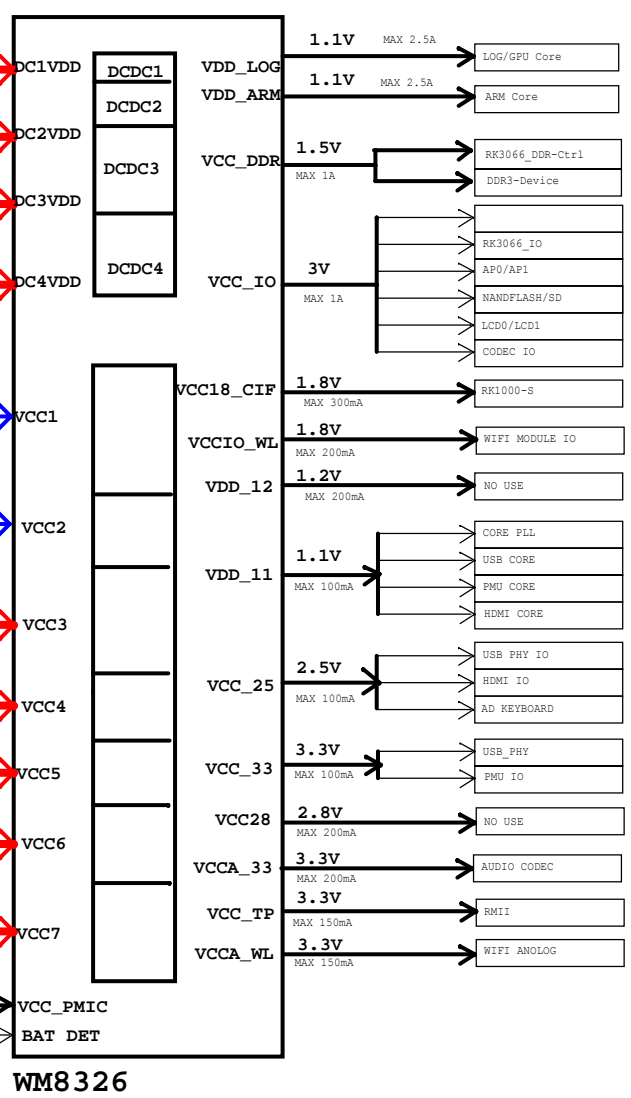
TOP		Prepreg 3313*1 4.0MIL(0.10mm)	Horz(18um) + plating copper(18um)
GND		Core 3.94MIL(0.1mm)	1oz(35um)
POWER(S1)		Adjust	1oz(35um)
S1(S2)		Core 3.94MIL(0.1mm)	1oz(35um)
GND(POWER)		Prepreg 3313*1 4.0MIL(0.10mm)	1oz(35um)
S2(BOTTOM)		Prepreg 3313*1 4.0MIL(0.10mm)	Horz(18um) + plating copper(18um)

Version	Date	Author	Change Note	Approved
V1.0	20120326		First edictor	
V1.1	20120418		1:Correct the 24MHz crystal circuit. 2:Change PLL circuit. 3:Correct the HDMI circuit.	

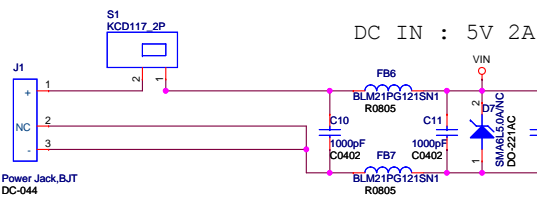
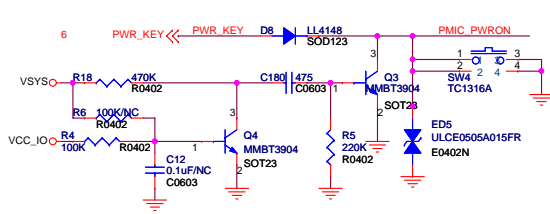
DC Adapter
12V/2A

BUCK
5V/3A
TPS62130

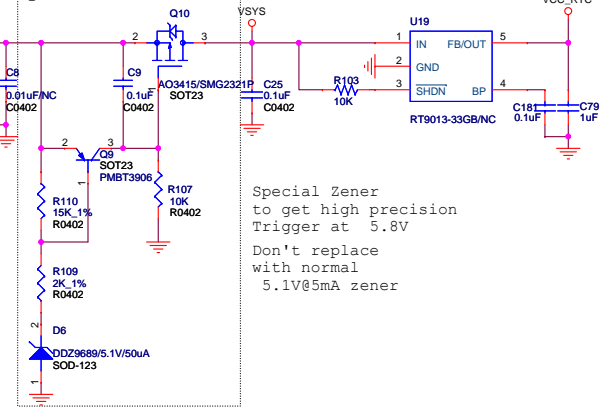
LDO



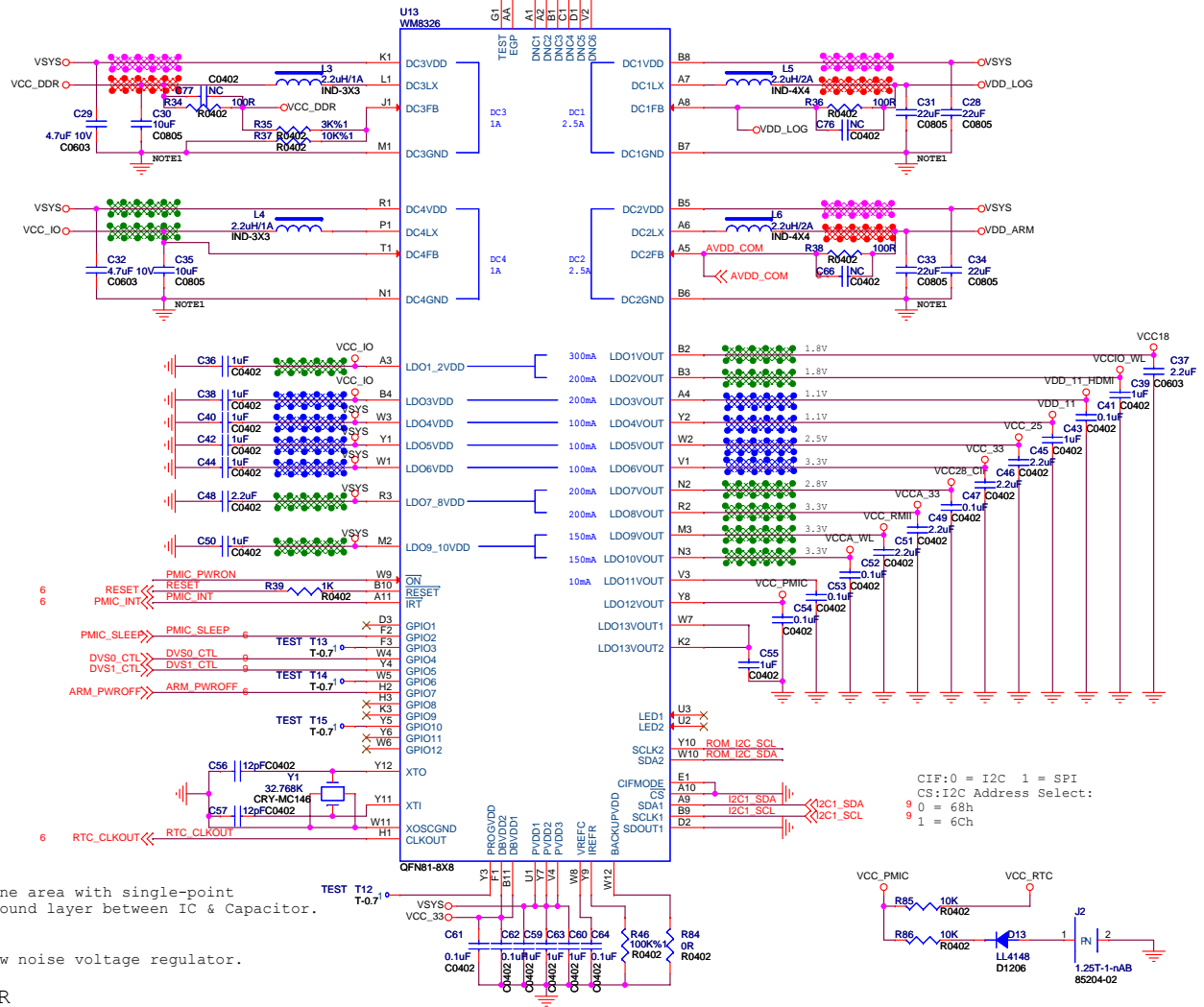
POWER DIAGRAM



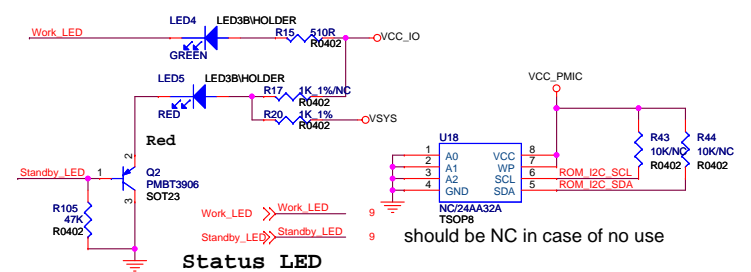
Over voltage protect Optional



Special Zener
to get high precision
Trigger at 5.8V
Don't replace
with normal
5.1V@5mA zener



Working: Green
Standby: Red



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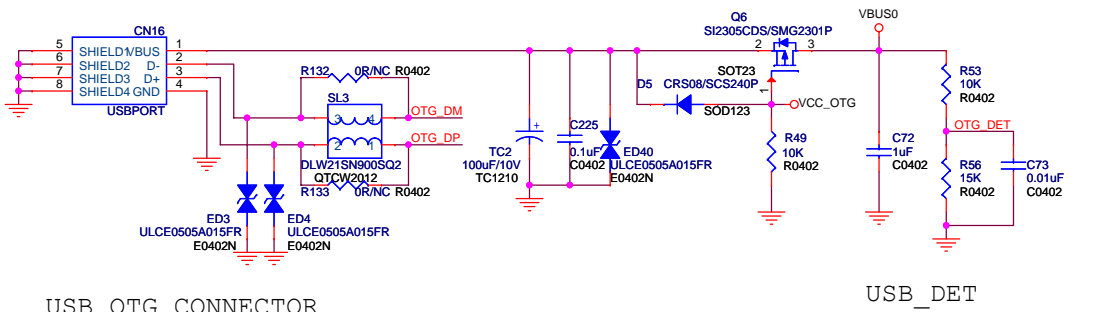
Title **TB2000**

Size Custom Document Number Rev <RevCde>

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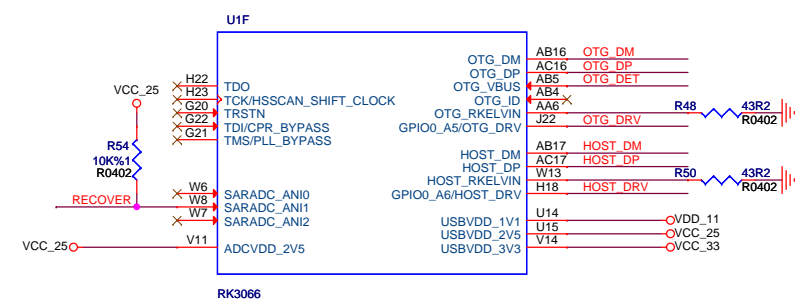
NOTE1:
Local ground plane area with single-point connection to ground layer between IC & Capacitor.
NOTE2:
LD07~LD010 is low noise voltage regulator.

SYSTEM POWER

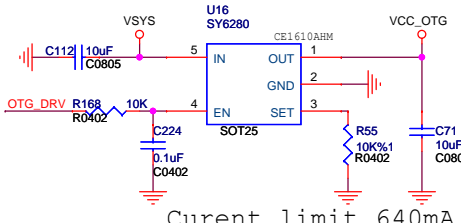


USB OTG CONNECTOR

USB_DET

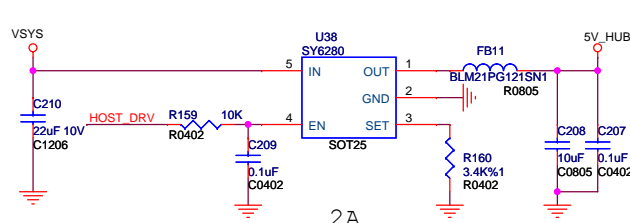


RK3066



Curent limit 640mA

OTG HOST POWER

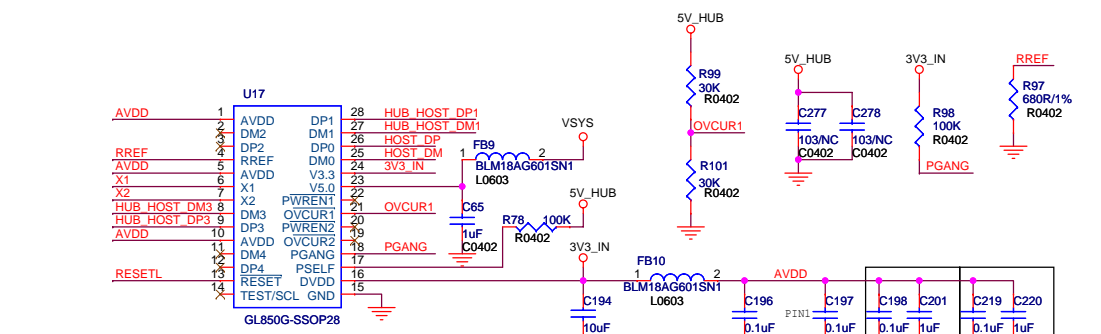


2A

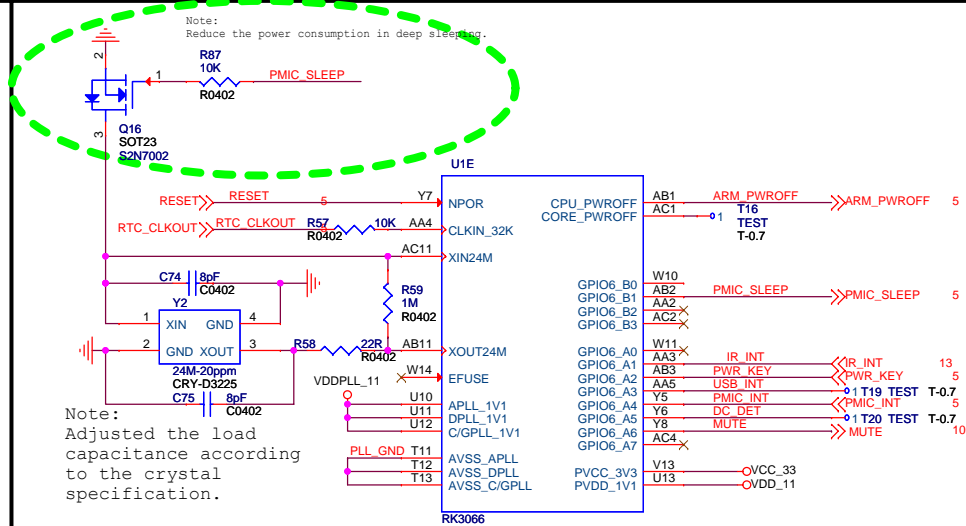
USB HOST POWER



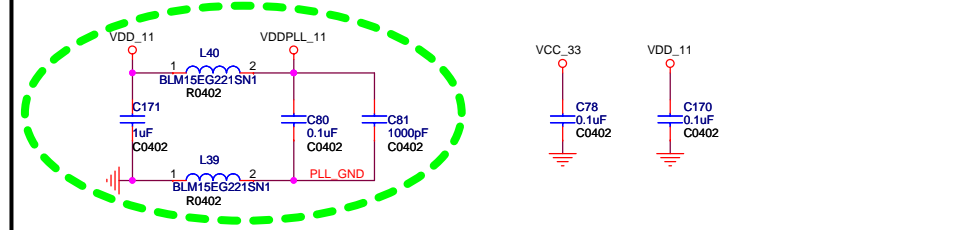
RK3066-F



13 HUB_HOST_DM3 <<< HUB_HOST_DM3
13 HUB_HOST_DP3 <<< HUB_HOST_DP3

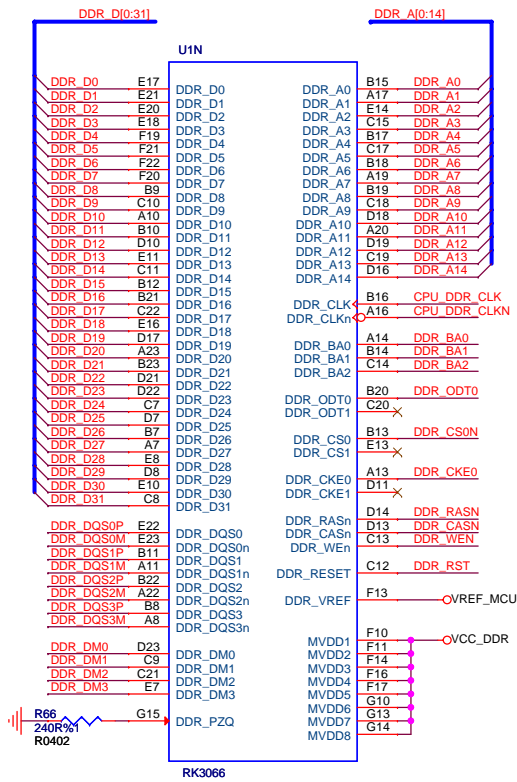


Note:
Adjusted the load capacitance according to the crystal specification.

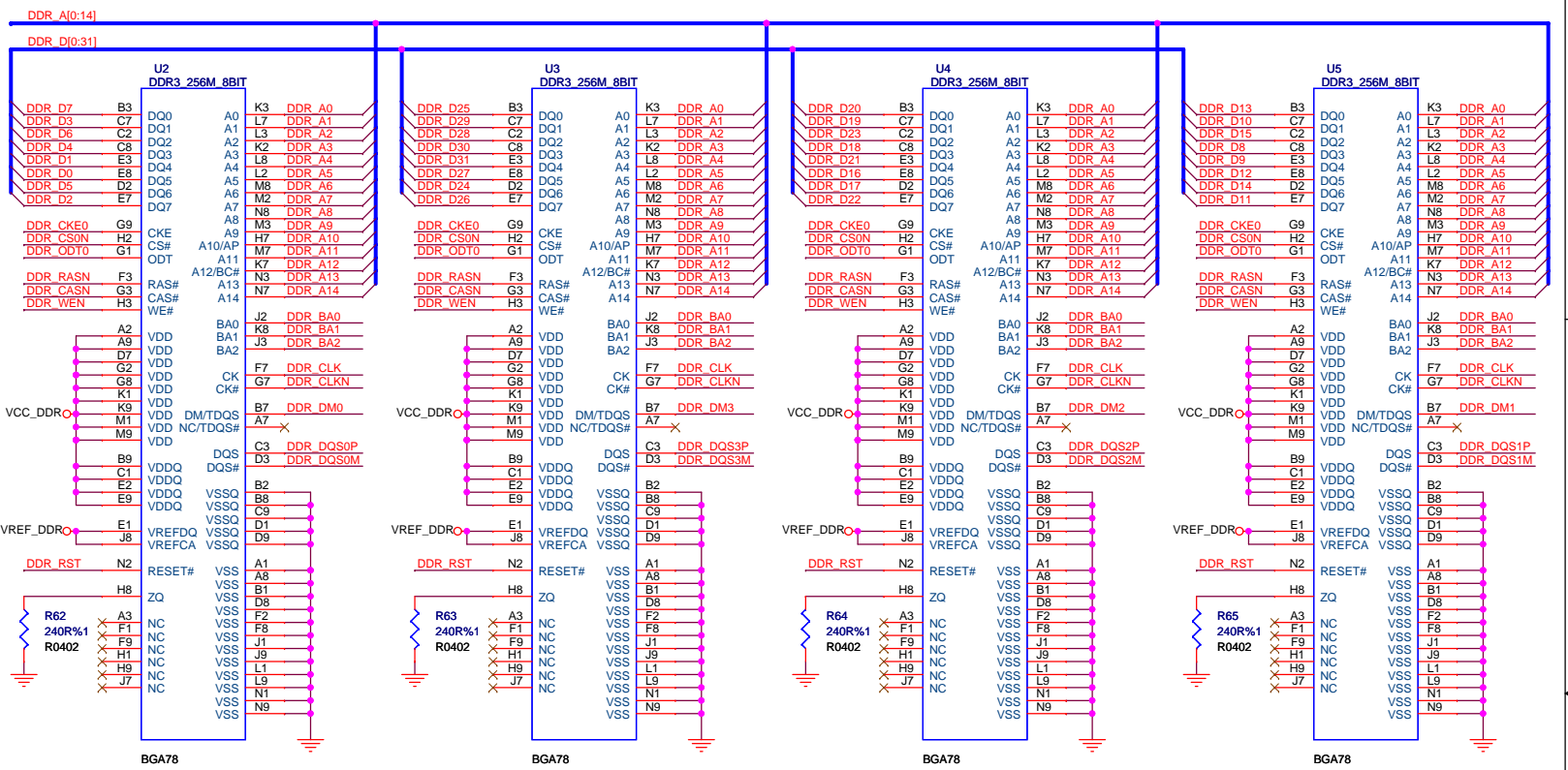


RK3066-E

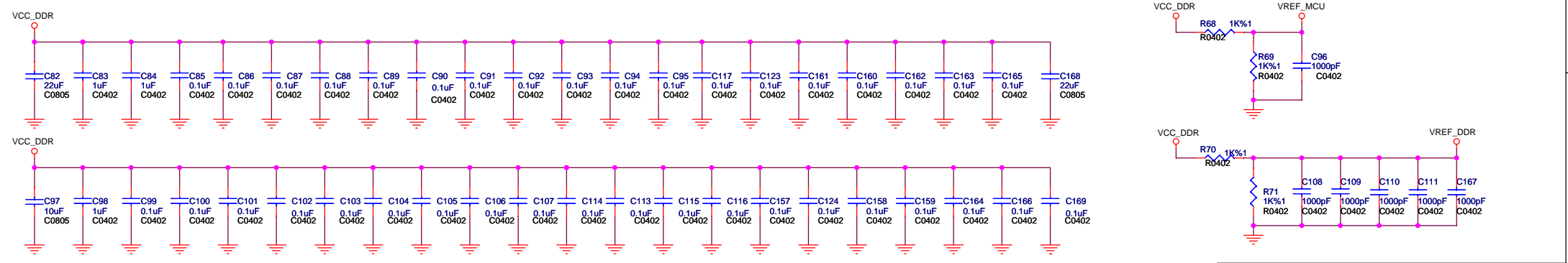
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RK3066-N



DDR3



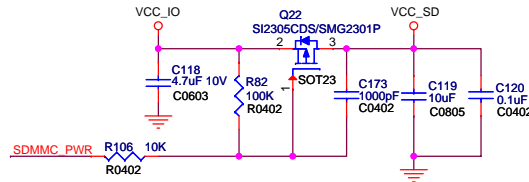
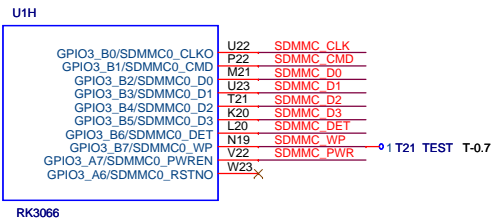
DDR FILTER

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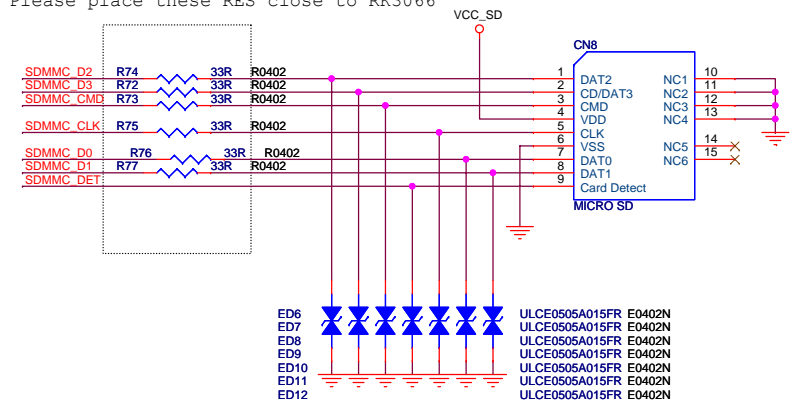
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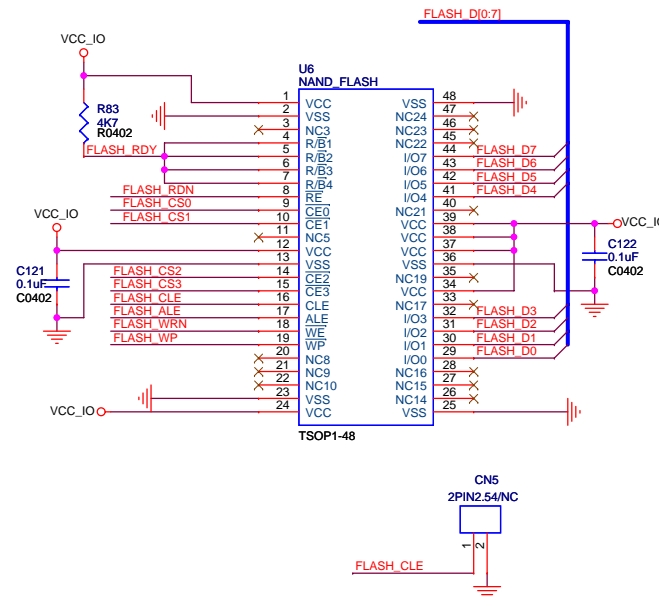
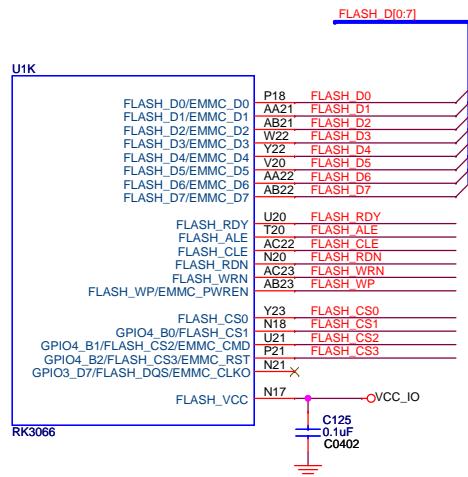


IMPORTANT NOTE:
Please place these RES close to RK3066



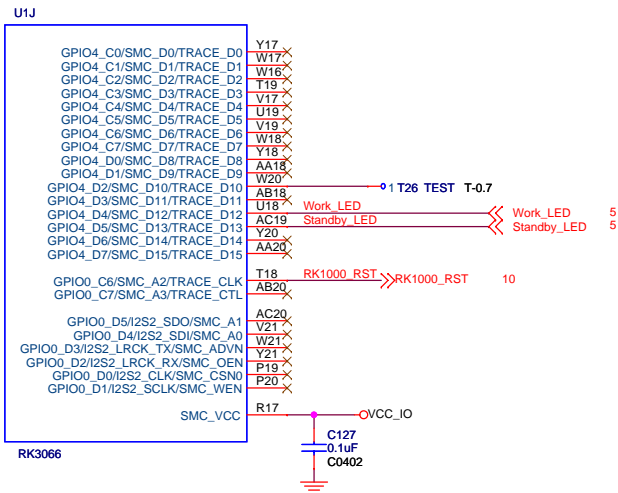
RK3066-H

TF CARD

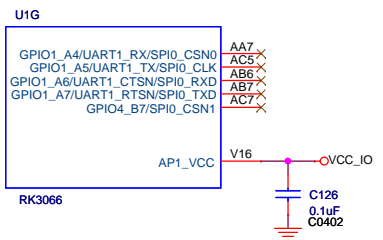


RK3066-K

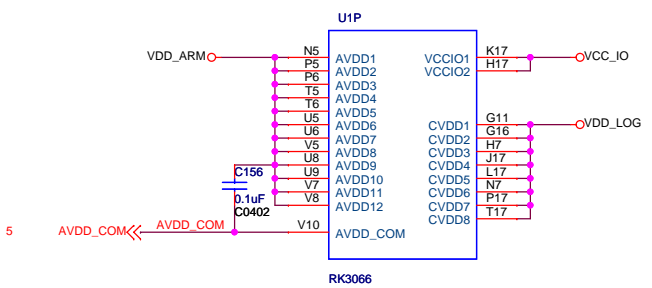
NAND FLASH



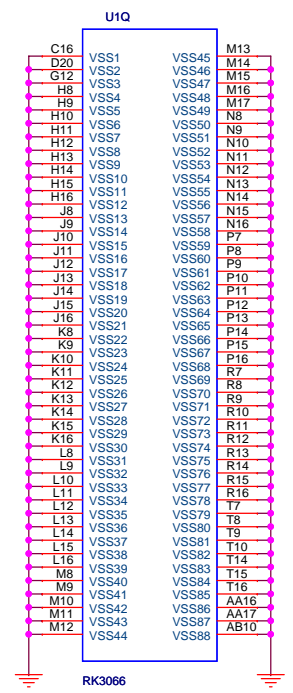
RK3066-J



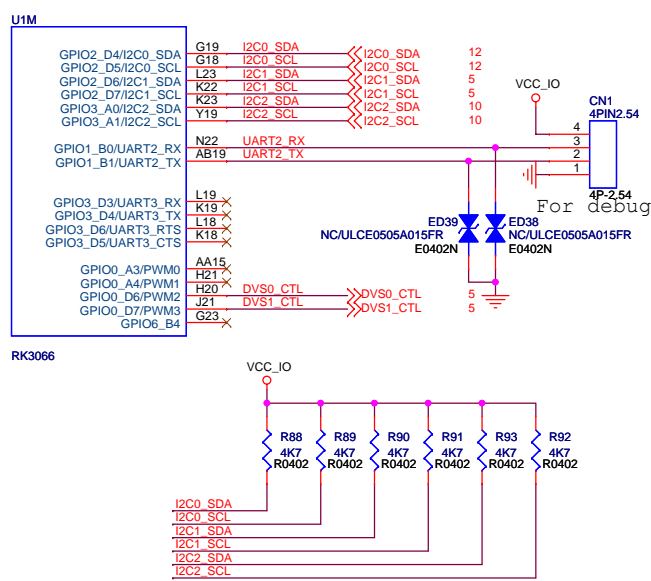
RK3066-G



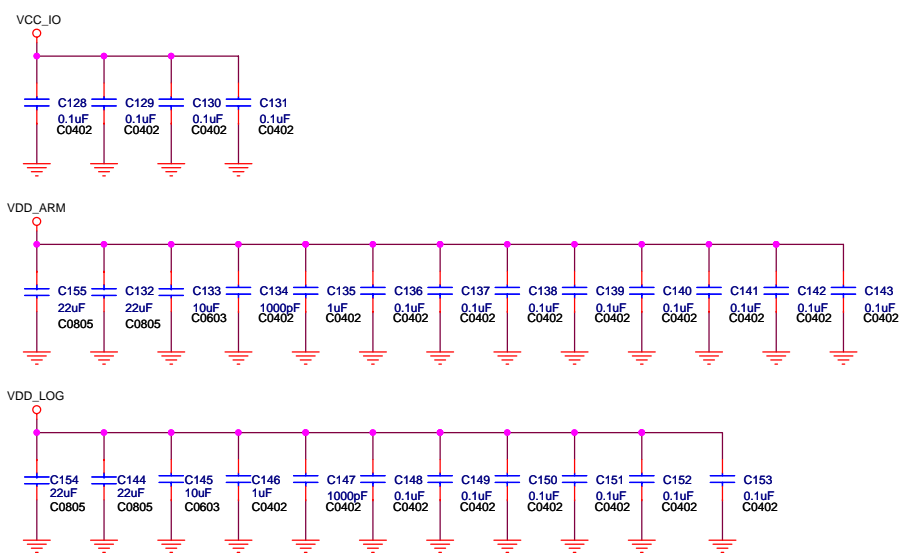
RK3066-P



RK3066-Q



RK3066-M



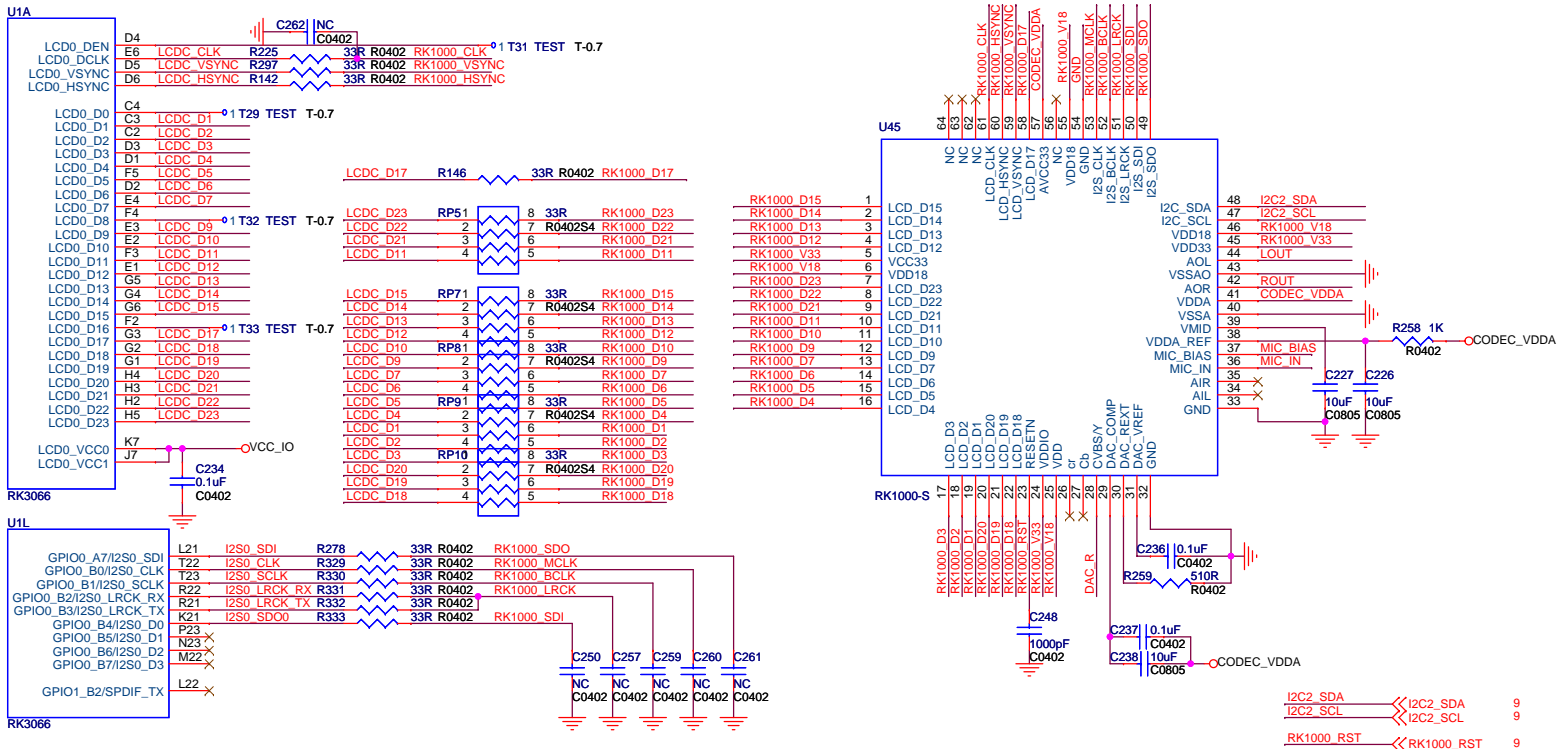
RK3066 CORE POWER FILTER

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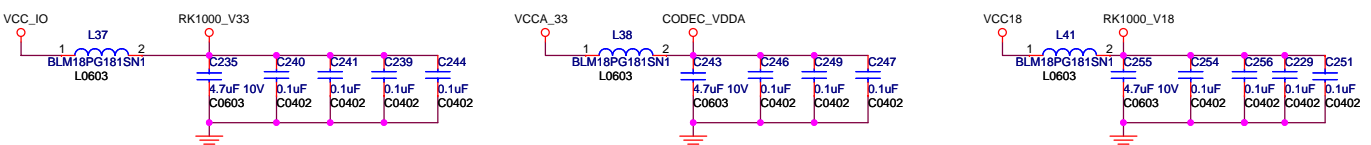
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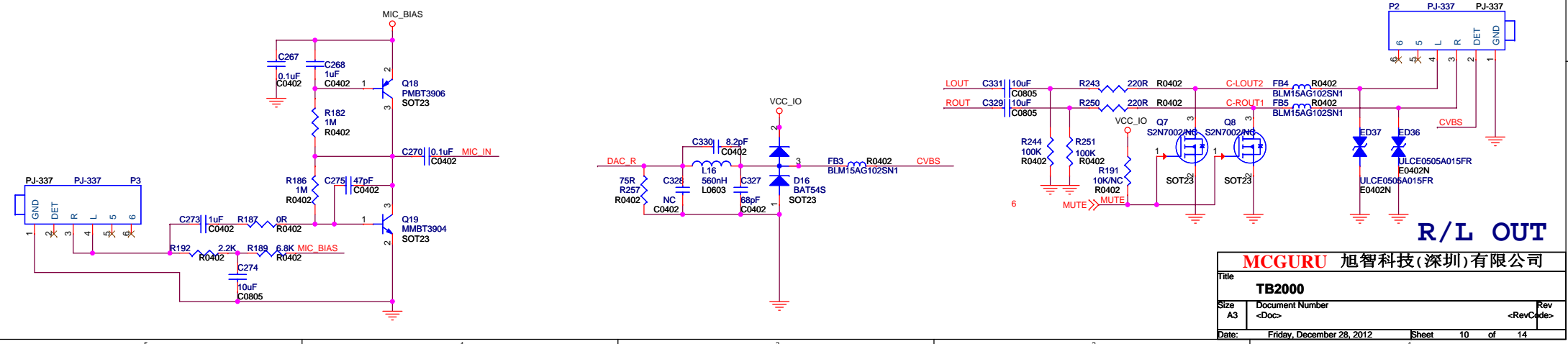
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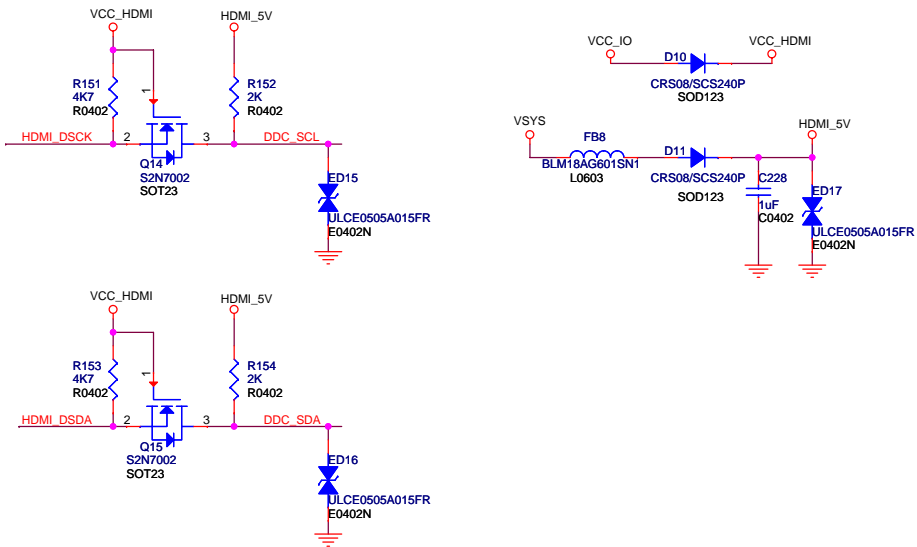
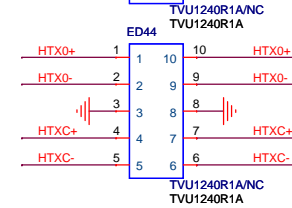
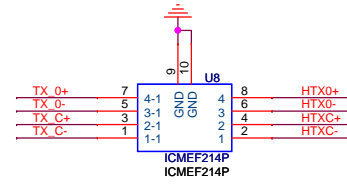
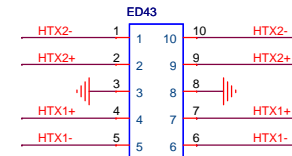
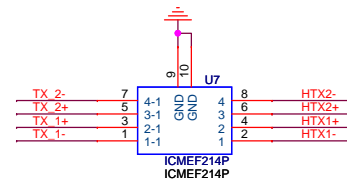
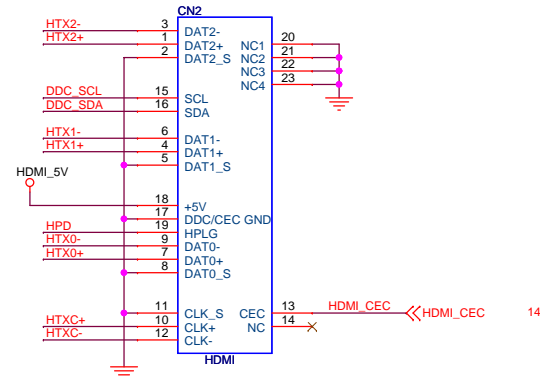
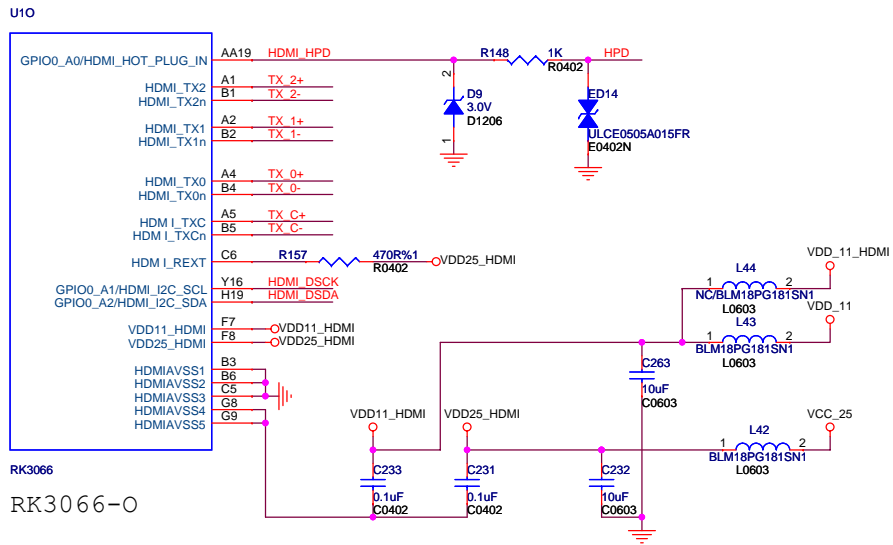


RK3066-L

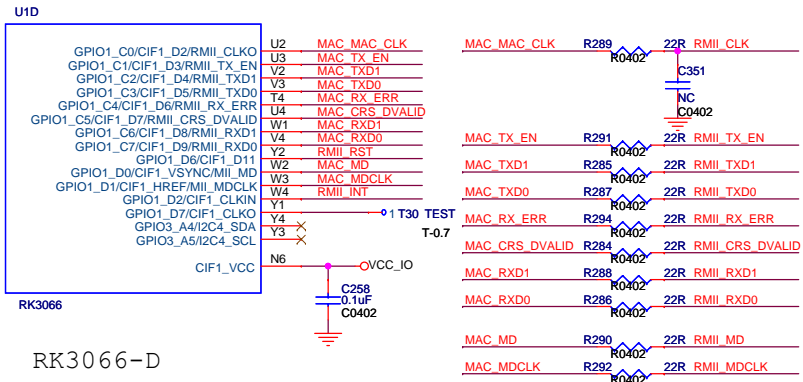


RK1000-S POWER

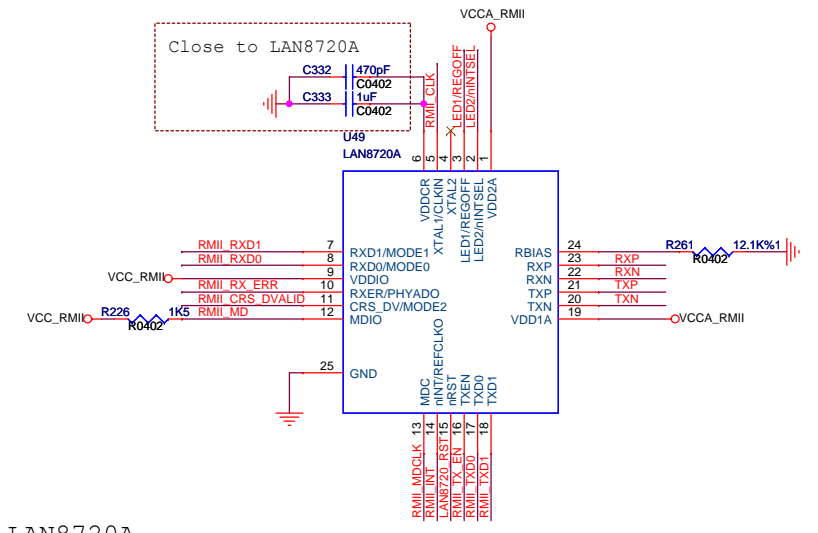
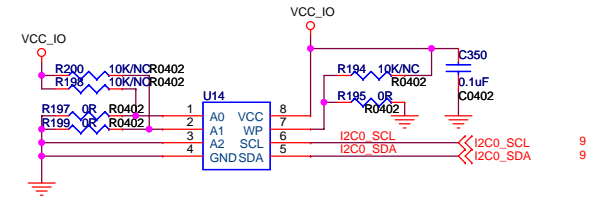
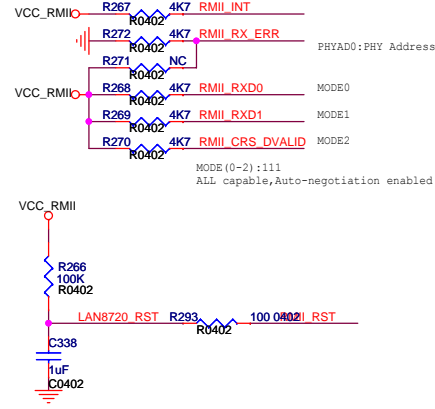




HDMI OUT

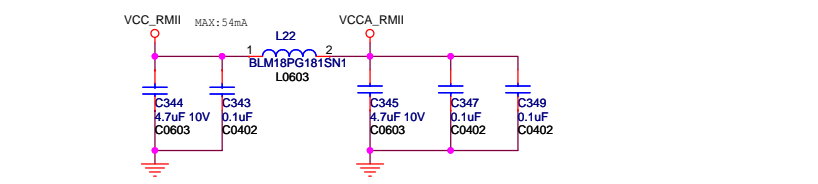
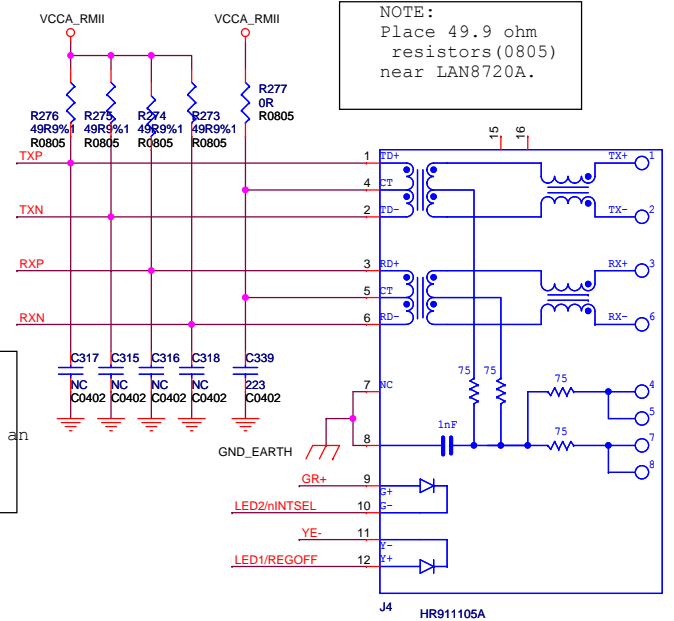


RK3066-D

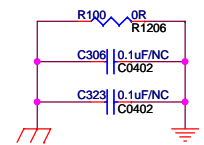


LAN8720A

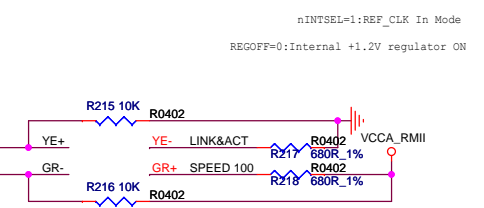
IMPORTANT NOTE:
Below Capacitors are optional and are not present on the LAN8720A CEB. These capacitors are required for operation in an EMI constrained environment.



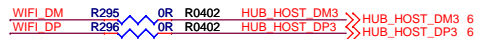
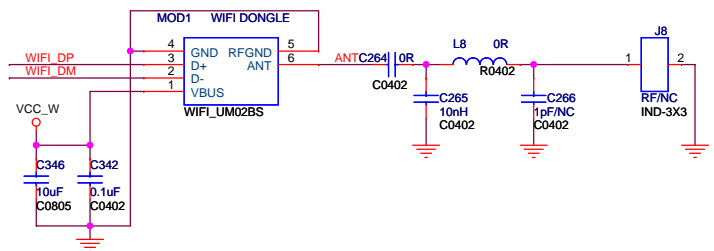
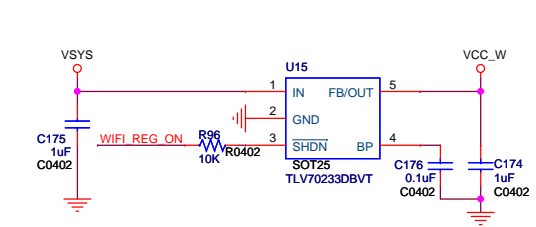
LAN8720A POWER



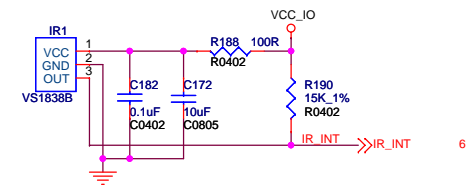
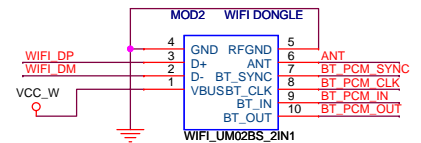
Net Status LED



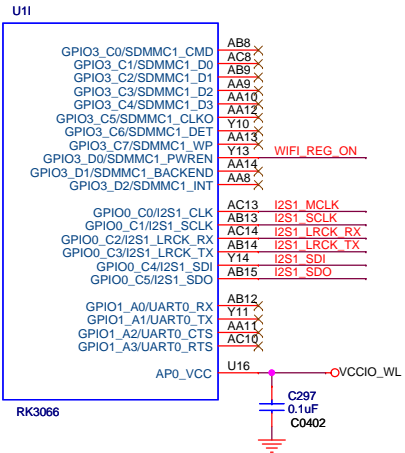
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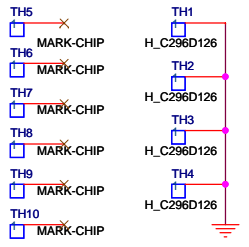
USB WIFI (OPTION)



IR RECEIVE



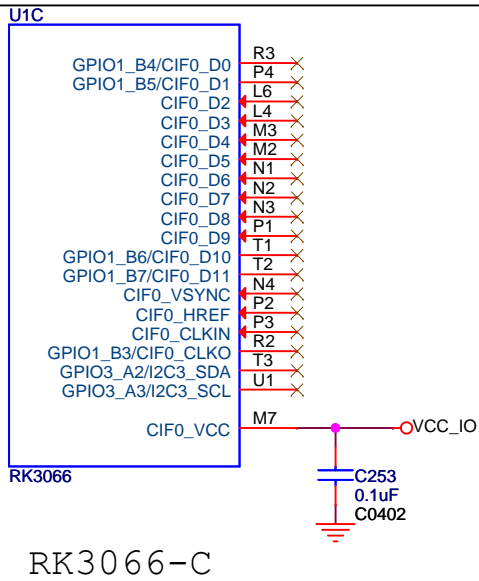
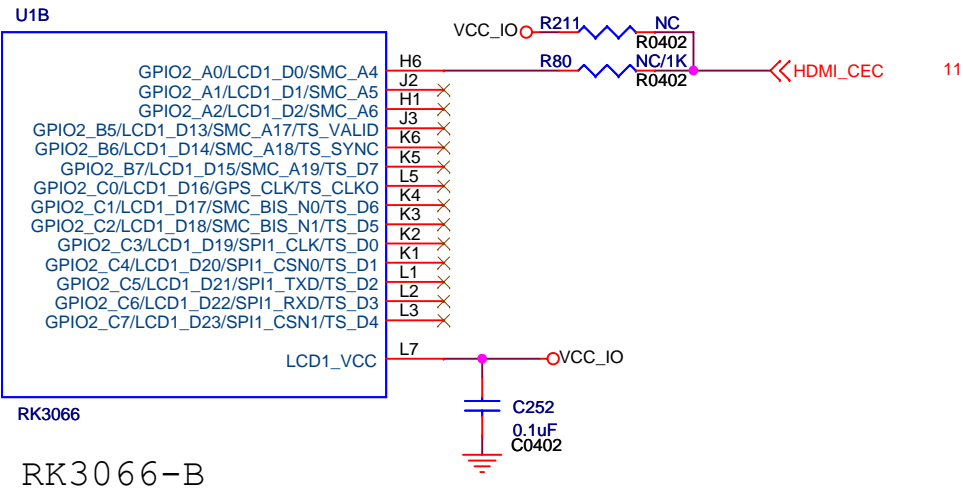
Mark Point



RK3066-I

Note :PLS design the RF under RK RF LAYOUT guide;
For more suggestions, please refer to the SPEC
of the wireless IC

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