

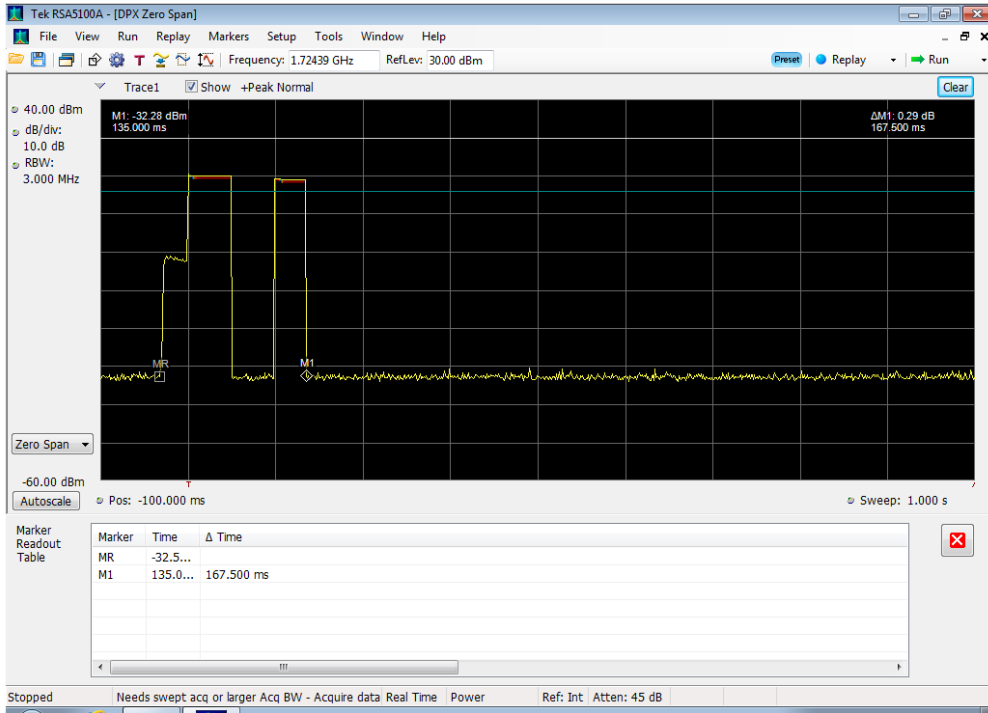


Annex I

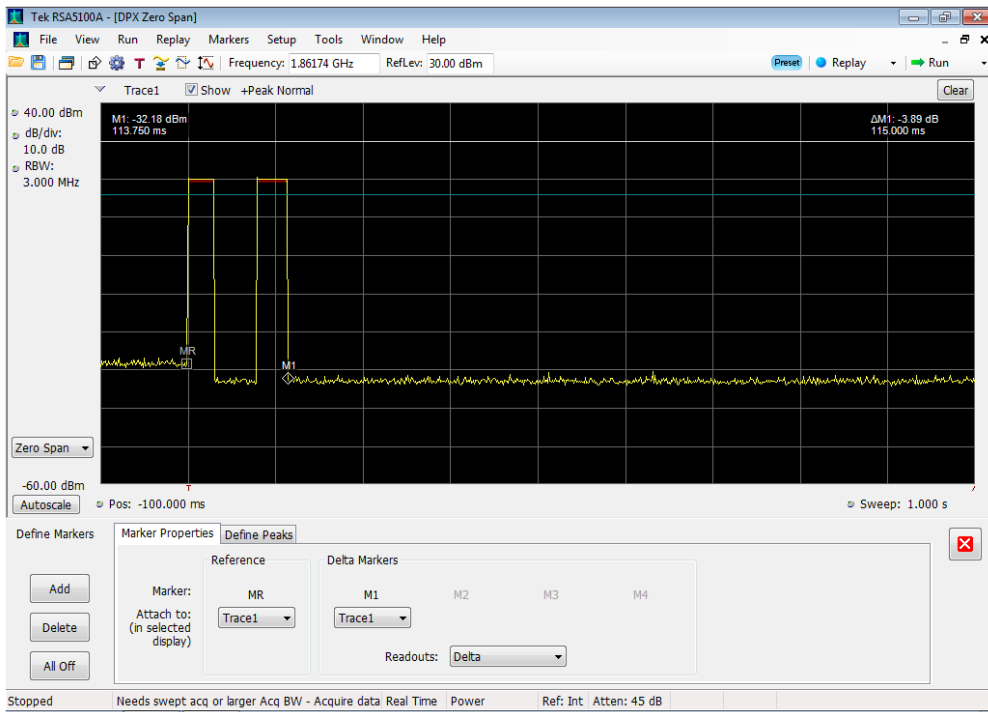
Oscillation Detection



Detection Time_Uplink_1710 - 1755 MHz_p1570026

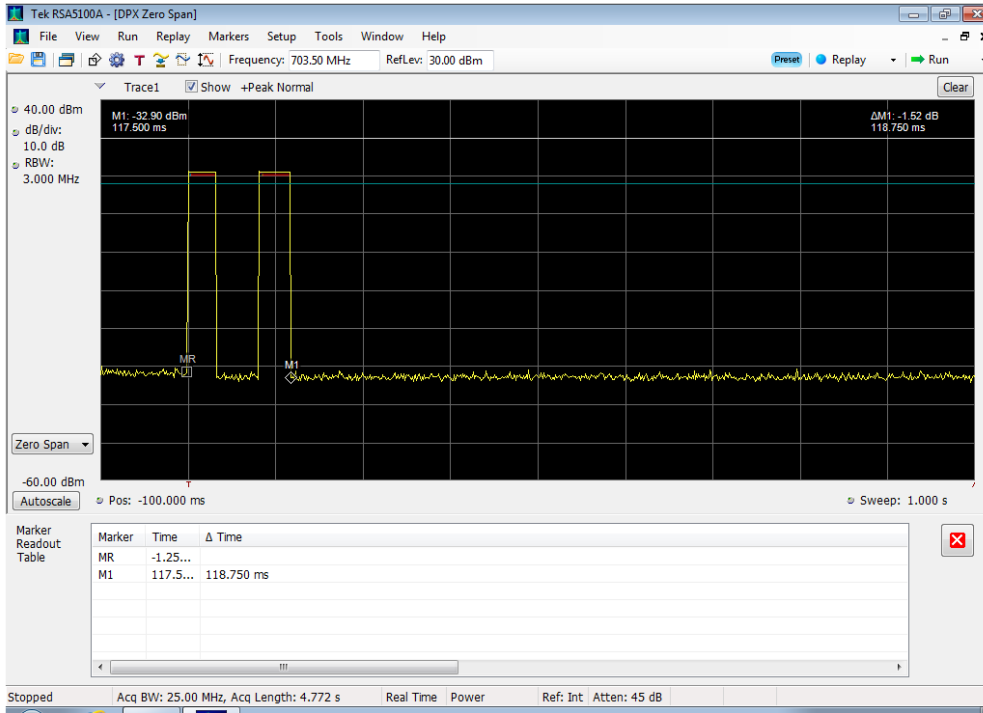


Detection Time_Uplink_1850 - 1915 MHz_p1570026

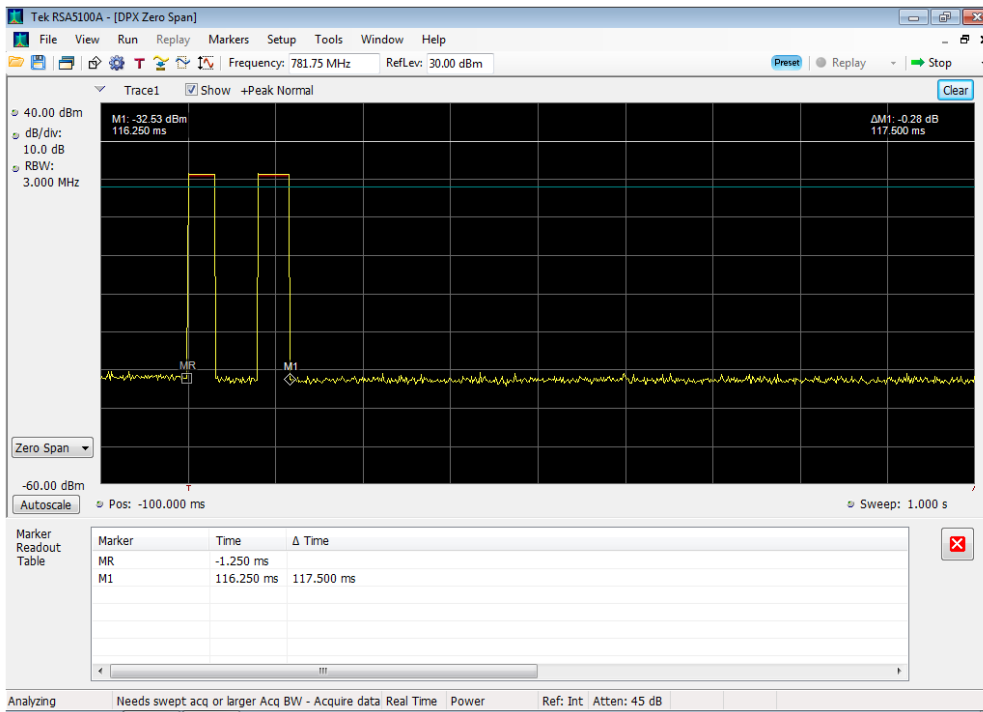




Detection Time_Uplink_698 - 716 MHz_p1570026

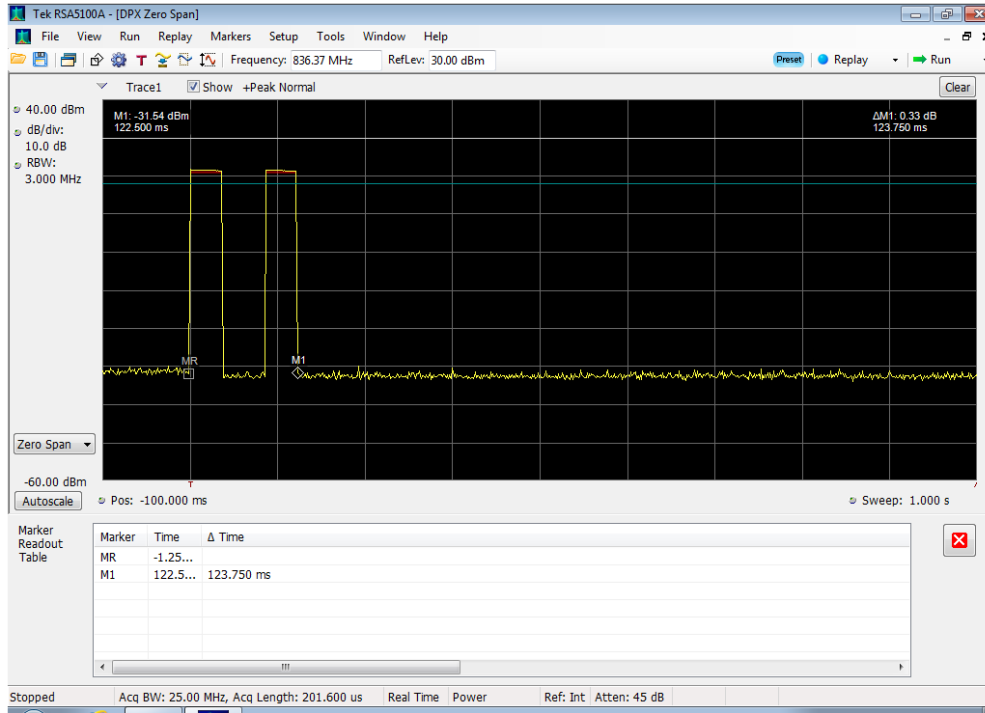


Detection Time_Uplink_776 - 787 MHz_p1570026

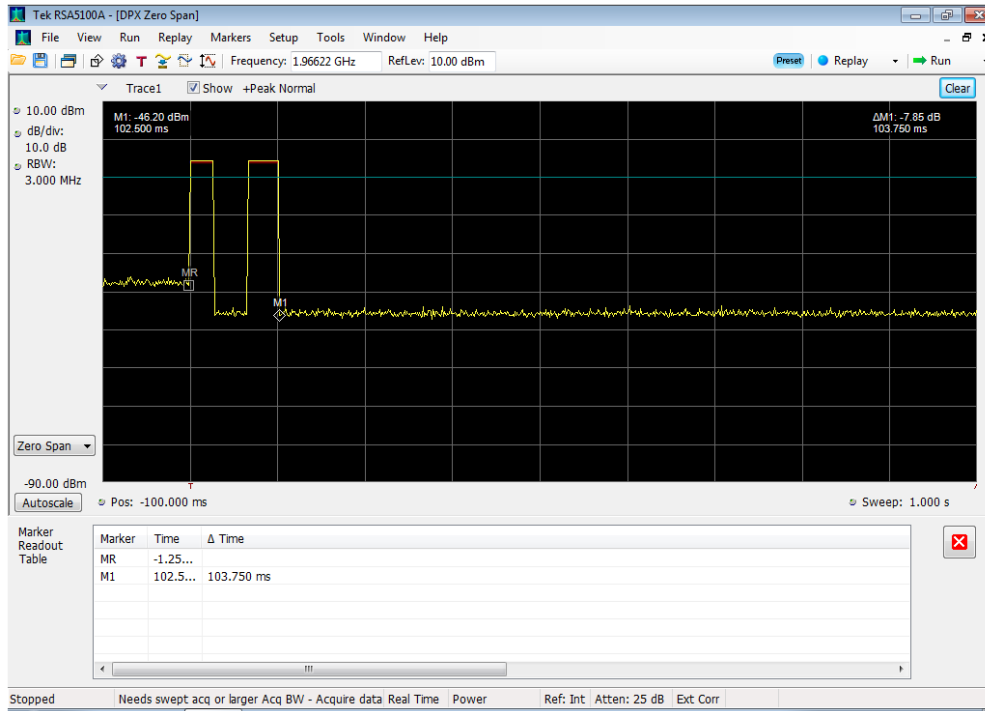




Detection Time_Uplink_824 - 849 MHz_p1570026

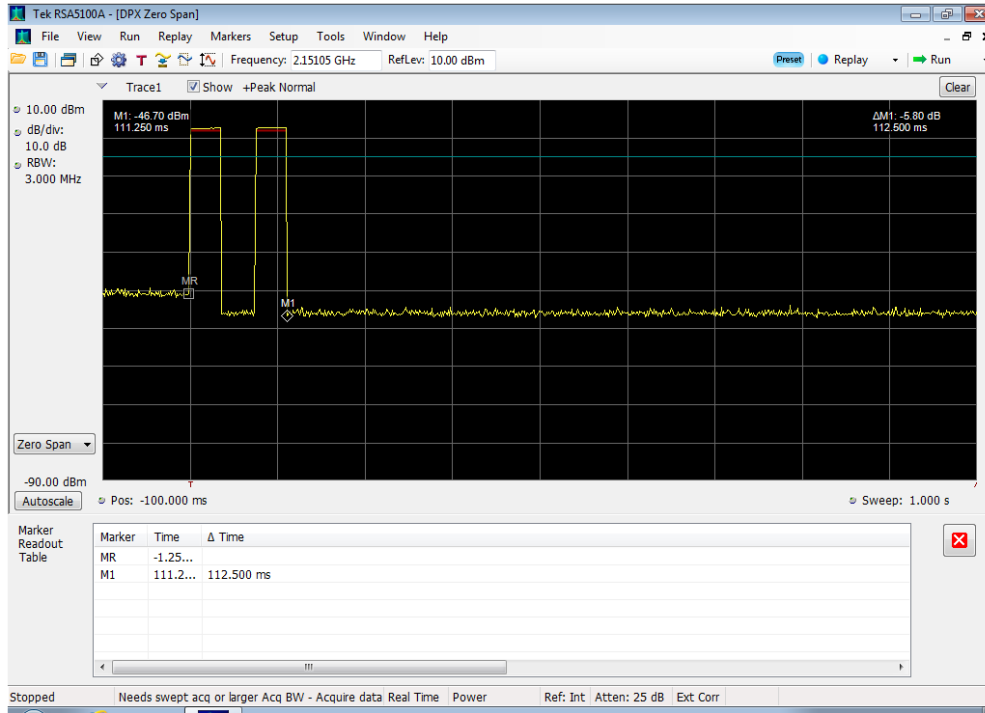


Detection Time_Downlink_1930 - 1995 MHz_p1570026

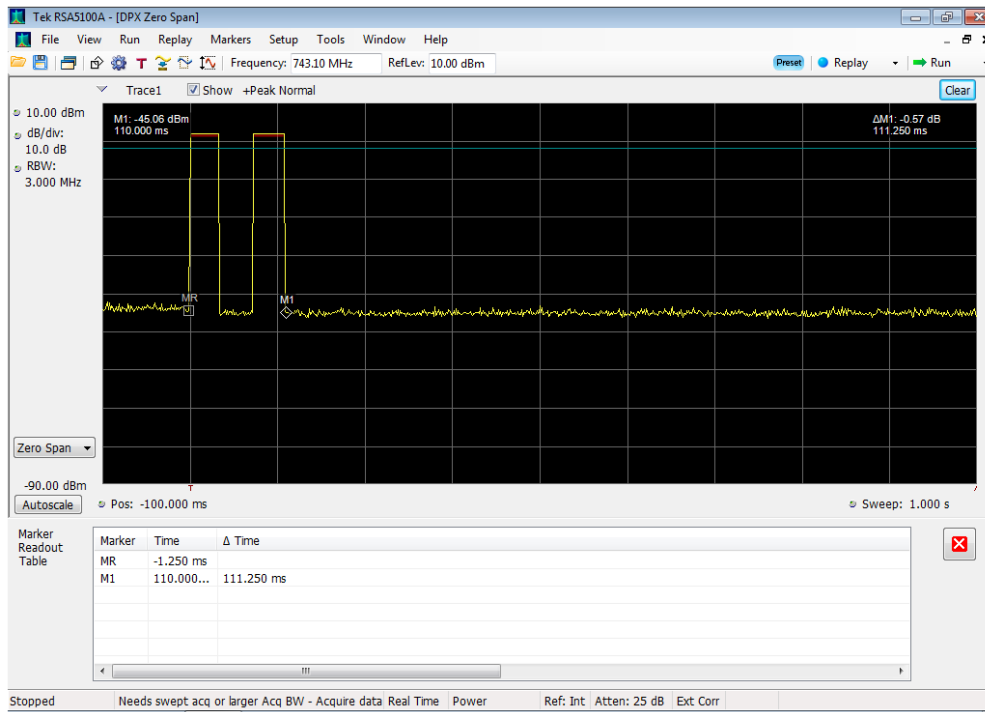




Detection Time_Downlink_2110 - 2155 MHz_p1570026

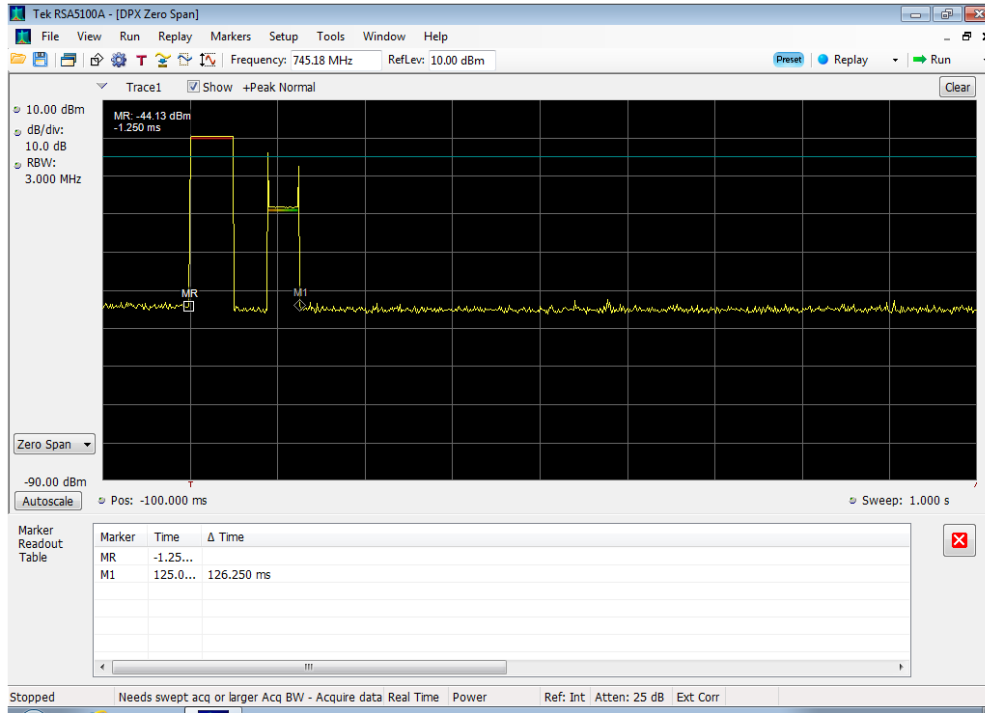


Detection Time_Downlink_728 - 746 MHz_p1570026

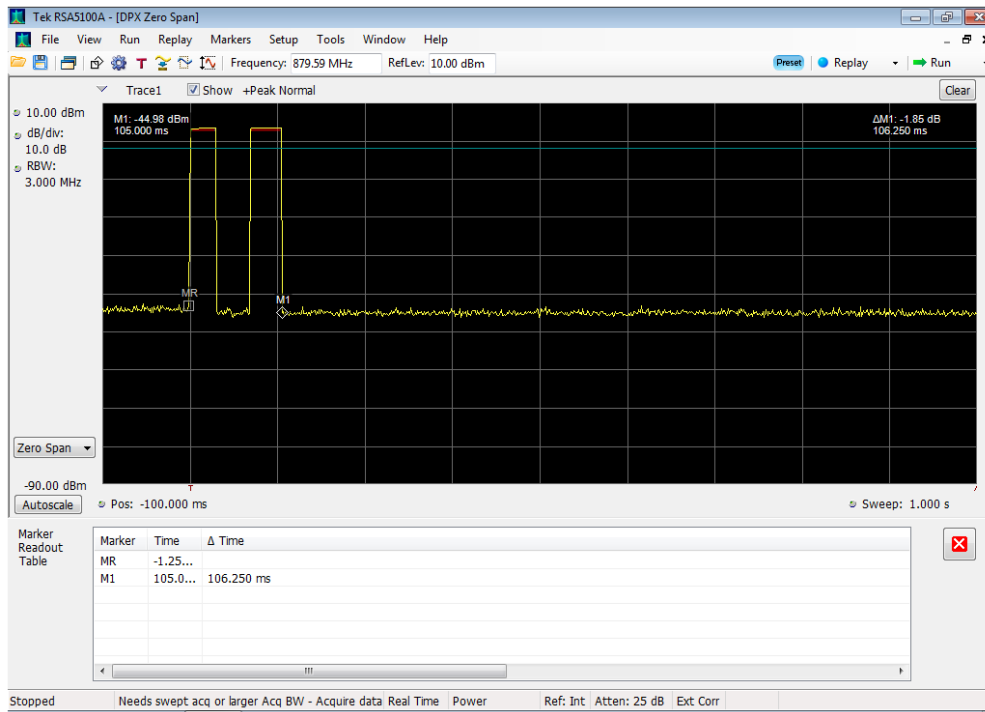




Detection Time_Downlink_746 - 757 MHz_p1570026

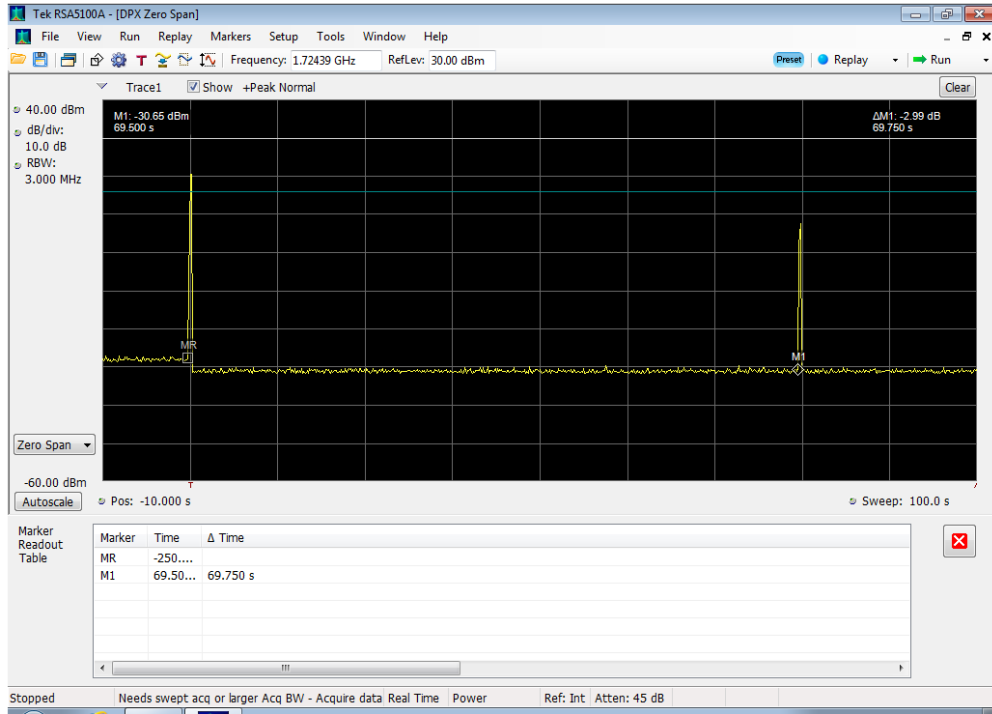


Detection Time_Downlink_869 - 894 MHz_p1570026

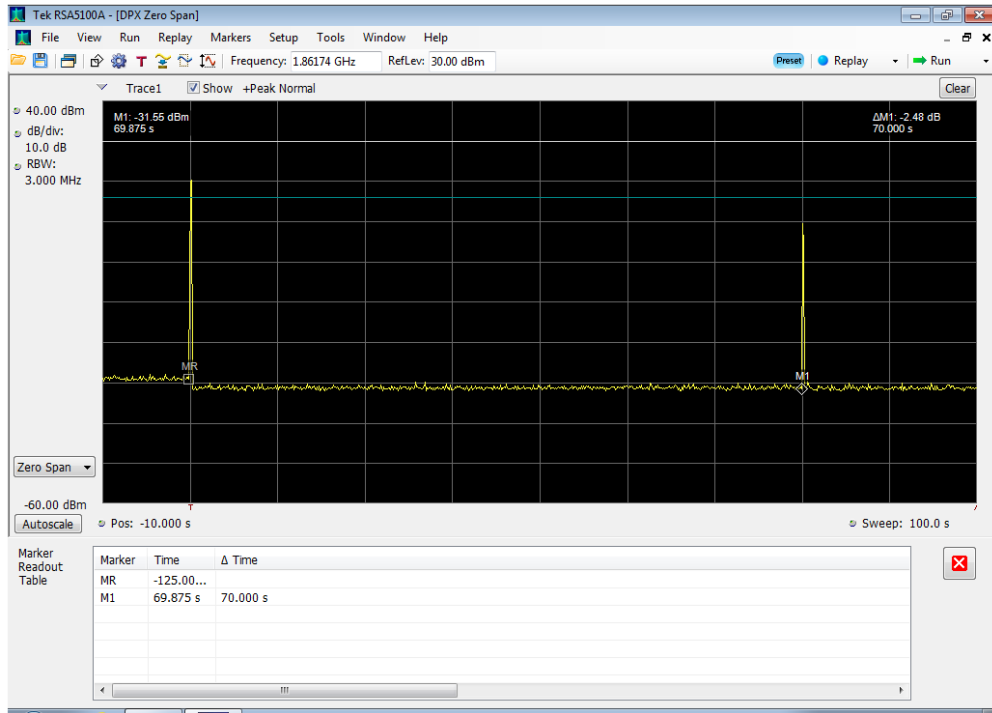




Restart Time_Uplink_1710 - 1755 MHz_p1570026

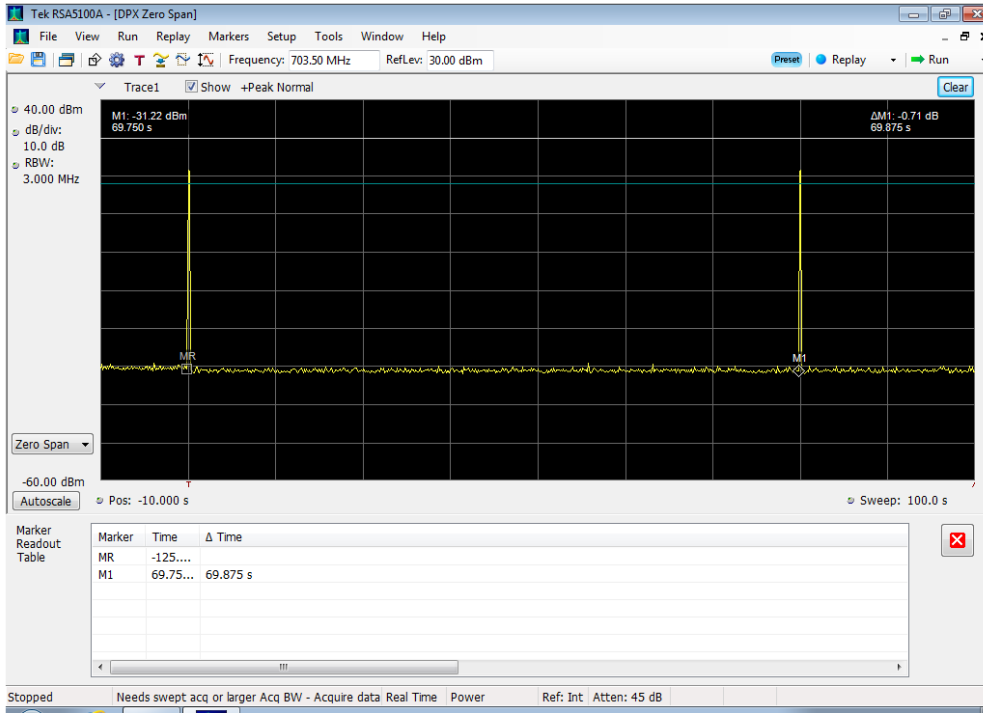


Restart Time_Uplink_1850 - 1915 MHz_p1570026

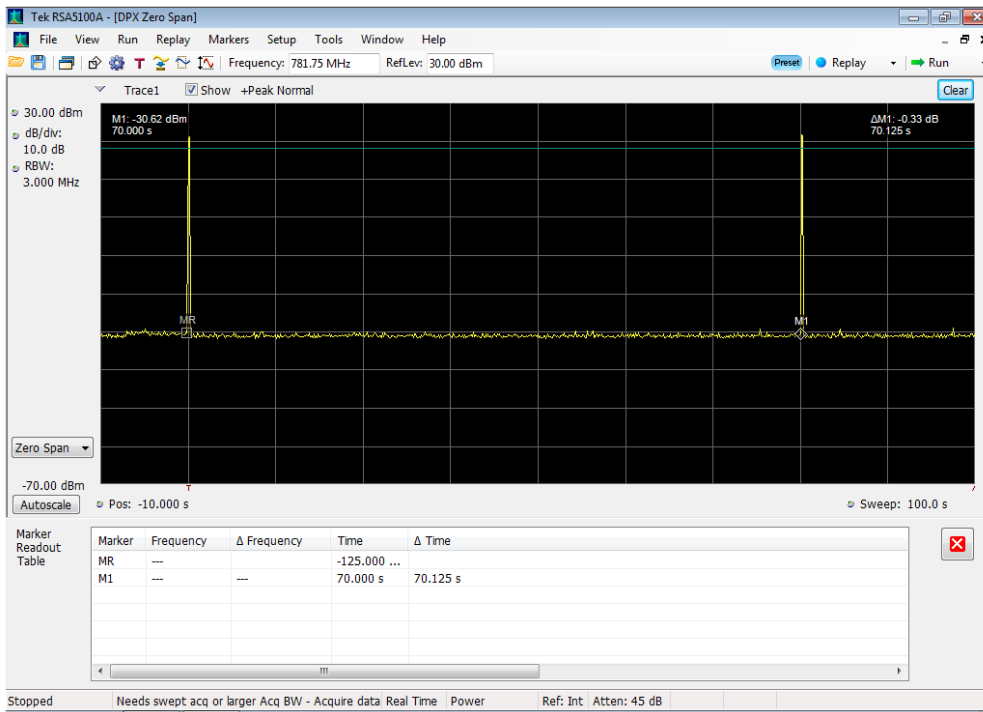




Restart Time_Uplink_698 - 716 MHz_p1570026

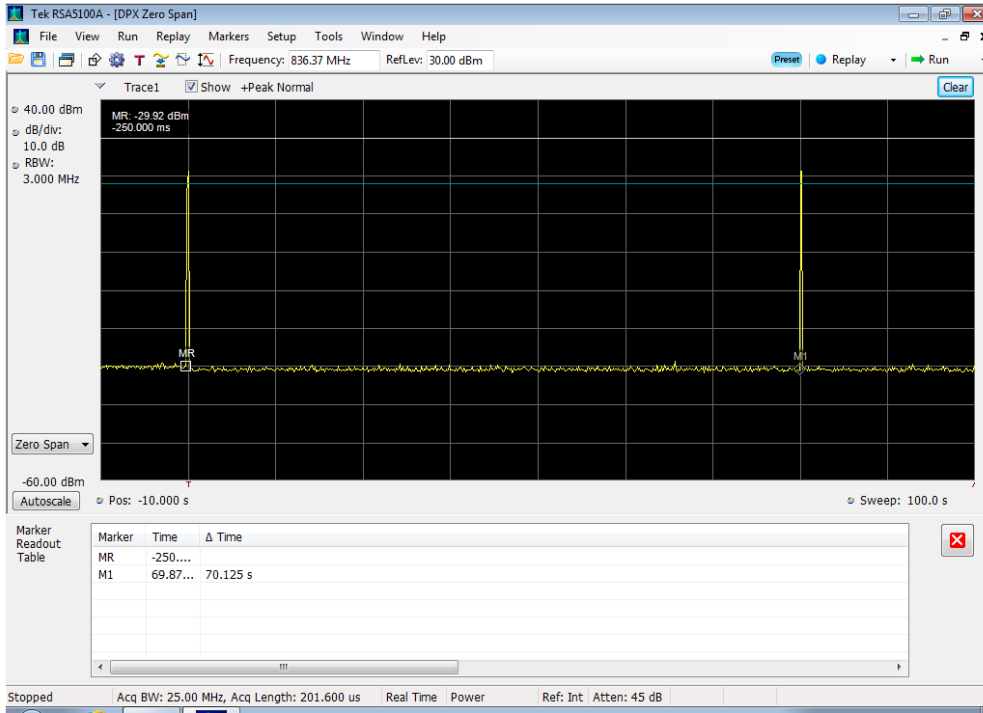


Restart Time_Uplink_776 - 787 MHz_p1570026

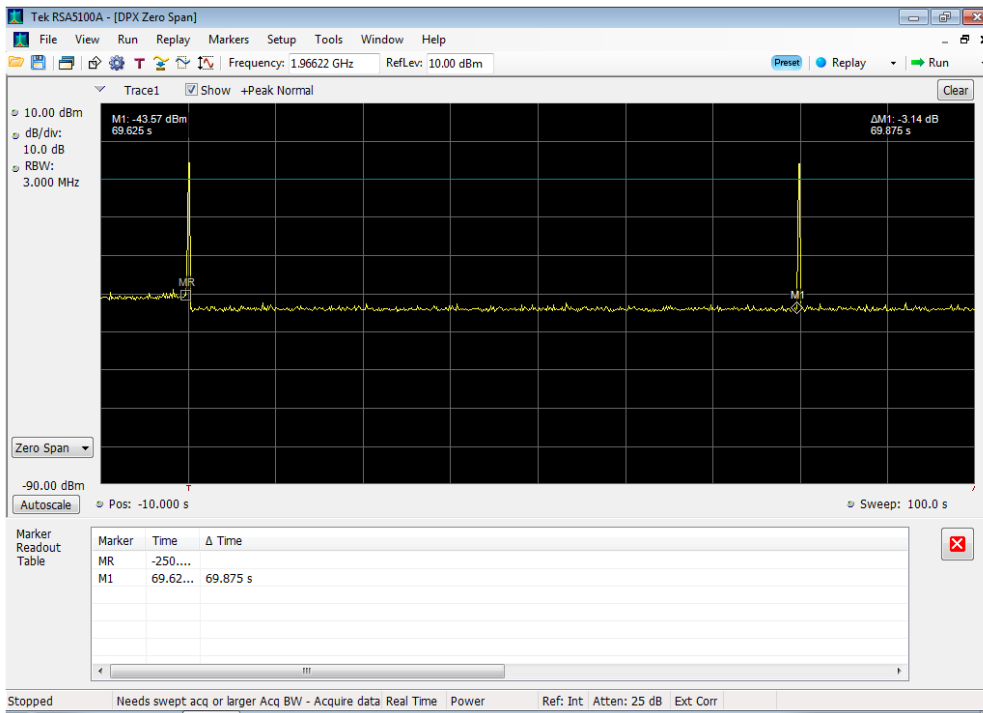




Restart Time_Uplink_824 - 849 MHz_p1570026

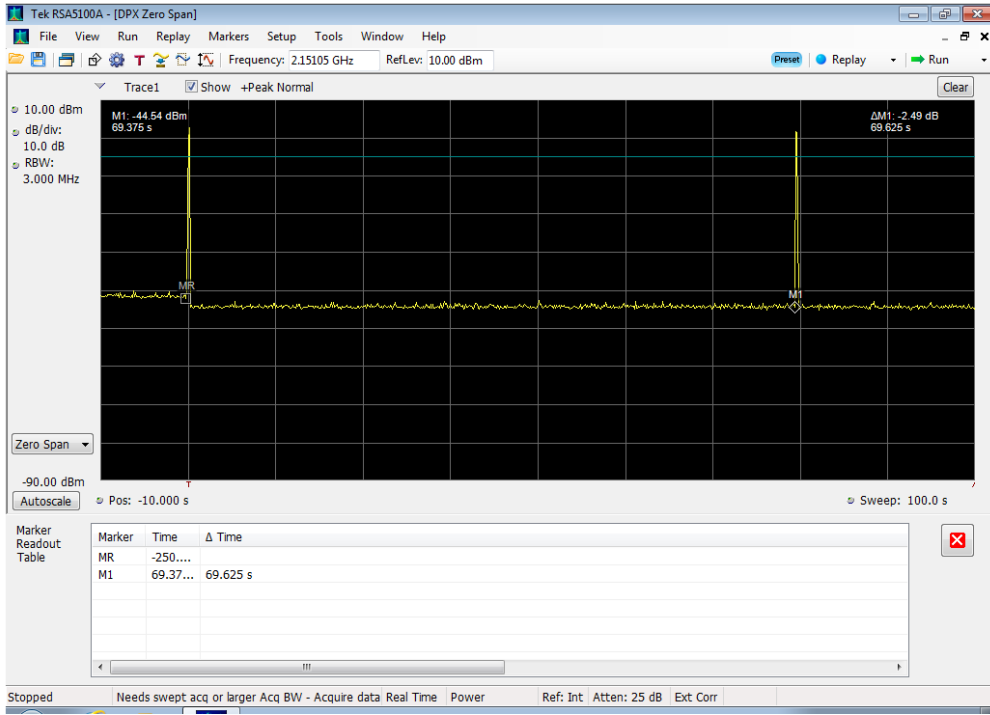


Restart Time_Downlink_1930 - 1995 MHz_p1570026

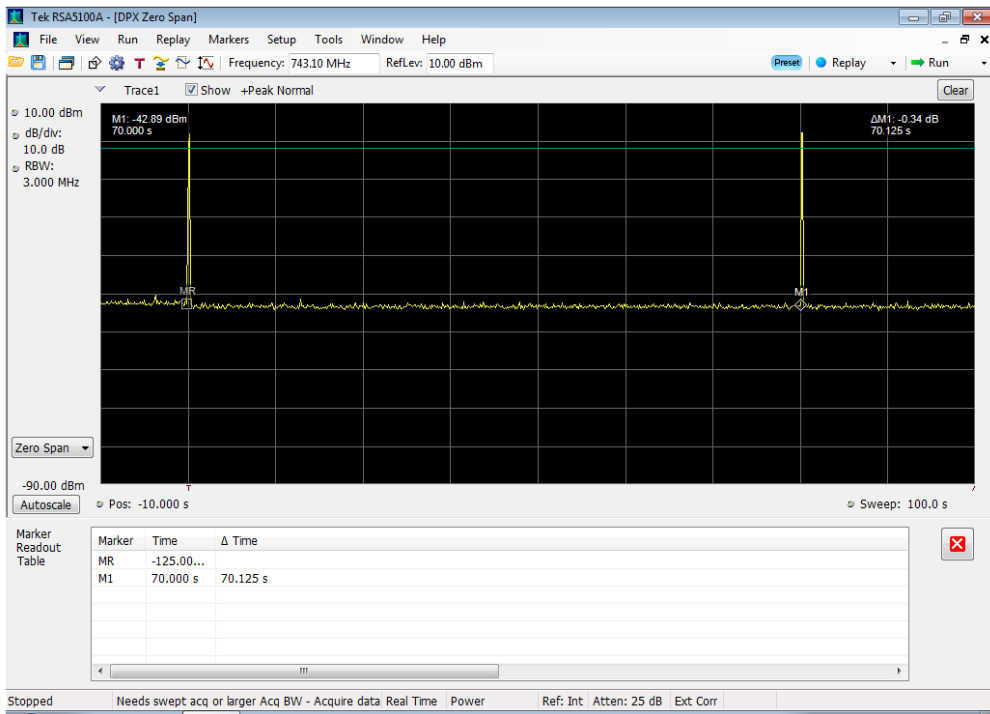




Restart Time_Downlink_2110 - 2155 MHz_p1570026

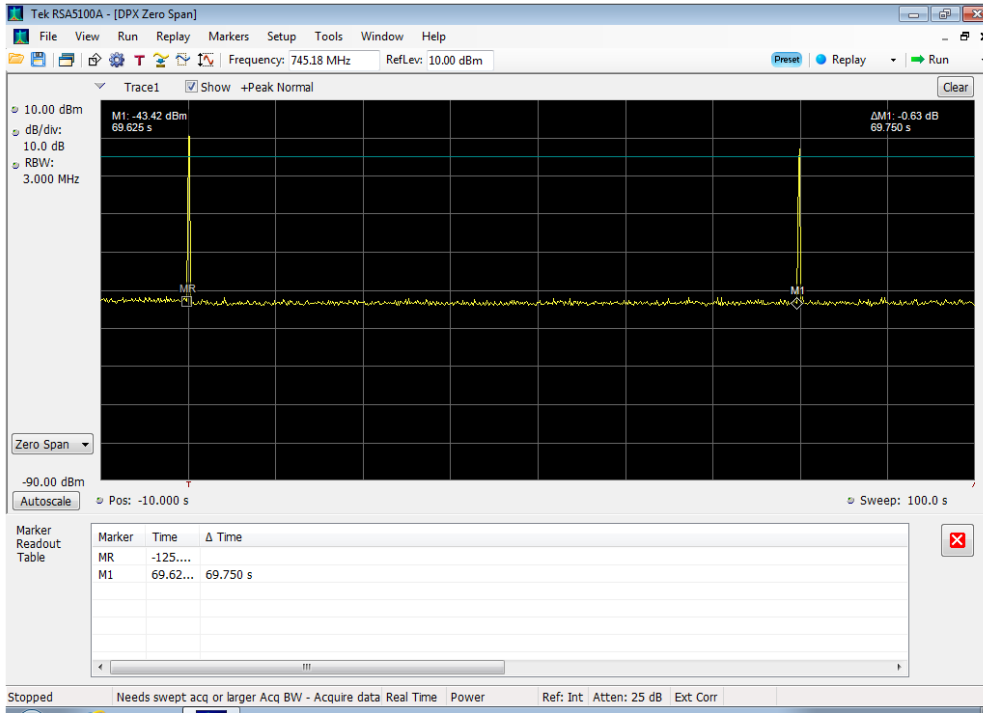


Restart Time_Downlink_728 - 746 MHz_p1570026

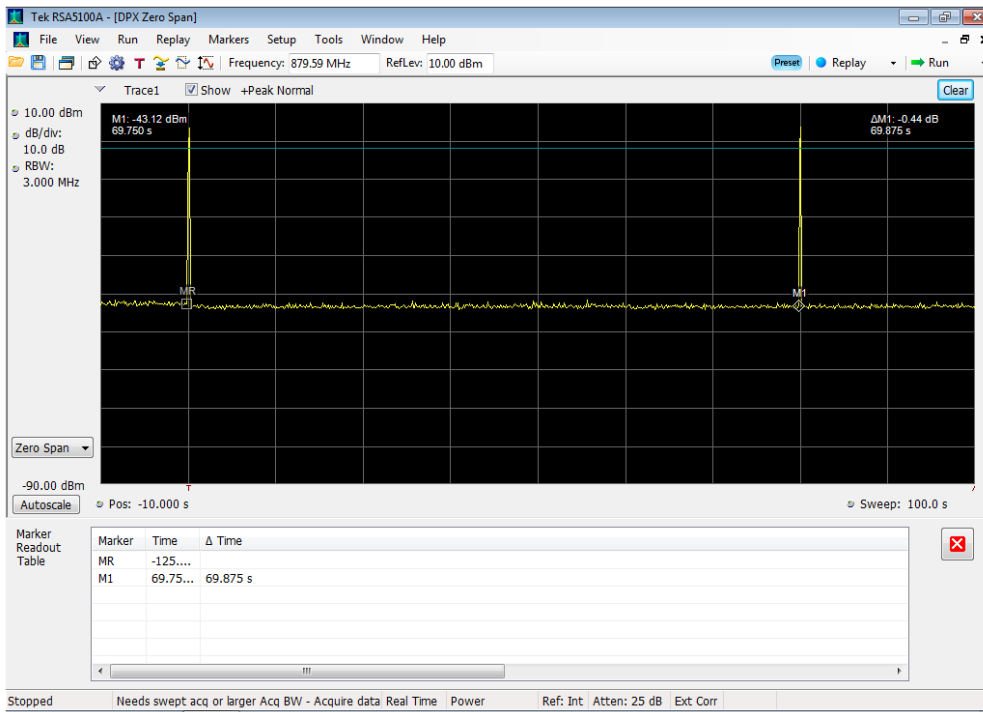




Restart Time_Downlink_746 - 757 MHz_p1570026

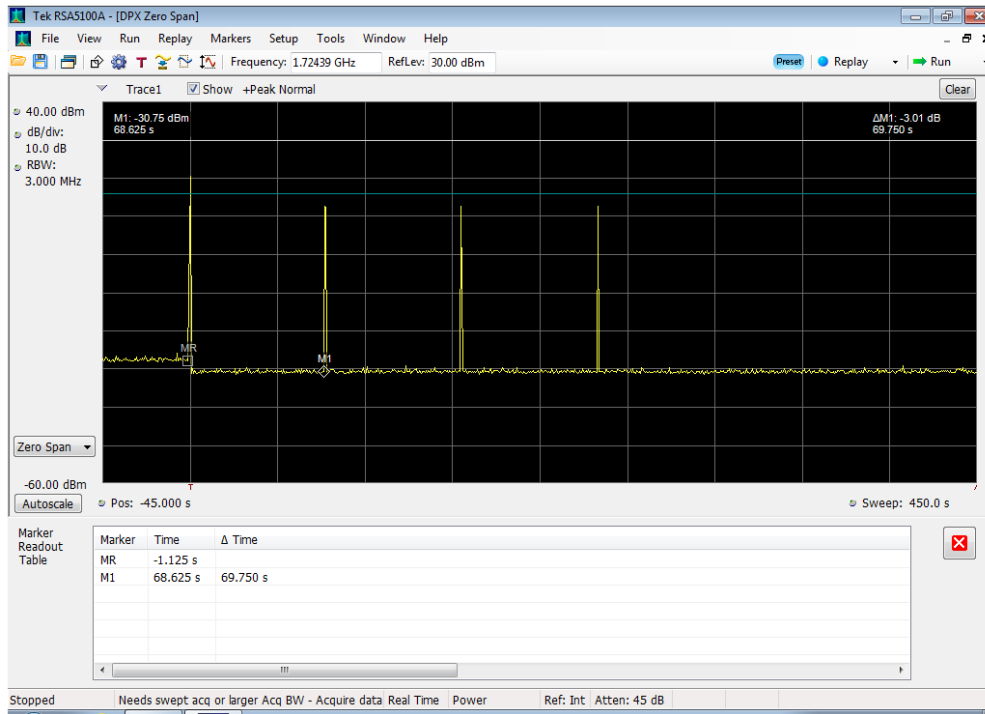


Restart Time_Downlink_869 - 894 MHz_p1570026

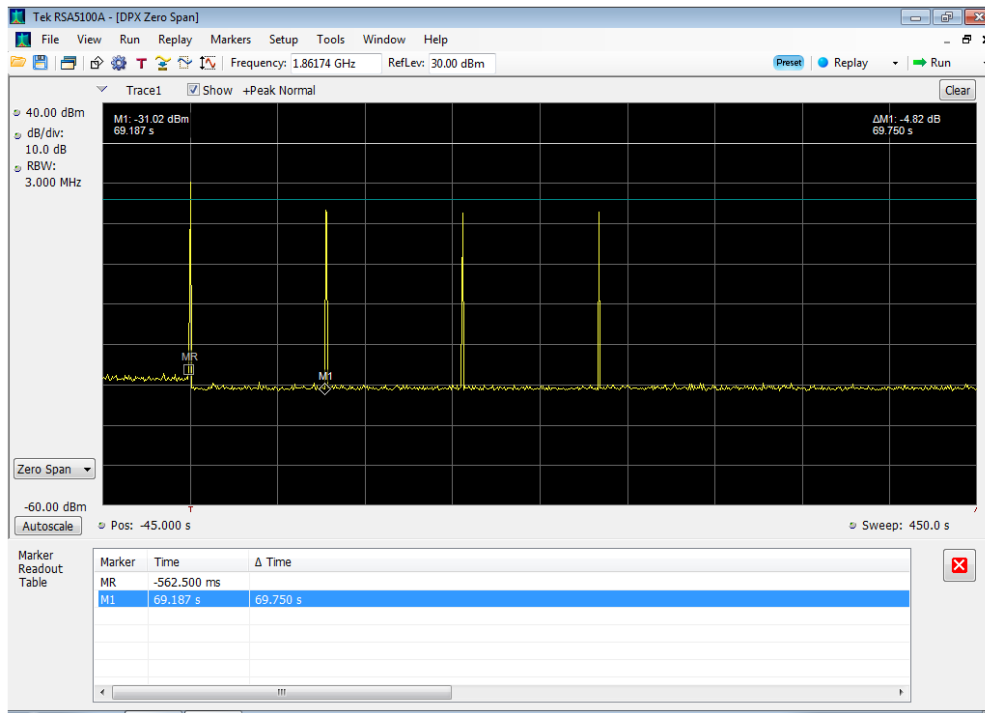




of Restarts_Uplink_1710 - 1755 MHz_p1570026

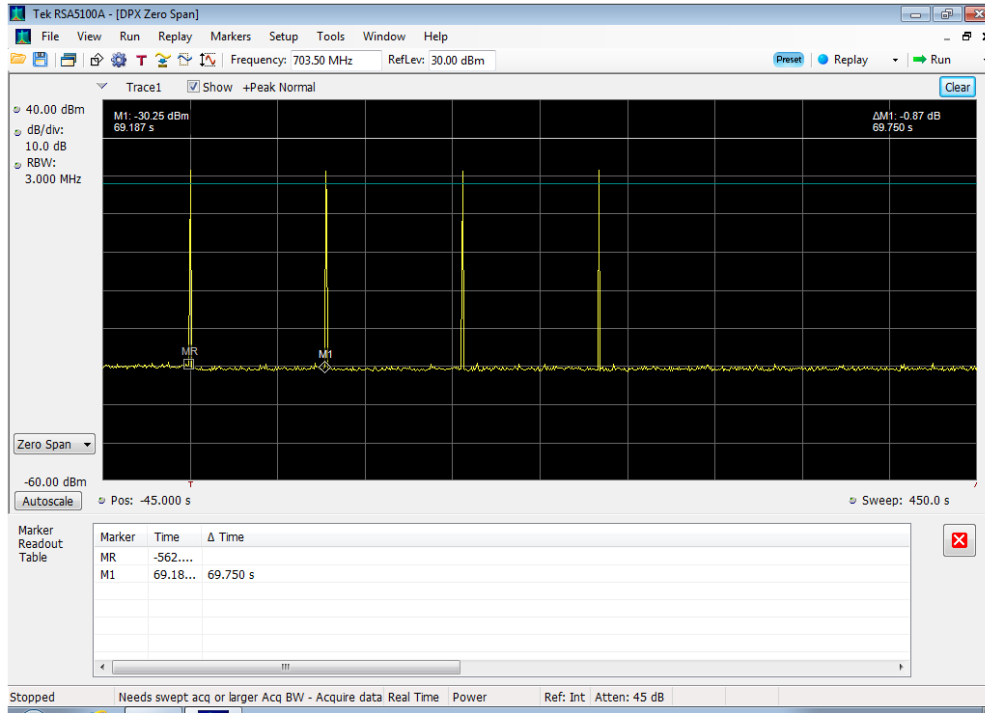


of Restarts_Uplink_1850 - 1915 MHz_p1570026

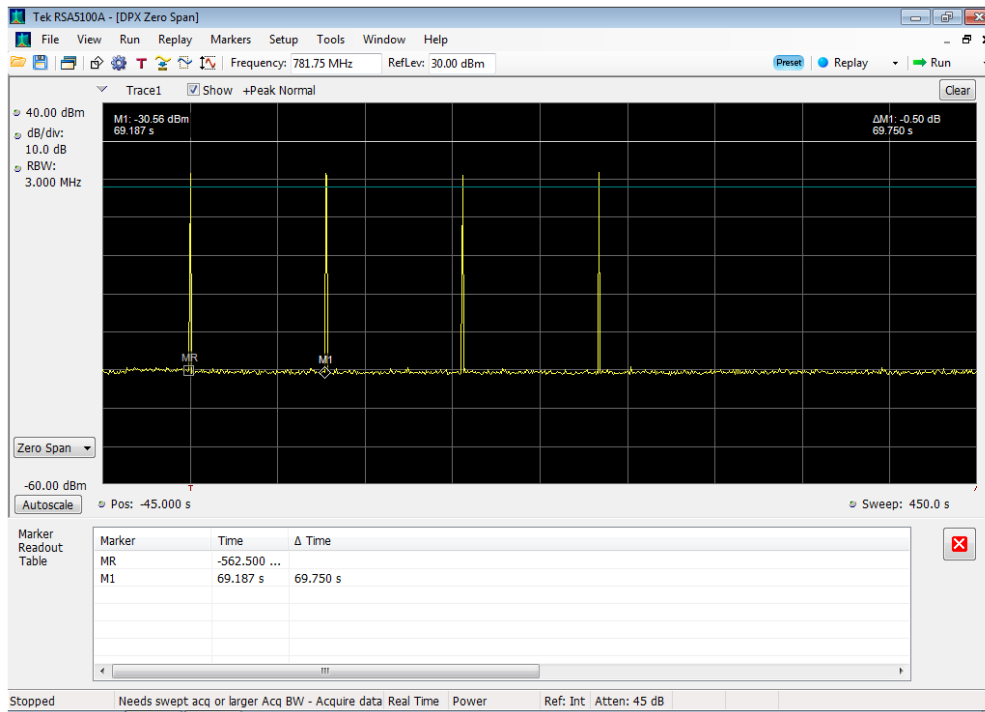




of Restarts_Uplink_698 - 716 MHz_p1570026

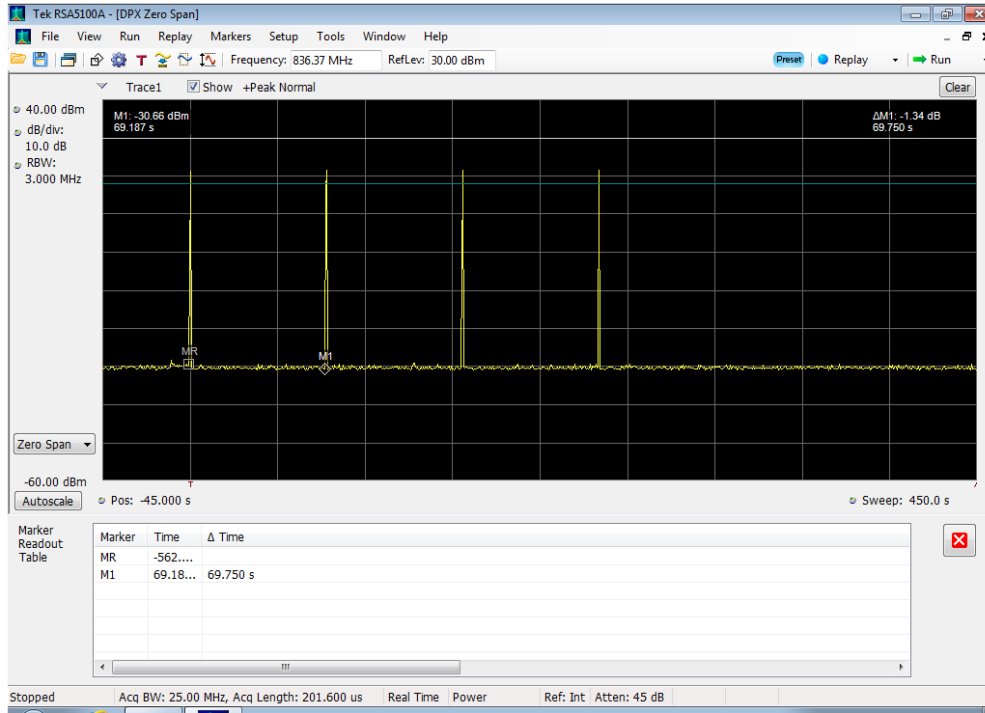


of Restarts_Uplink_776 - 787 MHz_p1570026

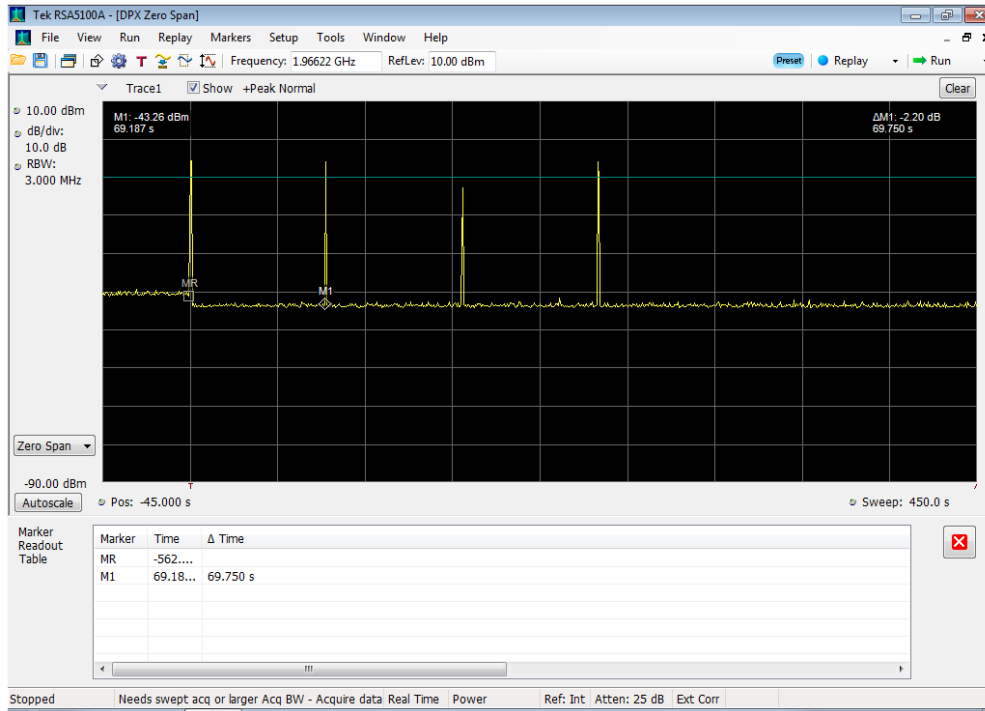




of Restarts_Uplink_824 - 849 MHz_p1570026

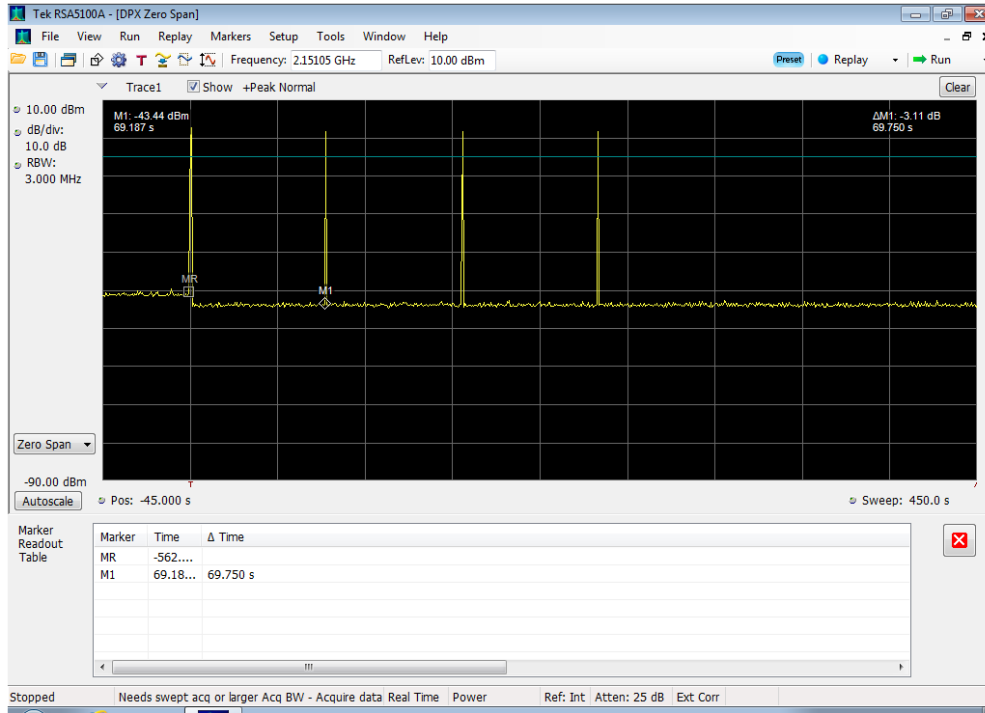


of Restarts_Downlink_1930 - 1995 MHz_p1570026

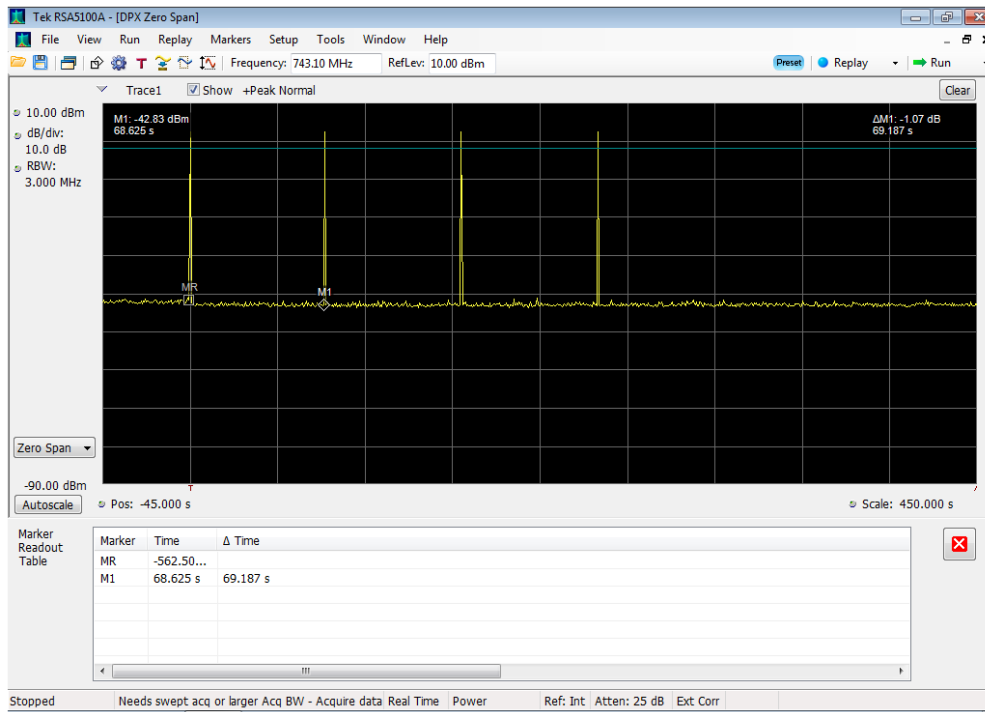




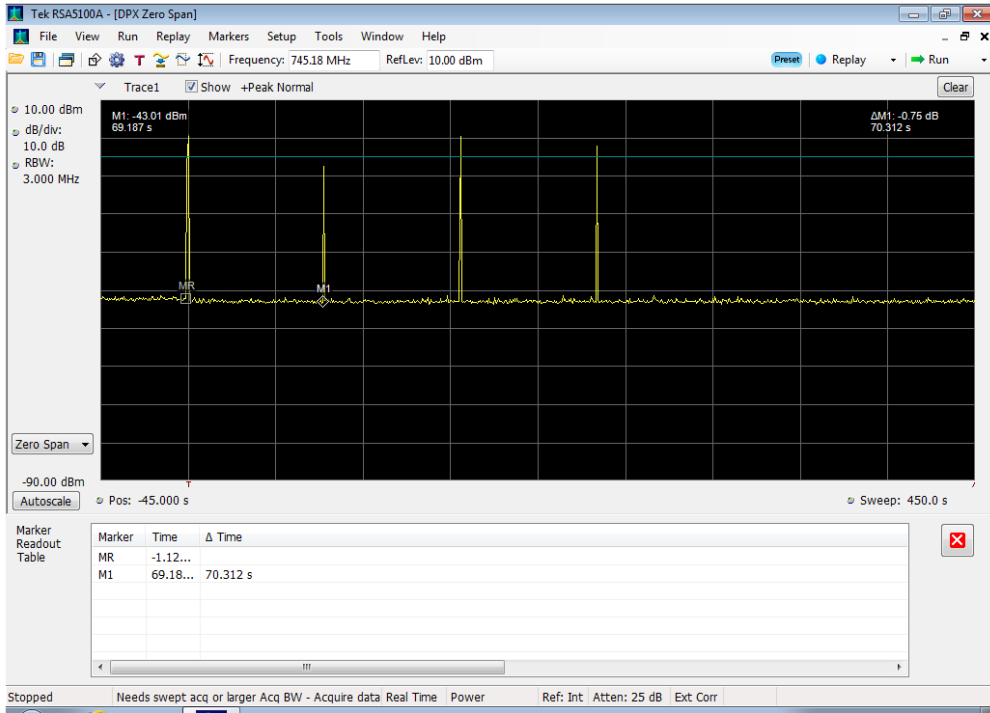
of Restarts_Downlink_2110 - 2155 MHz_p1570026



of Restarts_Downlink_728 - 746 MHz_p1570026



of Restarts_Downlink_746 - 757 MHz_p1570026



of Restarts_Downlink_869 - 894 MHz_p1570026

