Technical Description

The brief circuit description is listed as below:

1) U1 acts as 2.4GHz Bluetooth Transceiver.
2) U2 and associated circuit act as triaxial acceleration sensor
3) U3, U4, Q1 and associated circuit act as power control and charging circuit.
4) LED1~LED8 act as LEDs.
5) S1 act as key button.
6) Y1 acts as Crystal Oscillator of 16MHz for U1 RF Bluetooth transceiver.

Antenna Type: Internal integral antenna
Antenna Gain: 0dBi
Nominal rated field strength: 101.4dBμV/m at 3m
Maximum allowed field strength of production tolerance: +/- 3dB

nRF51822

Multiprotocol *Bluetooth*® 4.0 low energy/2.4 GHz RF SoC

Product Specification v1.2

Key Features

- 2.4 GHz transceiver
  - -93 dBm sensitivity in *Bluetooth*® low energy mode
  - 250 kbps, 1 Mbps, 2 Mbps supported data rates
  - TX Power -20 to +4 dBm in 4 dB steps
  - TX Power -30 dBm Whisper mode
  - 13 mA peak RX, 10.5 mA peak TX (0 dBm)
  - RSSI (1 dB resolution)
- ARM® Cortex™-M0 32 bit processor
  - 275 µA/MHz running from flash memory
  - 150 µA/MHz running from RAM
  - Serial Wire Debug (SWD)
- S100 series SoftDevice ready
- Memory
  - 256 kB or128 kB embedded flash program memory
  - 16 kB RAM
- Support for non-concurrent multiprotocol operation
  - On-air compatibility with nRF24L series
- Flexible Power Management
  - Supply voltage range 1.8 V to 3.6 V
  - 2.5 µs wake-up using 16 MHz RCOSC
  - 0.4 µA @ 3 V OFF mode
  - 0.5 µA @ 3 V in OFF mode + 1 region RAM retention
  - 2.3 µA @ 3 V ON mode, all blocks IDLE
- 8/9/10 bit ADC - 8 configurable channels
- 31 General Purpose I/O Pins
- One 32 bit and two 16 bit timers with counter mode
- SPI Master
- Two-wire Master (I2C compatible)
- UART (CTS/RTS)
- CPU independent Programmable Peripheral Interconnect (PPI)
- Quadrature Decoder (QDEC)
- AES HW encryption
- Real Timer Counter (RTC)
- Package variants
  - QFN48 package, 6 x 6 mm
  - WLCSP package, 3.50 x 3.83 mm

Applications

- Computer peripherals and I/O devices
  - Mouse
  - Keyboard
  - Multi-touch trackpad
- Interactive entertainment devices
  - Remote control
  - 3D Glasses
  - Gaming controller
- Personal Area Networks
  - Health/fitness sensor and monitor devices
  - Medical devices
  - Key-fobs + wrist watch
- Remote control toys

1 Introduction

nRF51822 is an ultra-low power 2.4 GHz wireless System on Chip (SoC) integrating the nRF51 series 2.4 GHz transceiver, a 32 bit ARM® Cortex™-M0 CPU, flash memory, and analog and digital peripherals. nRF51822 can support *Bluetooth*® low energy and a range of proprietary 2.4 GHz protocols, such as Gazell from Nordic Semiconductor.

Fully qualified *Bluetooth* low energy stacks for nRF51822 are implemented in the S100 series of SoftDevices. The S100 series of SoftDevices are available for free and can be downloaded and installed on nRF51822 independent of your own application code.

nRF51822 is available in different package and memory variants. When data in this product specification does not apply to all variants, those variants it does apply to will be clearly stated. An example of a variant name is nRF51822-QFAA. If no variant name is stated, or if just nRF51822 is used, the data will apply to all versions of nRF51822.

1.1 Required reading

The *nRF51 Reference Manual* is required reading.

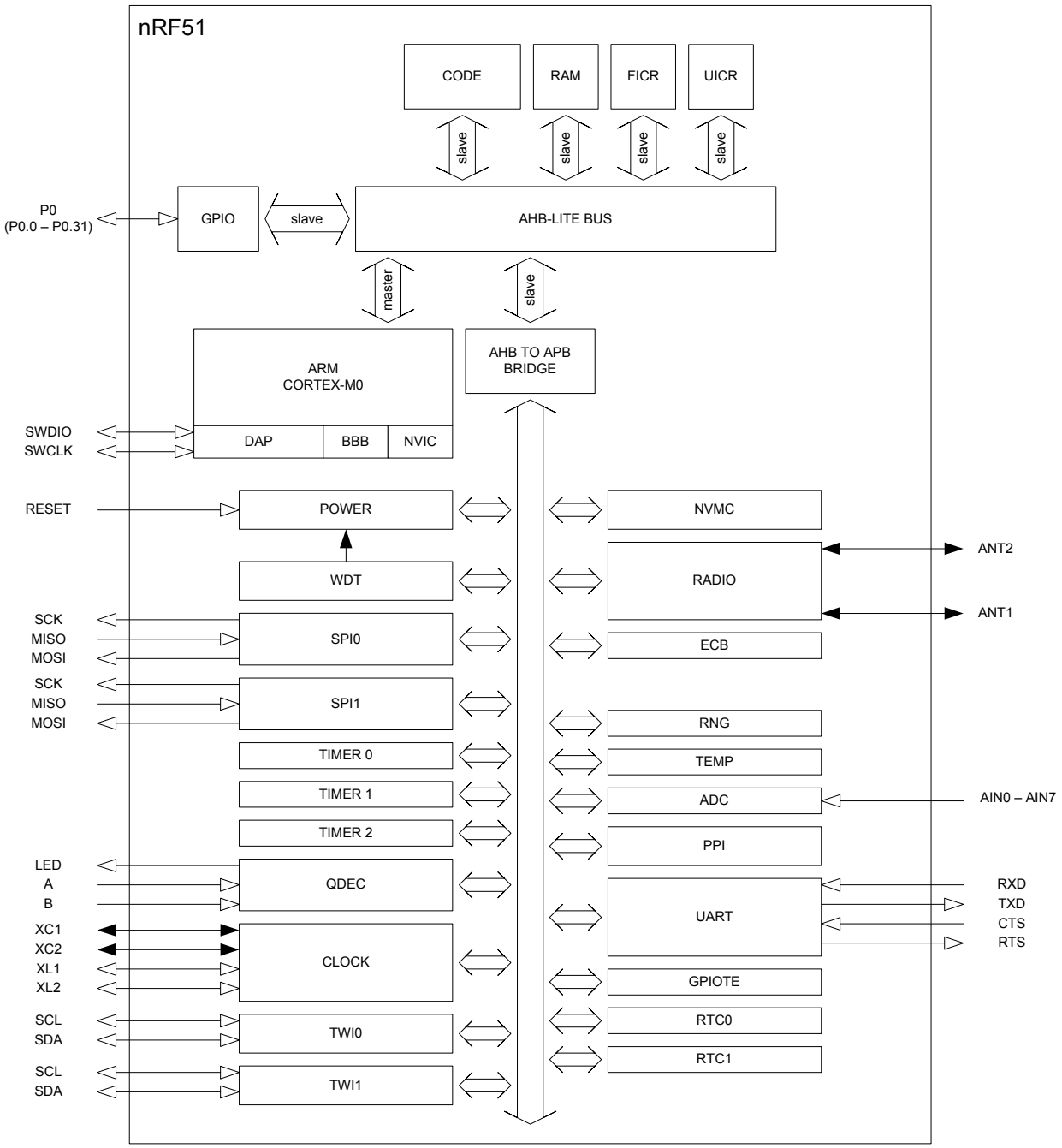
1.2 Writing conventions

This product specification follows a set of typographic rules to ensure that the document is consistent and easy to read. The following writing conventions are used:

- Command, event names, and bit state conditions, are written in `Lucida Console`.
- Pin names and pin signal conditions are written in **`Consolas.`**
- File names and User Interface components are written in **bold**.
- Internal cross references are italicized and written in ***semi-bold.***
- Placeholders for parameters are written in italic regular text font. For example, a syntax description of Connect will be written as:
  Connect*(TimeOut, AdvInterval)*.
- Fixed parameters are written in regular text font. For example, a syntax description of Connect will be written as:
  Connect(0x00F0, Interval).

2 Product overview

2.1 Block diagram



Note: RESET is disabled by default.

Figure 1 *nRF51822 block diagram*