

---

Main chip part:

The chip U28, MP1482 supplies the board main voltage +3.3V. +3.3V generate +2.5V by an audion Q15, +1.2V by a MOS tube U14. These three voltage driver the main chip BCM6368. A 64MHz crystal oscillator provides the clock to the main chip. The main chip provides a 133MHz clock with 2 pieces DDR SDRAM and provides a 33MHz clock with the flash chip U9.

Wireless part:

The BCM43222 wireless chip required 2 voltages. One is the board main voltage +3.3V, the other is a +1.2V alone generated by MP1482 U24. A 20MHz crystal oscillator provides the clock to the wireless chip. The wireless chip provides an 802.11b/g/n wireless solution. The maximum support rate is 270Mbps. The wireless chip and the main chip connection via MPI bus, it is a 33MHz clock bus.

DSL part:

The AFE chip BCM6302 is driven by +2.5V and +7.7V voltages. It has electronically switches between ADSL2+ and VDSL. The maximum support rate is 100Mbps/50Mbps.

100M eth port:

BCM6368 support 4 100M Ethernet port.

1000M eth port:

The BCM5481 chip required +3.3V and +1.2V voltage. A 25M crystal oscillator provides the clock to the 1000M eth chip. The chip and the main chip connection via GMII bus, it is a 125MHz clock bus.

The EUT supporting IEEE 802.11b/g, draft 802.11n and incorporates a MIMO function complies with the IEEE draft 802.11n specification. Physically, The EUT has two antennas. Both antenna ANTO and ANT1 can be used as transmitting antenna. Antenna 0,1 are used as receiving antenna also.