Main chip part:

The chip U1, supplies the board main voltage +1.2V and U6. The chip U8, DC/DC MPS1482 supplies the board main voltage +3.3V. +3.3V generate +2.5V by an audion Q1, +2.5V These three voltage driver the main chip U1. A 20MHz crystal oscillator provides the clock to the main chip. The main chip provides a 132MHz clock with a DDRAM and provides a 66MHz clock with the flash chip U3.

wlan part:

The chip U13 , The wlan is BCM4313 driver by $\pm 3.3v$ and 1.2v.

DSL part:

The AFE chip BCM6301 is driven by +5V voltages. The maximum support rate is 24Mbps/1Mbps.

100M eth port: