

Main chip part:

The chip U8/U24, MP1482 supplies the board main voltage +3.3V and +1.2V. +3.3V generate +2.5V by an audion Q1, +1.8V by a LDO U11. These three voltage driver the main chip BCM6332. A 64MHz crystal oscillator provides the clock to the main chip. The main chip provides a 120MHz clock with a SDRAM and provides a 33MHz clock with the flash chip U9.

DSL part:

The AFE chip BCM6301 is driven by +5V voltages. The maximum support rate is 24Mbps/1Mbps.

100M eth port:

BCM5325 support 4 10M/100M Ethernet port. A 25M crystal oscillator provides the clock to the 100M eth chip. The chip and the main chip connection via PHY to PHY bus, it is a 125MHz clock bus.