



EQUIPMENT FCC ID : RPMUSPICC
Exhibit 6
BLOCK DIAGRAM AND SCHEMATICS

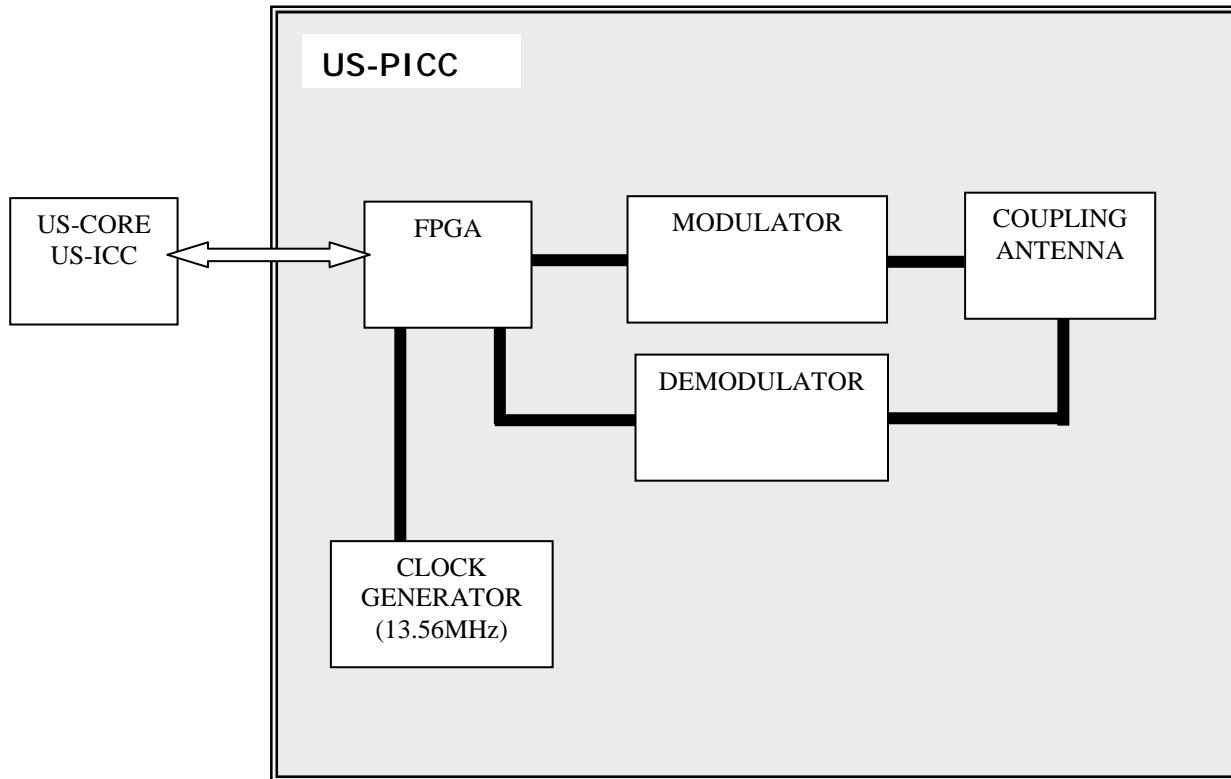
Page : 1/11

Written by : D. RAUD

November 4, 2003

Test report # : 195001DK

BLOCK DIAGRAM





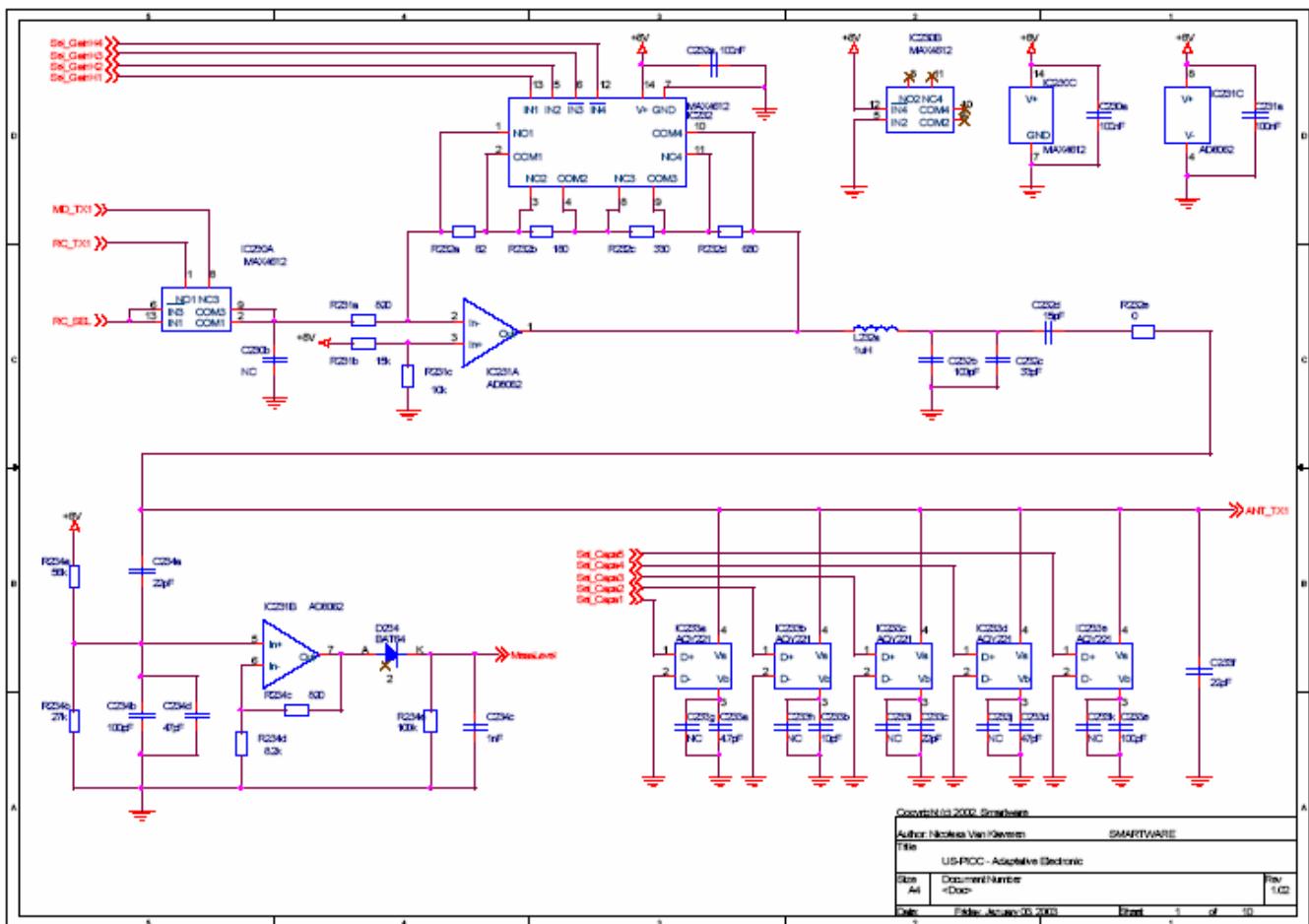
EQUIPMENT FCC ID : RPMUSPICC
Exhibit 6
BLOCK DIAGRAM AND SCHEMATICS

Page : 2/11

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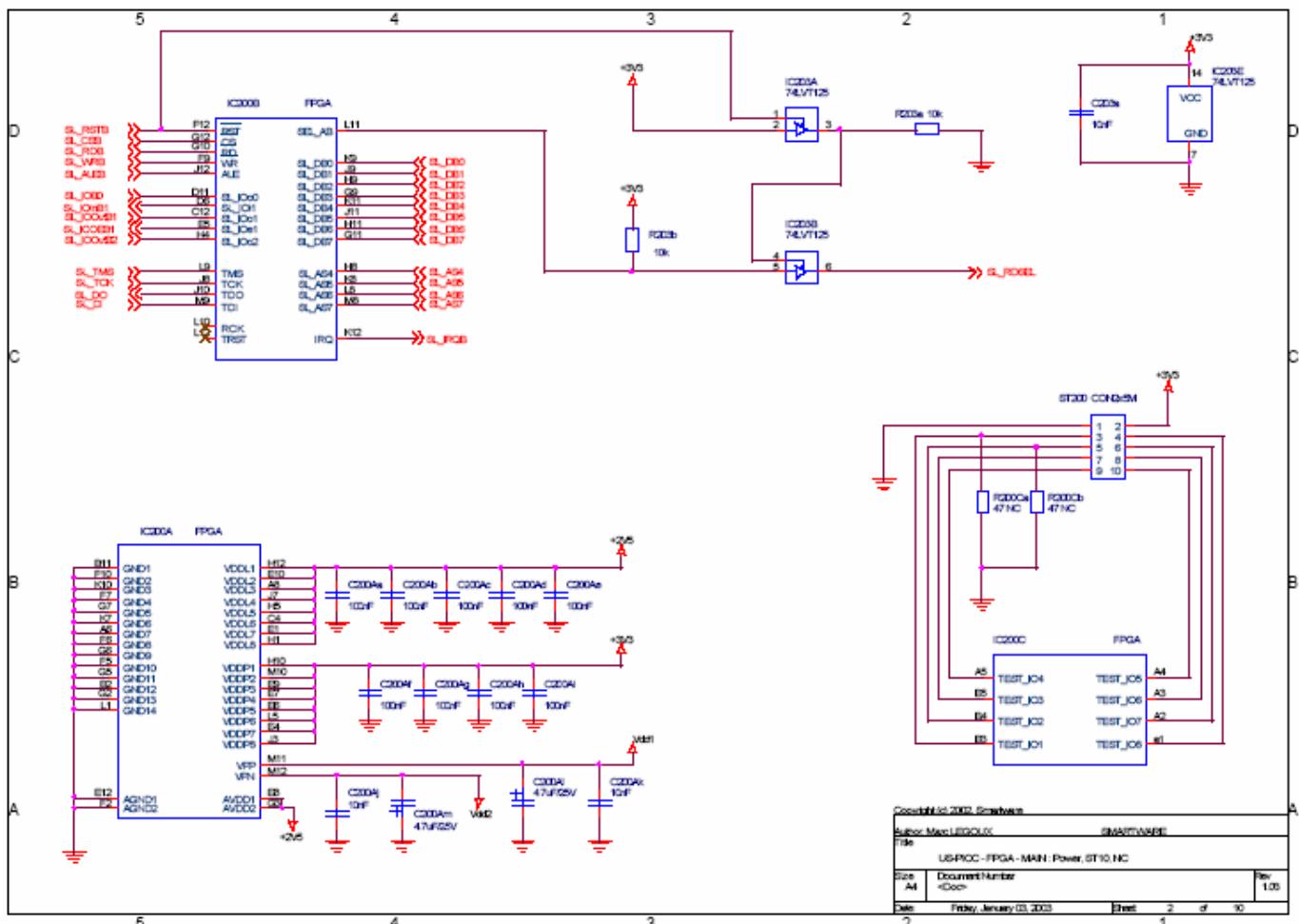
EQUIPMENT FCC ID : RPMUSPICC
Exhibit 6
BLOCK DIAGRAM AND SCHEMATICS

Page : 3/11

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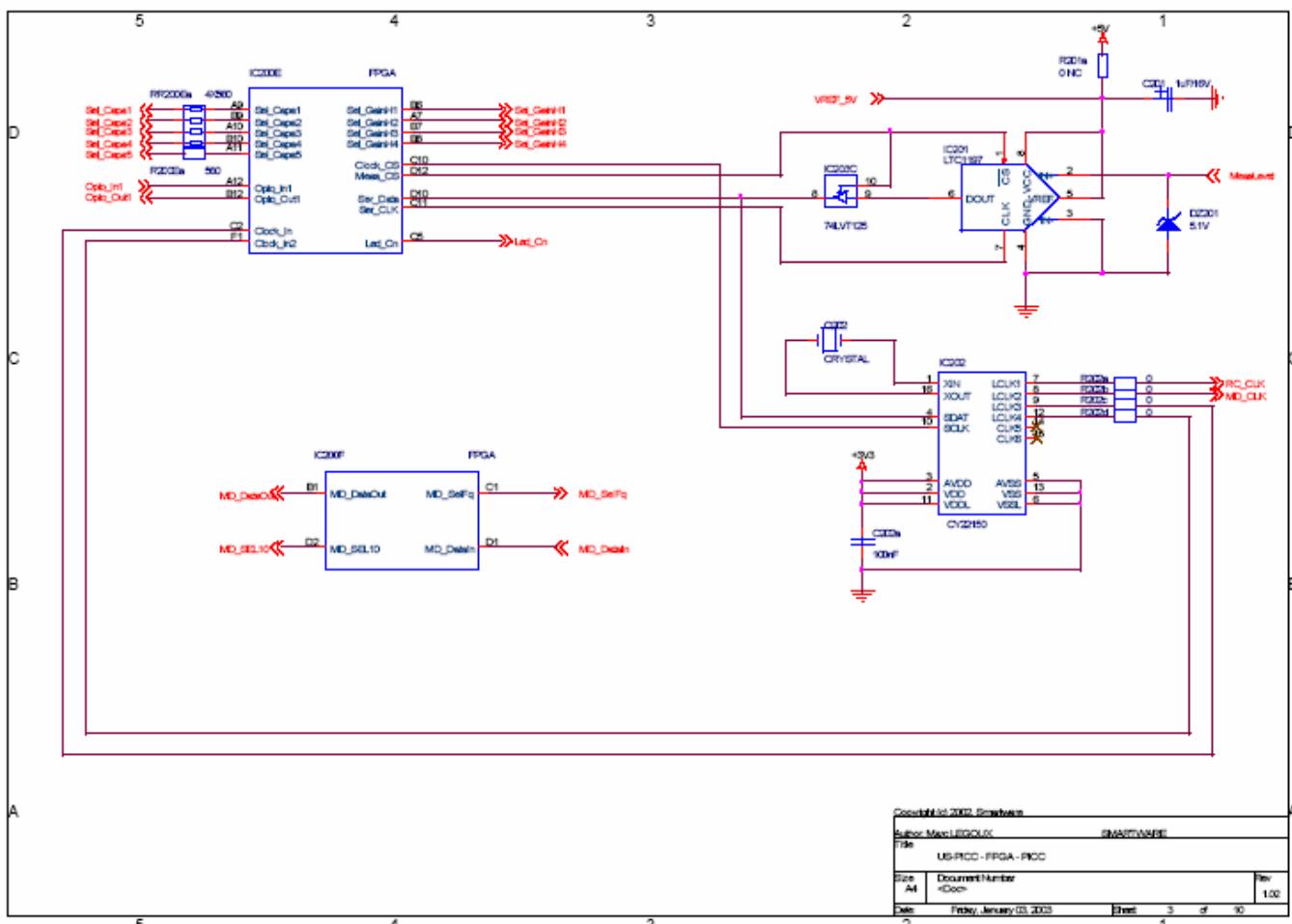
EQUIPMENT FCC ID : RPMUSPICC
Exhibit 6
BLOCK DIAGRAM AND SCHEMATICS

Page : 4/11

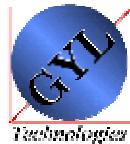
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Title	US-PICC - FPGA - PICC
Size	A4
Document Number	<Doc>
Date	Friday, January 03, 2003
Page	1 of 90



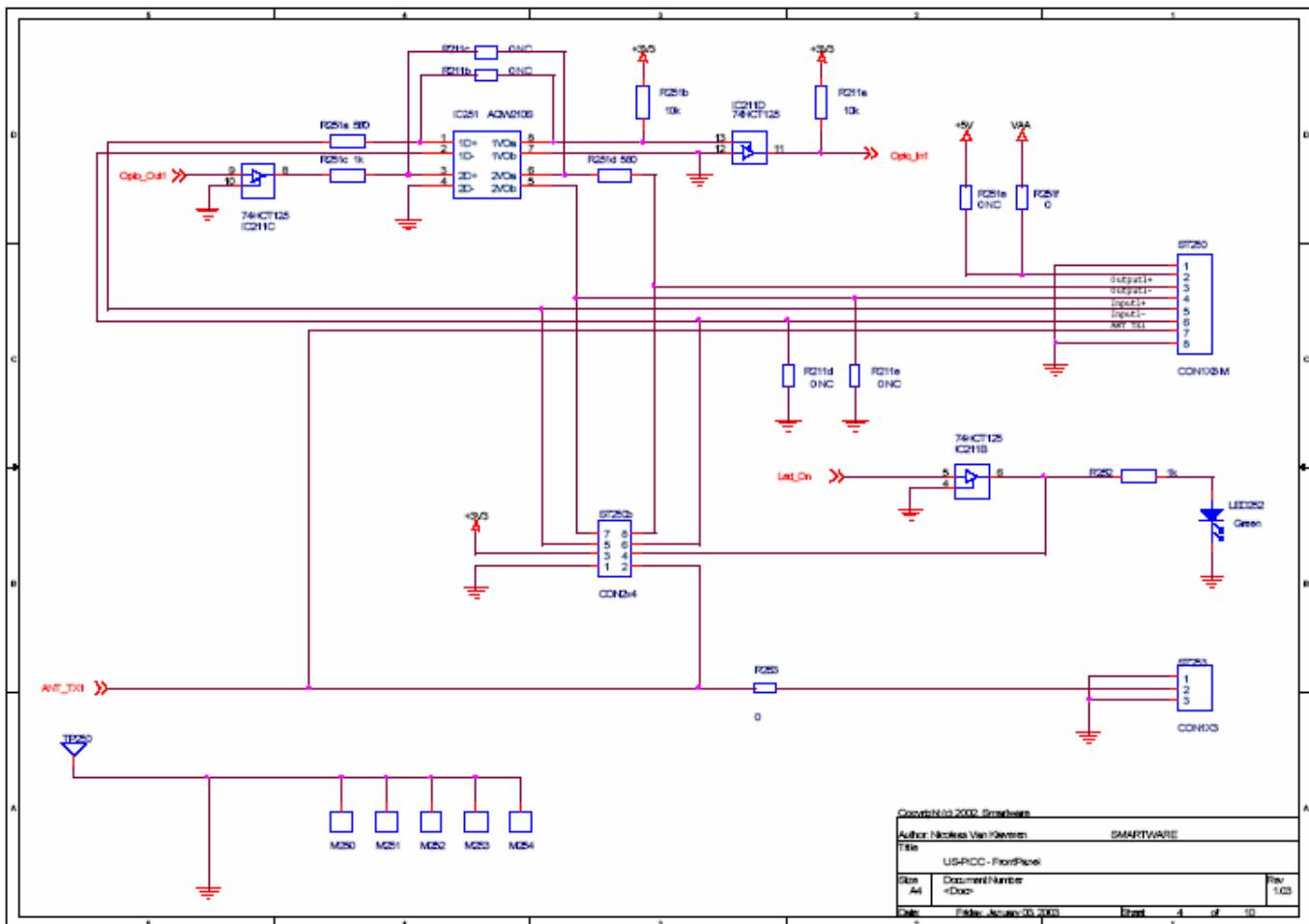
EQUIPMENT FCC ID : RPMUSPICC
Exhibit 6
BLOCK DIAGRAM AND SCHEMATICS

Page : 5/11

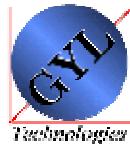
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Date:	Friday, October 10, 2003
Page:	1/10



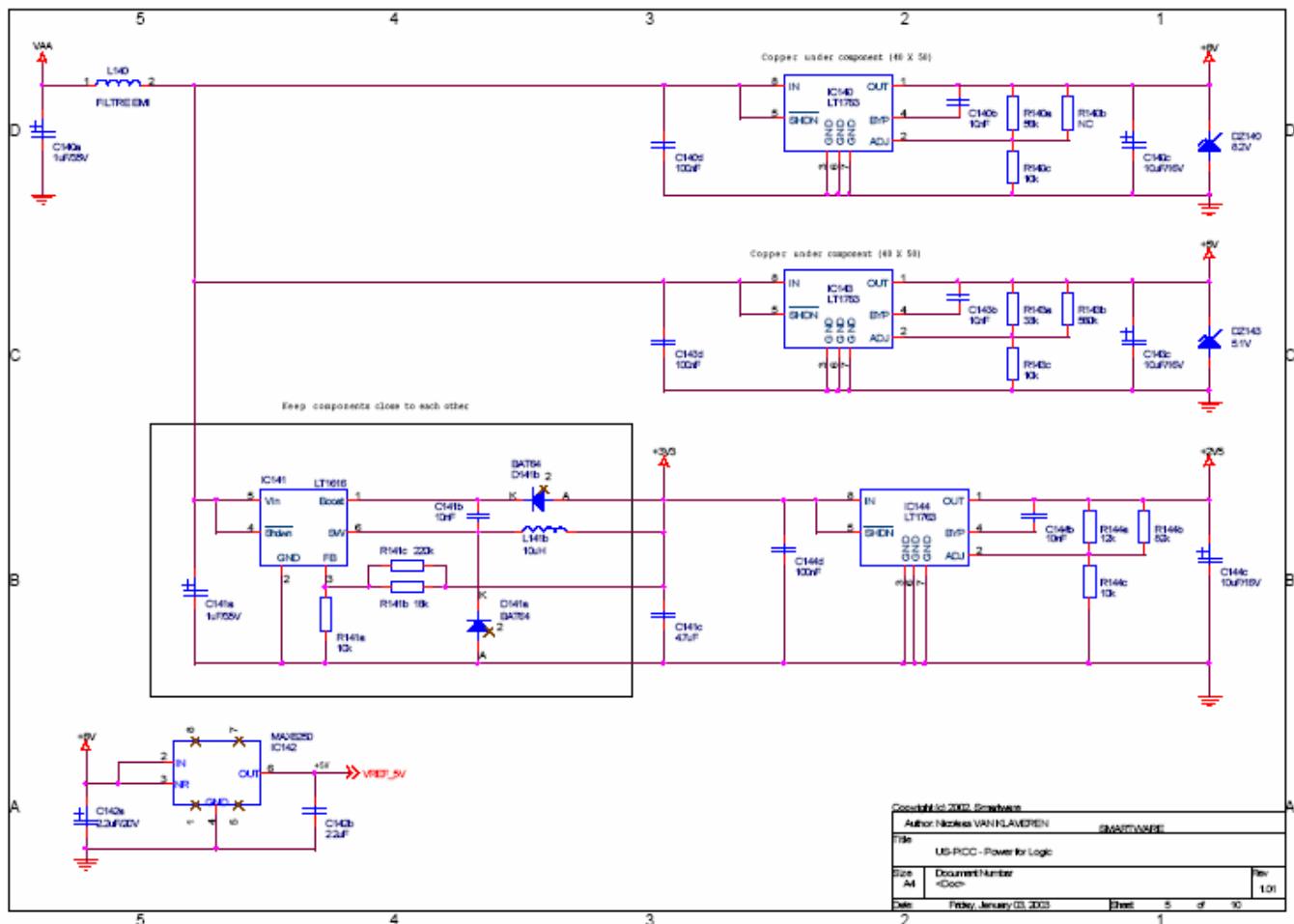
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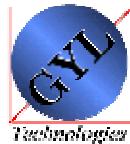
Page : 6/11

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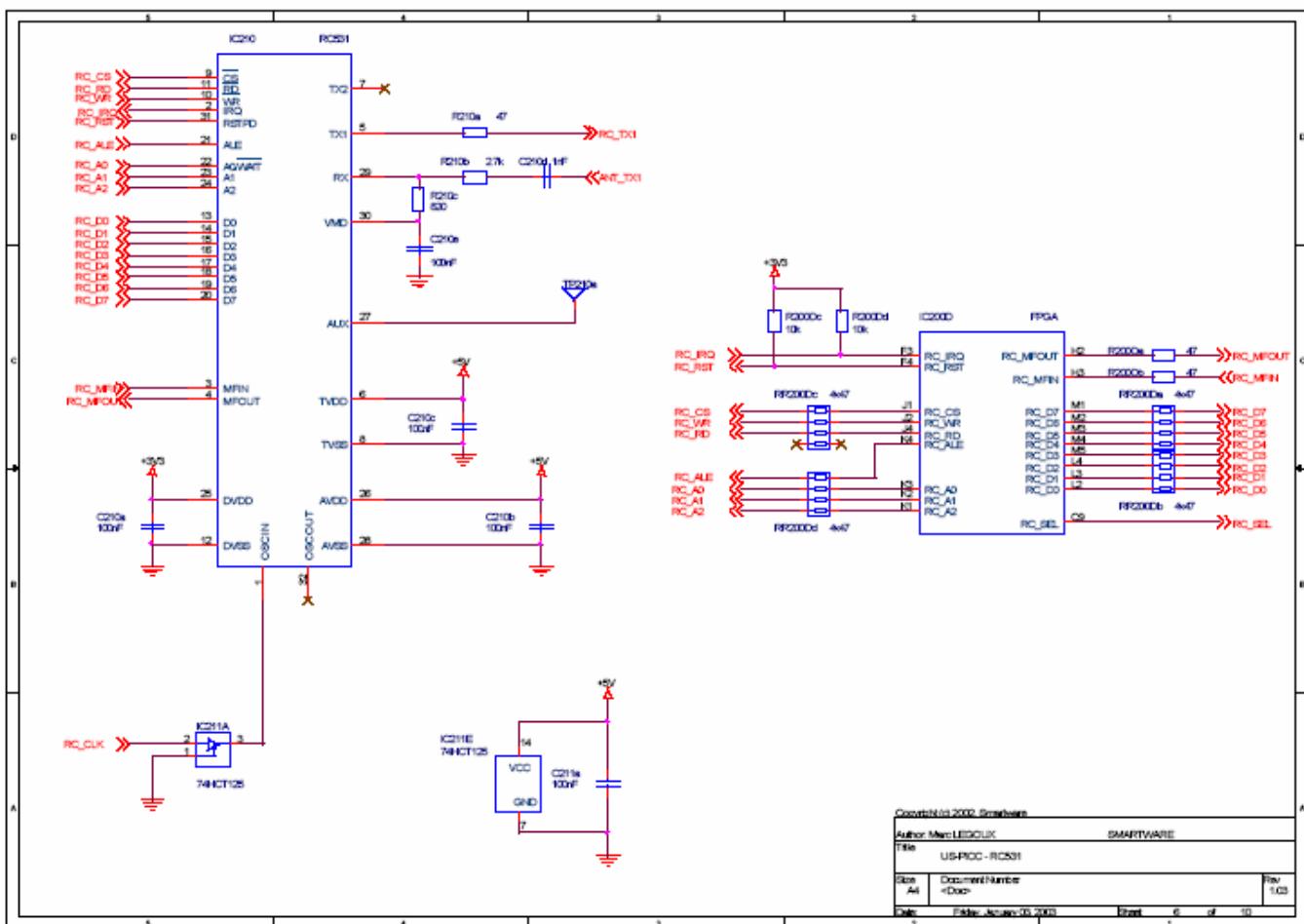
EQUIPMENT FCC ID : RPMUSPICC
Exhibit 6
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Page : 7/11

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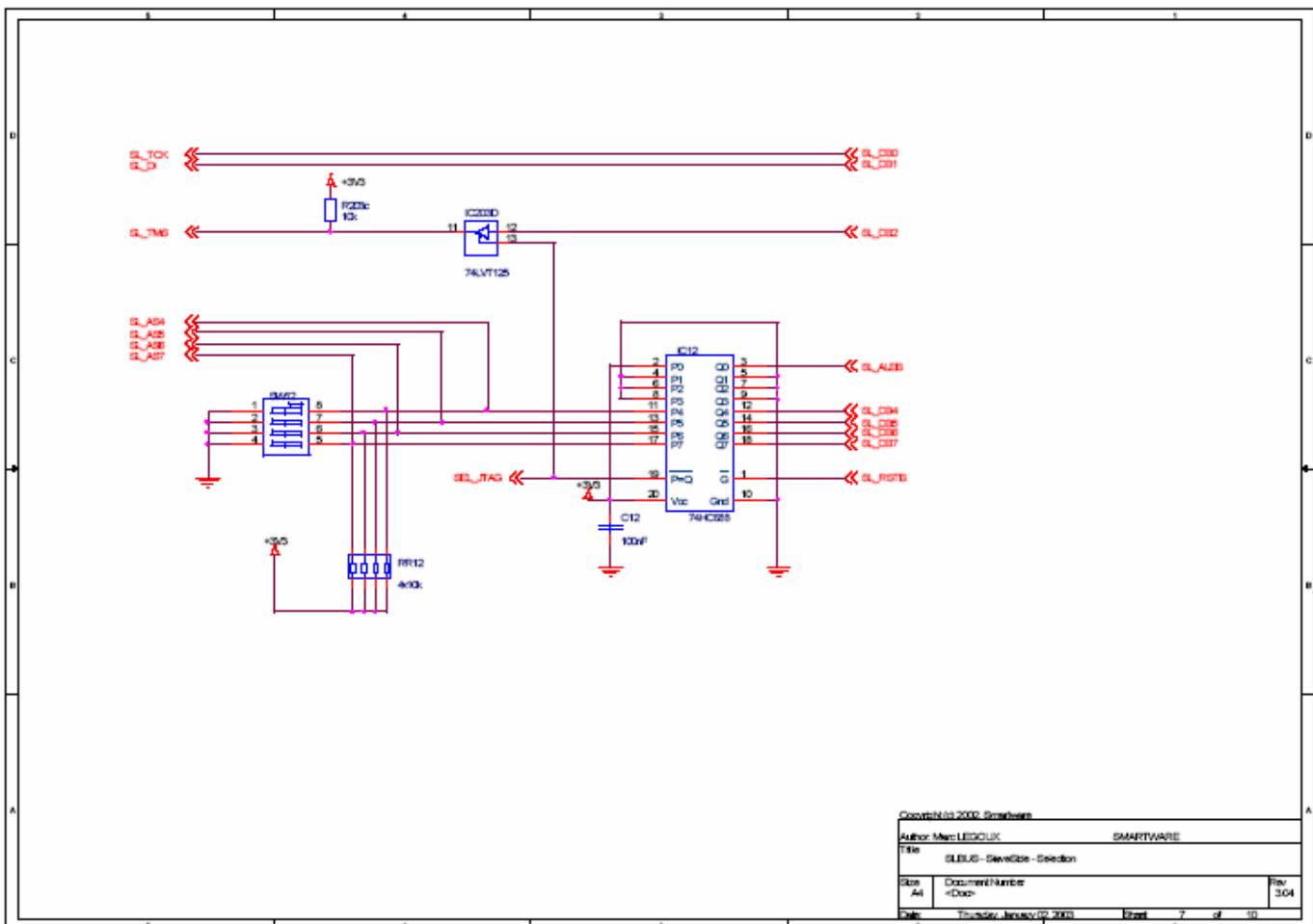
EQUIPMENT FCC ID : RPMUSPICC
Exhibit 6
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Page : 8/11

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Test report # : 195001DK





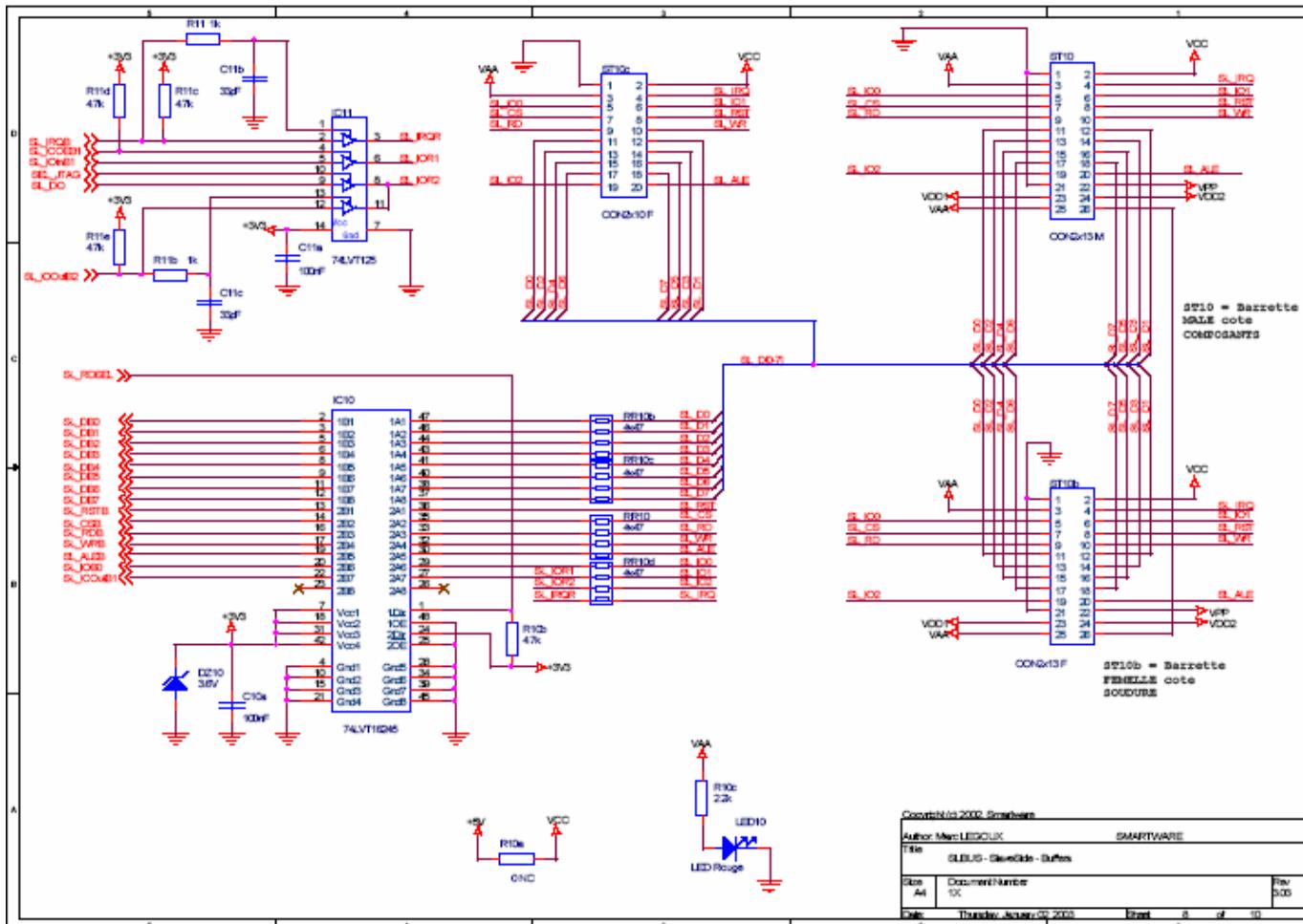
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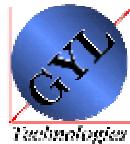
Page : 9/11

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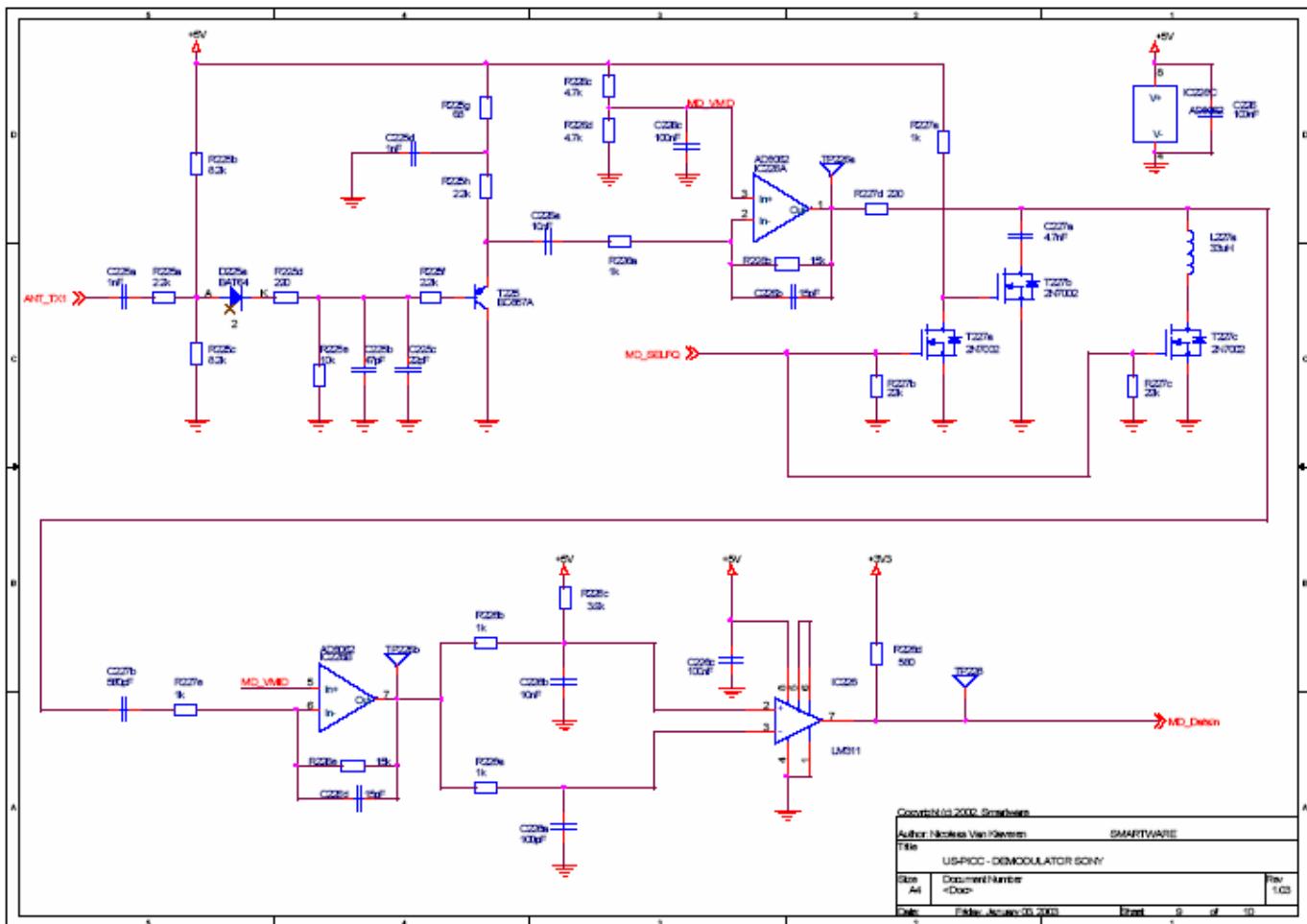
EQUIPMENT FCC ID : RPMUSPICC
Exhibit 6
BLOCK DIAGRAM AND SCHEMATICS

Page : 10/11

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Test report # : 195001DK





EQUIPMENT FCC ID : RPMUSPICC
Exhibit 6
BLOCK DIAGRAM AND SCHEMATICS

Page : 11/11

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