



LCIE SUD EST
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INTERNAL PHOTOS OF EQUIPMENT

FCCID: RPM-PSTSTAT2

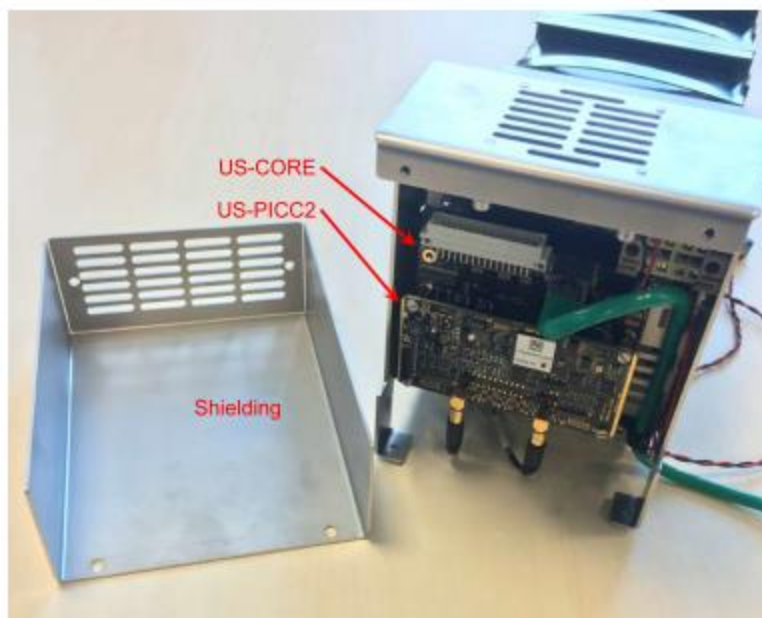


Fig. 7: US-PICC2 and US-CORE assembly with opened shielding.

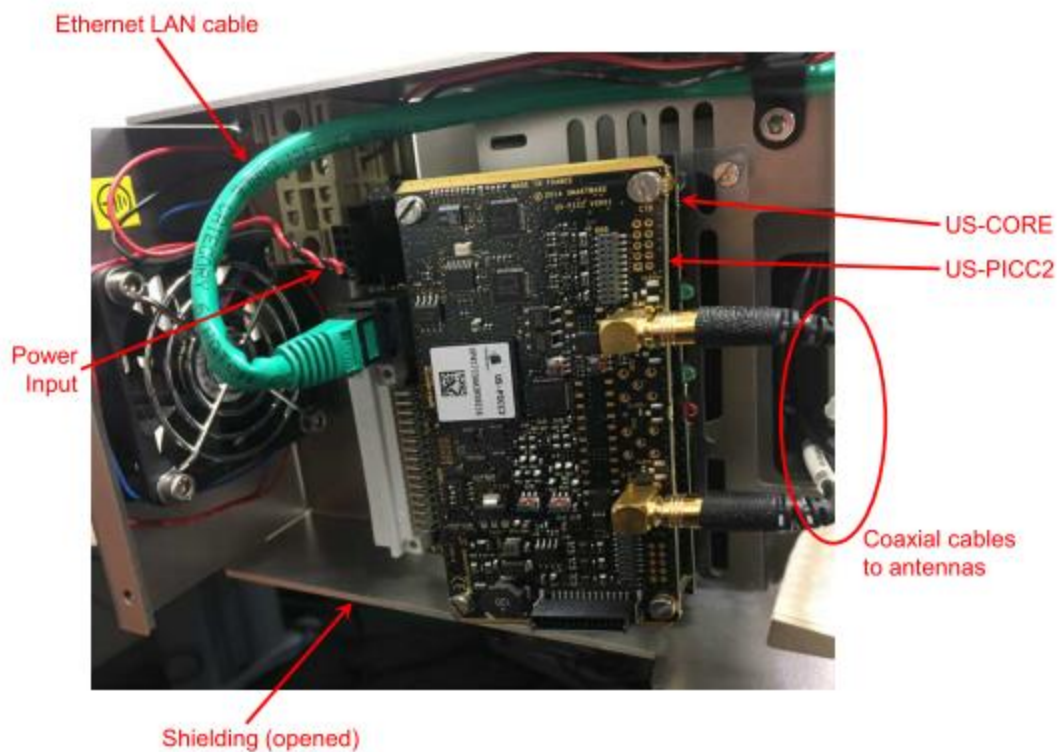


Fig. 8: US-PICC2 and US-CORE assembly close view.

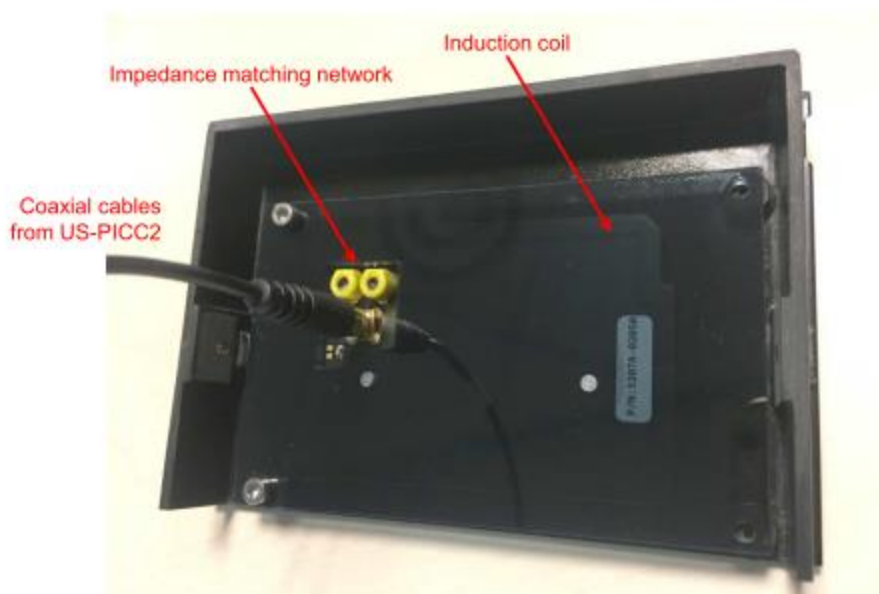


Fig. 9: Antenna (type type 5207A-02050) close view.

5.1 DESCRIPTION OF THE US-CORE BOARD

The US-CORE board basically includes a CPU running the proprietary software controlling the radio interfaces.

Figures 11 and 12 show the US-CORE board.

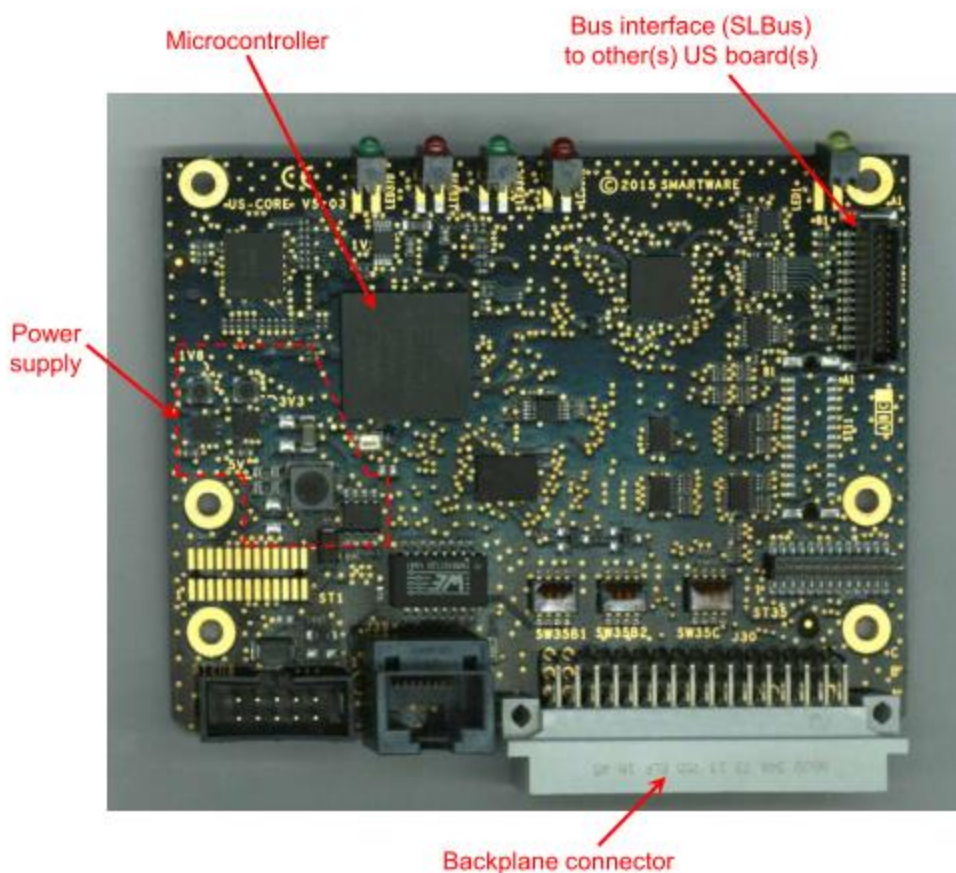


Fig. 11: US-CORE board (top view).

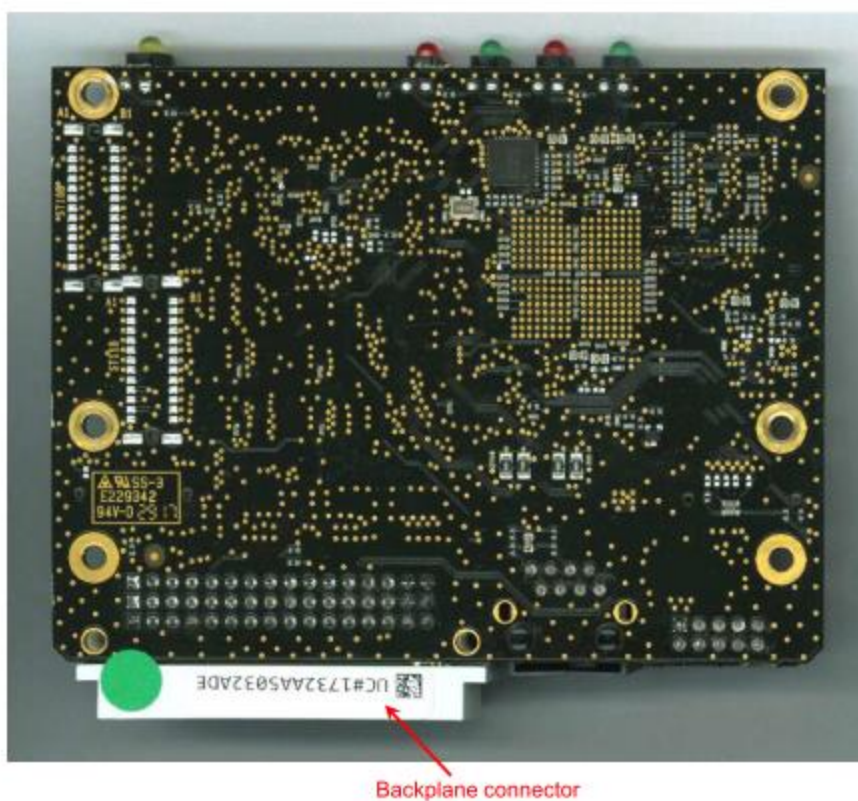


Fig. 12: US-CORE board (bottom view).

5.2 DESCRIPTION OF THE US-PICC2 BOARD

The US-PICC2 comprises two radio (contactless) interfaces for bidirectional RFID communication at 13.56 MHz carrier frequency.

Figures 13 and 14 show the US-PICC2 board. The main sections are depicted.

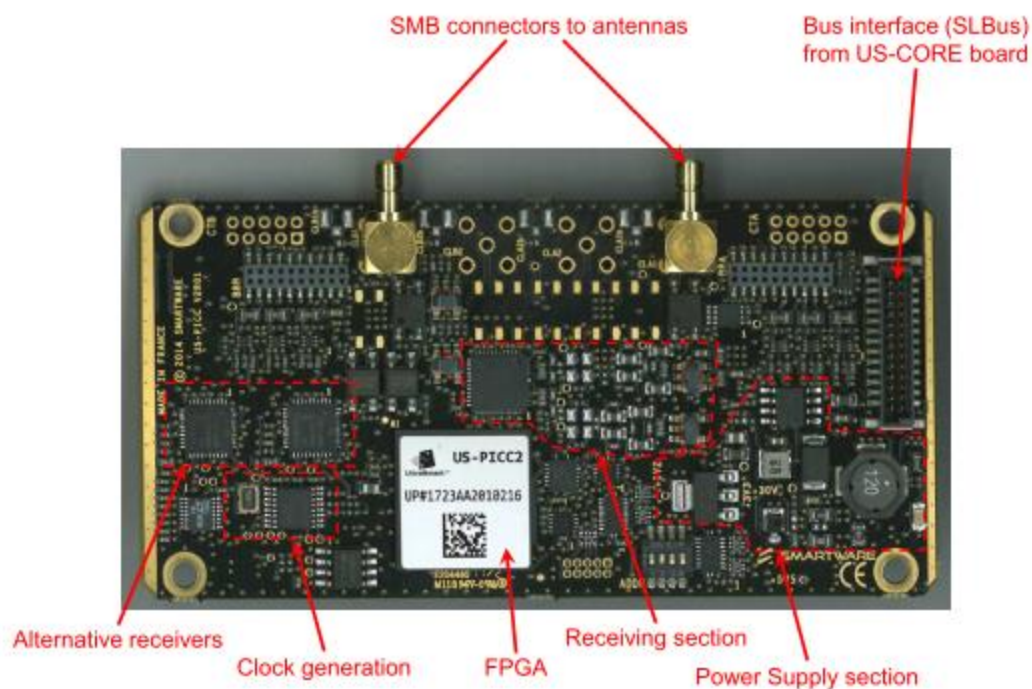


Fig. 13: US-PICC2 board, top view.

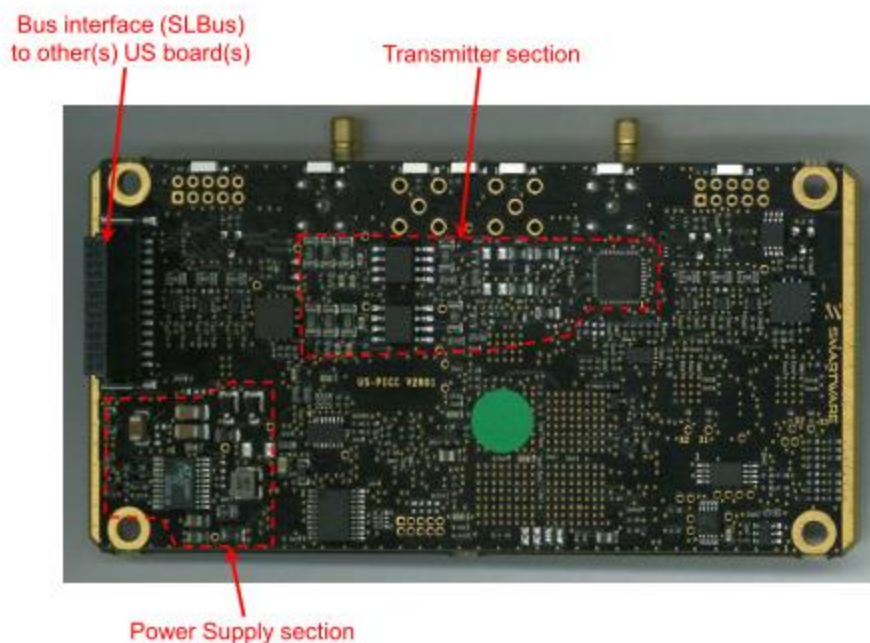


Fig. 14: US-PICC2 board, bottom view.

5.3 DESCRIPTION OF ANTENNAS BOARD

The antenna board contains:

- a printed coil
- a tuning circuit
- a printed shield (grounded)
- an SMB socket for connection to the US-PICC board.

Figure 9 shows an assembled antenna. Figure 15 depicts the PCB.



Fig. 15: antenna PCB, photograph (top) and CAD drawing (bottom), top side (left) and bottom side (right).