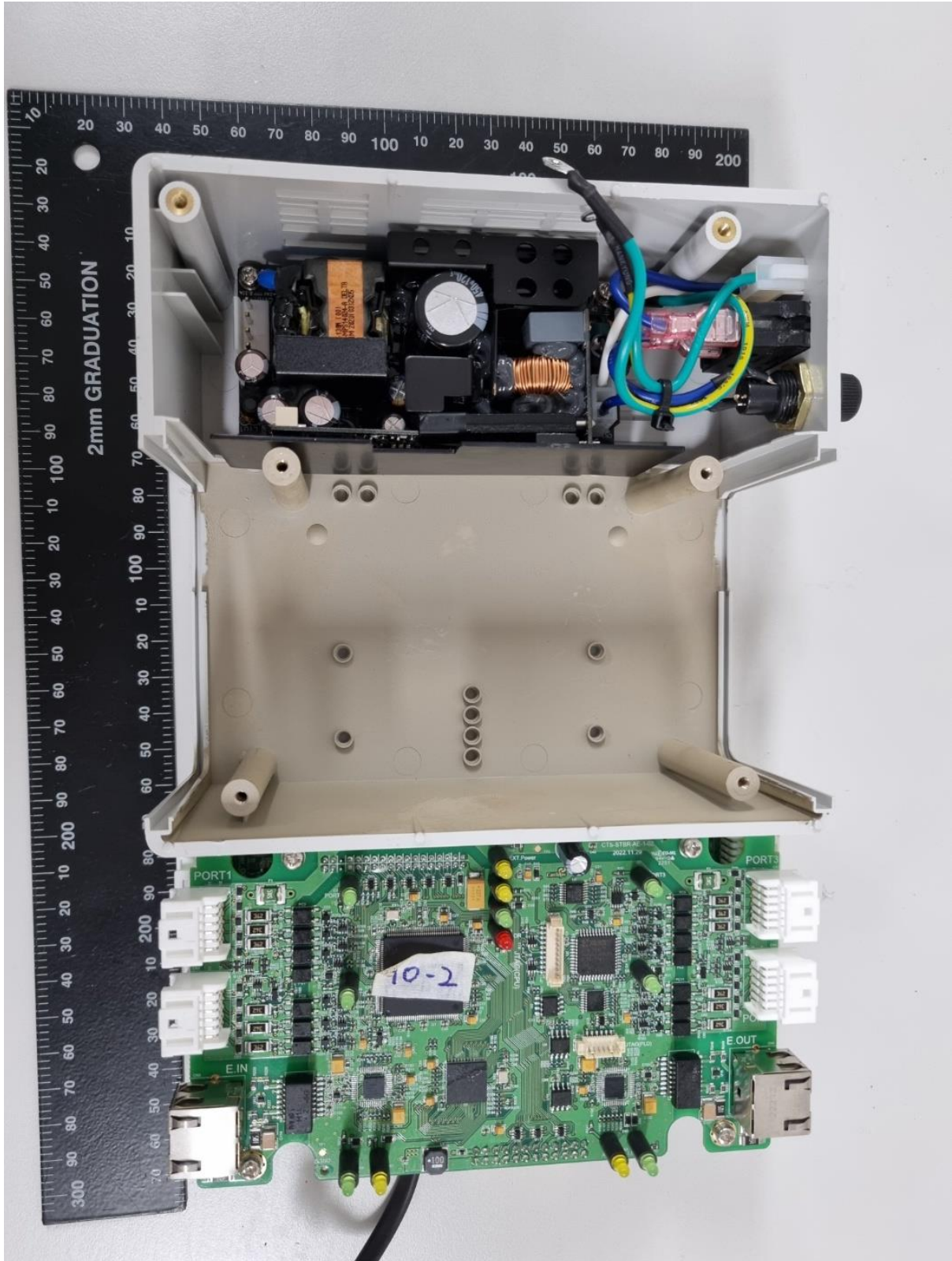
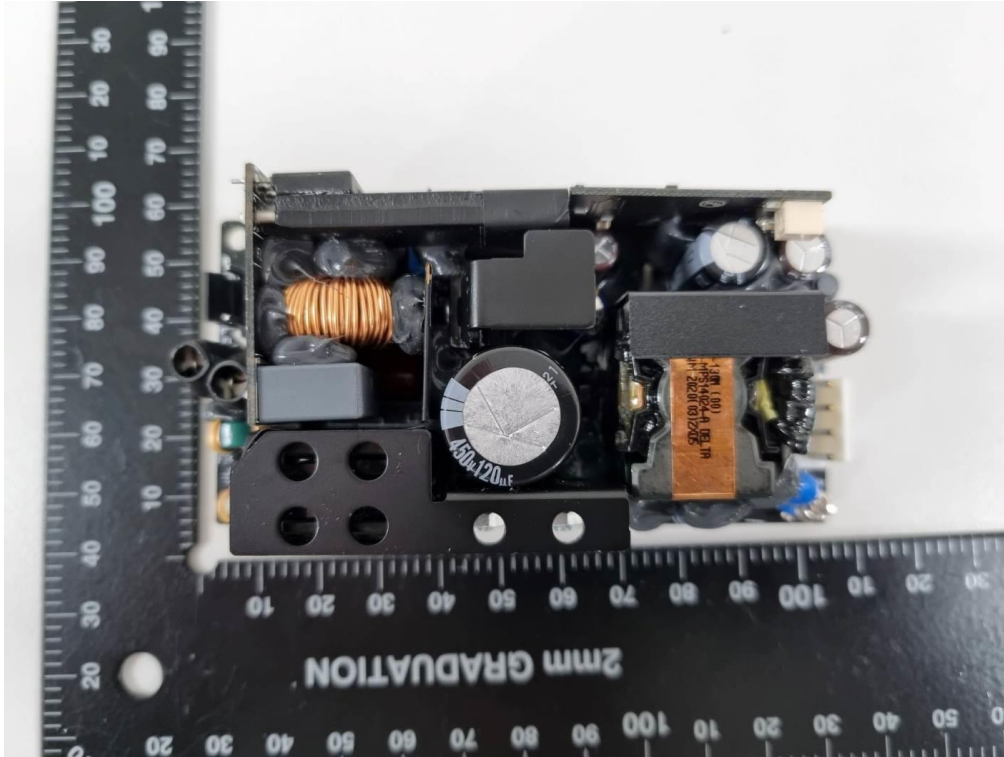


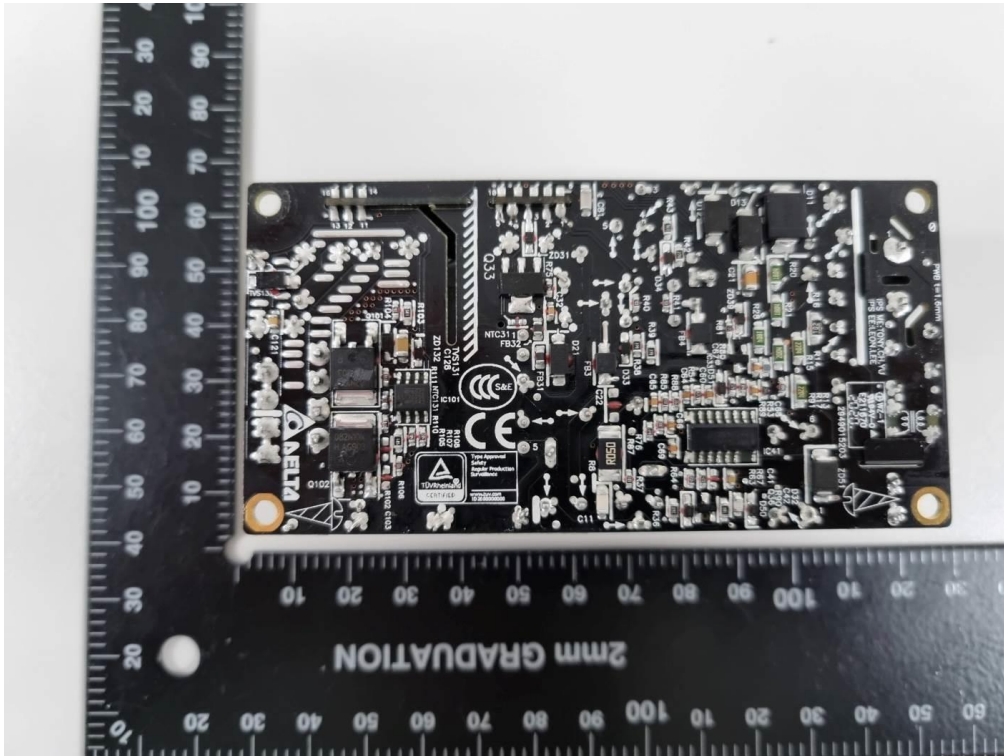
Internal of sample



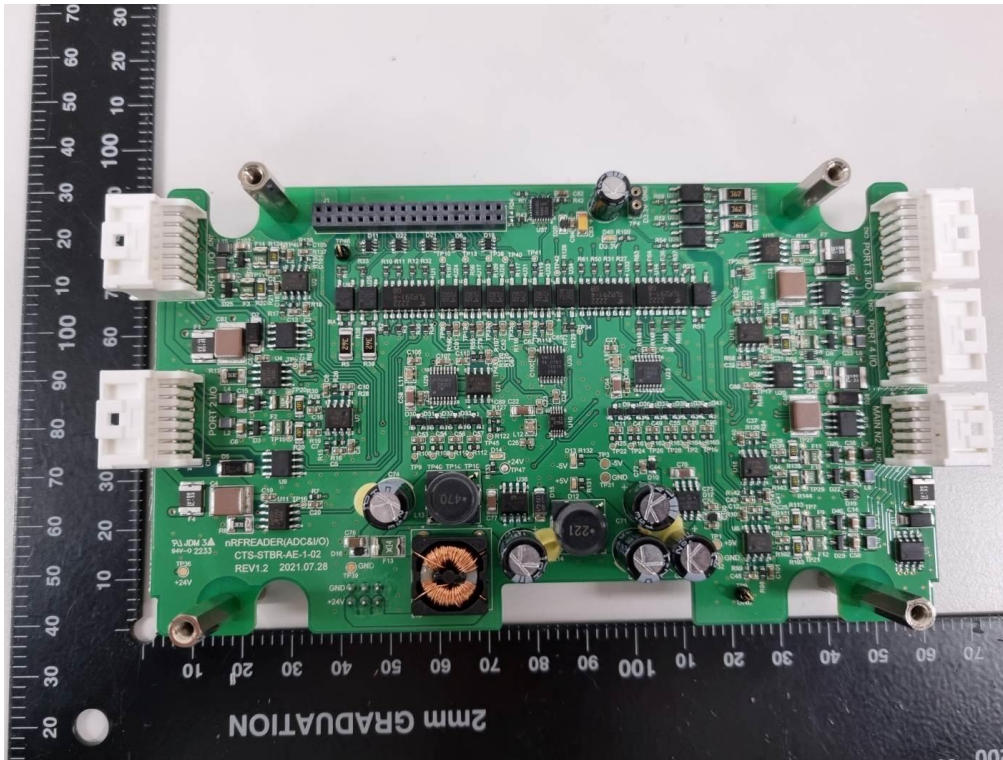
**Main Board(Power, Front)**



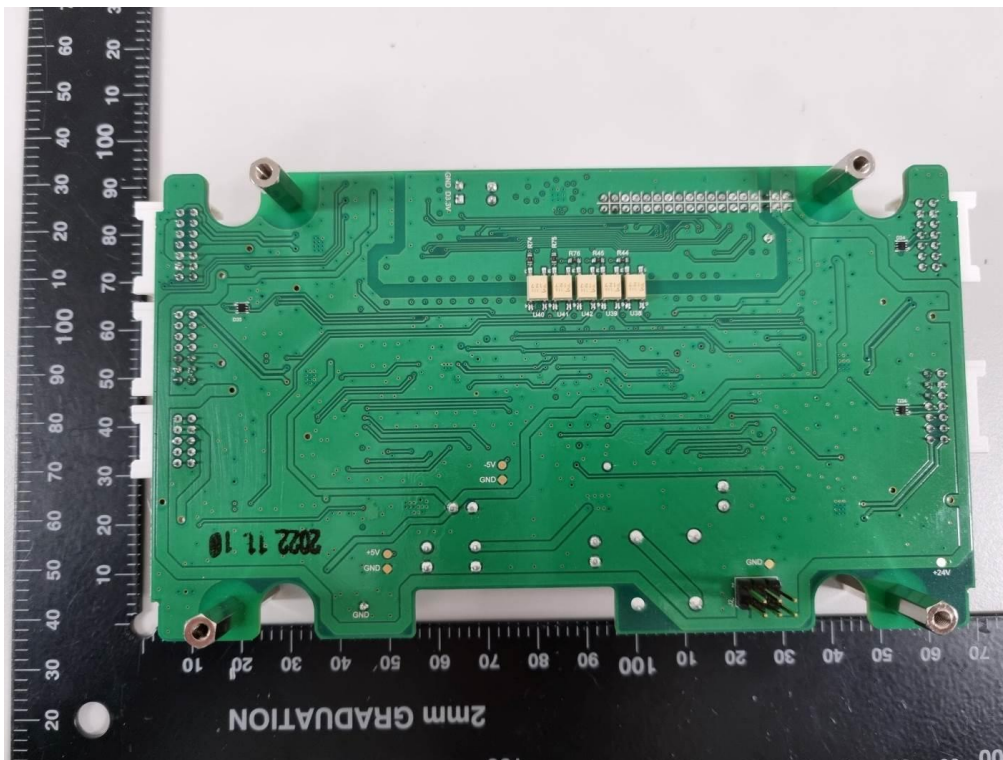
**Main Board (Power,Rear)**



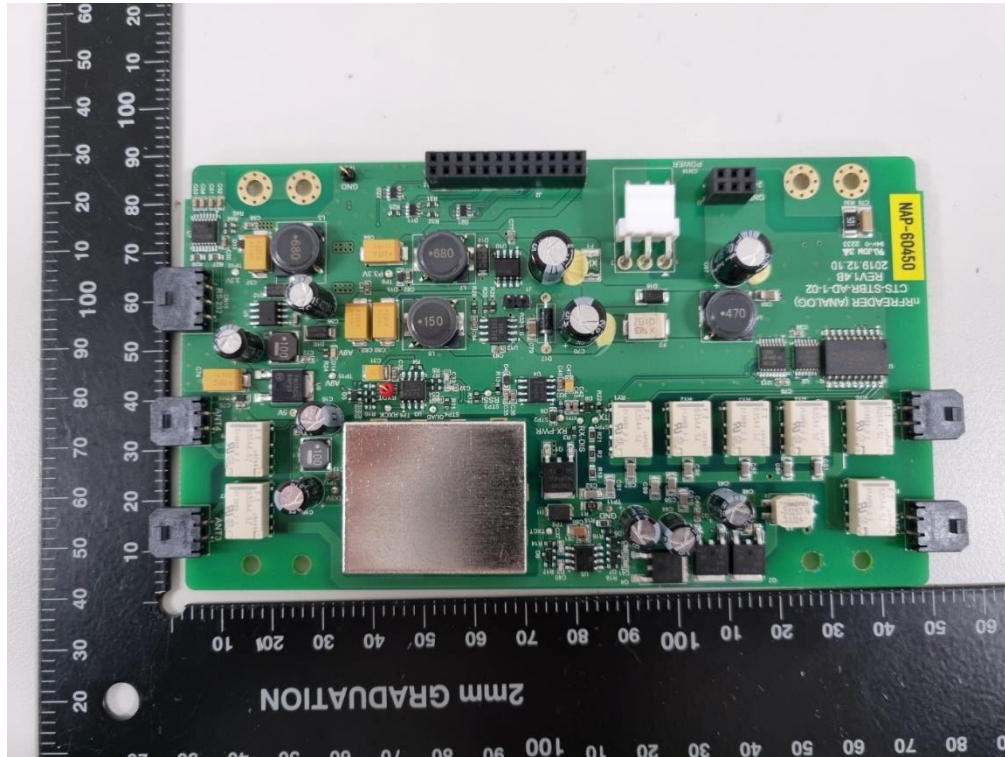
Main Board(N2IO, Front)



Main Board (N2IO ,Rear)



**Main Board(RF, Front)**



**Main Board(RF, Rear)**

