

## Change Request (post EI)

Function	N°	Prototype test	Problem description	Change
<b>Power supply</b>		YES	Change the 3v3 power supply chip cheaper	change with SPX3819
		YES	Change the 1v2 with DCDC(4-8v)	Change with Ricoh R1224
		YES	Load stops with low frequency audio outputs	turn down the Step Up voltage from 14.5v to 13 v (substitute R168 by 11kohms).
<b>Voltmeter</b>		YES	R63 is 0603 on schematic and 0402 in Gerber	put R63 en 0402 on schematic
		YES	modify the minimum measure level : R 120k à 150k	
		YES	Change the capacity to COG	
<b>Line IN</b>		YES	Enlarge solder paste printings	
<b>WK UP</b>		YES	A8 bus are looping on the WK_UP and can release it	Change R36 - R19 - R31 - R40 - R32 - R57 = 47k -> depend on precedent request
<b>Audio Amplificator</b>		YES	Reverse Left and Right Audio Outputs of the Amplificator	reverse the P5 outputs and not the Amplificator outputs
		YES	Filtering The Ampli output limited to 16k, set to 30k	The 4 capacitors at 4,7nF
		YES	Ampli Outputs disturbs EMC behaviour	
<b>BT</b>		YES	Q10 base do not have pull down	Put a Resistance of 47k
		YES	Design the Logic door	
		YES	Test R on TXD and RTS on BT to decrease leak current in Low Power mode	R193 et R194 = 0 -> 330 Ohm
<b>Battery</b>		YES	protect battery if reverse assembling	
<b>Wall Plug</b>		YES	Detect the battery presence	Soft change : if Voltage input is > 6v3 -> no battery
		YES	Step Down power supply to optimize	change in ST L6902D or SP6126
<b>Version Number</b>		YES	increase the n°	
<b>JTAG</b>		YES	JTAG debug must be 100% functional with the cable	Add a 10k between Q16 and PWRON_CLK and put the power supply control in Reset output(JTAG stay on MR/ of reset generator... plan the R_NU if need)
<b>Clarify the schematic</b>		YES	put off the external DAC	list of components off: External DAC : C137 - R150 - J6 - C126 - C164 - C136 - C169 - R151 - R155 - R159 - R160 Voltmeter : R61 components unused : D52
<b>Load</b>		YES	indication of load in OFF	
<b>QUARTZ</b>		YES	Make the printing compliant with the SMQ32SL	
<b>Other</b>		YES	Put off the Lead free solder masking agent on the Ground ring	
<b>After Prototype reception</b>		YES	UART bug	R591 = 1k - 0402
		YES	Power supply of wall plug adjust	L2 = SLF10145 - 15µH R190 = 287 Ohm
		YES	Step UP self can be optimized optimisable	L1 = SLF10145 - 15µH
		YES	Optimization of Input filter	R191 et R192 change to USED L7 change to NU R207 0 Ohm added R208 NU_0 Ohm added