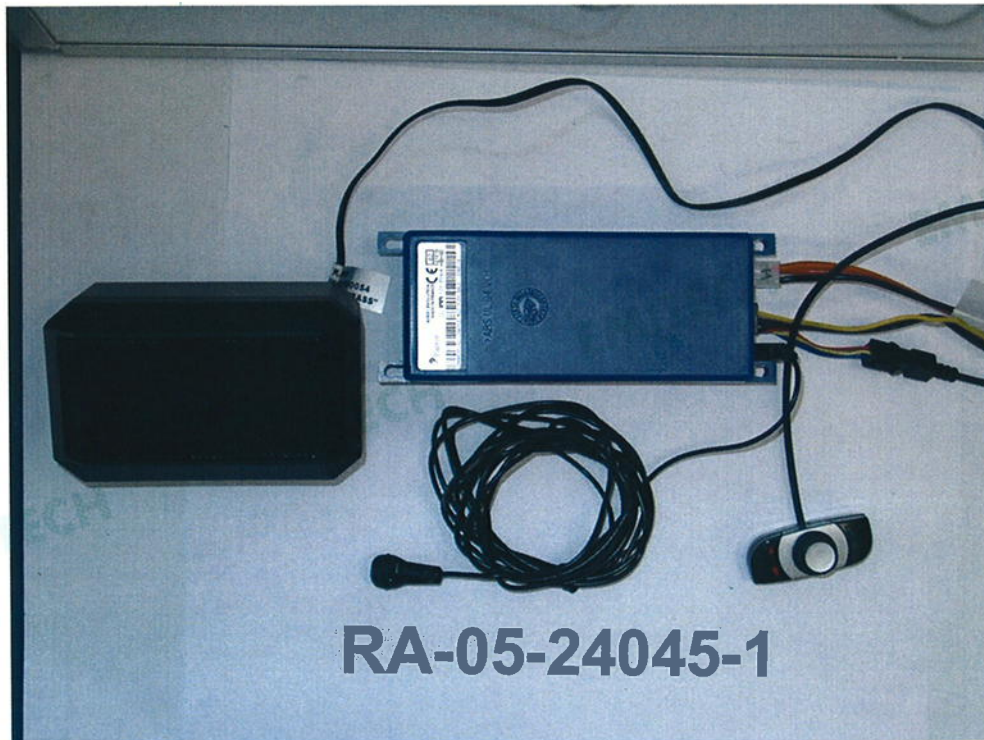
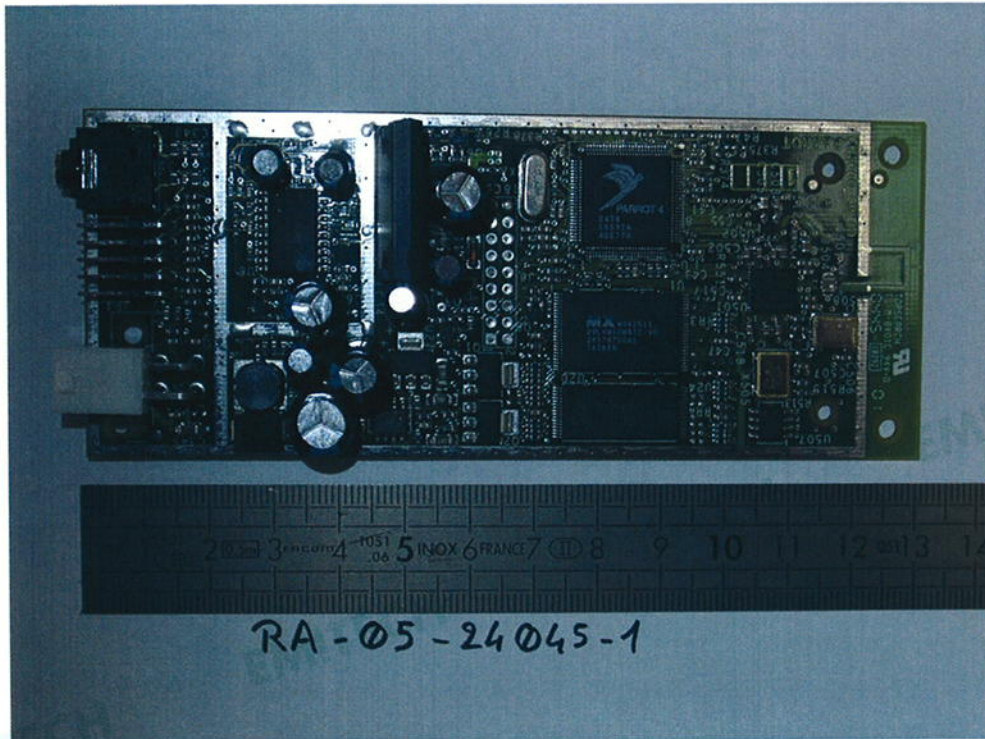


ANNEX 3: PHOTOS OF THE EQUIPMENT UNDER TEST

GENERAL VIEW



Printed circuit board: face 1



Printed circuit board: face 2

