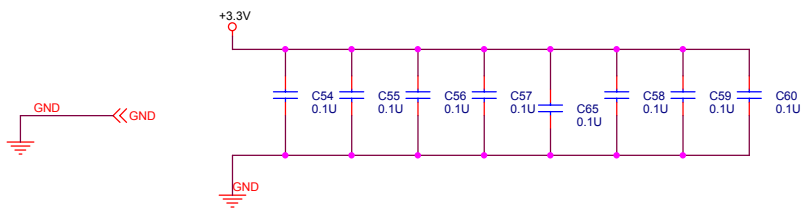
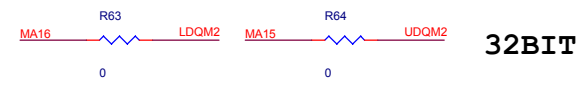
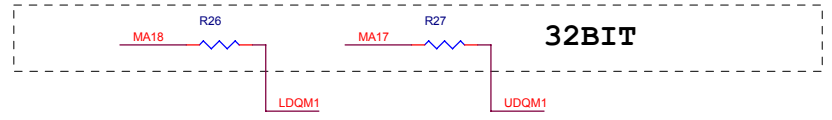
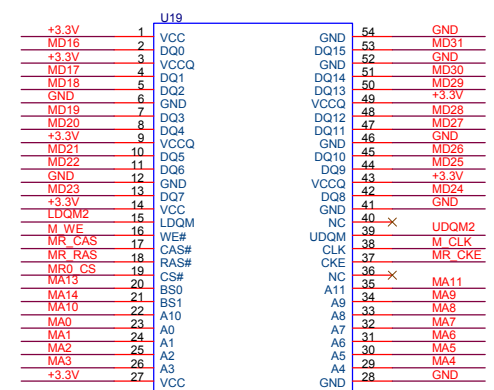
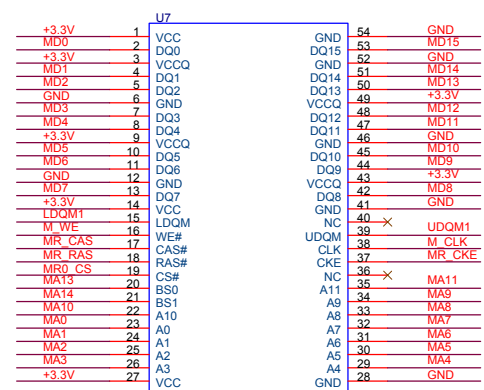
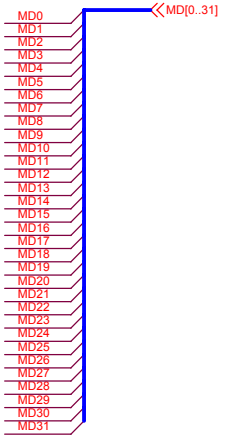
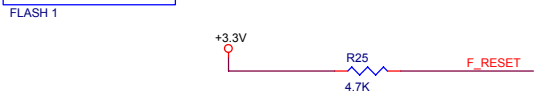
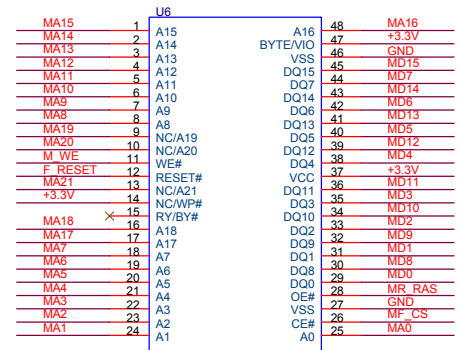
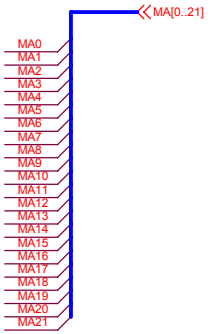
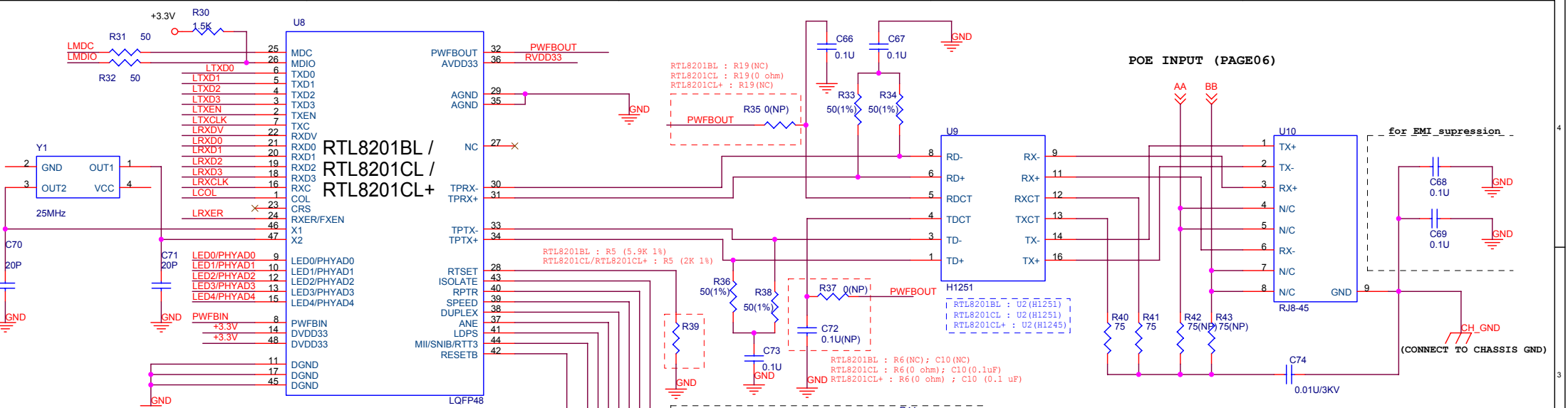


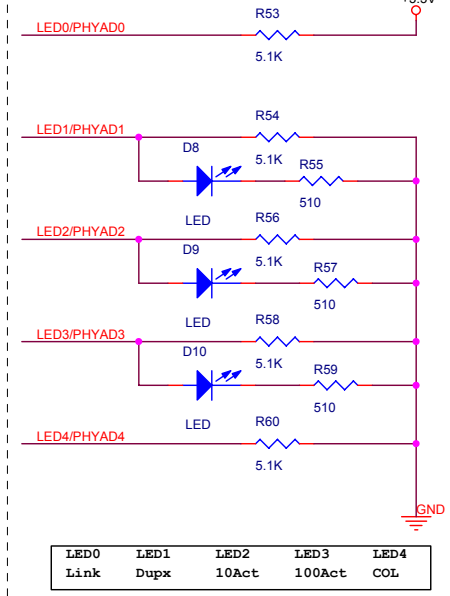
<b>ZINWELL CORP.</b>		
Title		
<b>POWER &amp; RS232 &amp; PoE</b>		
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A3	<b>ZWA-B191-OD</b>	
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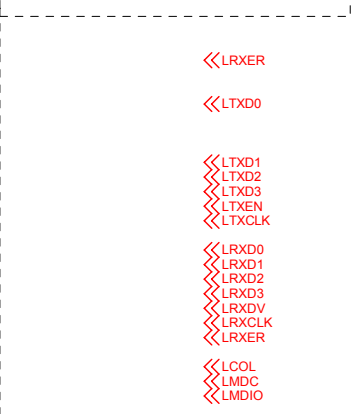




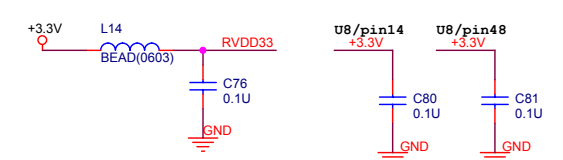
This schematic sets PHY address to 00001b. You could set PHY address from 00001b to 11111b. But the LED polarity must match the respective PHYAD setting. Refer to datasheet's detailed description.



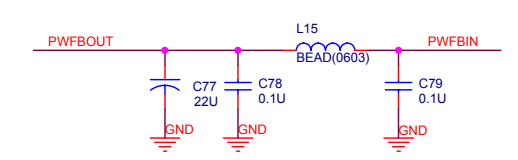
- You could simply connect RESETB to PCI Reset. And just discard R30, R28, C20.
- If Wake on Lan feature is needed, you have to supply power form auxiliary power for all LAN-related circuit including RTL8201BL/CL/CL+ and MAC. In this kind of application, discard R29 and retain other components for one resetting upon power up.



- Hardware Configuration network:**
- This configuration shows
    - Enable: Auto negotiation, Full duplex, 100Mbps, Link Down Power Saving, MII interface
    - Disable: Isolate, Repeater mode
  - These seven configuration pins could be connected to VDD or GND directly.

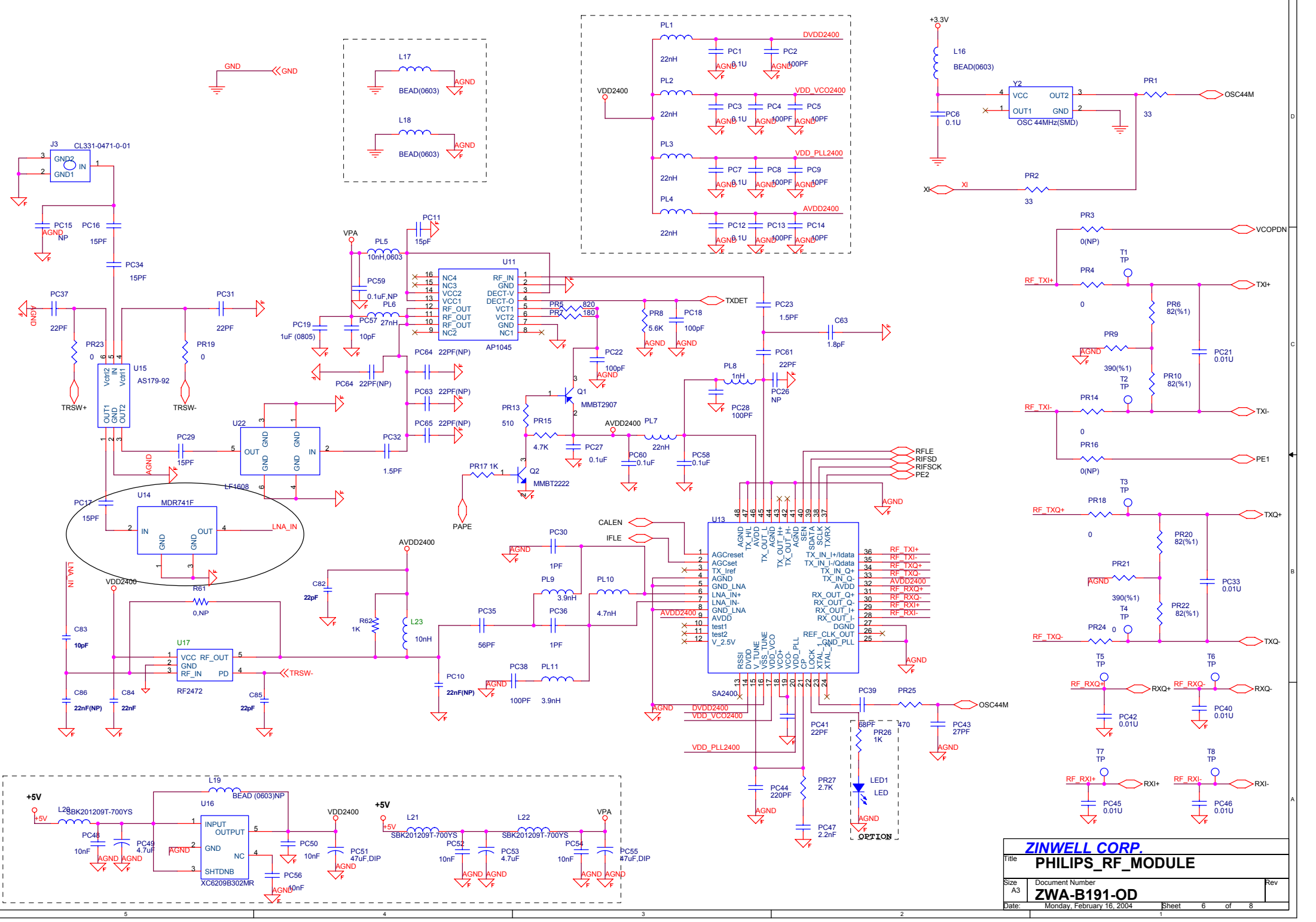


Place L2, C17, C18, C19 as close to each power pin as possible.



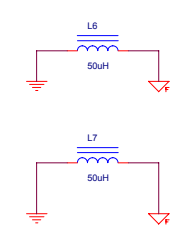
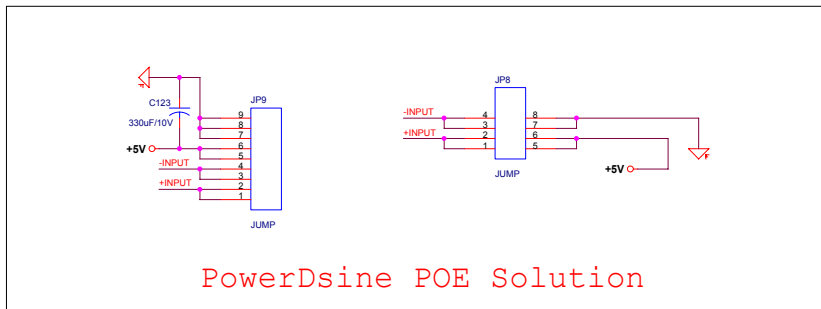
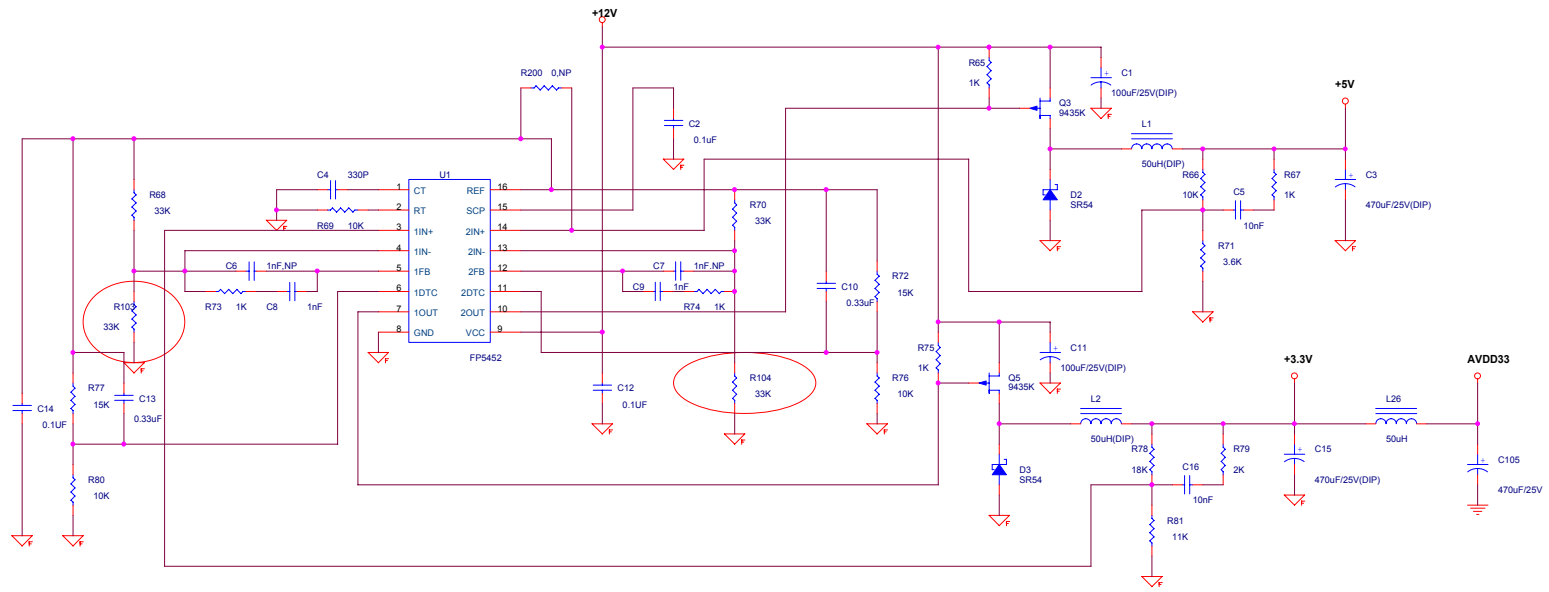
Place C14, C15, L1 close to PWFBI/O and place C16 close to PWFBIN.

Title		
RTL8201BL/CL/CL+ application circuit - interface with MAC(MII)		
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<b>PHILIPS_RF_MODULE</b>		
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