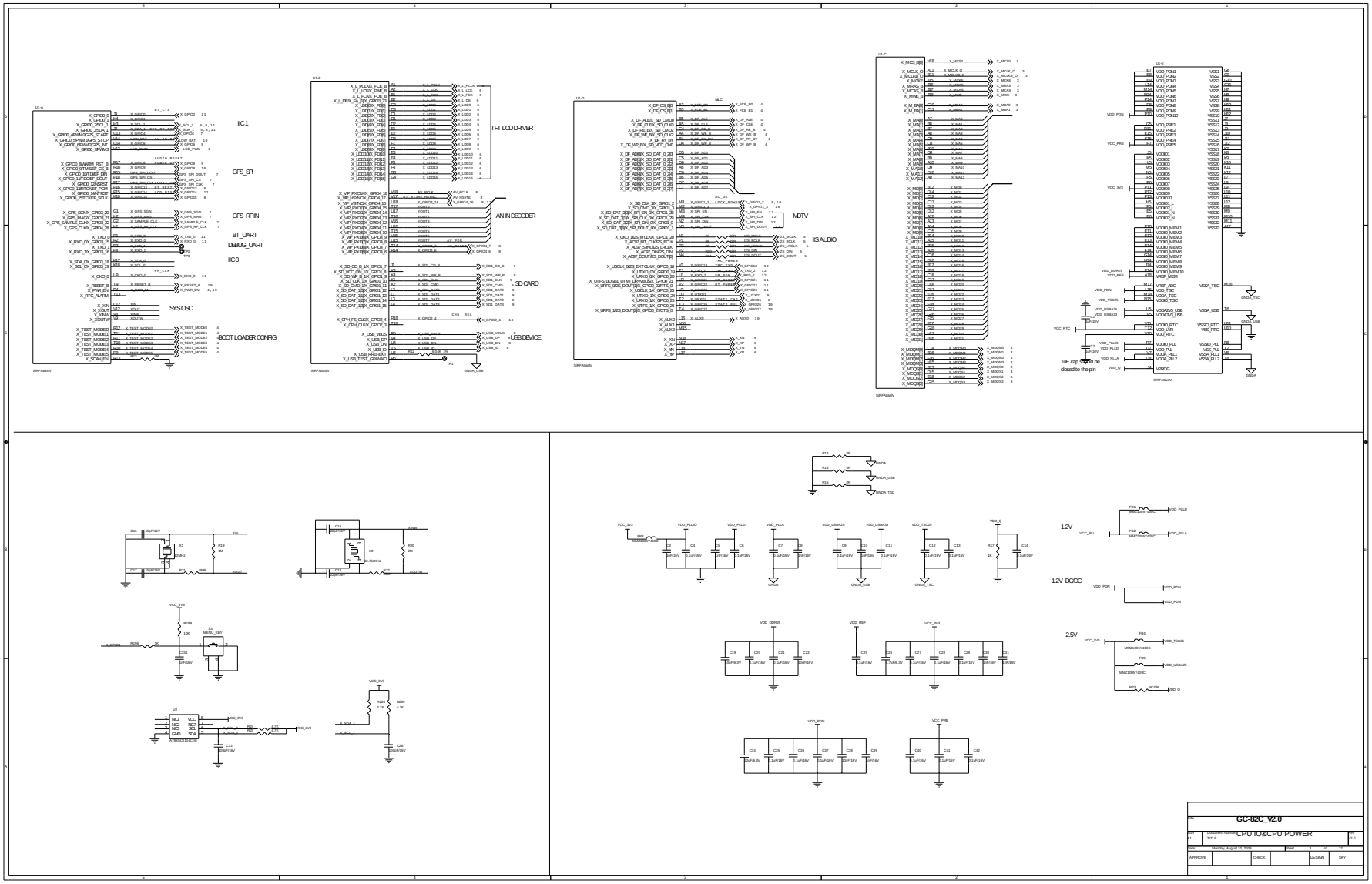
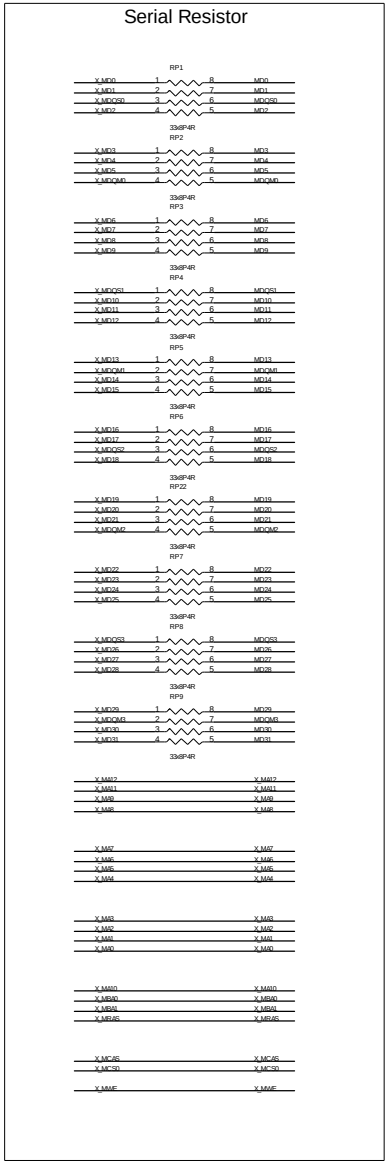
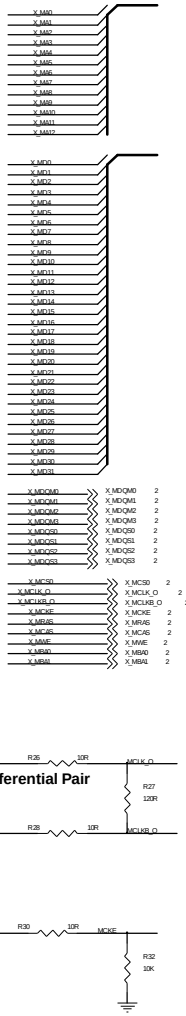
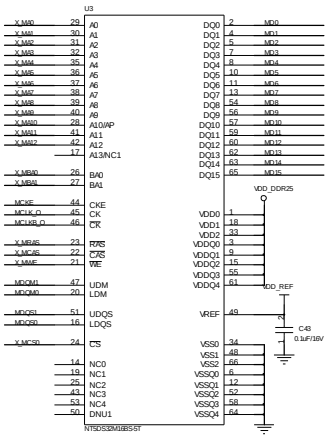

IIC1
TFT LCD DRIVER
GPS_SR
GPS_RFIN
ANAIN DECODER
MDTV
BT_UART
DEBUG_UART
IIS AUDIO
IIC0
SD/CARD
SYS/OSC
BOOT LOADER CONFIG
USB DEVICE
GC-82C_V2.0
CPU IO&CPU POWER
1.2V
1.2V DODC
2.5V

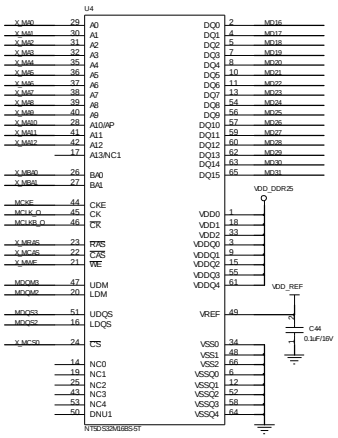
GC82C

Serial Resistor

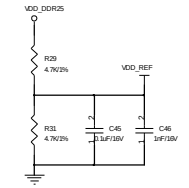


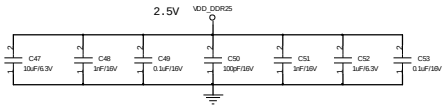






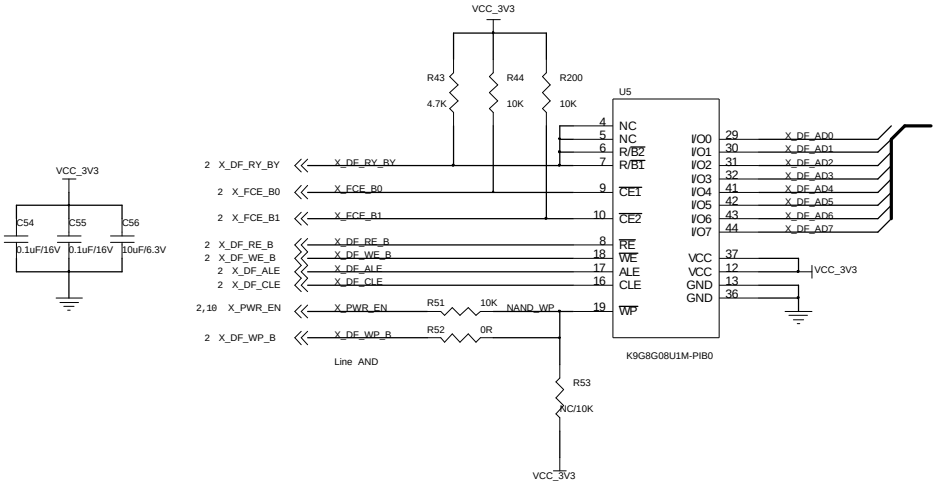
Default 64M byte
Part number is: NT5DS32M16BS-5T



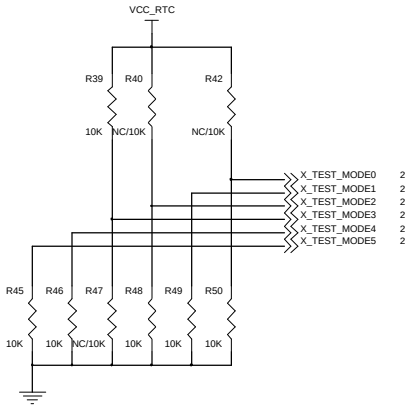


Title	GC-82C_V2.0		
Size A1	Document Number TITLE	DDR RAM	Rev V2.0
Date	Monday, August 10, 2009	Sheet 4 of 12	
APPROVE		CHECK	DESIGN SKY

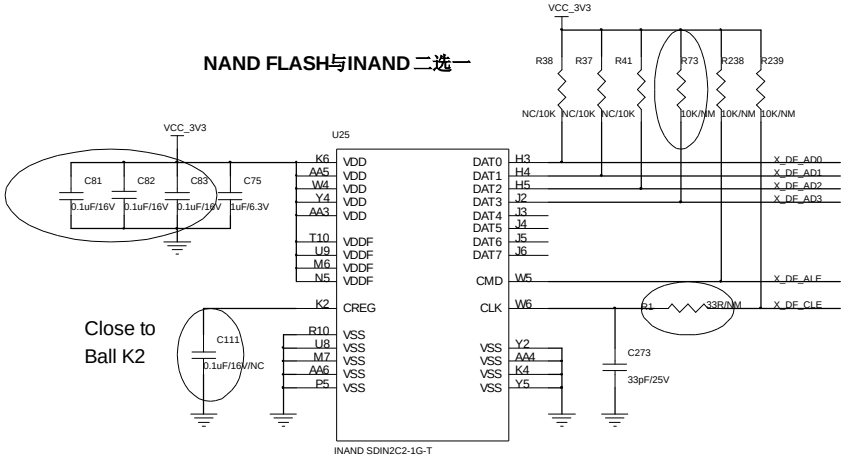
MLC Nand Flash boot Default



Boot Configure for three options



NAND FLASH与INAND 二选一

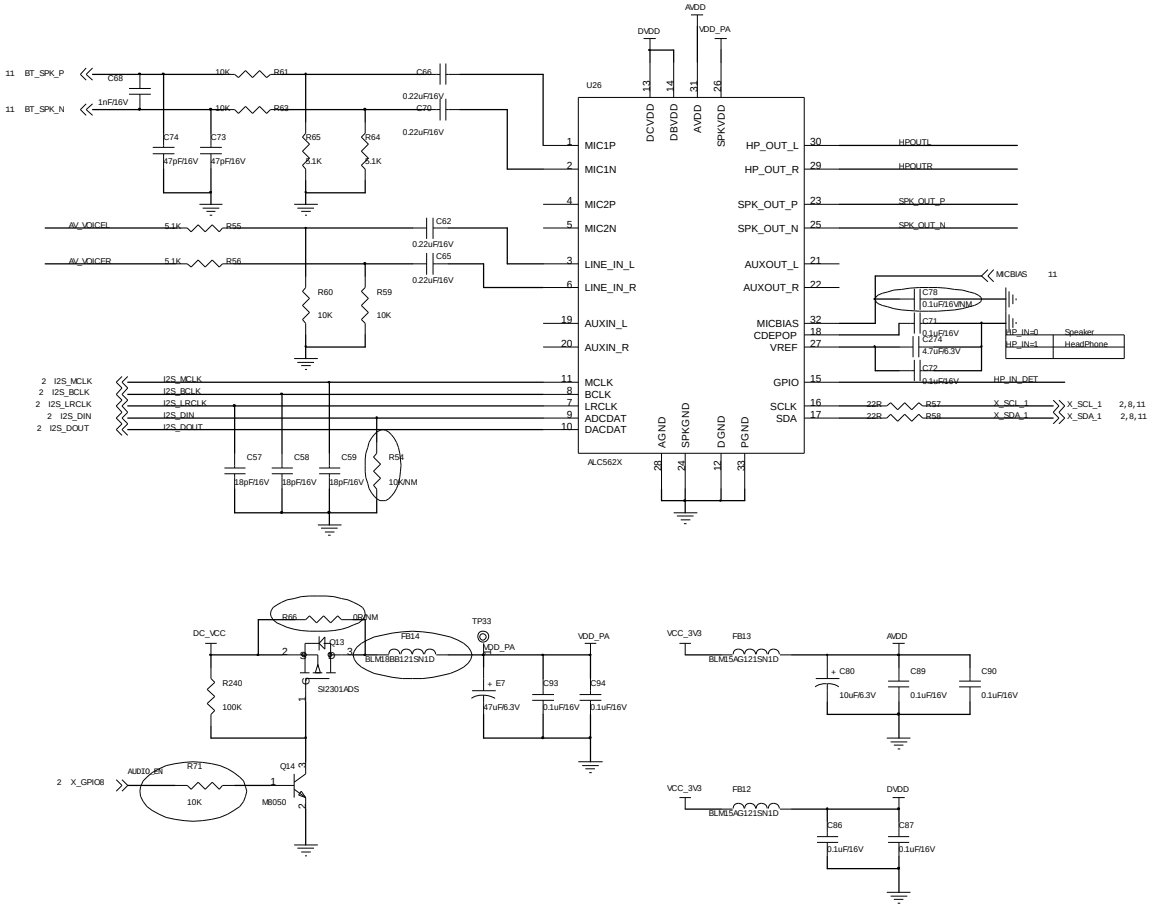


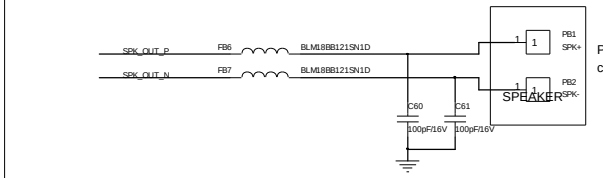
K9G8G08------2K/page:
K9LAG08------4K/page:

Default

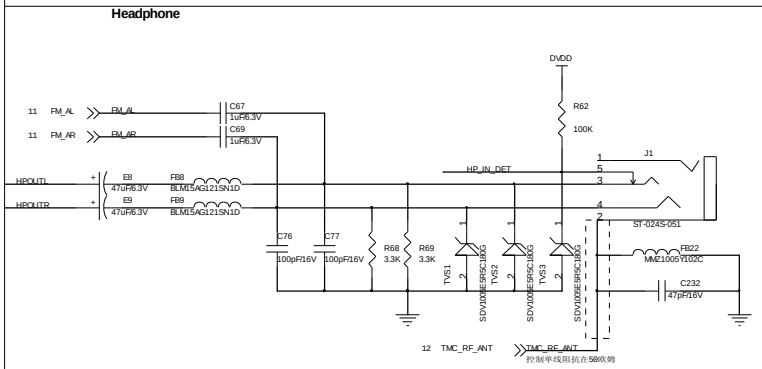
Boot configuration	X_TEST_MODE[5:0]
Boundary Scan	6b110000
SLC Nand Boot 2KB Page 8 Bit	6b000101
MLC Nand Boot 2KB Page 8 Bit	6b001000
INAND Boot	6b001100
EFuse	6b110010
MLC Nand Boot 4KB Page 8 Bit	6b001001

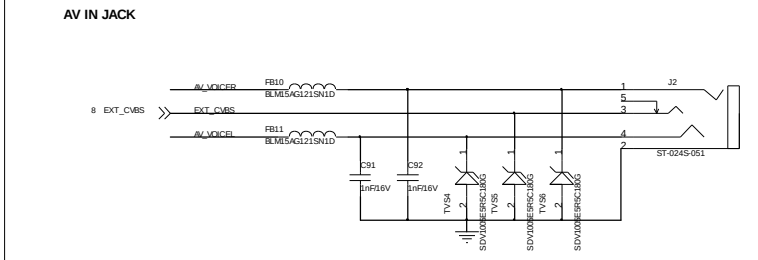
Title	GC-82C_V2.0				
Size A1	Document Number TITLE	NAND Flash&INAND			Rev V1.0
Date:	Tuesday, August 11, 2009	Sheet 5 of 12			
APPROVE		CHECK		DESIGN	SKY





PB1, should be closed to PB2

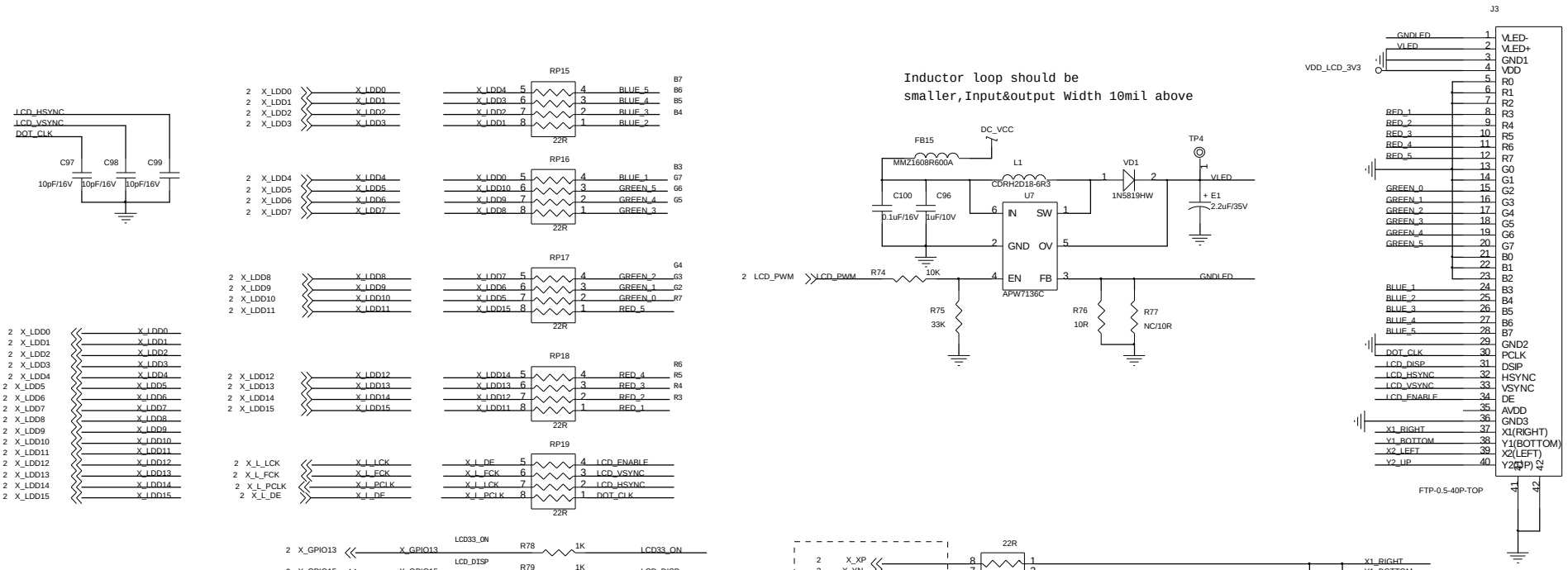




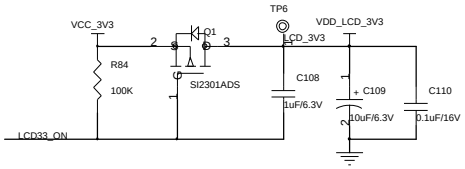
Title	GC-82C_V2.0		
Size A1	Document Number TITLE	AUDIO CODEC	Rev V2.0
Date: Thursday, August 13, 2009	Sheet 6 of 12		
APPROVE	CHECK	DESIGN	SKY

TFT LCD CONN

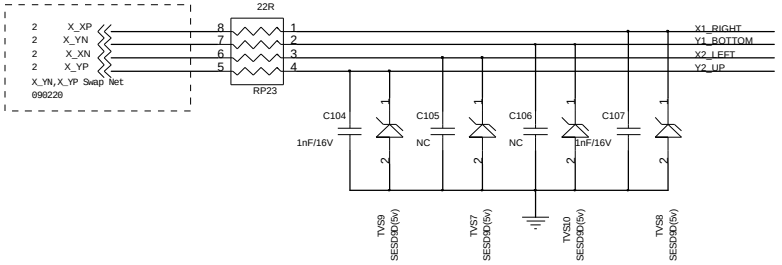
Connector type : 40Pin / 0.5mm pitch / Top contact



L_PCLK should be
far away from
other signal or be
masked by GND



Inductor loop should be
smaller,Input&output Width 10mil above



Title	GC-82C_V2.0				
Size A1	Document Number TITLE	LCD DRIVER			Rev V2.0
Date: Monday, August 10, 2009		Sheet 7 of 12			
APPROVE		CHECK		DESIGN	SKY

Notes:

1. This page is RF module, Please pay more attention to PCB layout. The RF Z0 is 50 ohm, the drawing should follow RF rules and RF portion must be put into one shielding case.

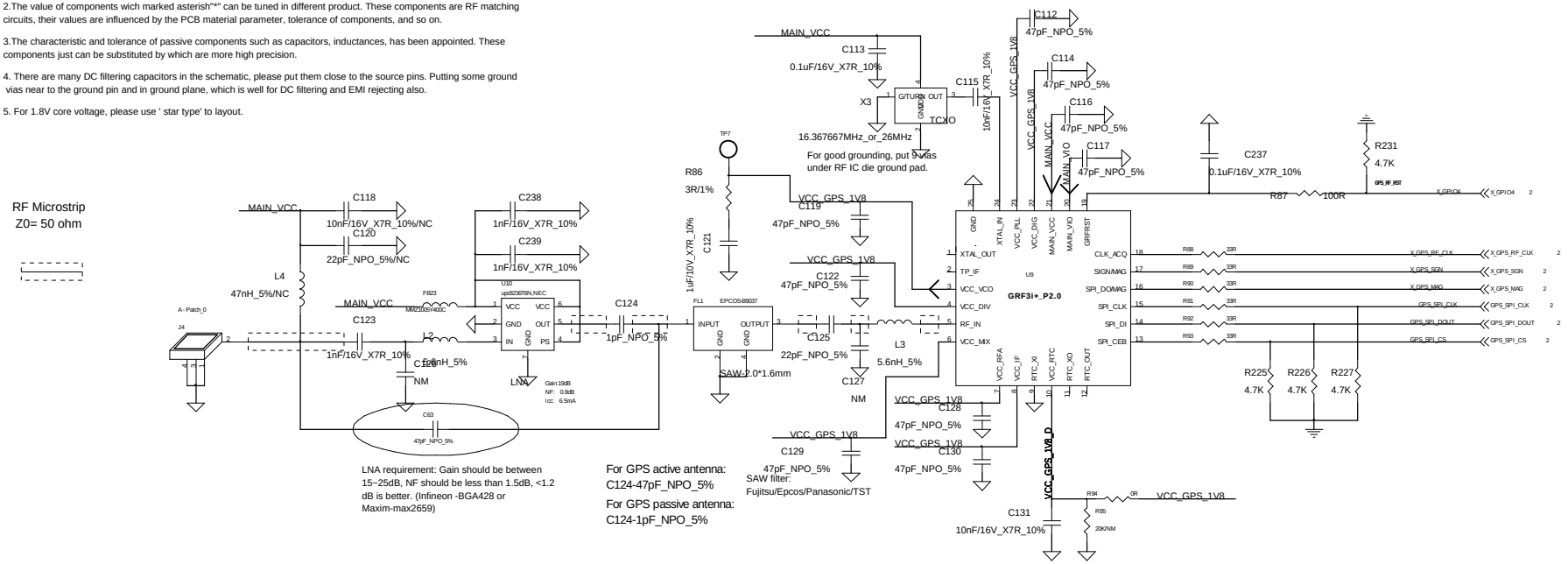
2.The value of components wich marked asterish"*" can be tuned in different product. These components are RF matching circuits, their values are influenced by the PCB material parameter, tolerance of components, and so on.

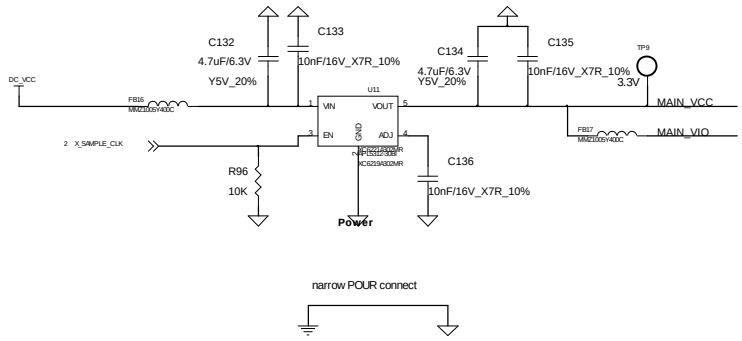
3.The characteristic and tolerance of passive components such as capacitors, inductances, has been appointed. These components just can be substituted by which are more high precision.

4. There are many DC filtering capacitors in the schematic, please put them close to the source pins. Putting some ground vias near to the ground pin and in ground plane, which is well for DC filtering and EMI rejecting also.

5. For 1.8V core voltage, please use ' star type' to layout.

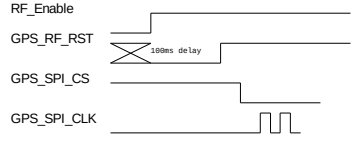
TCXO will be 16.367667MHz or 26MHz, 0.5ppm KDS/TEW/Rakon TXC





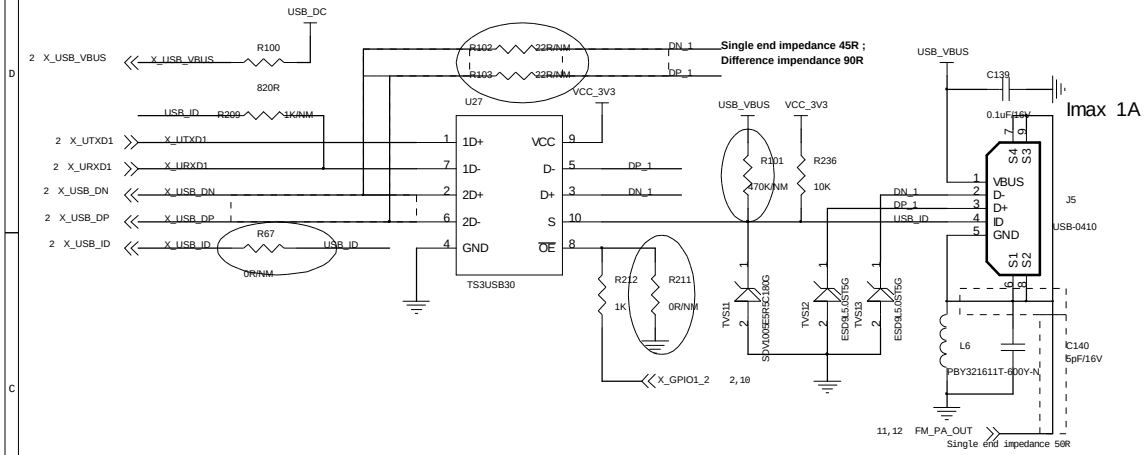
narrow POUR connect

We strongly recommend that one should use the following sequence and timing by GPIO to control the GRST pin and SPI.

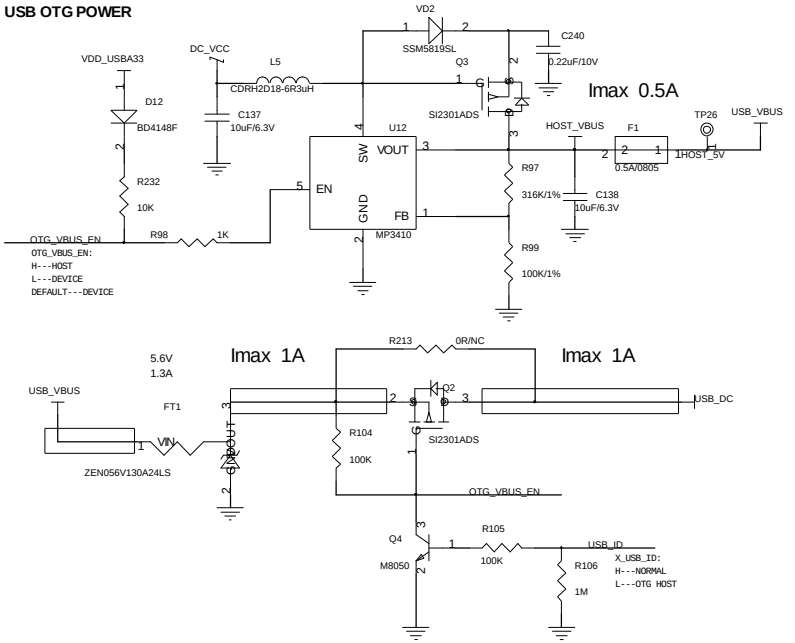


Title	GC-82C_V2.0				
Size A1	Document Number TITLE	GPS RF			Rev V2.0
Date	Monday, August 10, 2009		Sheet 8 of 12		
APPROVE		CHECK		DESIGN	SKY

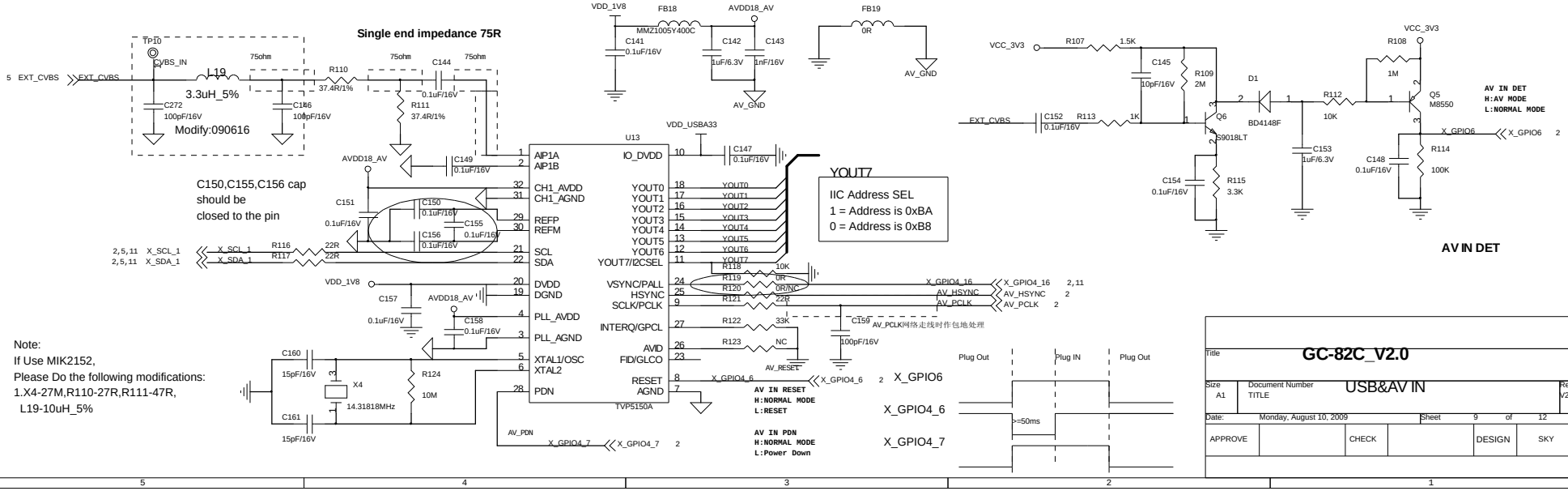
USB OTG



USB OTG POWER

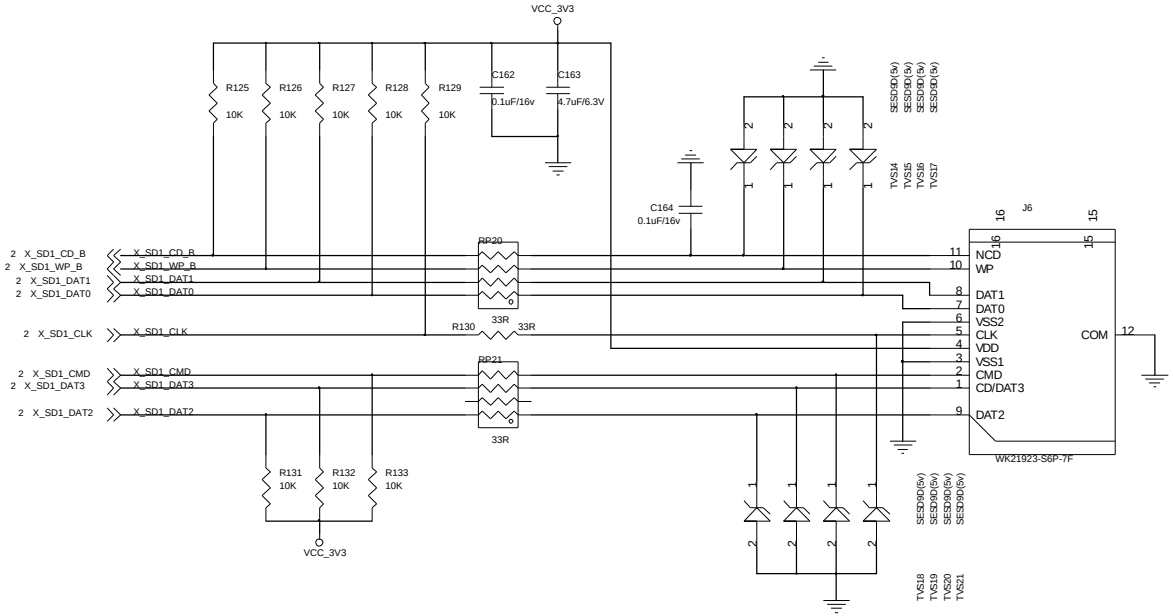


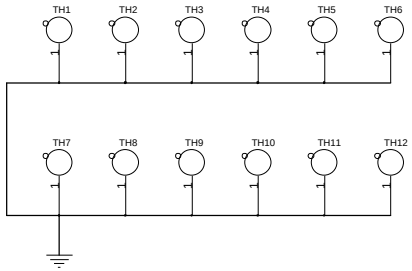
AV IN DECODER



Note:
If Use MIK2152,
Please Do the following modifications:
1.X4-27M,R110-27R,R111-47R,
L19-10uH_5%

Title	GC-82C_V2.0		
Size A1	Document Number TITLE	USB&AV IN	Rev V2.0
Date: Monday, August 10, 2009	Sheet 9 of 12		
APPROVE	CHECK	DESIGN	SKY

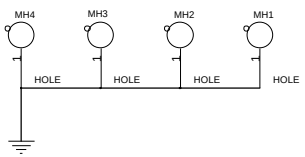


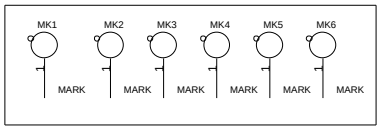


屏蔽罩定位孔

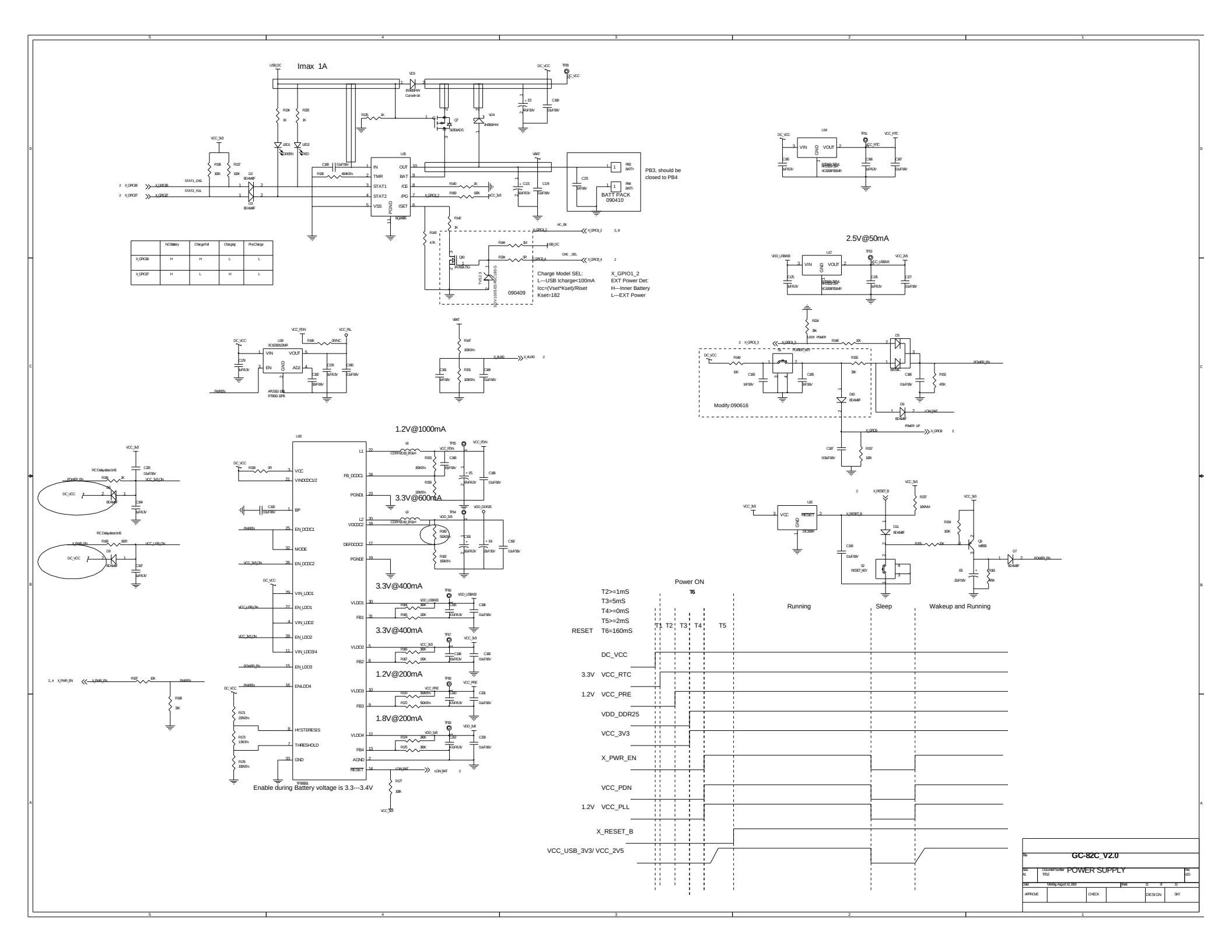
MARK点

螺栓孔

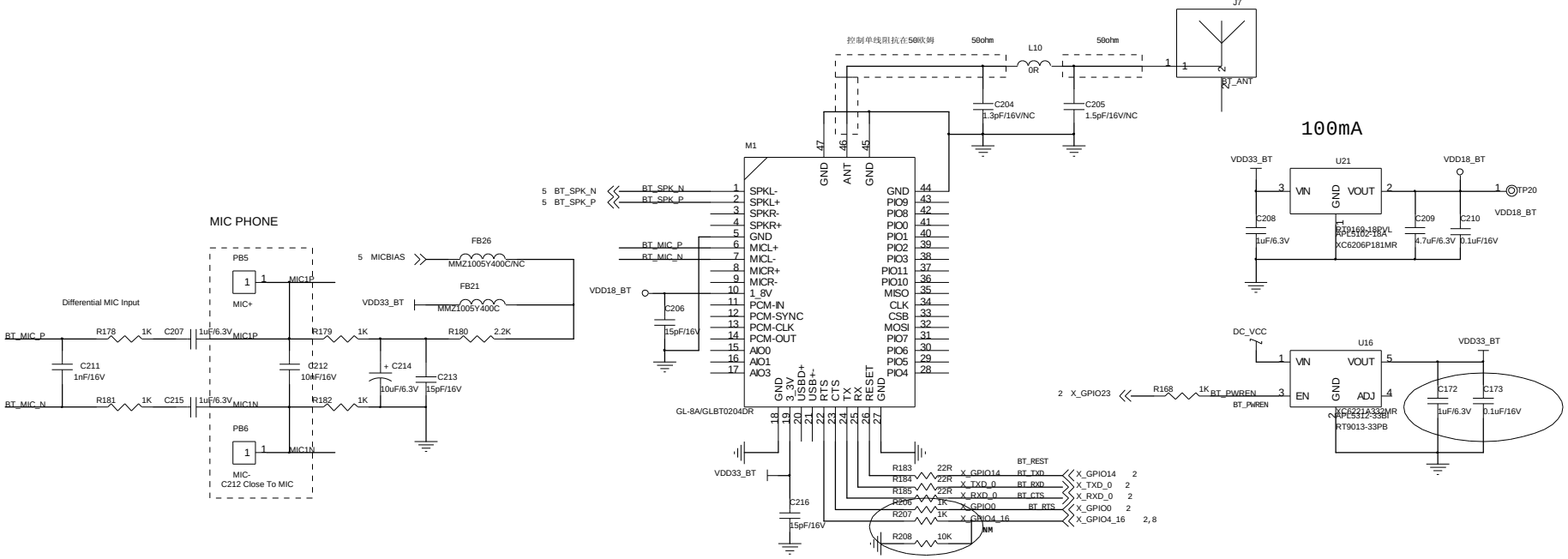




Title	GC-82C_V2.0				
Size A1	Document Number TITLE	SD CARD			Rev V2.0
Date: Monday, August 10, 2009		Sheet 10 of 12			
APPROVE		CHECK		DESIGN	SKY


Imax 1A
PB3, should be closed to PB4
BATT PACK 090410
Charge Model SEL:
L---USB Icharge<100mA
Icc=(Vset*Kset)/Riset
Kset=182
X_GPIO1_2
EXT Power Det:
H---Inner Battery
L---EXT Power
090409
2.5V@50mA
Modify:090616
1.2V@1000mA
3.3V@600mA
3.3V@400mA
3.3V@400mA
1.2V@200mA
1.8V@200mA
Enable during Battery voltage is 3.3---3.4V
Power ON
T2>=1mS
T3=5mS
T4>=0mS
T5>=2mS
T6=160mS
RESET
DC_VCC
VCC_RTC
VCC_PRE
VDD_DDR25
VCC_3V3
X_PWR_EN
VCC_PDN
VCC_PLL
X_RESET_B
VCC_USB_3V3/ VCC_2V5
Running
Sleep
Wakeup and Running
GC-82C_V2.0
POWER SUPPLY

Bluetooth



Title	GC-82C_V2.0				
Size A1	Document Number TITLE	BT			Rev V2.0
Date: Monday, August 10, 2009		Sheet 12 of 12			
APPROVE		CHECK		DESIGN	SKY