



Photo 7: type plate of the chip



Photo 8: type plate of the EUT





Photo 9: type plate of the EUT



Photo 10: EUT mounted on development board

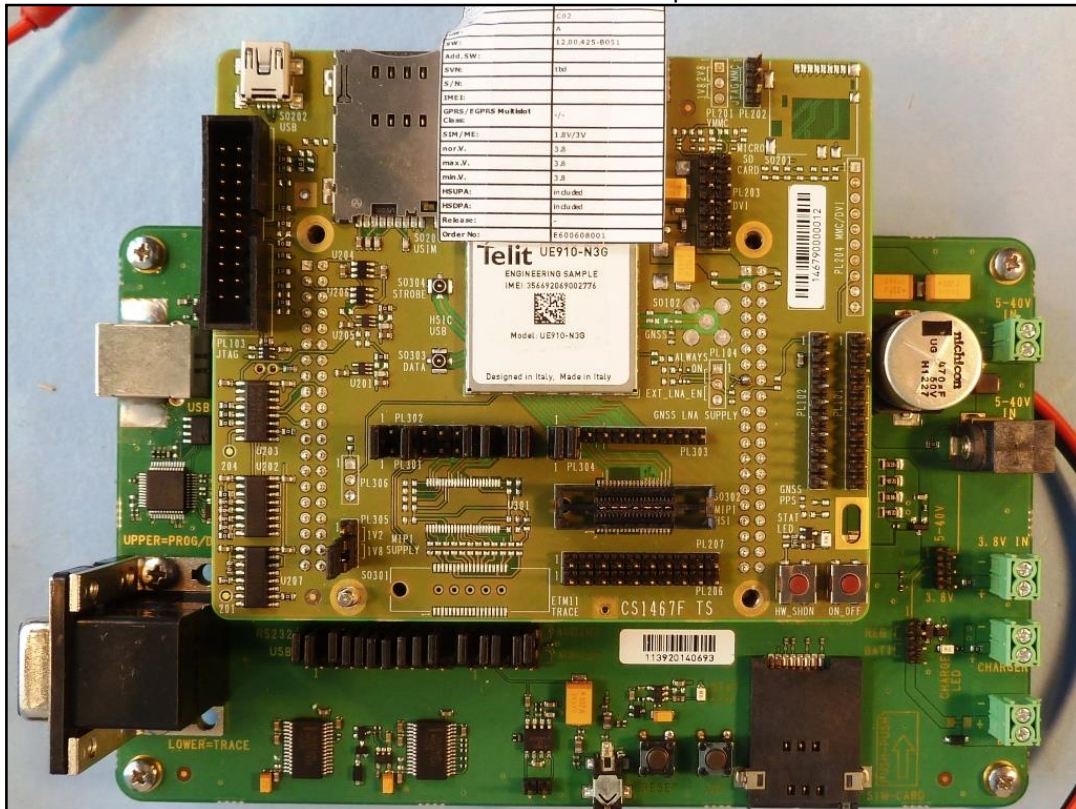




Photo 11: top view of the development board

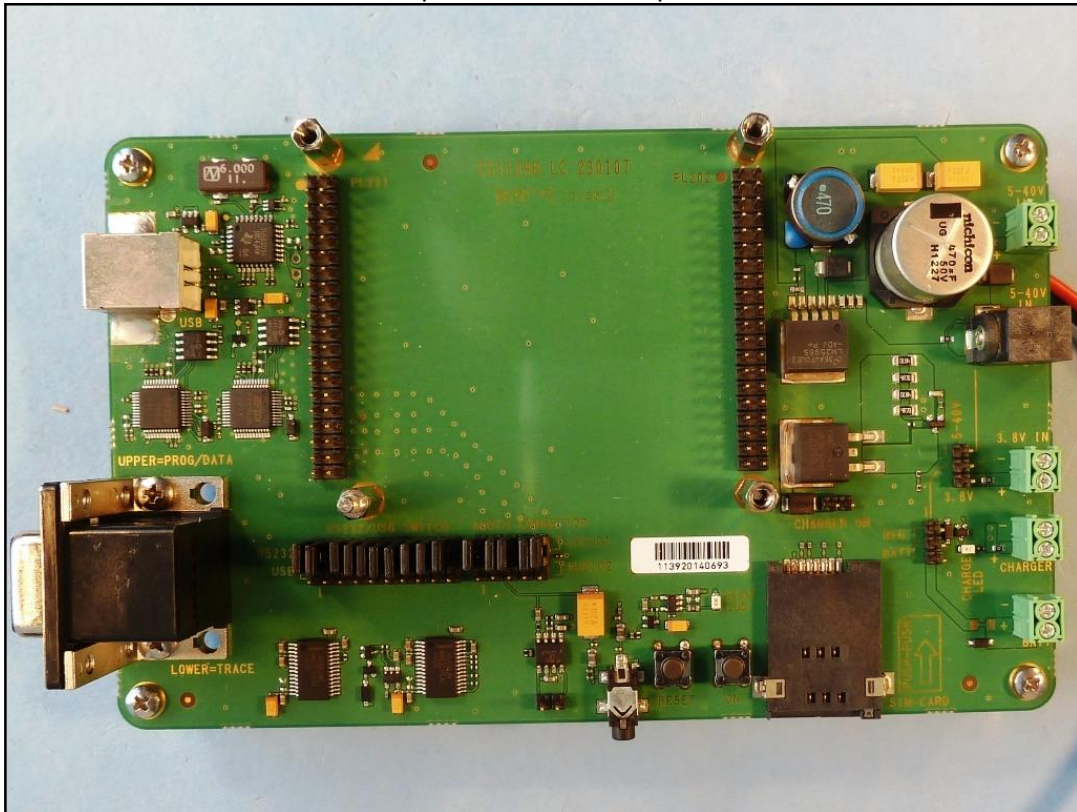


Photo 12: bottom view of the development board

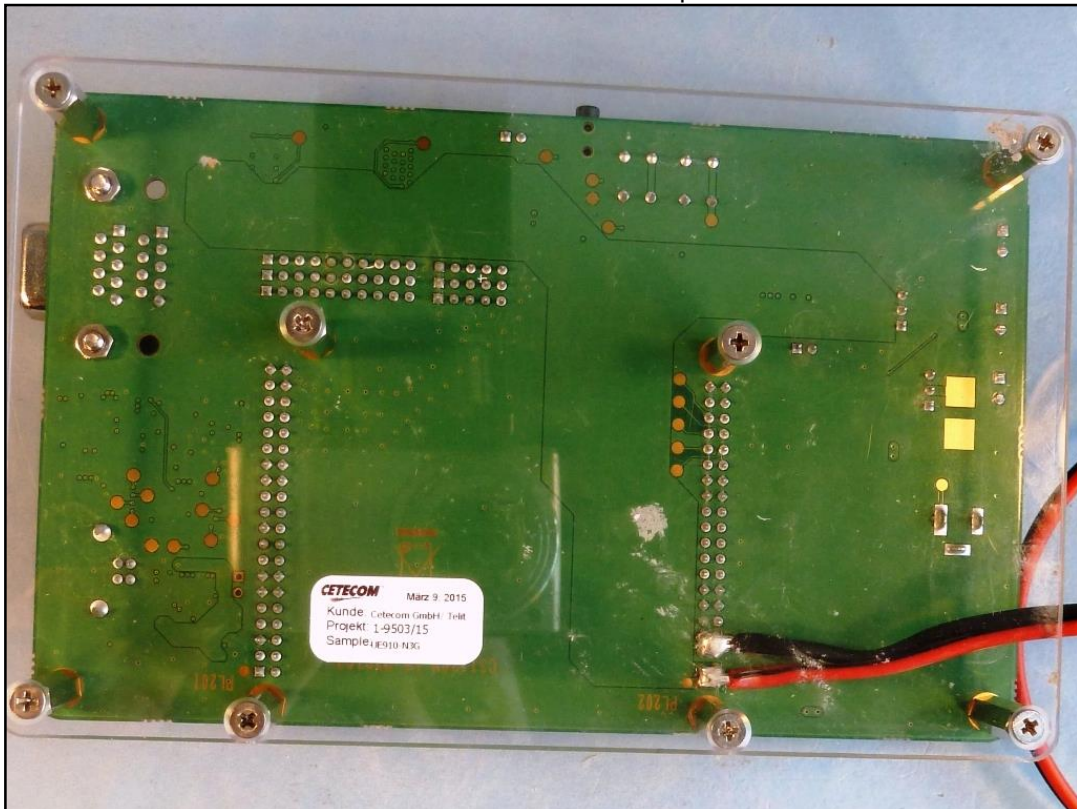


Photo 13: type plate of the development board



Photo 14: antenna

