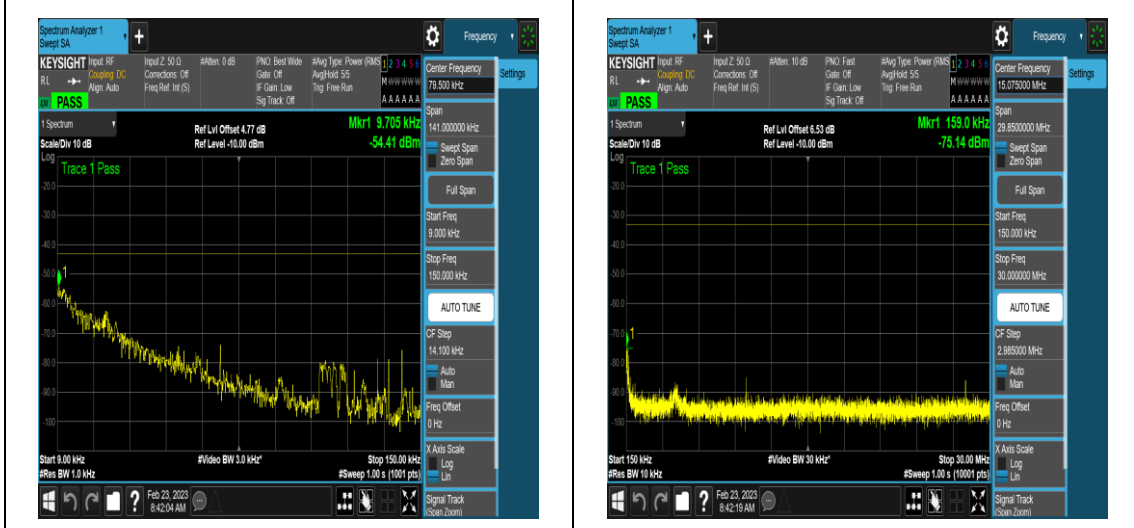
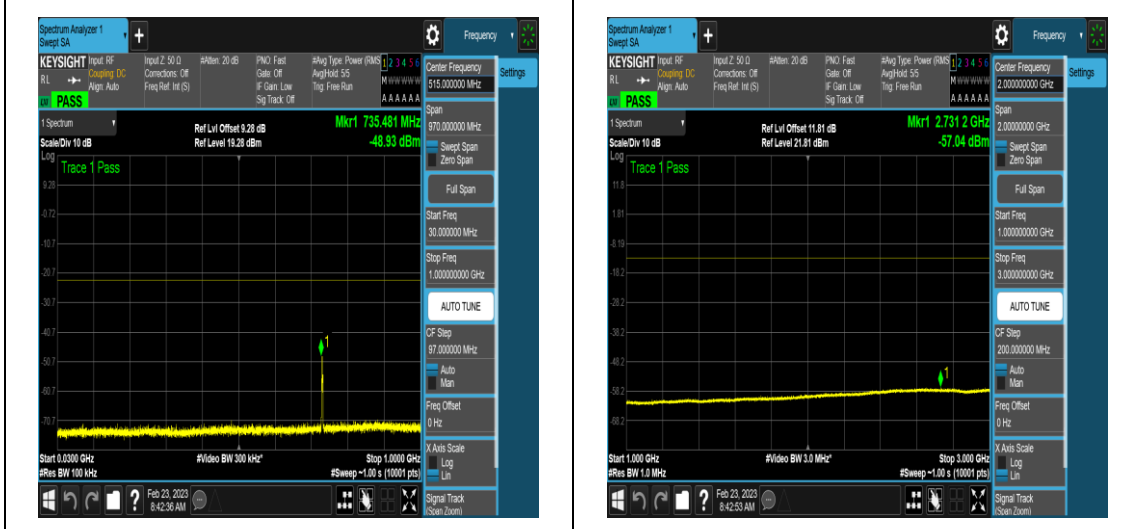
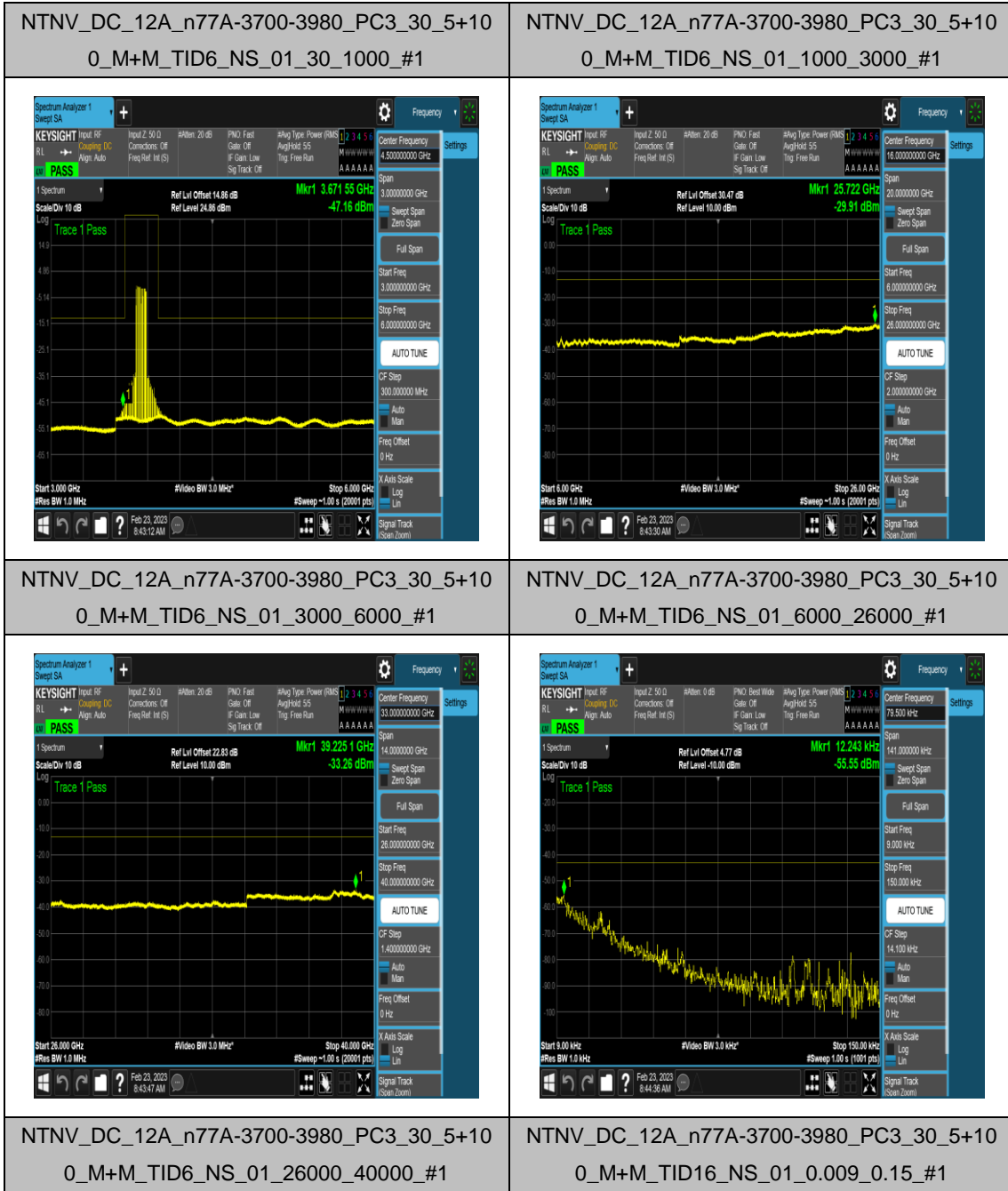


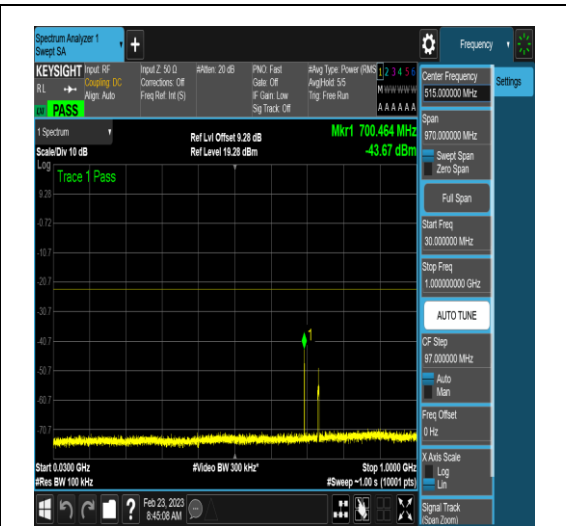
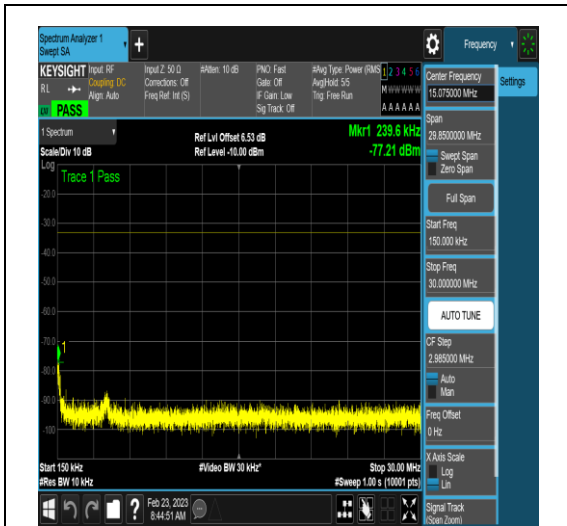
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+M_TID5_NS_01_6000_26000_#1



NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+M_TID6_NS_01_0.009_0.15_#1

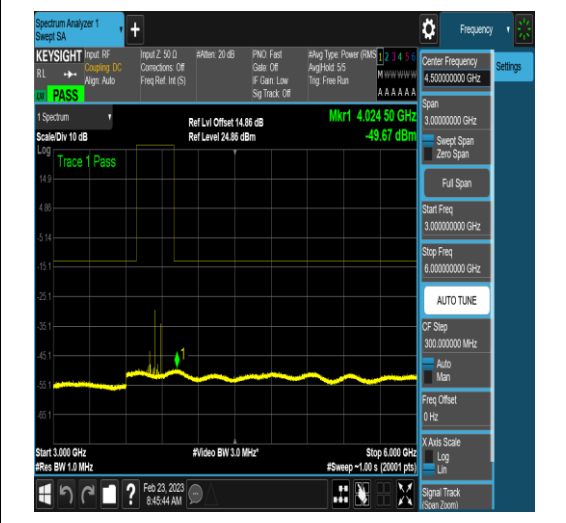
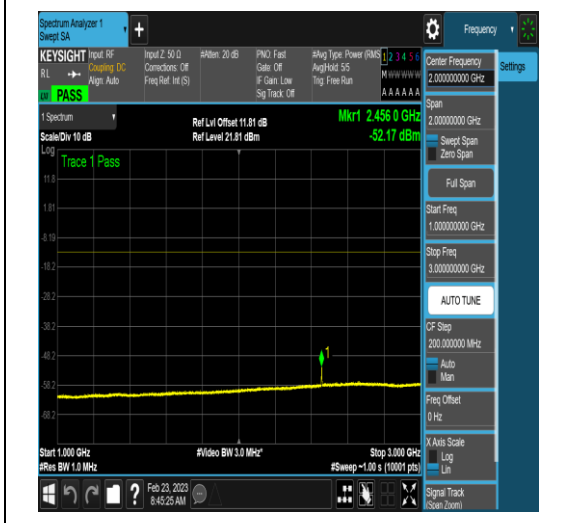






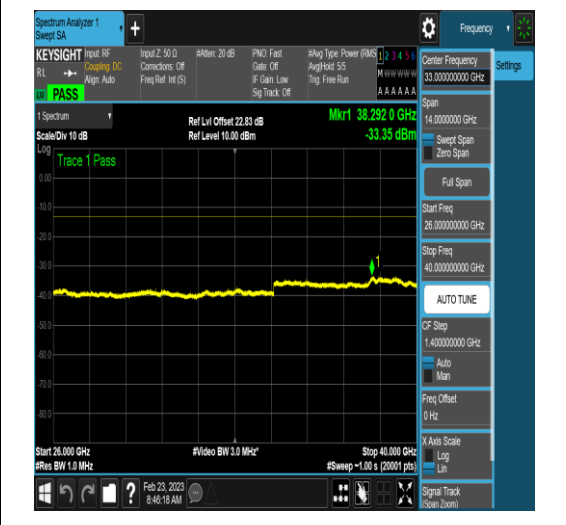
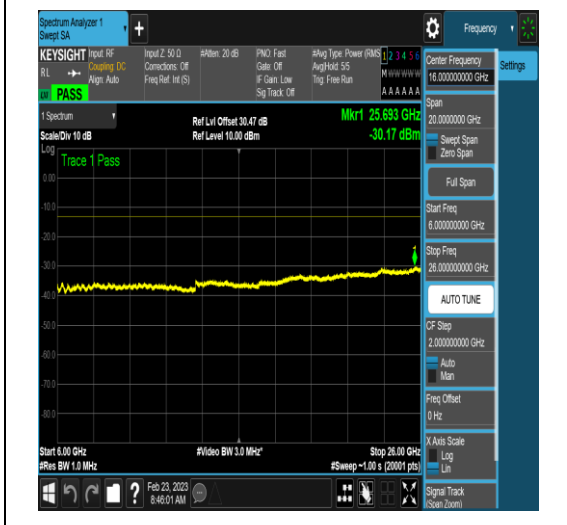
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+M_TID16_NS_01_0.15_30_#1

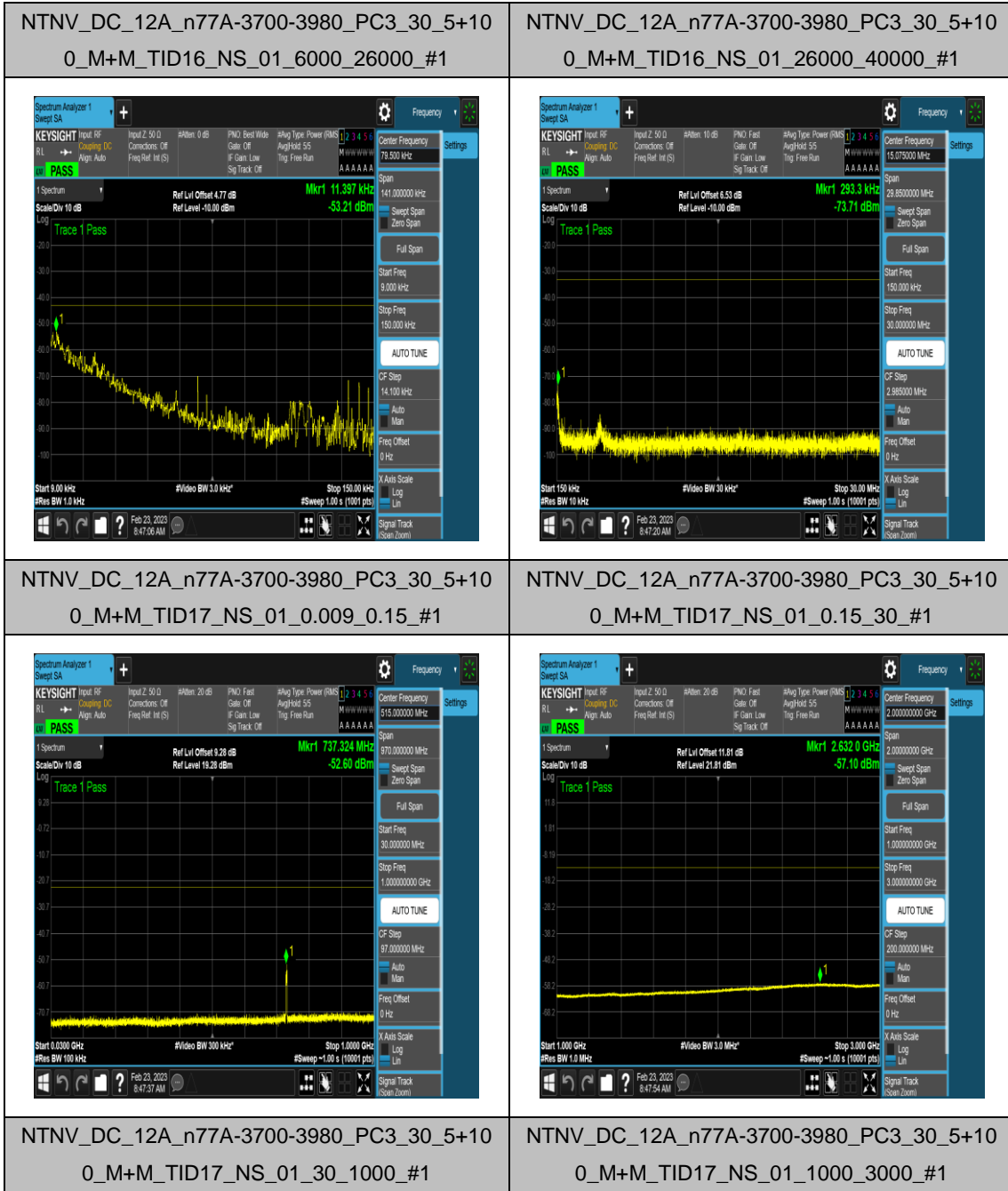
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+M_TID16_NS_01_30_1000_#1

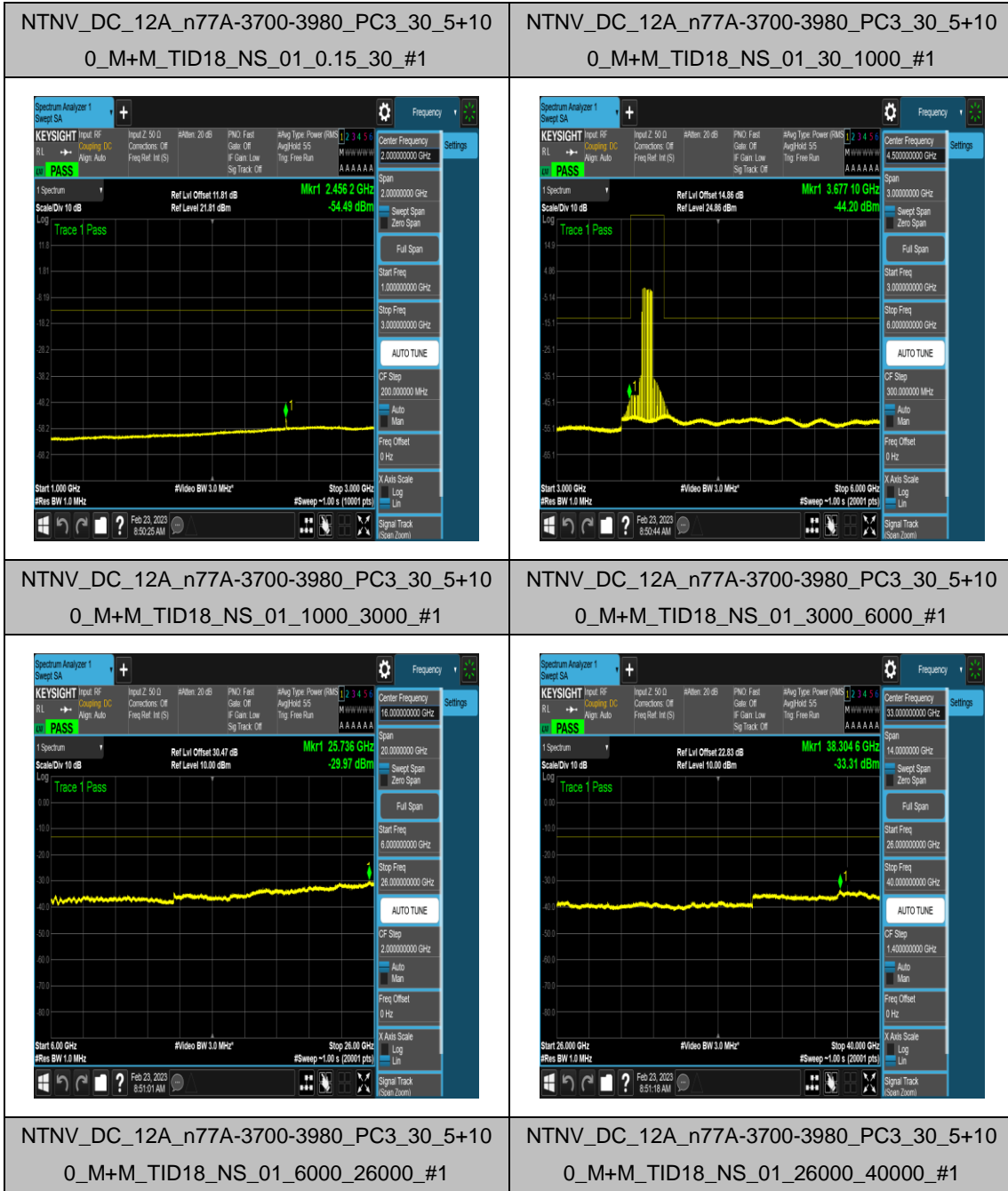


NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+M_TID16_NS_01_1000_3000_#1

NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+M_TID16_NS_01_3000_6000_#1



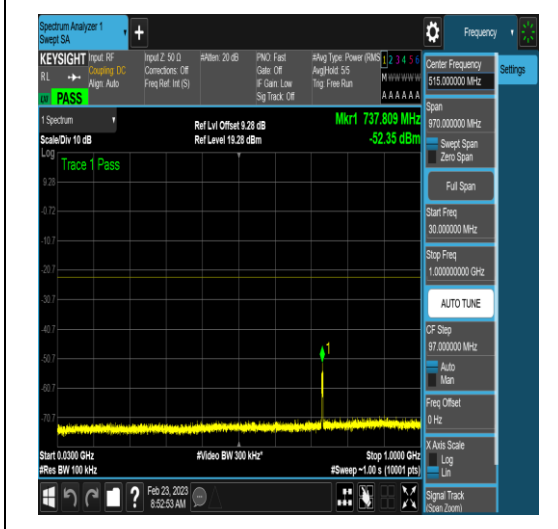




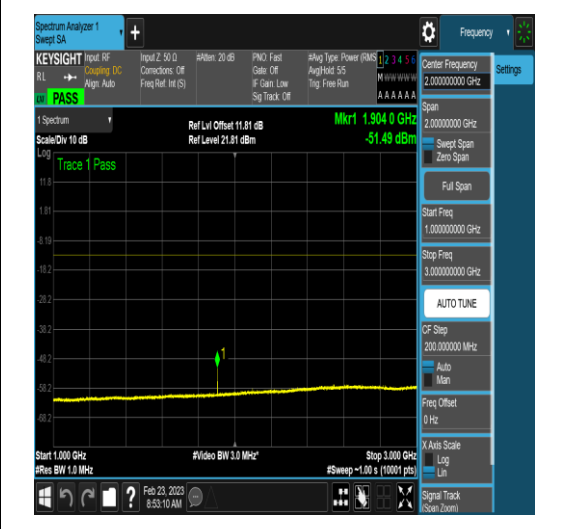


NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID4_NS_01_0.009_0.15_#1

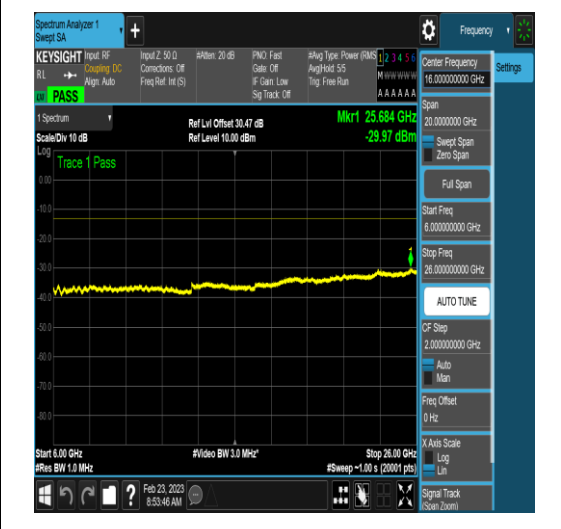
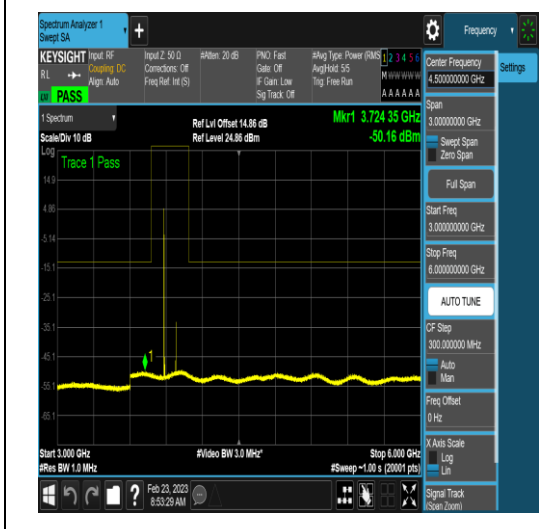
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID4_NS_01_0.15_30_#1

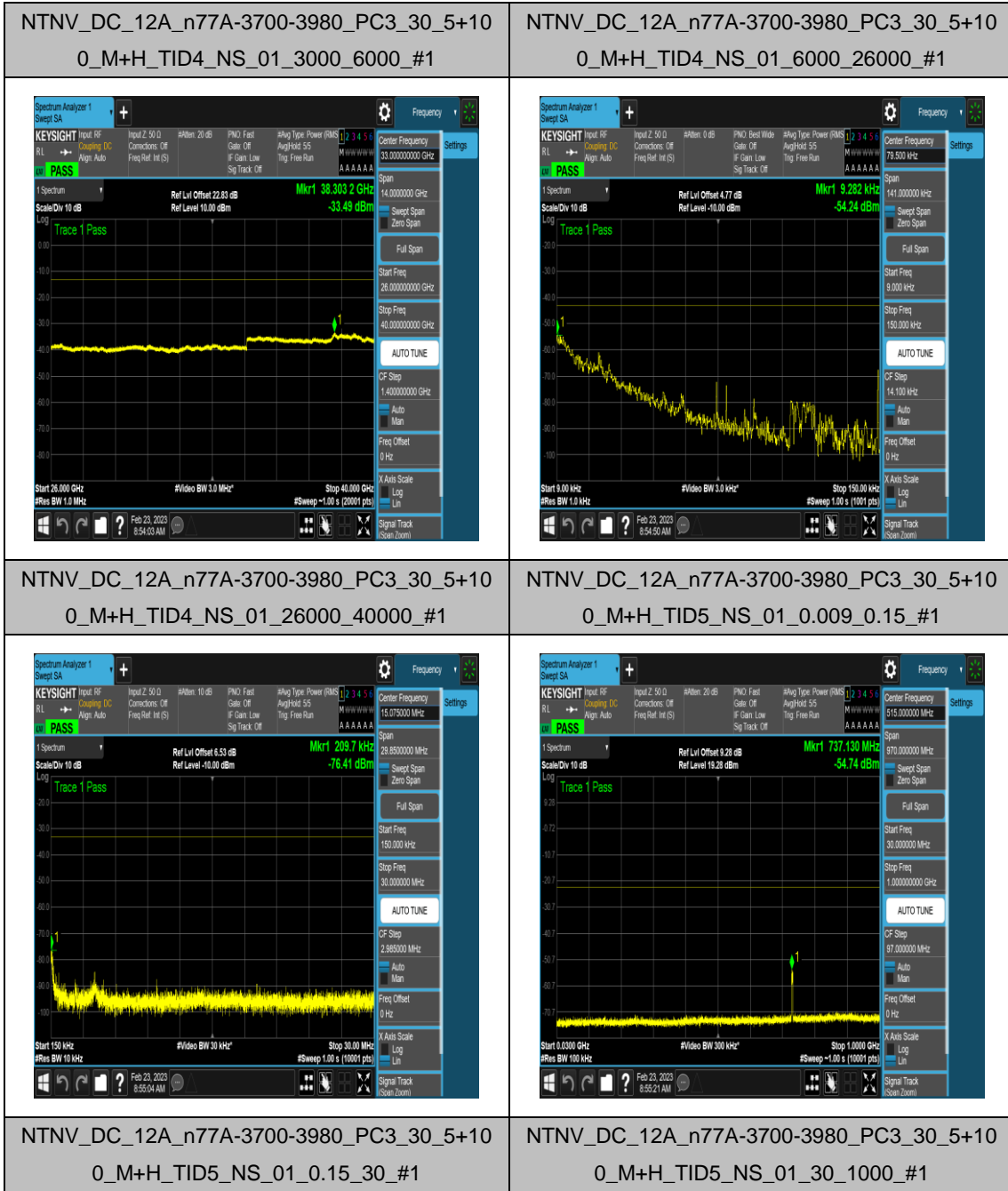


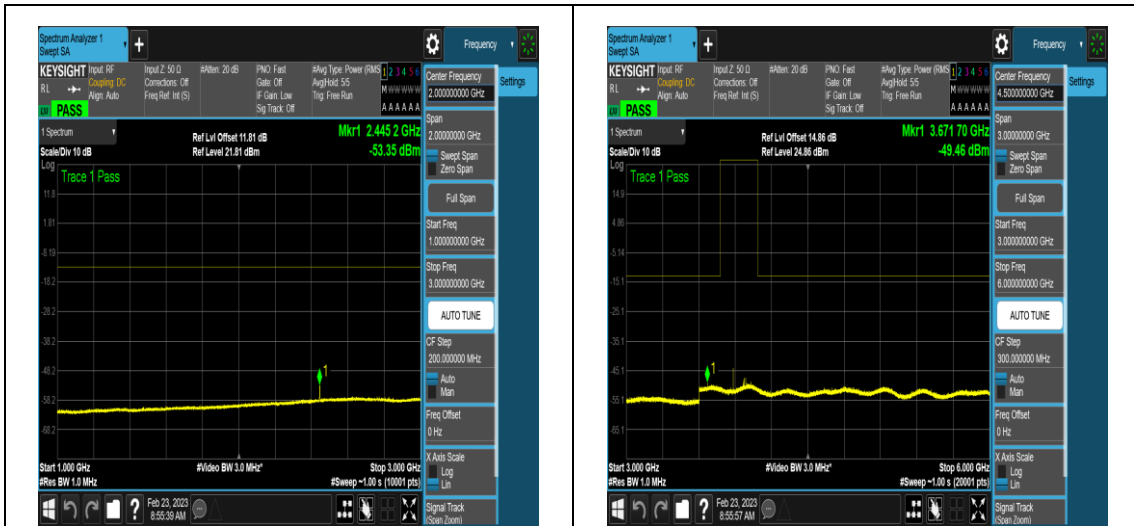
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID4_NS_01_30_1000_#1



NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID4_NS_01_1000_3000_#1

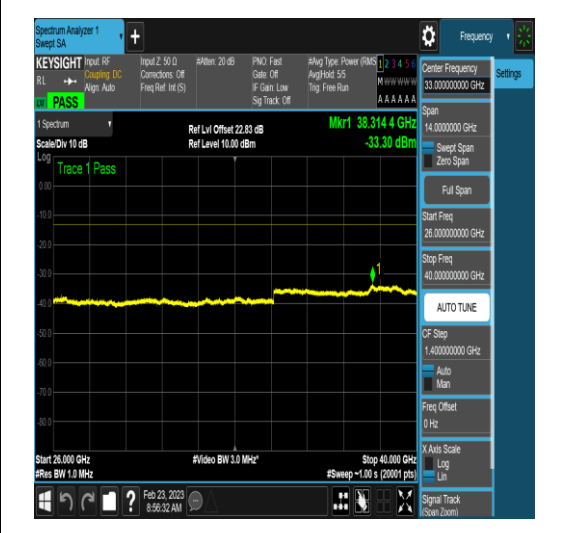
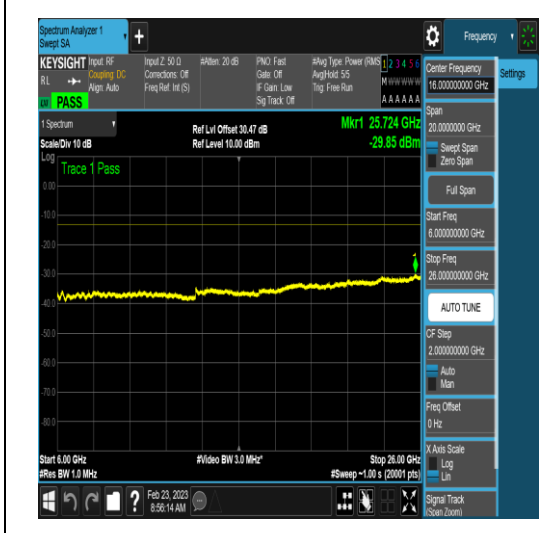






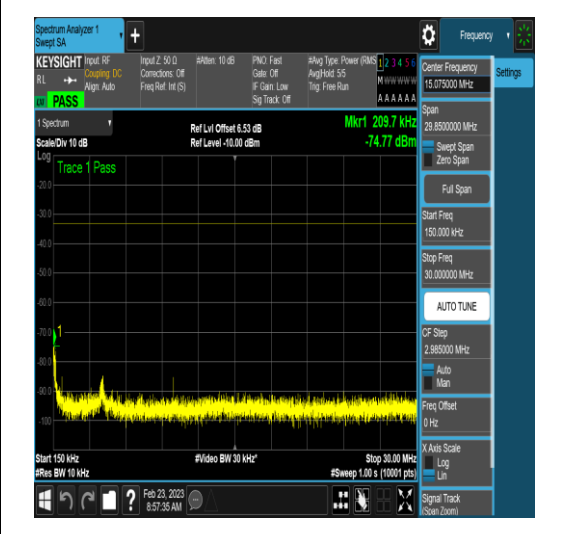
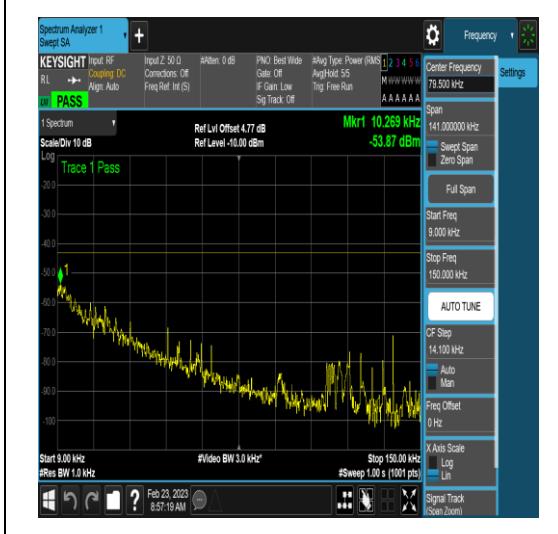
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID5_NS_01_1000_3000_#1

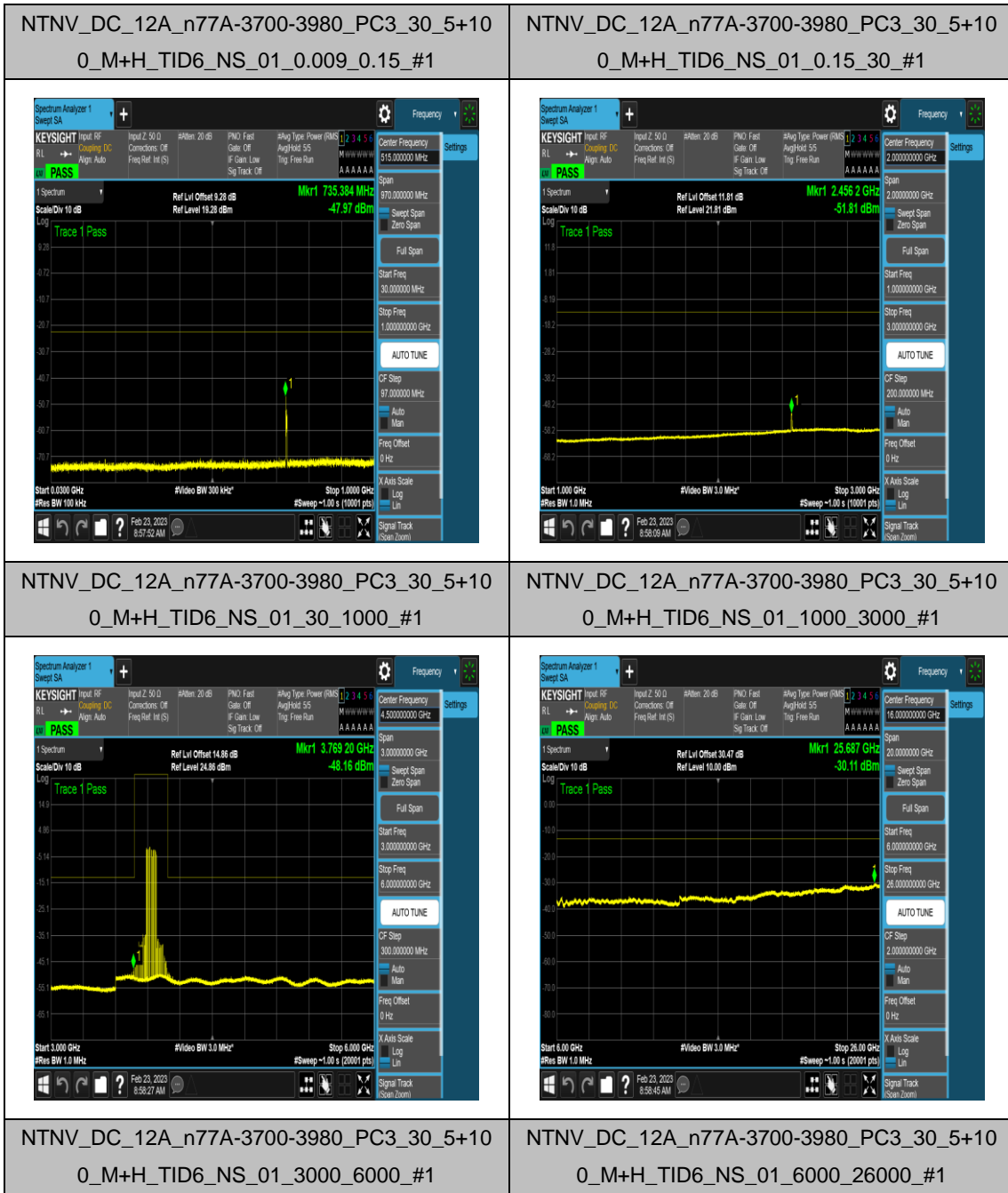
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID5_NS_01_3000_6000_#1

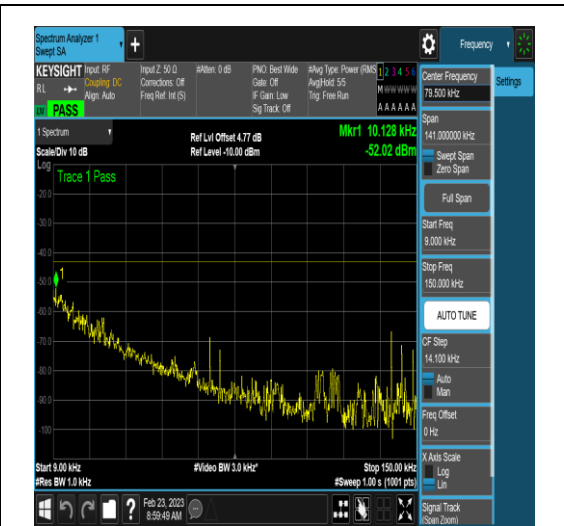
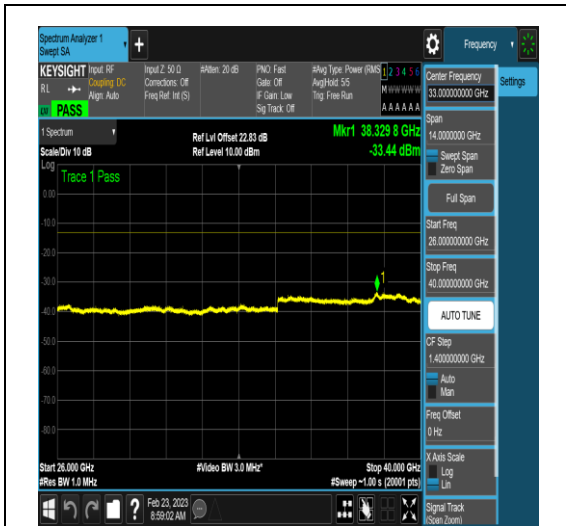


NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID5_NS_01_6000_26000_#1

NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID5_NS_01_26000_40000_#1

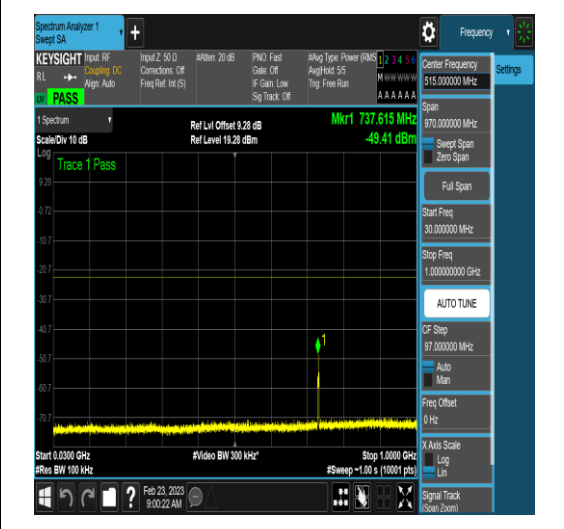
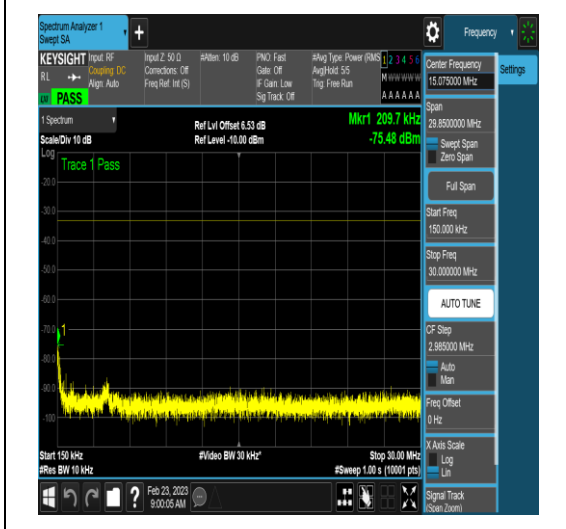






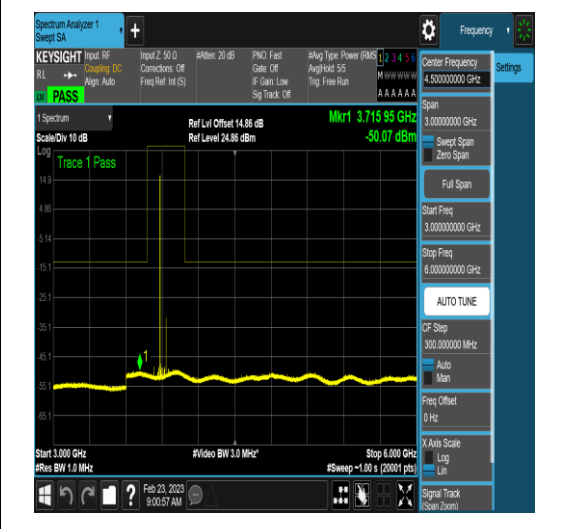
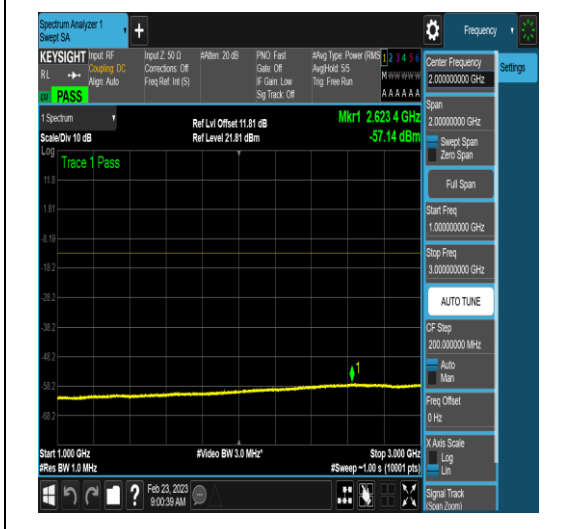
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
 0_M+H_TID6_NS_01_26000_40000_#1

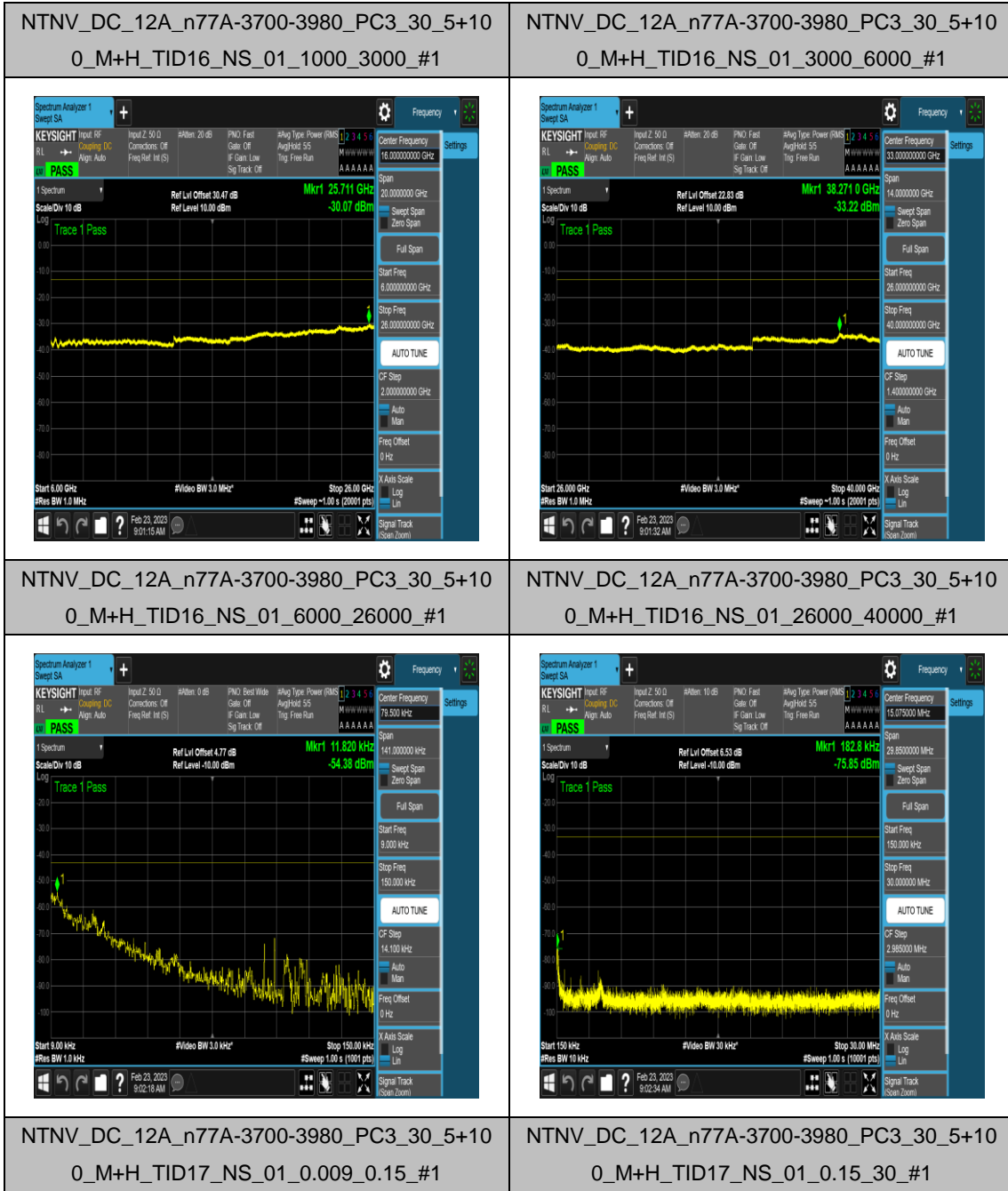
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
 0_M+H_TID16_NS_01_0.009_0.15_#1

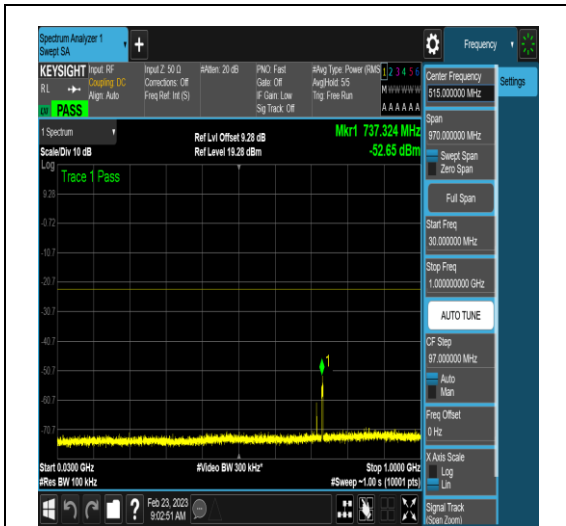


NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
 0_M+H_TID16_NS_01_0.15_30_#1

NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
 0_M+H_TID16_NS_01_30_1000_#1



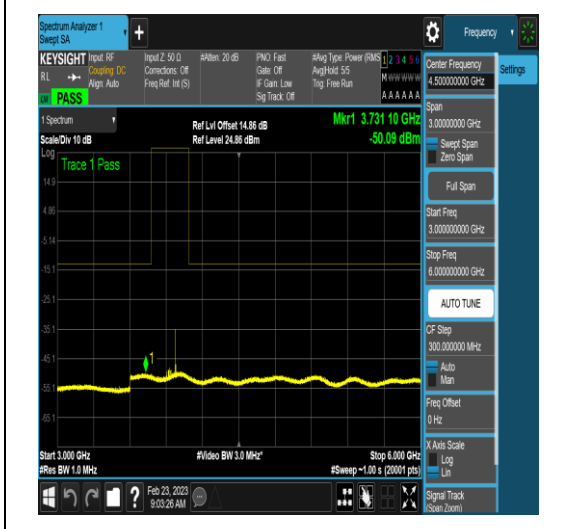




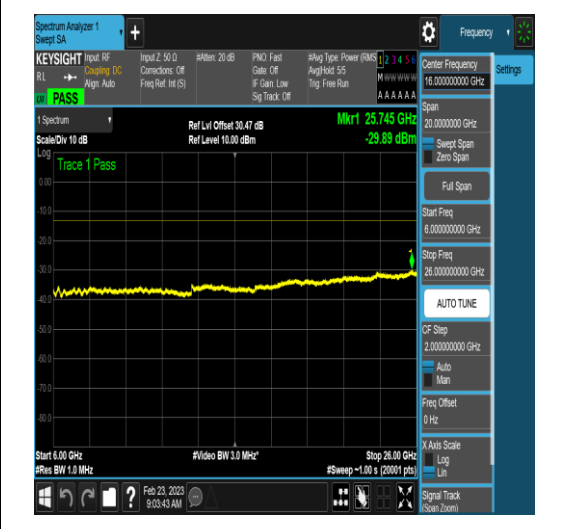
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID17_NS_01_30_1000_#1



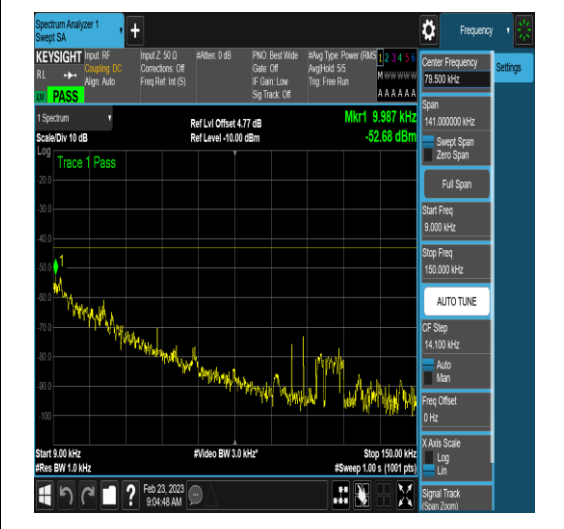
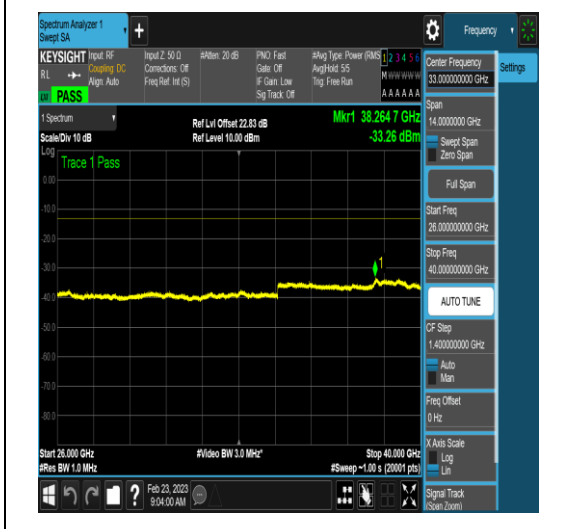
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID17_NS_01_1000_3000_#1

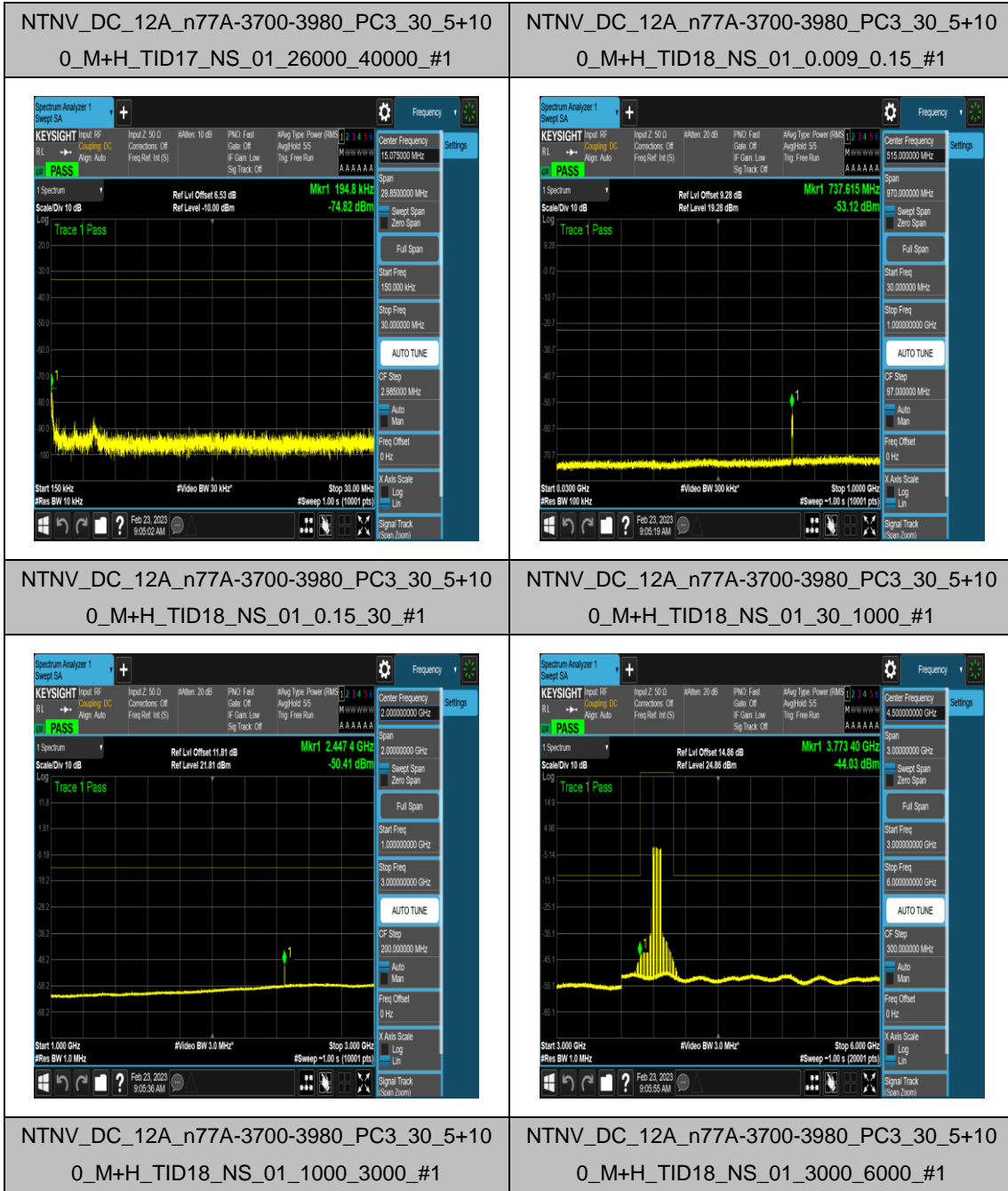


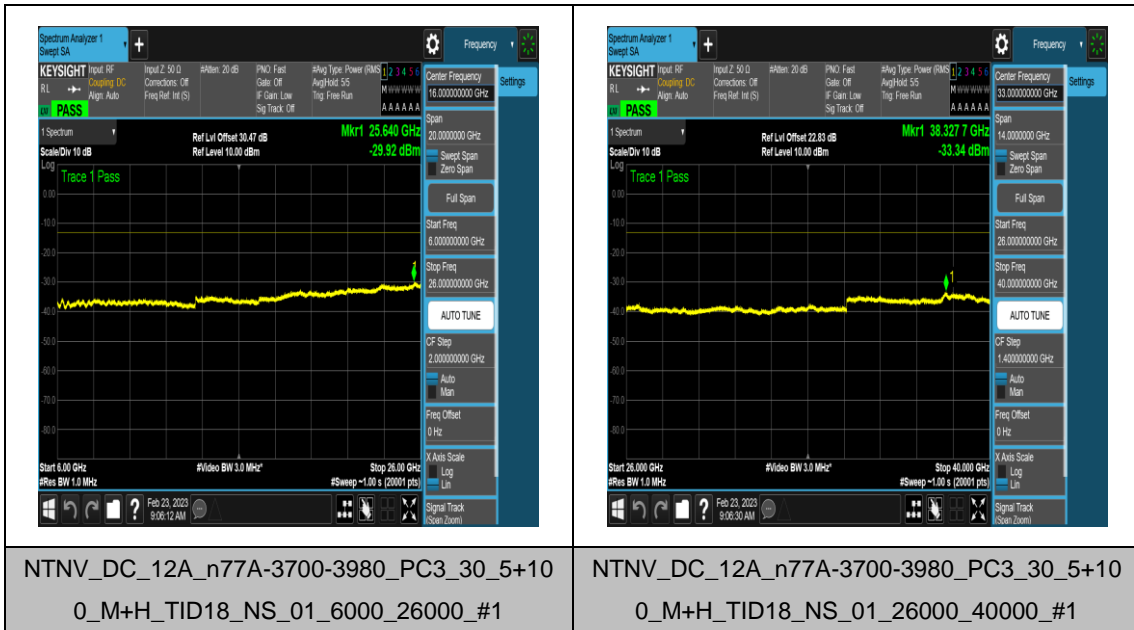
NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID17_NS_01_3000_6000_#1



NTNV_DC_12A_n77A-3700-3980_PC3_30_5+10
0_M+H_TID17_NS_01_6000_26000_#1







Appendix F: Frequency Stability for NSA

Test Result

Frequency Error VS. Voltage

Voltage										
Band	SC S	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
DC_12A_n77A-3 700-3980	3 0	5+10 0	DFT-Q PSK	M+L	Outer _Full	VN	NT	-10.87	0.00	PAS S
DC_12A_n77A-3 700-3980	3 0	5+10 0	CP-QP SK	M+L	Outer _Full	VN	NT	-3.47	0.00	PAS S
DC_12A_n77A-3 700-3980	3 0	5+10 0	DFT-Q PSK	M+M	Outer _Full	VN	NT	-10.21	0.00	PAS S
DC_12A_n77A-3 700-3980	3 0	5+10 0	CP-QP SK	M+M	Outer _Full	VN	NT	-8.98	0.00	PAS S
DC_12A_n77A-3 700-3980	3 0	5+10 0	DFT-Q PSK	M+H	Outer _Full	VN	NT	-0.09	0.00	PAS S
DC_12A_n77A-3 700-3980	3 0	5+10 0	CP-QP SK	M+H	Outer _Full	VN	NT	7.35	0.00	PAS S

Frequency Error VS. Temperature

Voltage										
Band	SC S	Bandwidth	Modulation	Channel	RB Config	Voltage	Temperature	Deviation(Hz)	Deviation (ppm)	Verdict
DC_12A_n77A- 3700-3980	3 0	5+10 0	DFT-Q PSK	M+L	Outer _Full	NV	TN	-4.8124 70	-0.001 283	PAS S
DC_12A_n77A- 3700-3980	3 0	5+10 0	CP-QP SK	M+L	Outer _Full	NV	TN	-6.0573 75	-0.001 615	PAS S
DC_12A_n77A- 3700-3980	3 0	5+10 0	DFT-Q PSK	M+M	Outer _Full	NV	TN	-5.6882 31	-0.001 481	PAS S
DC_12A_n77A- 3700-3980	3 0	5+10 0	CP-QP SK	M+M	Outer _Full	NV	TN	-4.1851 30	-0.001 090	PAS S
DC_12A_n77A- 3700-3980	3 0	5+10 0	DFT-Q PSK	M+H	Outer _Full	NV	TN	-18.103 123	-0.004 606	PAS S
DC_12A_n77A- 3700-3980	3 0	5+10 0	CP-QP SK	M+H	Outer _Full	NV	TN	-3.2076 06	-0.000 816	PAS S